

Register number \_\_\_\_\_

**SRM Institute of Science and Technology**  
**College of Engineering and Technology**  
**School of Computing**

SRM Nagar, Kattankulathur – 603203, Chengalpattu District, Tamilnadu

**Academic Year: 2023-24 (EVEN)**

**B.Tech-Computer Science & Engineering**

**SET – B-Answer Key**

**Test: CLA-T3**
**Date: 03.05.2024**
**Course Code & Title: 18CSE419T & GPU Programming**
**Duration: 2 periods**
**Year & Sem: III Year /VI Sem**
**Max. Marks: 50**
**Course articulation matrix:**

|      | PO 1 | PO 2 | PO 3 | PO 4 | PO 5 | PO 6 | PO 7 | PO 8 | PO 9 | PO 10 | PO 11 | PO 12 | PSO 1 | PSO 2 | PSO 3 |
|------|------|------|------|------|------|------|------|------|------|-------|-------|-------|-------|-------|-------|
| CO-1 | 3    |      |      |      |      |      |      |      |      |       |       |       |       |       | 3     |
| CO-2 |      | 3    | 2    |      |      |      |      |      |      |       |       |       |       |       | 3     |
| CO-3 |      | 3    | 3    |      |      |      |      |      |      |       |       |       |       |       | 3     |
| CO-4 |      | 3    | 3    |      |      |      |      |      |      |       |       |       |       |       | 3     |
| CO-5 |      |      | 3    | 1    |      |      |      |      |      |       |       |       | 2     |       | 3     |

| <b>Part – A(1*10=10 Marks)</b>  |  |              |           |           |           |                |
|---------------------------------|--|--------------|-----------|-----------|-----------|----------------|
| <b>Answer All the Questions</b> |  |              |           |           |           |                |
| <b>Q. No</b>                    | <b>Questions</b>   | <b>Marks</b> | <b>BL</b> | <b>CO</b> | <b>PO</b> | <b>PI Code</b> |
| 1                               | Which data clause of OpenACC allocates memory on GPU and copies data from host to GPU when entering region?<br>a) Copy(int)<br><b>b) Copyin(list)</b><br>c) Copyout(list)<br>d) Create(list)   | 1            | 1         | CO 4      | 2         | 2.2.1          |
| 2                               | The ASYNC clause allows us to run some operations<br>a) Parallely<br>b) Synchronously<br><b>c) Concurrently</b><br>d) One after another  | 1            | 1         | CO 4      | 2         | 2.2.1          |
| 3                               | When using the kernels directive in OpenACC,<br><b>a) The auto clause is implied</b><br>b) The independent clause is implied<br>c) The dependent clause is implied<br>d) The seq clause is implied   | 1            | 1         | CO 4      | 2         | 2.2.1          |
| 4                               | The clause firstprivate is like private except that<br>a) The private clauses are initialized to the same value used on the device<br>b) The private clauses are initialized to the default values<br><b>c) The private clauses are initialized to the same value used on the host</b><br>d) The private clauses are uninitialized | 1            | 1         | CO 4      | 2         | 2.2.1          |
| 5                               | When parallelizing our loops the highest level of parallelism is<br><b>a) Gang level parallelism</b><br>b) Worker level parallelism<br>c) Vector level parallelism<br>d) warp level parallelism  | 1            | 1         | CO 4      | 2         | 2.2.1          |
| 6                               | Data must be visible on the _____ when we run our parallel code<br>a) host<br><b>b) device</b><br>c) host and device<br>d) memory  | 1            | 1         | CO 5      | 3         | 3.2.1          |

Register number \_\_\_\_\_

|    |  |   |   |      |   |       |
|----|--|---|---|------|---|-------|
| 7  | Which one is not an unstructured data clause?<br>a) <b>Copy(list)</b><br>b) Copyin(list)<br>c) Copyout(list)<br>d) Create(list)  | 1 | 1 | CO 5 | 3 | 3.2.1 |
| 8  | The _____ directive informs the compiler to parallelize the iterations of the next loop<br>a) Parallel<br>b) <b>Loop</b><br>c) Kernel<br>d) gang   | 1 | 1 | CO 5 | 3 | 3.2.1 |
| 9  | Programming in which two or more unrelated operations can occur independently or even at the same time without immediate synchronization is<br>a) Synchronous programming<br>b) <b>Asynchronous programming</b><br>c) GPU programming<br>d) High-level programming | 1 | 1 | CO 5 | 3 | 3.2.1 |
| 10 | Which of the following is an unstructured data directive?<br>a) Data directive<br>b) <b>Enter data directive</b><br>c) Kernel directive<br>d) Exit parallel directive  | 1 | 1 | CO 5 | 3 | 3.2.1 |

**Part – B (4\*4=16 marks)**  
**Answer any four Questions**

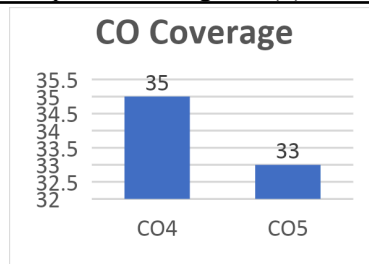
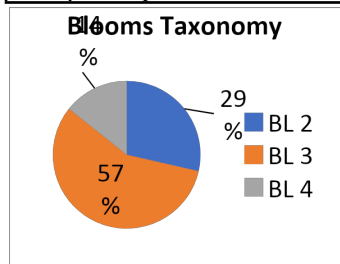
| Q. No | Question  | Marks | BL | CO  | PO    | PI Code |
|-------|---|-------|----|-----|-------|---------|
| 11    | Differentiate parallel and kernel directives in OpenACC.<br><br><ul style="list-style-type: none"> <li>The parallel directive instructs the compiler to create parallel <i>gangs</i> on the accelerator</li> <li>Gangs are independent groups of worker threads on the accelerator</li> <li>The code contained within a parallel directive is executed redundantly by all parallel gangs</li> <li>The kernels directive instructs the compiler to search for parallel loops in the code</li> <li>The compiler will analyze the loops and parallelize those it finds safe and profitable to do so</li> <li>The kernels directive can be applied to regions containing multiple loop nests</li> </ul> | 4     | 2  | CO4 | 2 & 3 | 2.2.1   |
| 12    | What is asynchronous programming in OpenACC?.What is the clause used to implement the same and how?   | 4     | 3  | CO5 | 3     | 2.2.1   |
| 13    | How auto clause and independent clause are used to implement parallelism in OpenACC?<br><ul style="list-style-type: none"> <li>The <b>auto</b> clause tells the compiler to decide whether or not the loop is parallelizable</li> <li>The auto clause can be very useful when you are unsure of whether or not a loop is safe to parallelize</li> </ul>   | 4     | 3  | CO4 | 2 & 3 | 2.2.1   |

Register number \_\_\_\_\_

|  |  |    |   |     |   |       |
|--|--|----|---|-----|---|-------|
|  | <ul style="list-style-type: none"> <li>The <b>independent</b> clause asserts to the compiler that the loop is parallelizable</li> <li>This will overwrite any decision that the compiler makes about the loop</li> <li>Adding the independent clause could force the compiler to parallelize a non-parallel loop</li> <li>Allows the programmer to force parallelism when using the kernels directive</li> </ul> <p>Refer ppt for code and notes</p>   |    |   |     |   |       |
| 14   | <p>What are private and firstprivate variables in OpenACC?</p> <ul style="list-style-type: none"> <li>The <b>private</b> clause allows the programmer to define a list of variables as "thread-private".</li> <li>Each thread will be given a private copy of every variable in the comma-separated list</li> <li><b>firstprivate</b> is like private except that the private values are initialized to the same value used on the host. <b>private</b> variables are uninitialized.</li> <li>Variables in <b>private</b> or <b>firstprivate</b> clause are private to the loop level on which the clause appears.</li> <li>Private variables on an outer loop are shared within inner loops.</li> <li>The <b>private</b> clause allows the programmer to define a list of variables as "thread-private".</li> <li>Each thread will be given a private copy of every variable in the comma-separated list</li> <li><b>firstprivate</b> is like private except that the private values are initialized to the same value used on the host. <b>private</b> variables are uninitialized.</li> </ul> <p>Refer ppt for code</p> | 4  | 3 | CO5 | 3 | 2.2.1 |
| 15   | What are vectors in OpenACC? Illustrate with code.   | 4  | 3 | CO5 | 3 | 2.2.1 |
| <p align="center"><b>Part – C (2*12=24 marks)</b><br/> <b>Answer any Two Questions</b></p> |  |    |   |     |   |       |
| 16   | <p>Apply Gang and worker level parallelism to parallelize the loop in order to optimize it.(12)<br/> Refer PPT</p> <pre>#pragma acc kernels loop gang worker(1) for(int x = 0; x &lt; 4; x++){     #pragma acc loop vector(8)     for(int y = 0; y &lt; 8; y++){         array[x][y]++;     } }</pre> <ul style="list-style-type: none"> <li>The diagram shows a single gang, though the compiler will be able to generate as many gangs as it wants</li> <li>These gangs are completely separate from each other, and are indistinguishable</li> </ul>  | 12 | 3 | CO5 | 3 | 2.2.1 |

Register number \_\_\_\_\_

|    |   |    |   |     |       |       |
|----|---|----|---|-----|-------|-------|
|    | <ul style="list-style-type: none"> <li>We will show these gangs apply to a physical loop diagram, but this representation may not be 100% accurate to what the compiler might decide</li> </ul> |    |   |     |       |       |
| 17 | List any three directives of OpenACC and illustrate the same with the code segment.(12)   | 12 | 3 | CO4 | 2 & 3 | 2.2.1 |
| 18 | Illustrate how collapse clause is used to combine multiple parallel loops into a single loop.(6)<br>Compare and Contrast explicit and implicit data regions.(6)                                 | 12 | 3 | CO4 | 2 & 3 | 2.2.1 |



Approved by Audit Professor/ Course Coordinator

Register number \_\_\_\_\_