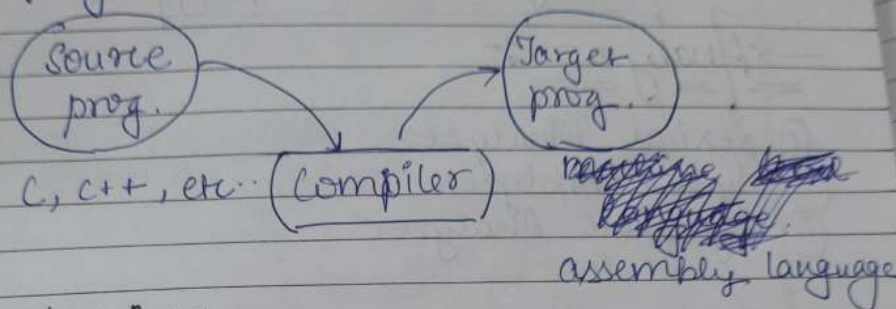


# Compiler Design :- Unit - I :-

- ① Translator
- ② Phases.

Compiler :- translate  
Compiler can convert a source program to target program.



## Interpreter :-

Translates the code line by line.

## Language processor :-

Pre processor | feeds source prog to compiler.

↓  
Compiler | source prog to target prog.

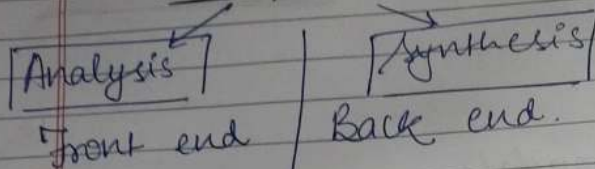
↓  
Assembler | assembly language to machine level language

↓  
Loader / linker | links all the files.

↓  
loads the machine level language to destination

(Single point of execution)

## Compiler.



Analyze Source prog.

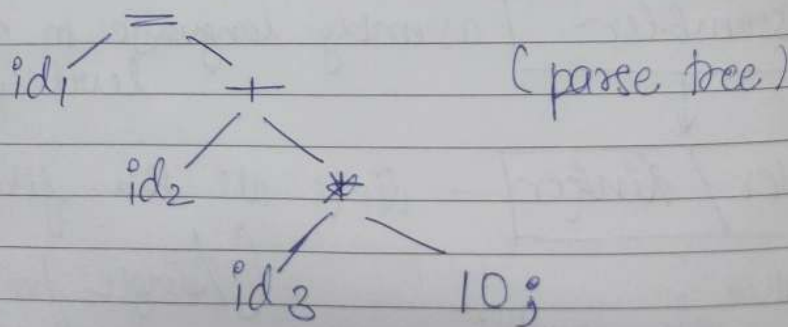
Analyze :-

- ① Lexical analyzer
- ② Syntax Analyzer
- ③ Semantic Analyzer

### ① Lexical Analyser :-

(eg:-)  $c = a + b * 10 ;$   
 $id_1 = id_2 + id_3 * 10 ;$  (tokens)

### ② Syntax Analyzer :-



### ③ Semantic

$id_1 =$   
 $id_2$

### 16m → Phases

- ① Lexical
- ② Syntax
- ③ Semantic
- ④ Intermediate
- ⑤ Code
- ⑥ Code

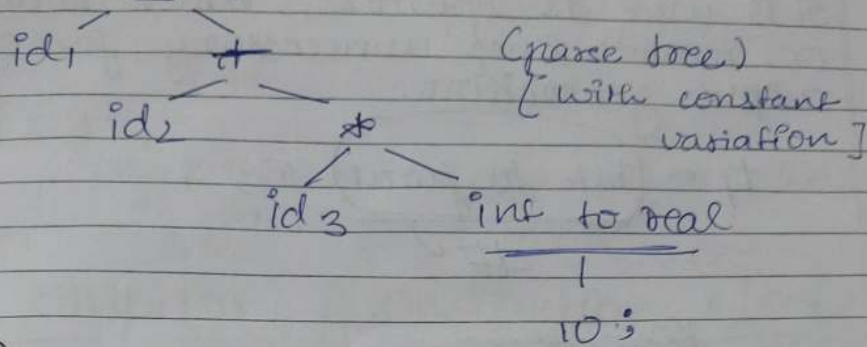
### ④ Inter

conv  
code

$t_1$   
 $t_2$   
 $t_3$   
 $t_4$   
 $id$



### ③ Semantic Analyzer :-



### 16m → Phases of compiler :-

- |                               |              |
|-------------------------------|--------------|
| ① Lexical analyzer            | } Front End. |
| ② Syntax analyzer             |              |
| ③ Semantic Analyzer           |              |
| ④ Intermediate code generator |              |
| ⑤ Code optimizer              | } Back End.  |
| ⑥ Code generator.             |              |

### ④ Intermediate code Generator :-

converts semantic analyze to 3 address code.

```

t1 = int to float 10;
t2 = id3 * t1;
t3 = id2 + t2;
t4 = t3;
id1 = t4;
  
```

### ⑤ Code optimizer :-

It is used to optimize either redundancy or remove lines unnecessary for effective output generation.

$t_1 = \text{int to float } 10;$   
#

~~xxxxxx~~

$t_1 = id_3 * \#10;$

$t_2 = id_2 + t_1;$

$id_1 = t_2;$

Optimized Code

### ⑥ Code Generator :-

It is used to produce the target program. If assembly language is produced without any redundancy.

mov  $\#10, R_1$   
mov  $id_3, R_2$   
mul  $R_2, R_1$   
mov  $id_8, R_3$   
add  $R_3, R_1$   
mov  $R_1, id_1$

Q. What is  
One con

2nd Pass :-

(15m) Com

① Parser  
Genera

② Scan  
Gen

③ Syn  
Tree

Human  
analysis



redundancy  
or effective

Q What is a pass?

One complete scan of a program is called as pass.

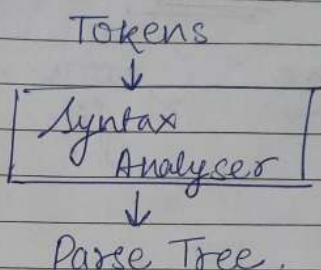
2m Pass: 1 Pass → 6 phases of compiler.

4m 2 Pass → (differentiates between front end & back end.)

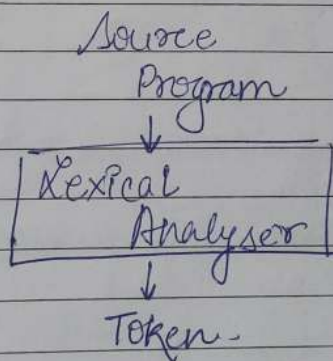
(15m)

## → Compiler Construction Tools :-

① Parser → Generator

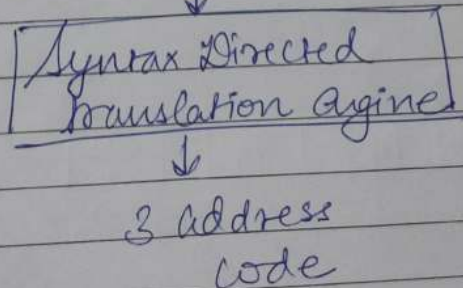


② Scanner → Generator



③ Syntax Directed Translation Engine → Parse tree

||  
Semantic + ICG  
analyzer



#### ④ Automatic Code Generator :-

||  
Optimiser + Code  
Generator

3 address  
code

↓  
[ A C G ]

↓  
Assembly  
language

#### ⑤ Compiler Construction :-

tool.exe (converts to executable  
file.)

→ Activity  
position



Activity - 1 :-

position = initial + rate \* 100;

↓  
[Lexical Analyser]

id<sub>1</sub> = id<sub>2</sub> + id<sub>3</sub> \* 100;

↓  
[Syntax Analyser]

id<sub>1</sub> = + id<sub>2</sub> \* id<sub>3</sub> 100;

↓  
[Semantic Analyser]

id<sub>1</sub> = + id<sub>2</sub> \* int to real  
100;

↓  
[Intermediate Code Generator]

↓  
t<sub>1</sub> = #100;  
t<sub>2</sub> = id<sub>3</sub> \* t<sub>1</sub>;  
t<sub>3</sub> = id<sub>2</sub> + t<sub>2</sub>;  
t<sub>4</sub> = t<sub>3</sub>;  
id<sub>1</sub> = t<sub>4</sub>;

↓  
[Code Optimizer]

↓  
t<sub>1</sub> = id<sub>3</sub> \* #100;  
t<sub>2</sub> = id<sub>2</sub> + t<sub>1</sub>;  
id<sub>1</sub> = t<sub>2</sub>;

↓

### Code Generator

```

mov #100, R1
mov id3, R2
mul R2, R1
mov id3, R3
add R3, R1
mov R1, id1
    
```

Ans

### → Input Buffering :-

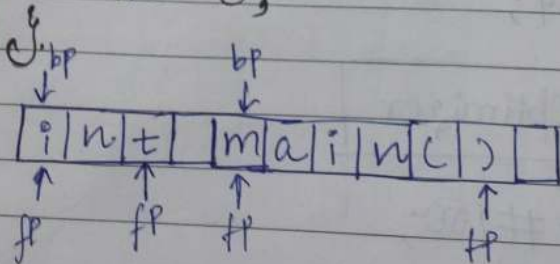
- ① Begin Pointer - Reads input from blank space
- ② Forward Pointer - Reads the buffer and calls the BP when a blank space is found

#### Activity-I

Q Find the number of tokens which is given in the following input.

```
int main()
```

```
printf("SRM University");
return 0;
```





classmate

Date \_\_\_\_\_

Page \_\_\_\_\_

→ Specification of token :-

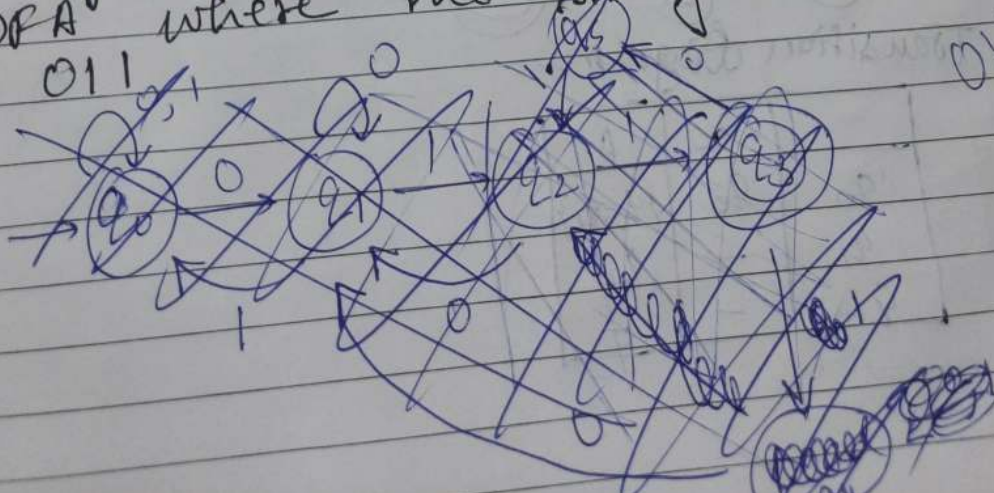
② Lexeme :- It is ~~in~~ predefined rules

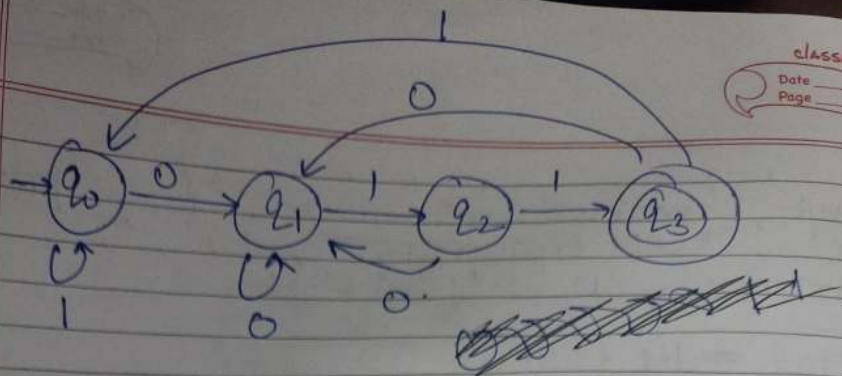
③ Pattern :- It is a set of rules that define a token.

→ Finite Automata

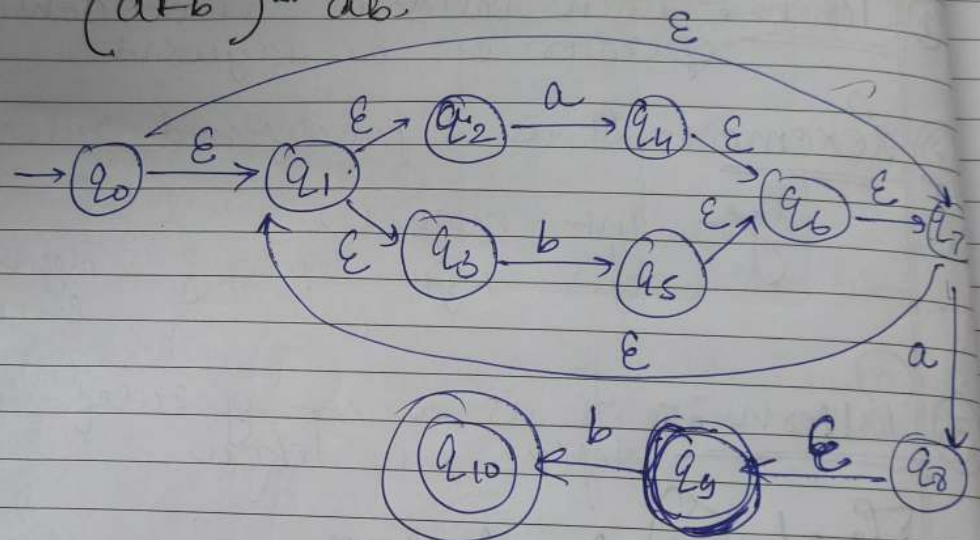
## Activity - 2.

Q. Design a state transition diagram for DFA where the string ends with 11



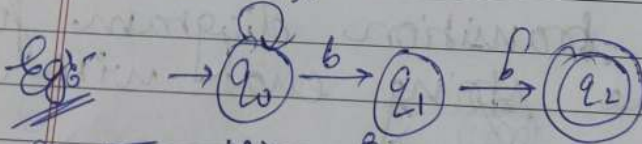


Q Draw an  $\epsilon$ -NFA for given exp.  
 $(a+b)^* ab$



→ NFA to DFA (8m)

Step-I  
 ①  $q_0$   
 ②  $T_{ab}$

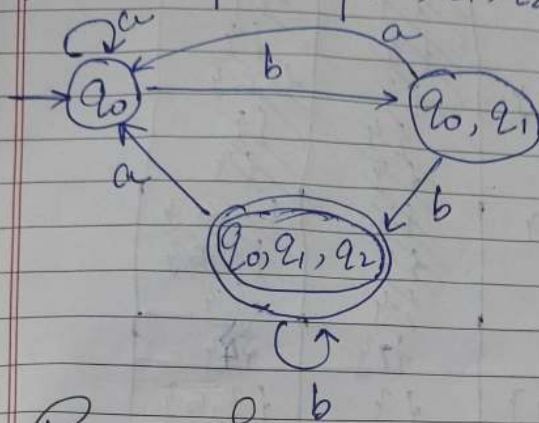


① Transition Diagram

	a	b
$q_0$	$q_0$	$\{q_0, q_1\}$
$q_1$	-	$q_2$
$q_2$	-	-



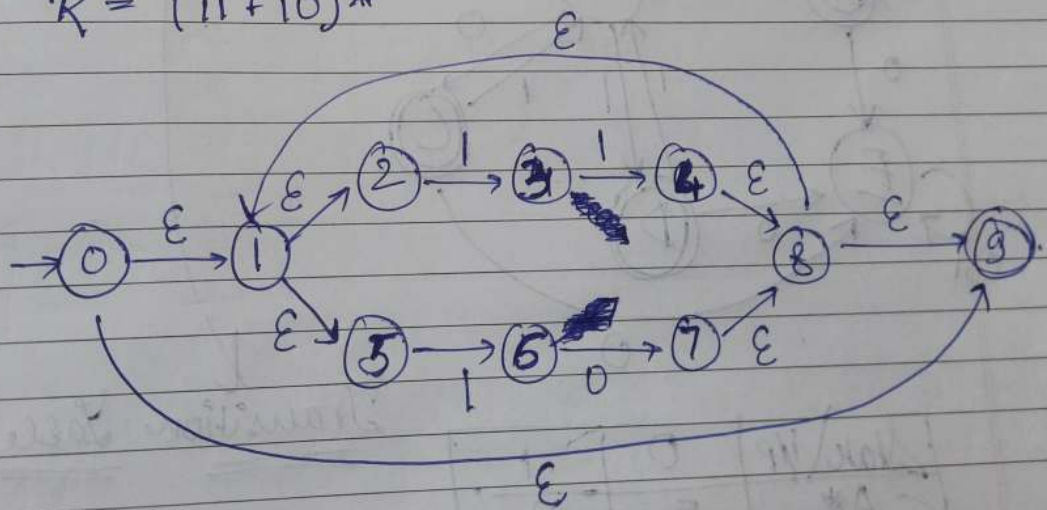
	a	b
$q_0$	$q_0$	$\{q_0, q_1\}$
$\{q_0, q_1\}$	$q_0$	$\{q_0, q_1, q_2\}$
$\{q_0, q_1, q_2\}$	$q_0$	$\{q_0, q_1, q_2\}$



→ Regular Expression for DFA :- (10.m)

①  $RL \rightarrow RE \rightarrow ENFA \rightarrow DFA \rightarrow \text{min DFA}$

$$R = (11 + 10)^*$$



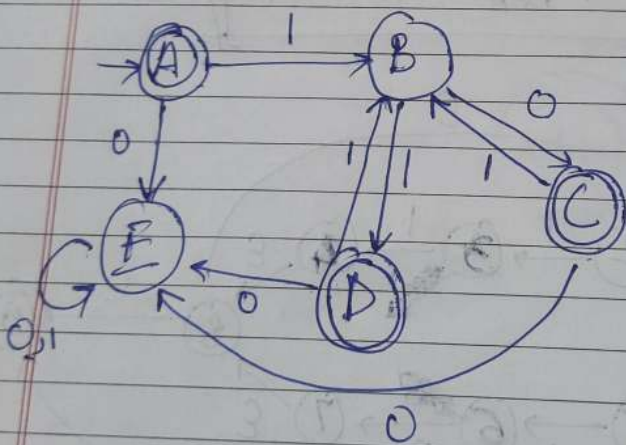
Transition Table

# ENFA to DFA :-

classmate

Date  
Page

X	$Y = \epsilon \text{ closed}(x)$	$\delta(x, 0)$	$\delta(x, 1)$
A {0, 3}*	{0, 1, 2, 5, 9}	{3}	{3, 6}
1	{2, 5}	{3}	{3, 6}
2	{3}	{3}	{3}
3	{3}	{3}	{3}
4	{8, 1, 2, 5, 9}	{3}	{3, 6}
5	{5}	{3}	{3}
6	{8}	{3}	{3}
7	{8, 1, 2, 5, 9}	{3}	{3, 6}
8	{1, 2, 5, 9}	{3}	{3, 6}
9	{3}	{3}	{3}
B	{3, 6}	{7}	{4}
C	{7}*	{7, 8, 1, 2, 5, 9}	{3, 6}
D	{4}*	{4, 8, 1, 2, 5, 9}	{3, 6}
E	{}	{}	{}

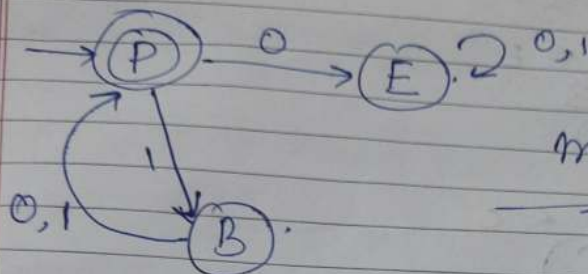


Transition Table :-

State \ i/p	0	1
A*	E	B
B	C	D
C*	E	B
D*	E	B
E	E	E



State \ Input	0	1
* P	E	B
B	P	P
E	E	E

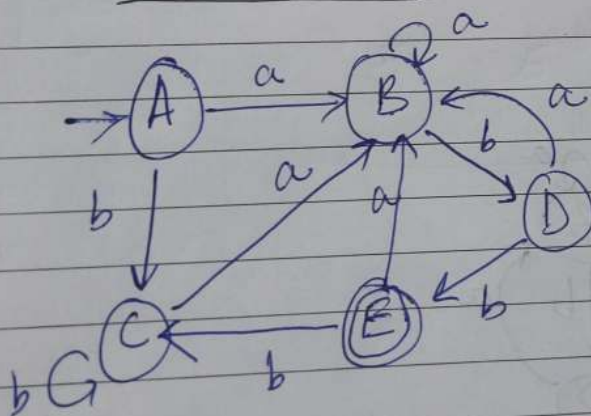


min DFA

→ Minimization of DFA.

Eg:- Construct an NFA Automata.

State \ Input	a	b
A	B	C
B	B	D
C	B	C
D	B	E
* E	B	C



(i)  $\pi = \{A, B, C, D, E\}$

$\pi' = \{A, B, C, D\} \quad \{E\}$

$$② \quad f(A, a) (A, b) \leftarrow A$$

$$f(A, a) = B$$

$$f(A, b) = C$$

$$f(B, a) (B, b) \leftarrow B$$

$$f(B, a) = B$$

$$f(B, b) = D$$

$$f(C, a) (C, b) \leftarrow C$$

$$f(C, a) = B$$

$$f(C, b) = C$$

$$f(D, a) (D, b) \leftarrow D$$

$$f(D, a) = B$$

$$f(D, b) = E$$

$$f(E, a) (E, b) \leftarrow E$$

$$f(E, a) = B$$

$$f(E, b) = C$$

③

	a	b
A	B	A
B	B	D
D	B	E
*E	B	A

