



ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

GAPses breakout pcb

Drawing number:

Rev: v1.0

Format:

Laboratory: IIS ETH Zürich

Sheet: 00_EEG_AFE.SchDoc

Date: 26.09.2024 13:57:50

A4 Q

Drawn by: S.Frey

Page * of *

File: C:\Users\s-fre\Github\ExG_glasses\Hardware\PCBs\breakout_pcb_flex\00_EEG_AFE.SchDoc