

A

B

C

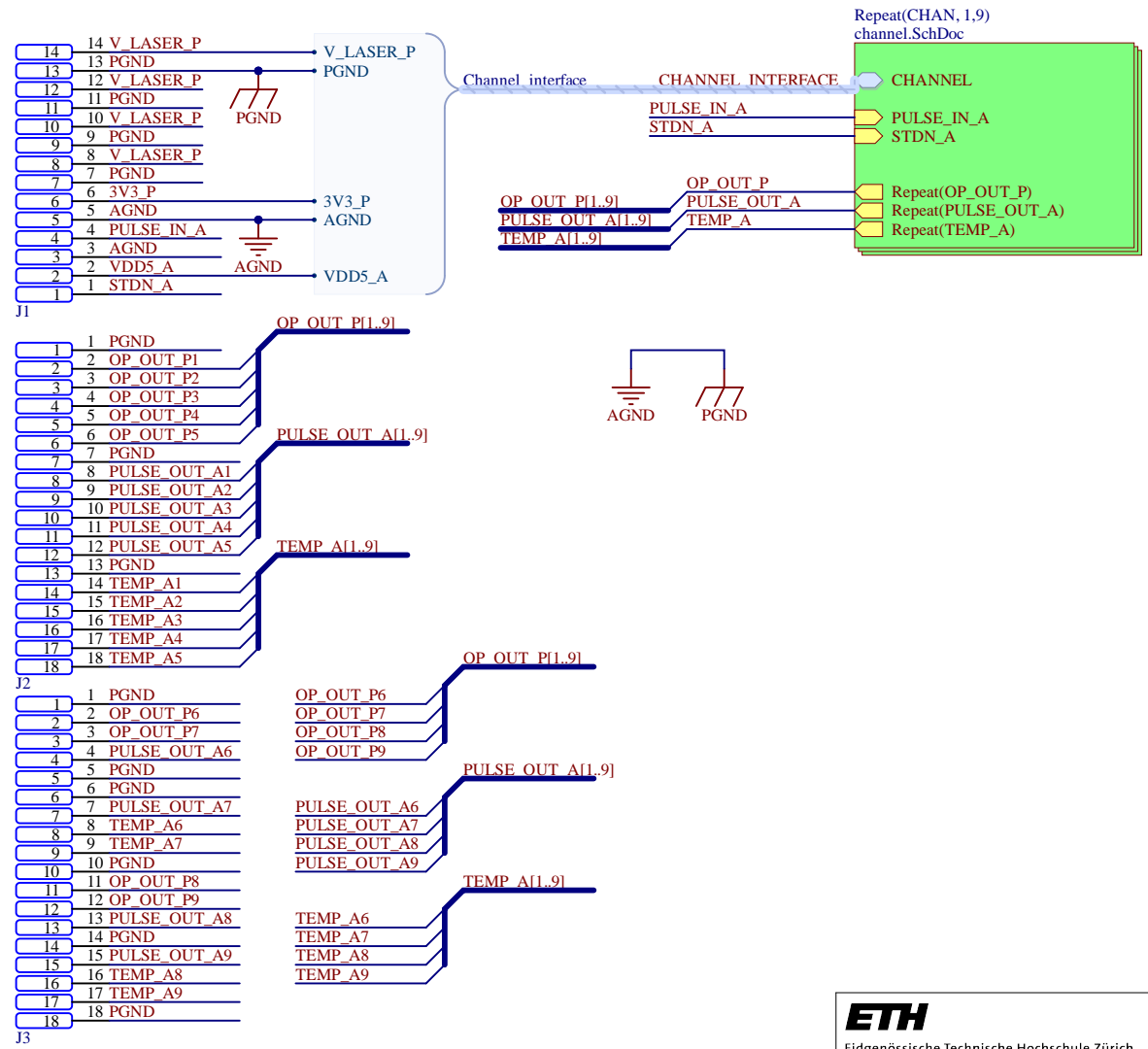
D

A

B

C

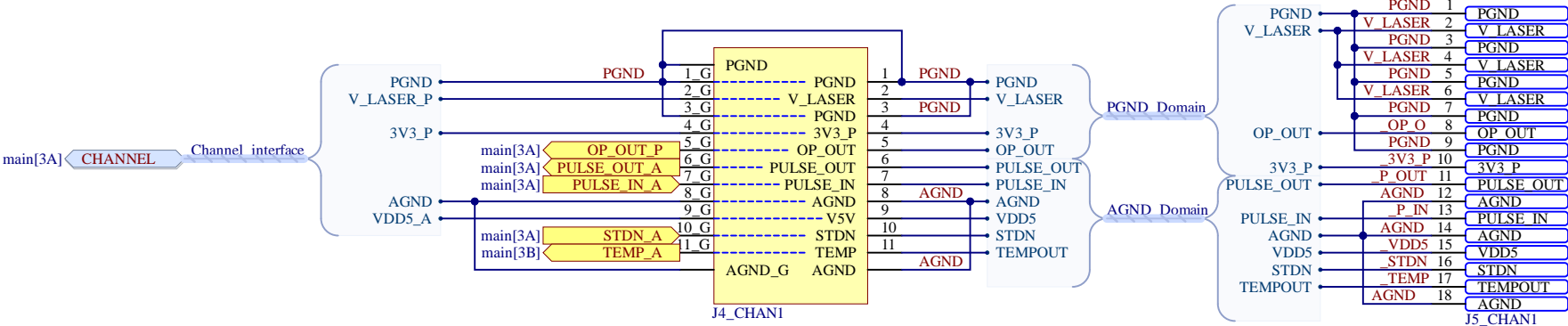
D



ETH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich	Project: HELIOS-R LD_Harness
	Drawn By: villanif Checked By: liuxian
	File: main.SchDoc
Laboratory: IIS Date: 17/07/2025 15:20:25 Rev: 1 Variation: [No Variations]	Sheet: 1 / 2

P_IN_CHAN1 has to be set to an i

Note: the connectors ar on opposite sides of the pcb to make space for cooling



ETH

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Swiss Federal Institute of Technology Zurich

Laboratory: IIS

Date: 17/07/2025 15:20:25

Rev: 1

Variation: [No Variations]

File: channel.SchDoc

Project: HELIOS-R

Pulsar Harness Channel

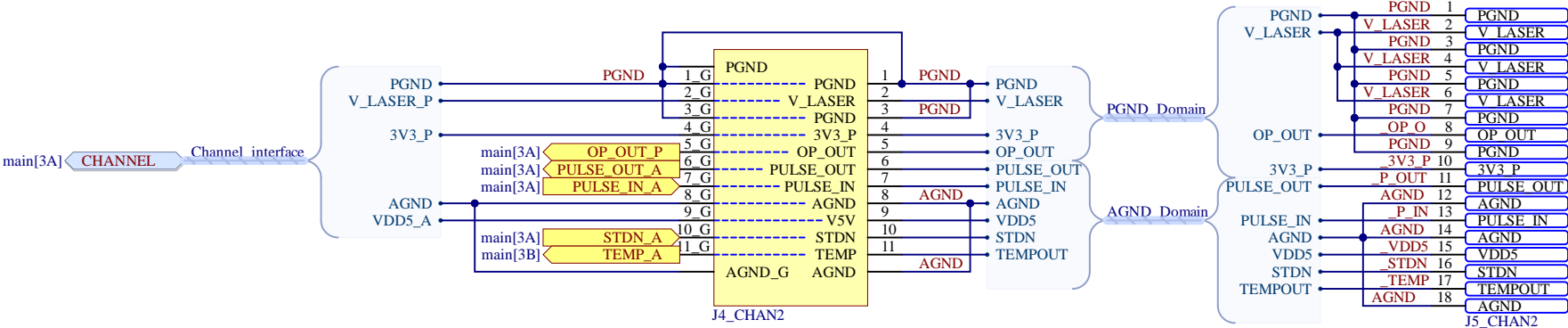
Drawn By: villanif

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Sheet: 2 / 2

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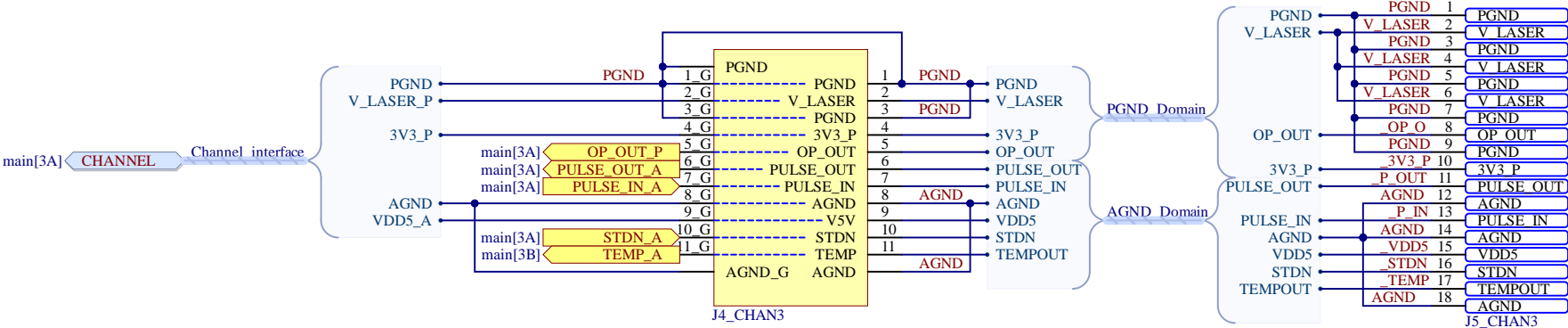
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Pulsar Harness Channel

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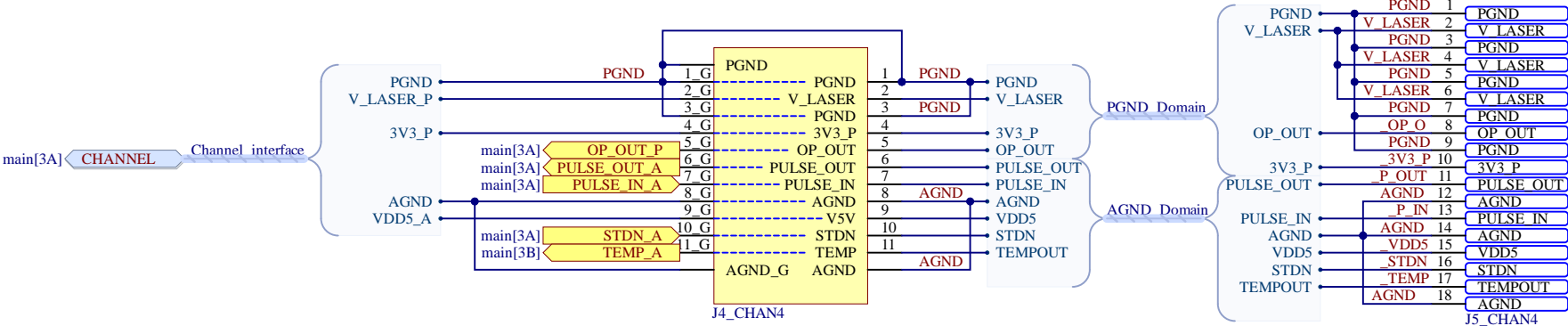
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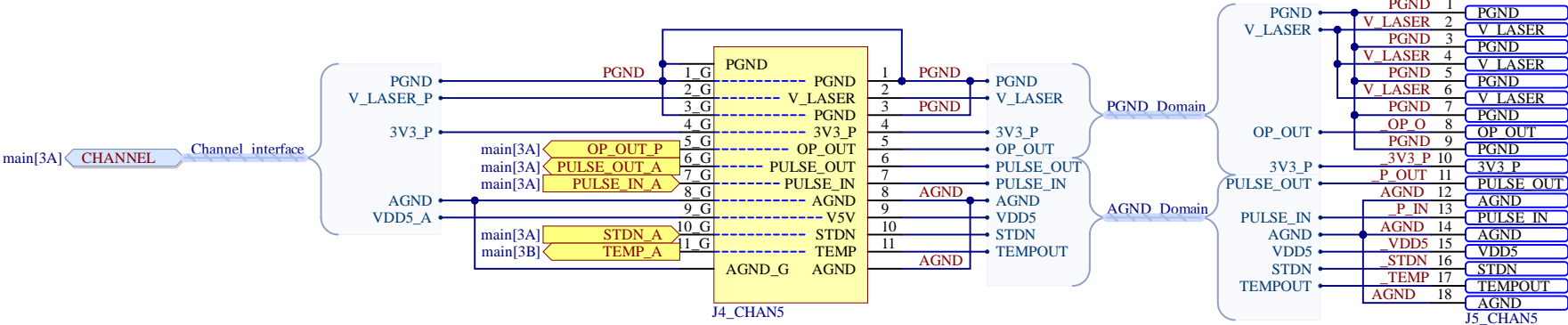
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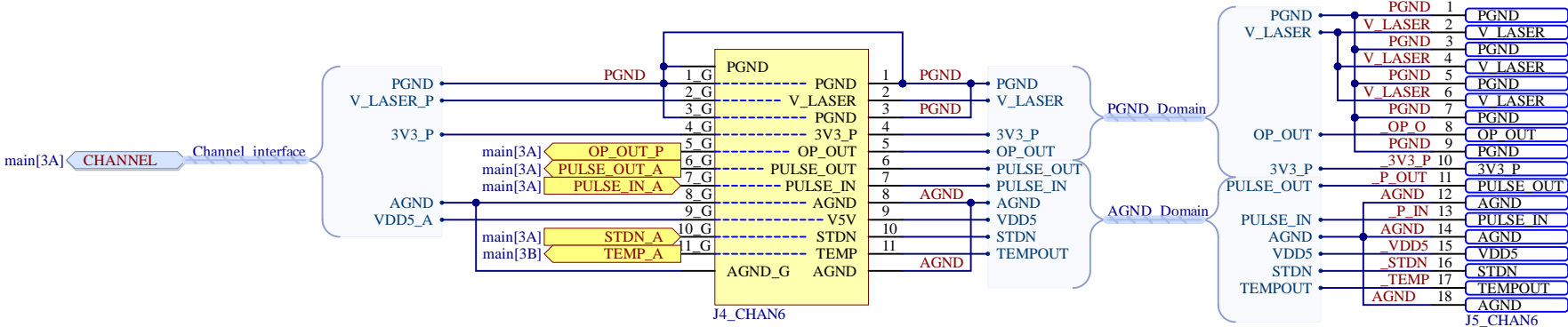
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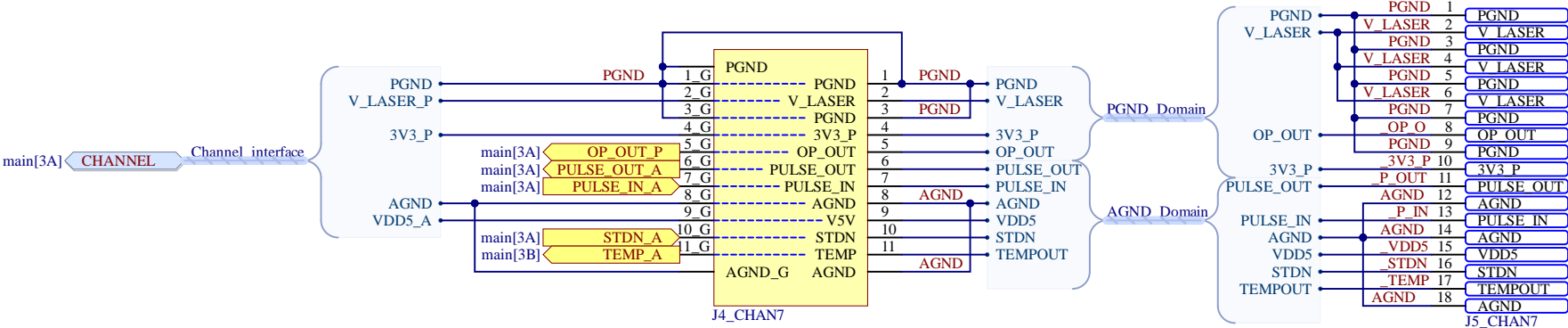
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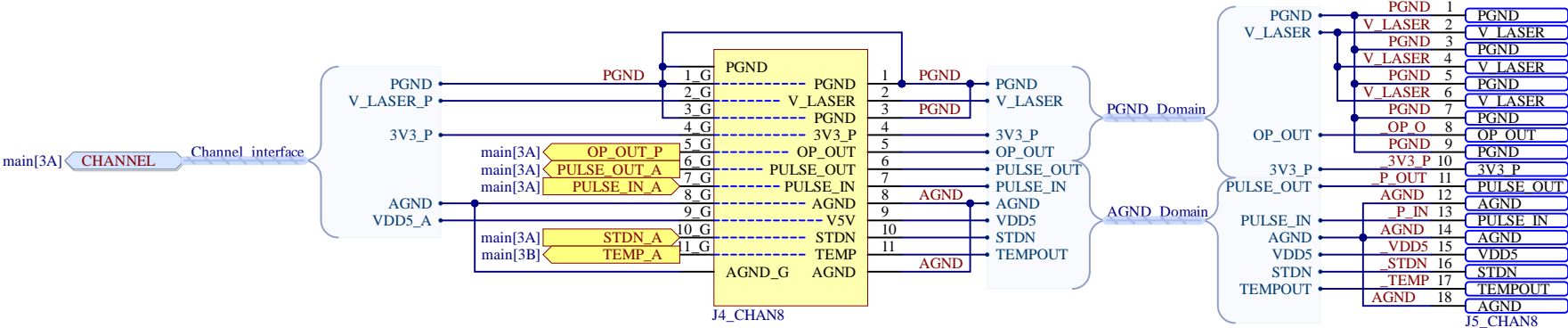
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