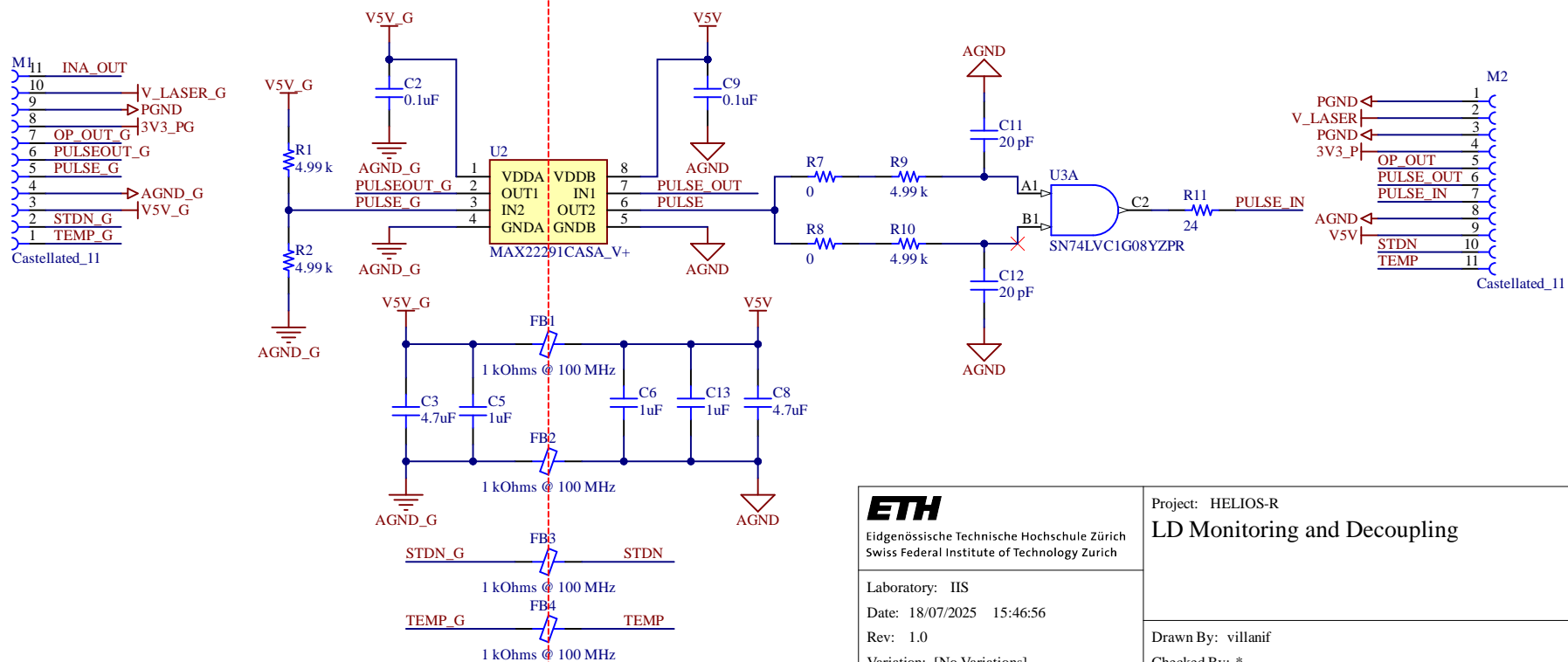


Global domain -  
Controller and power supply side

High Frequency Isolated domain -  
Pulser side



Requirements for digital isolator:  
min pulse (unshortened) is 100 ns, max pulse 250ns  
Specifications might be exceeded without causing extra damage

power consumption of the whole board <10 mA  
1 global to isolated high speed (PULSE)  
1 global to isolated low speed (STDN)  
Optional: 1 high-speed isolated to global (PULSEOUT)

**ETH**

Eidgenössische Technische Hochschule Zürich  
Swiss Federal Institute of Technology Zurich

Laboratory: IIS

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Rev: 1.0

Variation: [No Variations]

File: ld\_monitoring\_decoupling.SchDoc

Project: HELIOS-R

LD Monitoring and Decoupling

Drawn By: villanif

Checked By: \*

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