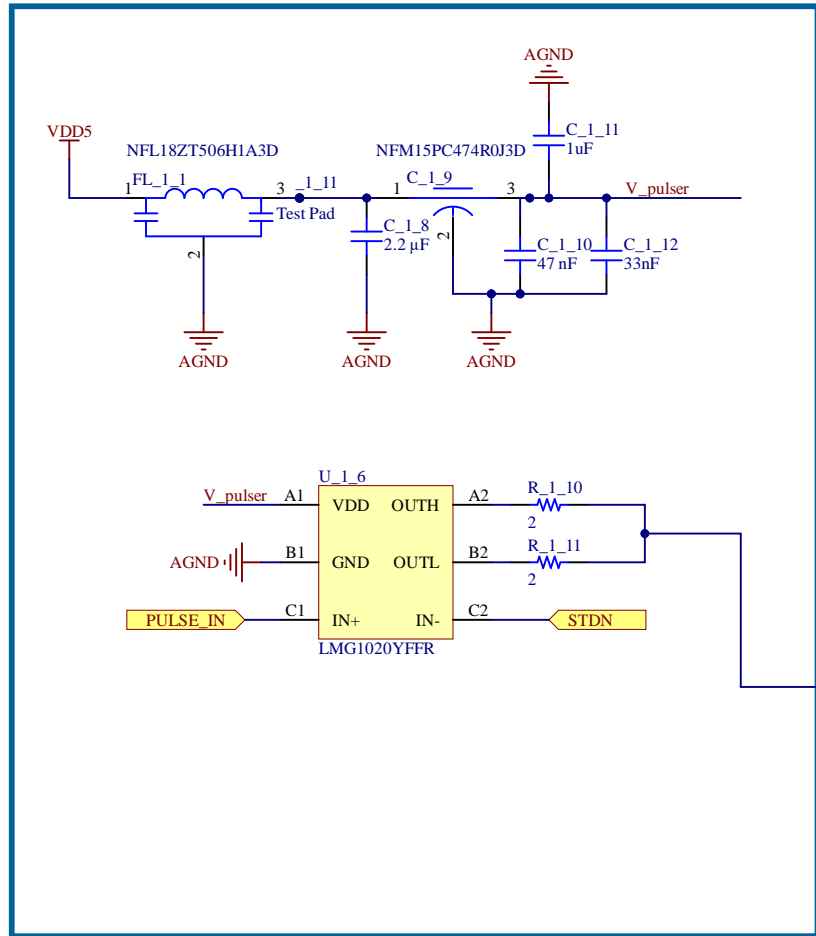
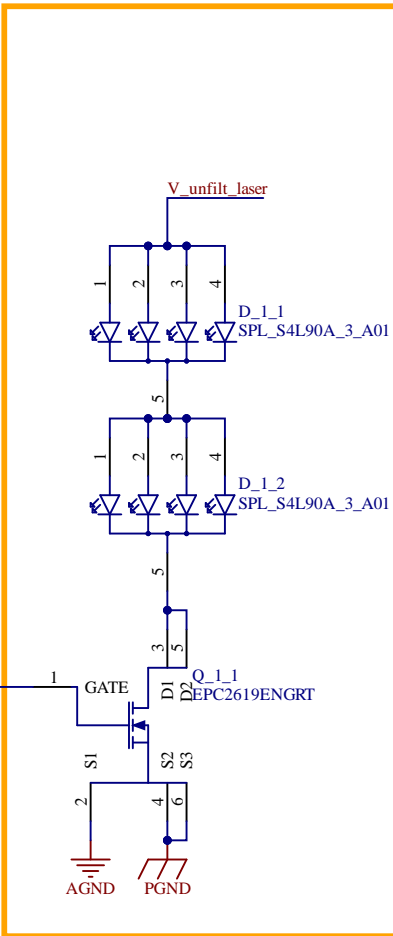


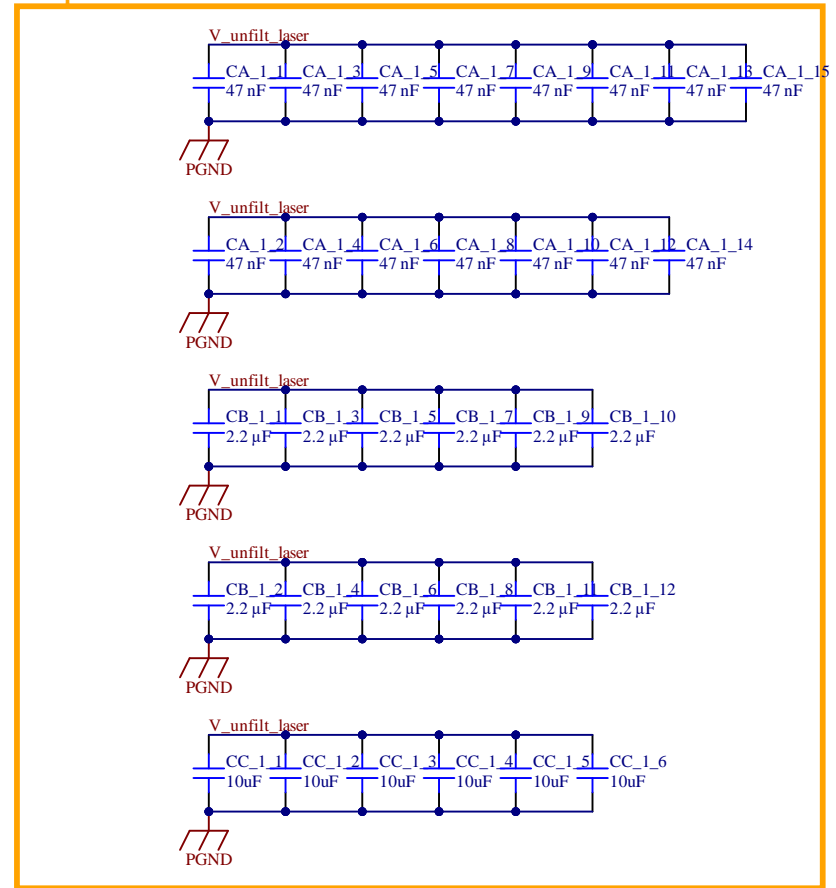
Pulser



Laser Diodes



Capacitor Bank



Mosfet source is ground star point. Provide sufficient external isolation where necessary to prevent ground loops. every AGND domain should be isolated independently

ETH

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Laboratory: IIS

Date: 17/07/2025 15:02:31

Rev: 2

Variation: fabrication

File: Pulser.SchDoc

Project: HELIOS-R

Pulser Laser Board

The frontend pcb for the pulser module

Has to be used together with a controller board

Pulses 4 laser modules with 320 A and 20-24 V

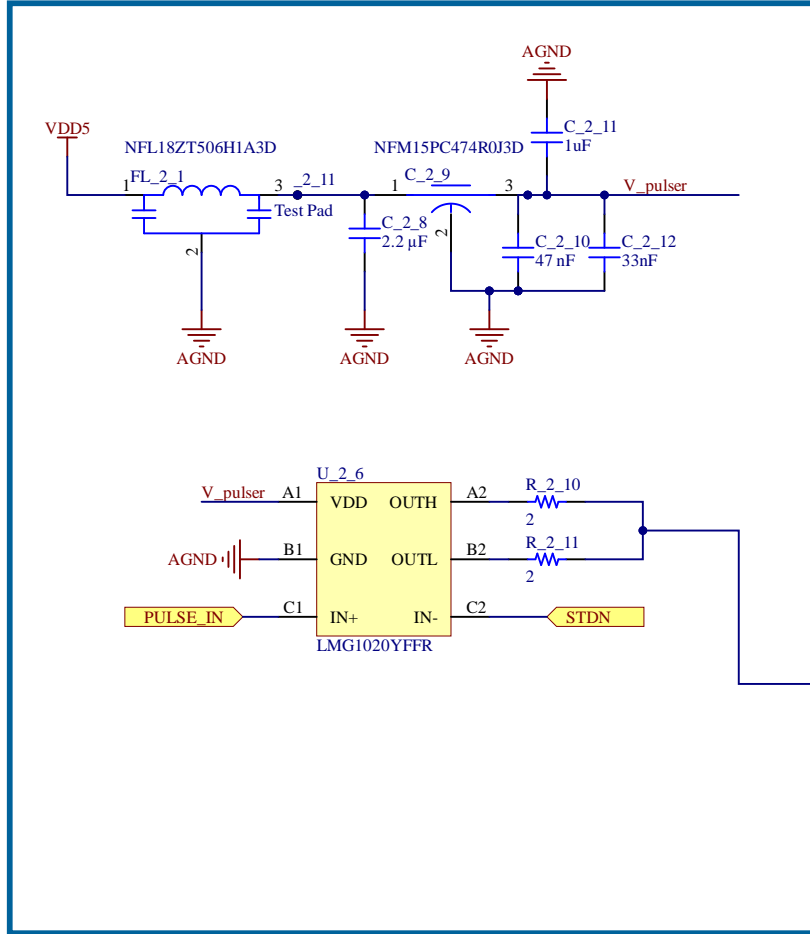
Do not pulse longer than 150 ns

Drawn By: villanif

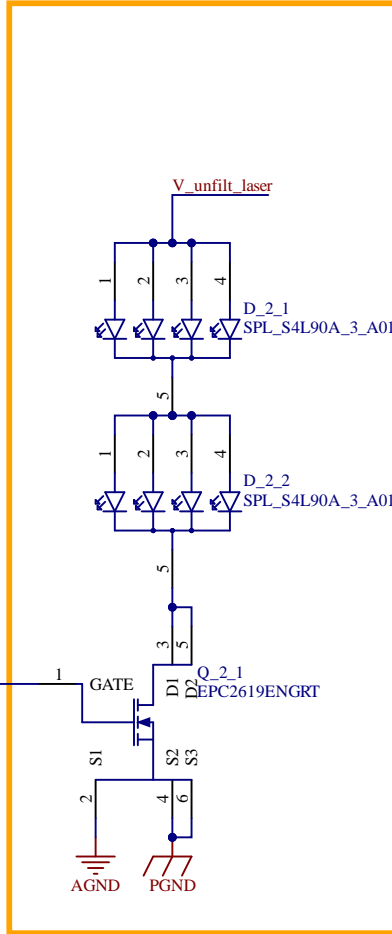
Checked By: liuxian

Sheet: 2 / 2

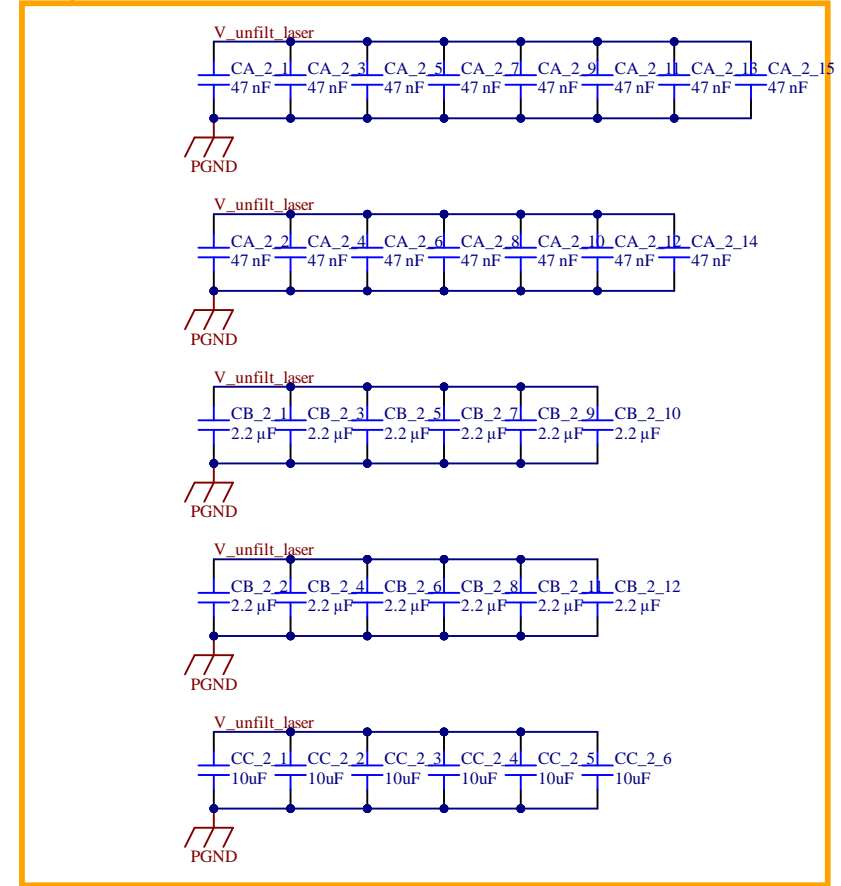
Pulser



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Sheet: 2 / 2