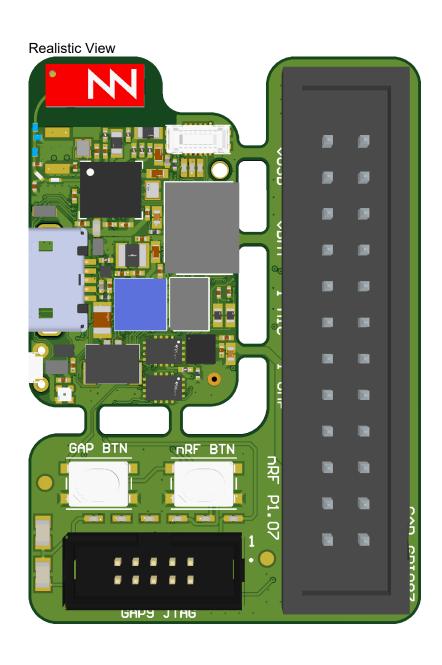
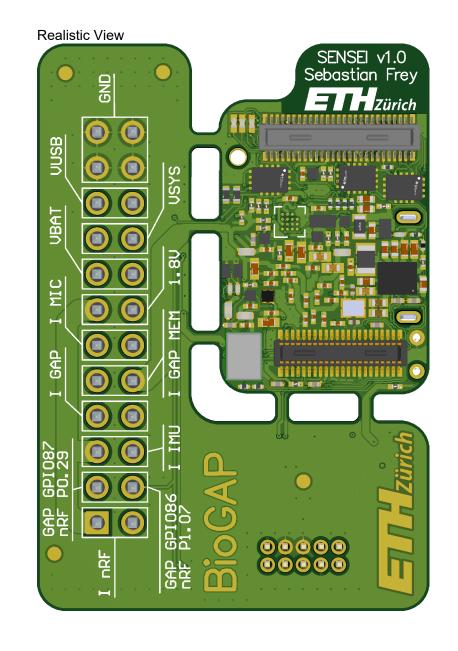


1 2 3 4 5 6 7 8





ETH	Project:	D
Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich	SENSEI Base Board	
Drawing number: 2 Rev: v1.0	Format: Laboratory: Integrated Systems Laboratory Sheet: DOC_3D.PCBDwf	1
Date: 23.09.2024 16:17	A3 Drawn by: Sebastian Frey Page 2 of 2	
File: H:\Documents\SENSEI\Hardware\sensei-sensor-s	shield\Hardware\UT_baseBoard\Hardware\01_Mainboard\DOC_3D.PCBDwf	

1 2 3 4 5 6 7