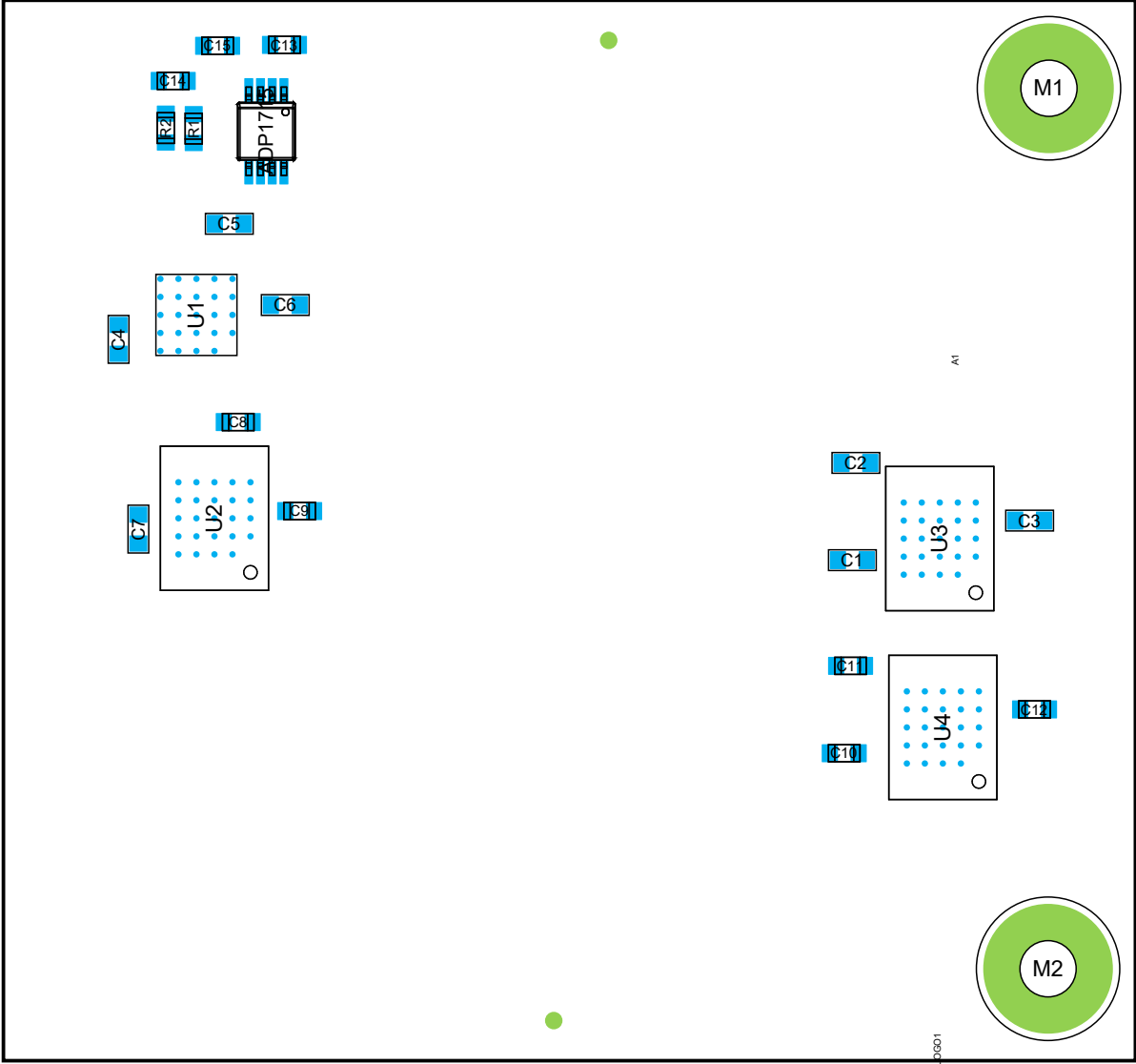
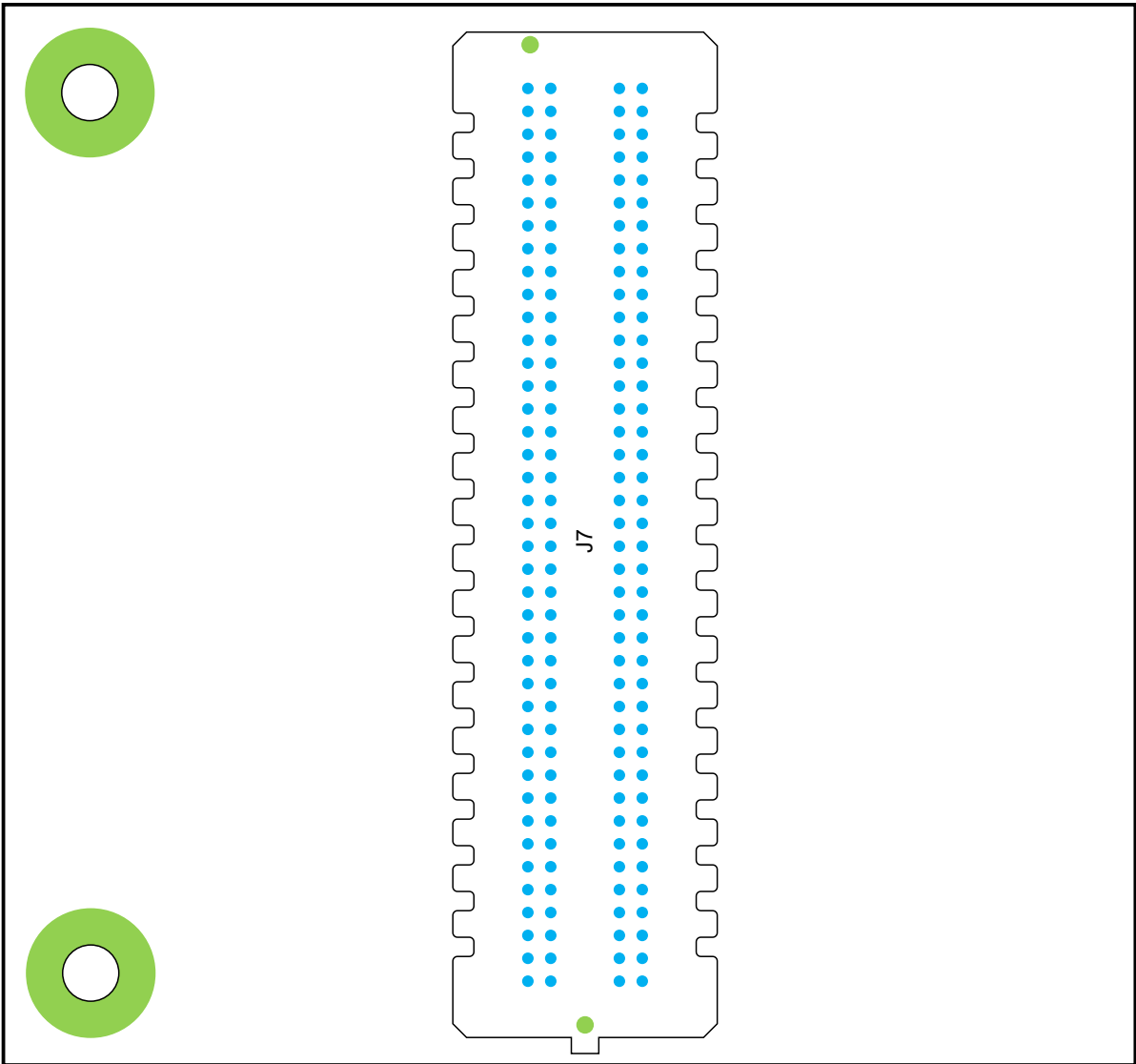


Assembly documentation:

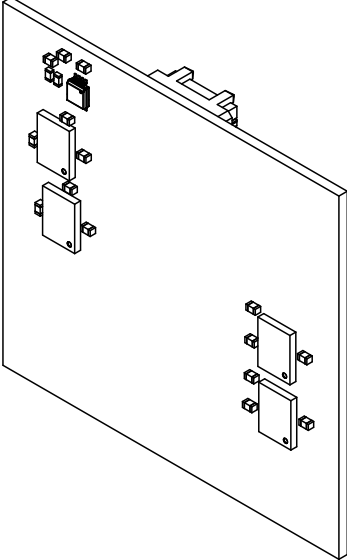
View from Top side (Scale 5:2)



View from Bottom side (Scale 5:2)



View from Top side (Scale 1:1)



Bill Of Materials

Line #	Designator	Name	Quantity
	ADP1715	ADP1715	1
	R1, R2	10K 5% 0603(1608)	2
	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15	CAP 100nF 25V 0603(1608)	15
	J7	ASP-134604-01	1
	U1, U2, U3	S71KS512SC0	3
	U4	S71KS512SC0	1

Notes:

- Pin 1 respectively marked with dot
- Cathode marked with K

<b>ETH</b> zürich			Project: FMC_4_Hyper	
Drawing number:	Rev: PA	Format: A3	Laboratory: IIS	Sheet: FMC_4_Hyper_assembly.PCBDwf
Date: 20/05/2024			Drawn by: Mattia Sinigaglia	Page: 1 of 1
File: C:\Users\User\Desktop\fmc_peripheral_boards-master\05_FMC_4_Hyperram_32MB\FMC_4_Hyper_assembly.PCBDwf				