

from
**PERIPHERAL
INTERCONNECT**

CONTROL

**CTRL
FSM**

**UCODE
PROCESSOR**

**REGISTER
FILE**

INTERNAL ENGINE

**ACCELERATOR
DATAPATH**

FIFO

FIFO

FIFO

- stream source
- stream sink
- tcdm master
- tcdm slave
- periph slave

STREAMER / LSUs

**LOAD
UNIT**

**LOAD
UNIT**

**STORE
UNIT**

INTERNAL INTERCONNECT

**TIGHTLY COUPLED
DATA MEMORY
INTERCONNECT**

