

Lab #8: Disassembler Implementation

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1 Functions Overview

The implementation of the disassembler has been done in C++ with the help of `iostream`, `fstream`, `vector` and `string` libraries. The code consists of the following (group of) functions:

1. Instruction Printing Function: `printVec`
2. Generators:
 - (a) Generates register names (if valid): `genReg`
 - (b) Generates immediate values (I-type) in decimal: `genImm`
 - (c) Generates immediate values (I-type) in hexadecimal: `genImmHex`
 - (d) Generates offset for B-type instructions: `getBimm`
 - (e) Generates offset for J-type instructions: `getJimm`
3. Helper Functions for Hexadecimal Parsing:
 - (a) Checks if a character is from 'a' to 'f' or 'A' to 'F': `isHexAlpha`
 - (b) Checks if a character is numeric ('0' to '9'): `isNum`
 - (c) Removes white-spaces if any from the right-end (disregards white-spaces at the end of lines of input file): `rstrip`
 - (d) Converts a hexadecimal string to an integer: `strHex`
4. Helper Functions for Instruction Parsing (Case-Switch for `funct3` and `funct7`):
 - (a) Prints instruction for R-format: `printR`
 - (b) Prints instruction for I-format: `printI`
 - (c) Prints instruction for S-format: `printS`
 - (d) Prints instruction for B-format: `printB`
 - (e) Prints instruction for J-format: `printJ`
 - (f) Prints instruction for U-format: `printU`

2 main() Overview

1. The `main()` function starts with taking `argv[1]` as the file name and attempts to open the file. If unable, it terminates the program and returns an error message.
2. The program now parses through the lines of the file and removes leading white-spaces to ensure uniformity and stores the hexadecimal strings in a vector.
3. Now, each string is converted to an integer if it is a valid hexstring. Otherwise, an error is reported and the program is terminated. A separate list for storing future labels is parallelly created (each instruction having default value as 0).

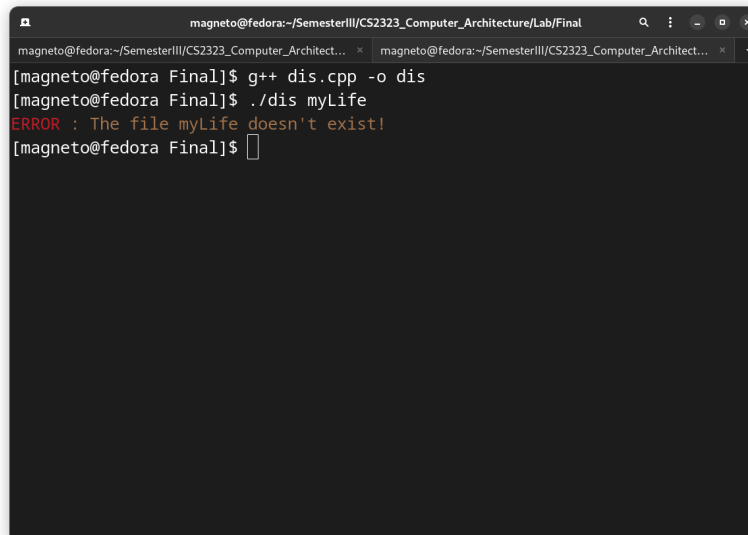
4. The program now parses through the instructions and extracts corresponding opcodes and transfers control to the appropriate format via `enum` definitions supplemented with `case-switch` blocks. The offsets for the B and J format instructions are noted. If the opcode is wrong, an error message is stored in the final vector (to be printed at the end) instead of the usual instruction. If the offset of the B/J instruction either exceeds the program size or leads to a negative index, the program is terminated and an appropriate error message is printed. If the offset is valid, the instruction to which the present instruction leads to is given a label (integer which auto increments). The instruction without the offset is stored in the final vector.
5. The program now appropriately suffixes/prefixes labels based on offsets and the table of labels for reference.
6. The program now prints the final vector and terminates the program. Any errors in individual lines (`opcode`, `funct3`, `funct7` mismatches) will be shown here.

3 Code Output (and Errors)

For the following discussion, we will consider the test-case files in the directory `TestCases` which consists of 5 files with hexadecimal coded instructions in separate lines. Note that the program executable will take the file-name as command line argument (`argv[1]`).

3.1 Wrong File

We will attempt to run the executable on a non-existent file (say `myLife` for example). The following is the output:



```
magneto@fedora:~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
magneto@fedora:~/SemesterIII/CS2323_Computer_Architect... x magneto@fedora:~/SemesterIII/CS2323_Computer_Architect... x
[magneto@fedora Final]$ g++ dis.cpp -o dis
[magneto@fedora Final]$ ./dis myLife
ERROR : The file myLife doesn't exist!
[magneto@fedora Final]$
```

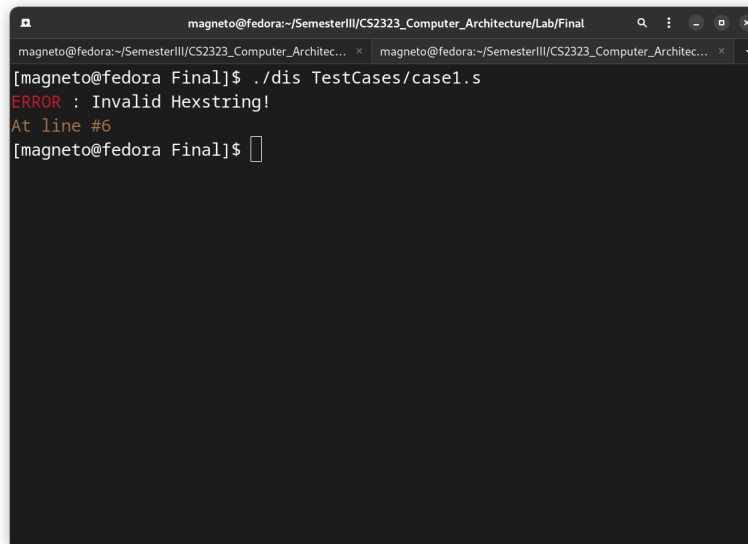
Figure 1: Wrong File

3.2 Invalid Hexadecimal String

Consider test-case `case1.s`. In this case, the 6th line has the line `00a585x3` has an invalid hexadecimal character `x`. Hence the output is as follows.

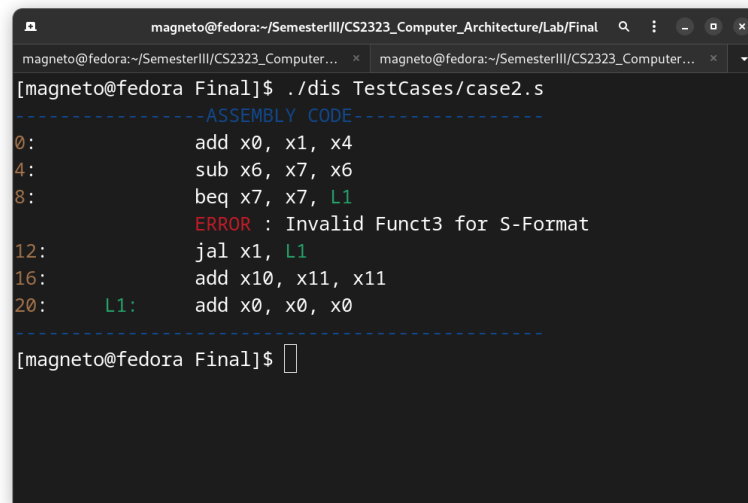
3.3 Invalid funct3 Values

Consider test-case `case2.s`. Here, the 4th line has the line `0081D023`. Note that the `funct3` are the bits [14 : 12] which in this case is `0x5`. This does not correspond to any valid instruction from the S-type instruction (`opcode = 0x23 = 35`). Hence we get the following output.

A terminal window with a dark background. The prompt is [magneto@fedora Final]\$. The user has entered ./dis TestCases/case1.s. The output is ERROR : Invalid Hexstring! followed by At line #6. The prompt is now [magneto@fedora Final]\$.

```
magneto@fedora:~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case1.s
ERROR : Invalid Hexstring!
At line #6
[magneto@fedora Final]$
```

Figure 2: Wrong Hexadecimal Instruction

A terminal window with a dark background. The prompt is [magneto@fedora Final]\$. The user has entered ./dis TestCases/case2.s. The output shows assembly code for lines 0 to 20. Line 8 has an instruction beq x7, x7, L1. The output for this line is ERROR : Invalid Funct3 for S-Format. The prompt is now [magneto@fedora Final]\$.

```
magneto@fedora:~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case2.s
-----ASSEMBLY CODE-----
0:      add x0, x1, x4
4:      sub x6, x7, x6
8:      beq x7, x7, L1
      ERROR : Invalid Funct3 for S-Format
12:     jal x1, L1
16:     add x10, x11, x11
20:     L1: add x0, x0, x0
-----
[magneto@fedora Final]$
```

Figure 3: Invalid funct3

3.4 Invalid funct7 Values

Consider test-case `case3.s` which is code to multiply two matrices. This code uses `mult` whose `funct7` code is not recognized. Hence we get the following output.

3.5 Out of Bounds

Consider test-case `case4.s`. This program consists of a branch statement that has an offset of -40 at line 4, which results in the "goto" instruction as $12 - 40 = -28$. Hence we get the following output.

3.6 Sample Input

Consider test-case `case5.s` which is the test-case in the problem statement. The following is the output:

```
magneto@fedora~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case3.s
-----ASSEMBLY CODE-----
0:      auipc x3, 0x10000
4:      addi x3, x3, 0
8:      addi x6, x3, 0
12:     addi x6, x6, 4
16:     lw x9, 0(x6)
20:     lw x18, 4(x6)
24:     slli x5, x9, 2
      ERROR : Invalid Funct7 for R-Format
28:     addi x5, x5, 8
32:     add x6, x6, x5
36:     lw x19, 0(x6)
40:     lw x20, 4(x6)
44:     addi x8, x0, 1
48:     bne x18, x19, L1
52:     addi x8, x0, 0
56:     addi x24, x6, 8
60:     addi x7, x24, 0
64:     addi x26, x24, 0
68:     sub x6, x6, x5
72:     addi x25, x6, 8
76:     addi x6, x25, 0
80:     slli x5, x19, 2
      ERROR : Invalid Funct7 for R-Format
84:     add x28, x7, x5
88:     sw x9, 0(x28)
92:     sw x20, 4(x28)
96:     addi x28, x28, 8
100:    addi x5, x0, 0
```

Figure 4: Invalid funct7

```
magneto@fedora~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case3.s
-----ASSEMBLY CODE-----
128:    L5:    addi x30, x30, 1
132:    bne x30, x20, L2
136:    addi x30, x0, -1
140:    slli x5, x18, 2
144:    add x25, x25, x5
148:    addi x6, x25, 0
152:    addi x24, x26, 0
156:    addi x7, x24, 0
160:    beq x0, x0, L3
164:    L2:    bne x31, x18, L4
168:    addi x24, x24, 4
172:    addi x6, x25, 0
176:    addi x7, x24, 0
180:    addi x31, x0, 0
184:    sw x21, 0(x28)
188:    addi x28, x28, 4
192:    addi x21, x0, 0
196:    beq x0, x0, L5
200:    L4:    lw x22, 0(x6)
204:    lw x23, 0(x7)
208:    addi x6, x6, 4
212:    slli x5, x20, 2
216:    add x7, x7, x5
      ERROR : Invalid Funct7 for R-Format
220:    add x21, x21, x5
224:    addi x31, x31, 1
228:    beq x0, x0, L2
232:    L1:    sw x8, 0(x3)
-----
[magneto@fedora Final]$
```

Figure 5: Invalid funct7

```
magneto@fedora:~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case4.s
ERROR : Program out of bounds
At line #4
Offset: -40
Program Size: 28
Expected Counter: -28
[magneto@fedora Final]$
```

Figure 6: Out of Bounds

```
magneto@fedora:~/SemesterIII/CS2323_Computer_Architecture/Lab/Final
[magneto@fedora Final]$ ./dis TestCases/case5.s
-----ASSEMBLY CODE-----
0:      add x3, x4, x7
4:      beq x4, x7, L1
8:      jal x0, L1
12:     sd x5, 12(x6)
16:     lui x9, 0x10000
20:     L1: addi x9, x10, 12
[magneto@fedora Final]$
```

Figure 7: Program from Problem Statement