	Abbreviations	
	~X_1 = HMAC_SHA256(~M_3,concat(concat	
	n256(gen exp. P. 1) concat(concat(concat(
	p256(gen,exp_P_1),concat(concat(concat(concat(concat(concat(a,nb_6),zero),iocap_A),addr_A),addr_B))	
	Attacker	
Beginning of process step3p Beginning of process step	ep4c Beginning of process step4p	
2_1)		
4 IIN (AC CII) OF C() C		
$4 = HMAC_SHA256(nb_6, concat(concat(p256(gen, exp_P_1), gen), zero))$		
a		
\sim M_5 = nb_6		
p2p(addr_A,a,nb_6,zero,zero,p256(gen, exp_P_1))		
exp_P_1)		
~X_	1	
nt recv_peripheral(p256(gen,exp_P_1))		

		$ \sim X_1 = HMAC_SHA256(\sim M_3,concat(concat)) $
	Honest Process	Attacker
	{1}new exp_C_1 {2}new exp_P_1	
Beginning of process BC_stack_central Beginning of process BC_stack_central [56] new skdm_2 [57] new ivm_2 [70] new ivs_2 [70]	Eapp_peripheral Beginning of process step1c Beginning of process step1p Beginning of process step2cjw Beginning of process step2pj	jw Beginning of process step3c Beginning of process step3p Beginning of process step4c Beginning of process step4p
	~M	I = p256(gen,exp_C_1)
	$(\sim M_1, \sim M_2) = (skdm_2, ivm_2)$	
		gen
		$\sim M_3 = p256(gen, exp_P_1)$
	{96} insert p1p(addr_A,gen,p256(gen,exp_P_1),p256(gen,exp_P_1))	
	{117}get p1p(addr_A,gen,p256(gen,expgen,exp_P_1))	p_P_1),p256(
		$\sim M_4 = HMAC_SHA256(nb_6, concat(concat(p256(gen, exp_P_1), gen), zero))$
	4	a
		$\sim M_5 = nb_6$
	{116}insert p2p(addr_A,a,nb_6,zero,zero,zero	ro,p256(gen,
		$ \begin{array}{c} \{145\} \\ \text{get p2p(addr_A,a,nb_6,zero,zero,p256(gen,\\ exp_P_1))} \end{array} $
		${\sim} \text{X_1}$
		{141}event recy peripheral(p256(gen.exp P 1))