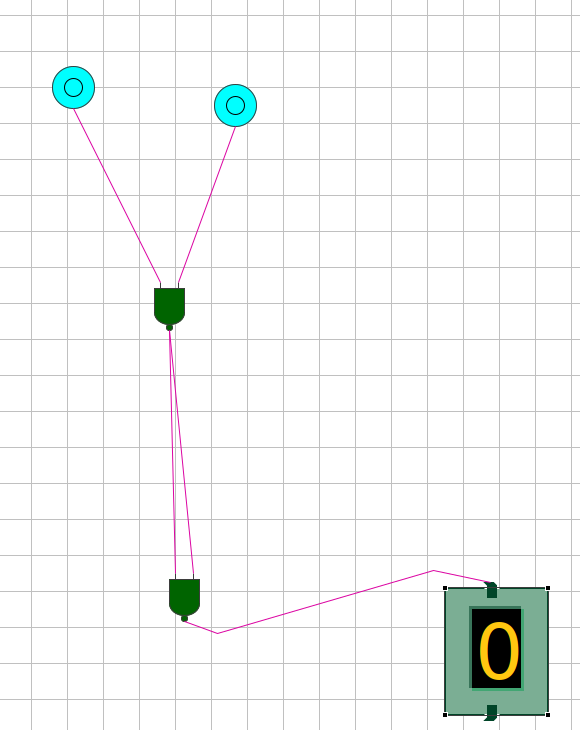
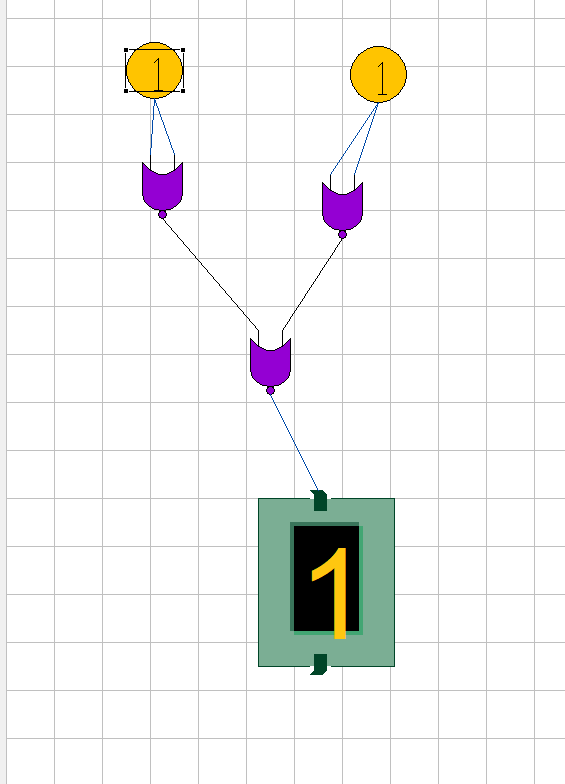
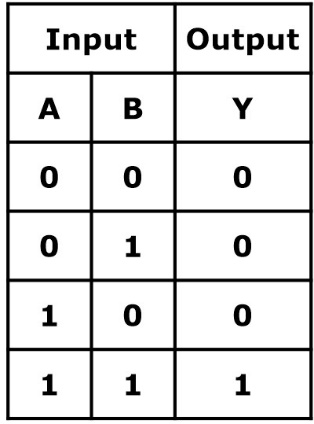
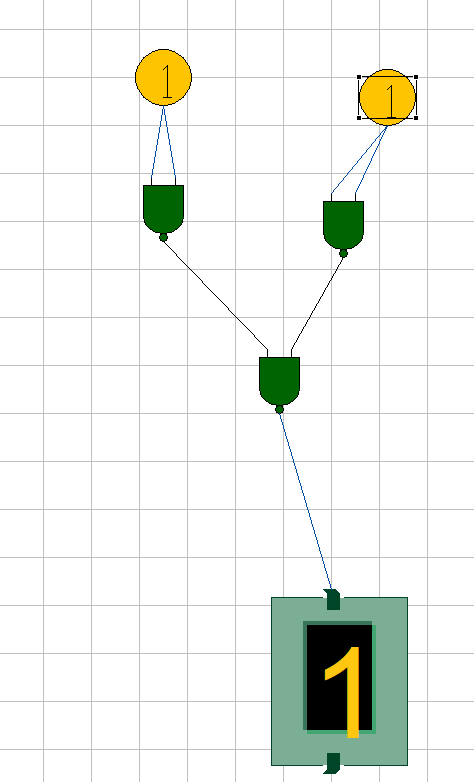
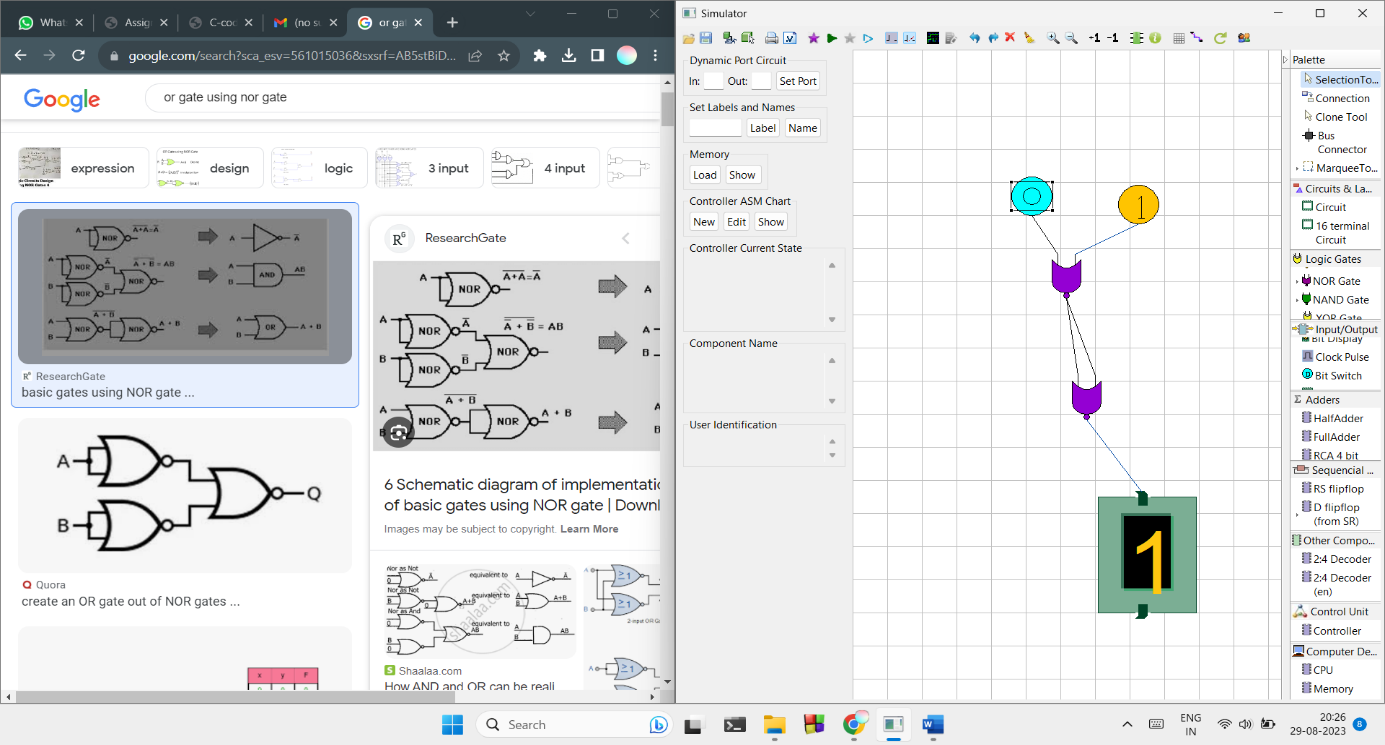
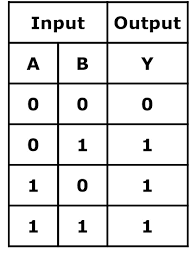
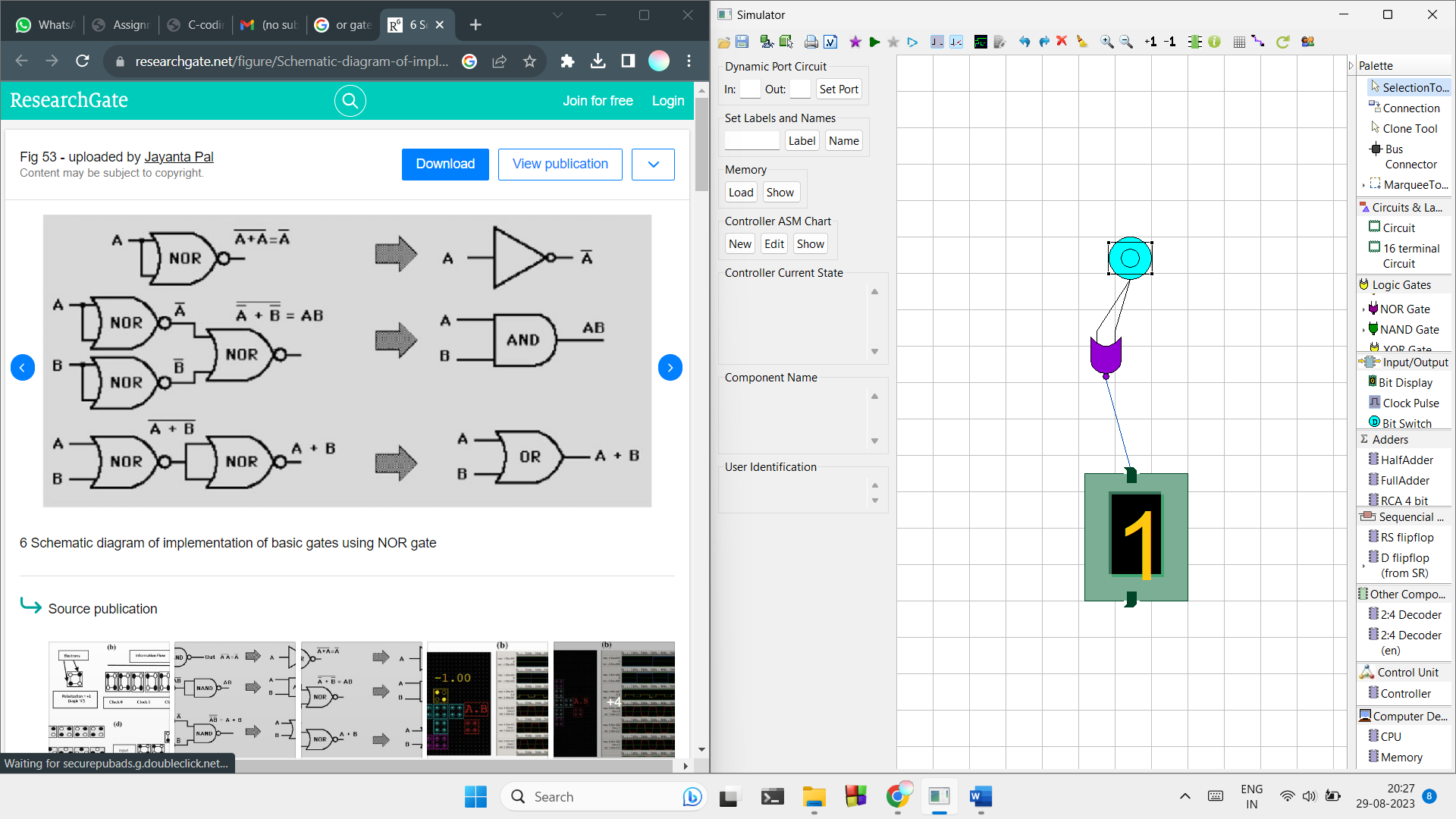
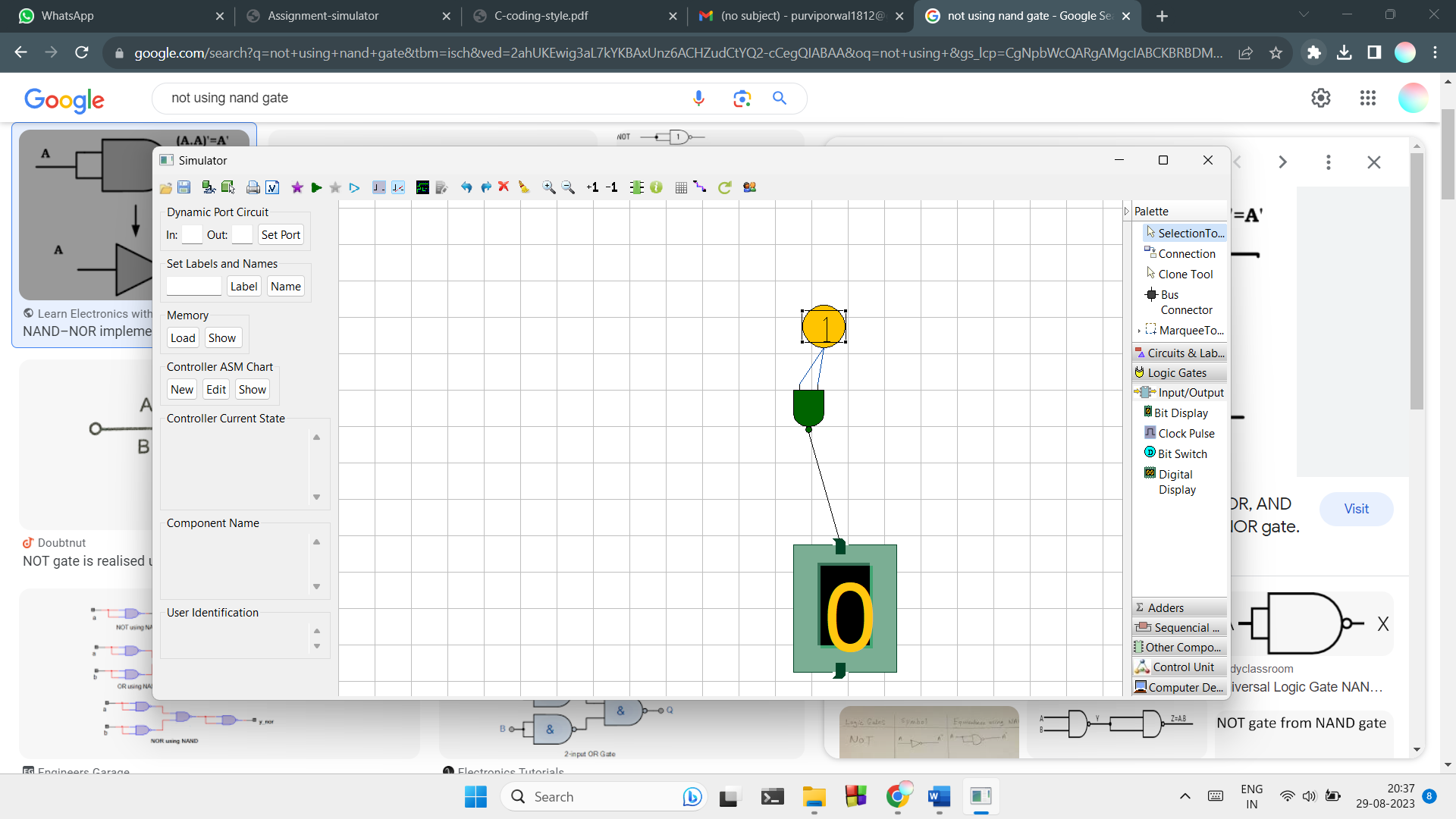
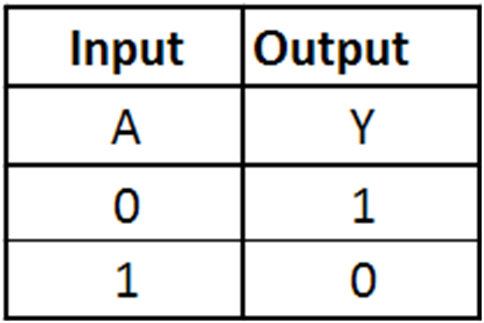
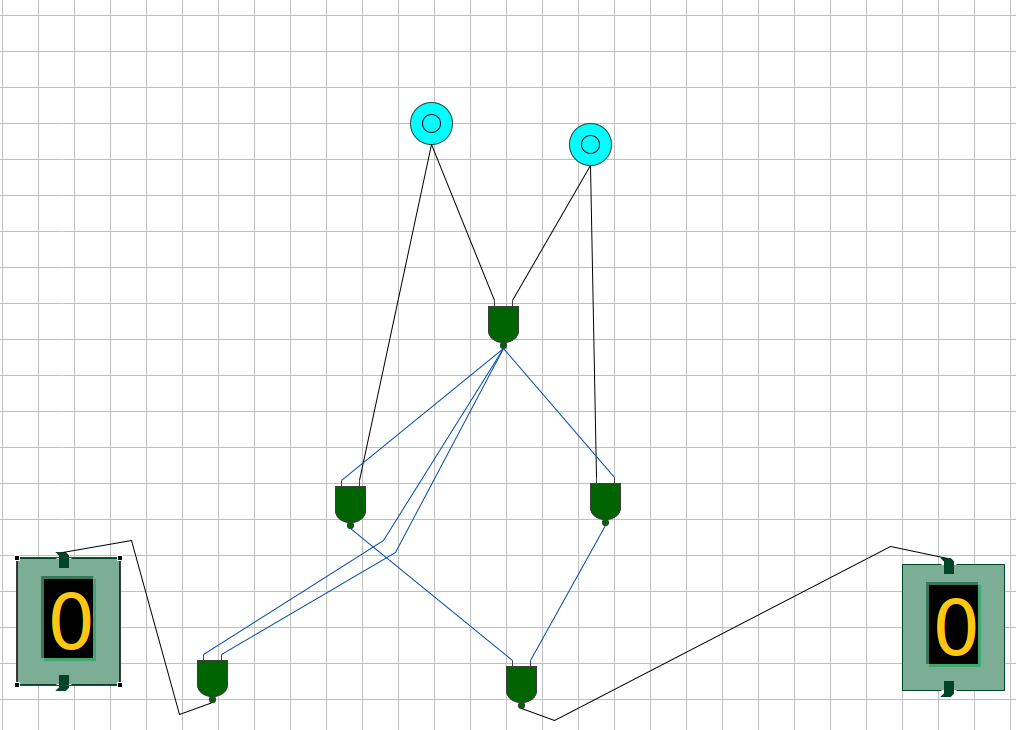
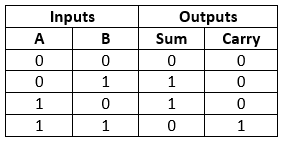
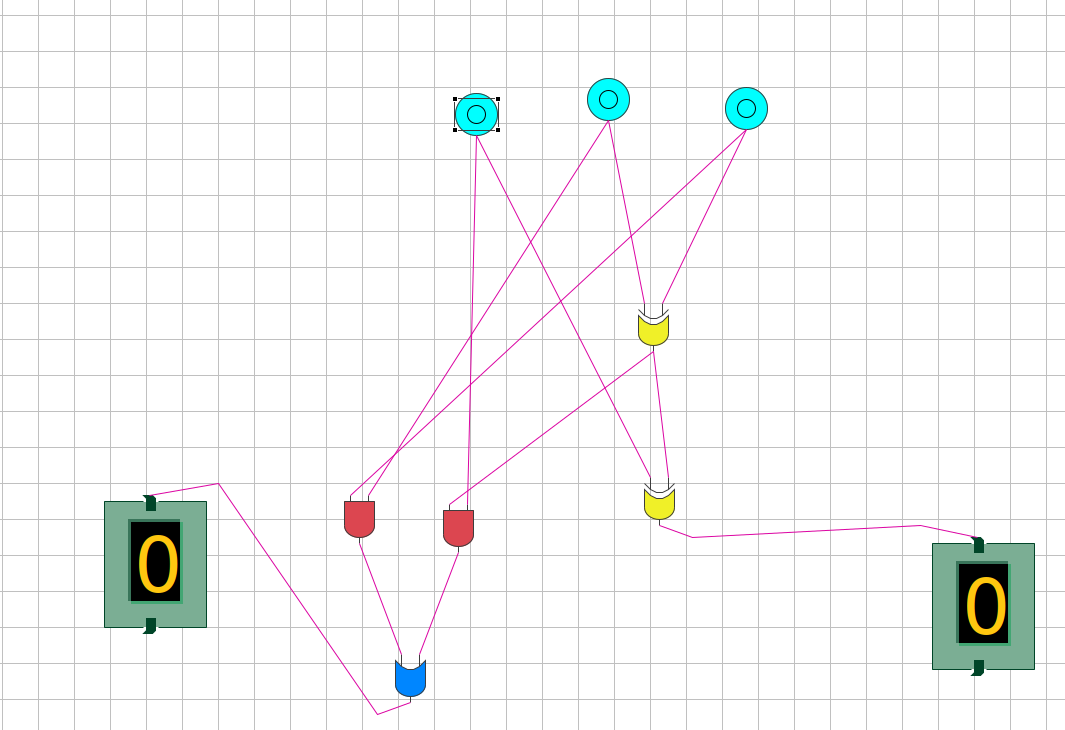
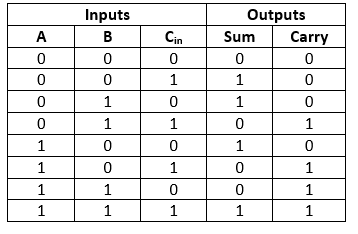
1. Design AND , OR and NOT gate using NAND and NOR gate in the Simulator.

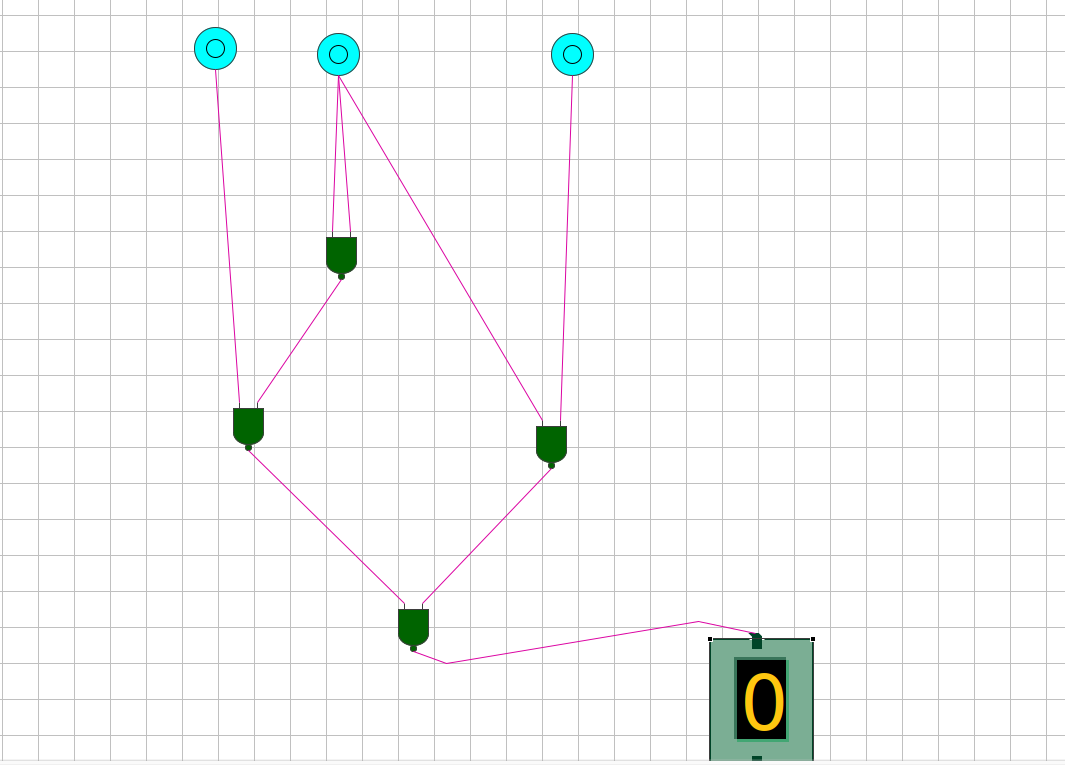
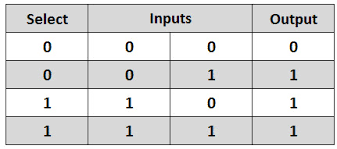


1. Design a Half adder and Full adder using basic gates.





1. To design and set up a 2:1 Multiplexer (MUX) using only NAND gates



1. To Design and set up a 4:1 Multiplexer (MUX) in the simulator

