PUSHKAR D

|12th November 2001| JP Nagar, Bengaluru, IN |
|+919353499649||pushkardwarkanath@gmail.com|
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EDUCATION

B.E (Bachelors in Engineering)- East West Institute of Technology – (Electronics and Communication)   **May,2024**
- Scored a Total CGPA of 7.2
- Participated in multiple Project Expos and Conferences.

CLASS 12 - DEEKSHA CFL PU College, Bengaluru. - PCMC   **Mar,2020**
- Participated in multiple Project Expos.
- Scored a Total CGPA of 7.0.

CLASS 10 - SUDARSHAN VIDYA MANDIR (ICSE), Bengaluru.   **Mar,2017**
- Passed out of High-school with a Total CGPA of 8.1.
- Was elected as House Leader.

WORK EXPERIENCE

HINDUSTAN AERONAUTICS LIMITED - BENGALURU, INDIA   **Technical Internship**
- On-Site experience on Jaguar Aircraft Overhauling. Hand Held experience at Accessories Lab, servicing of electronic and radio components.

PROJECTS

- Design and implementation of Traffic Light Controller For T-Intersection on FPGA using Verilog HDL and SPARTAN-6 FPGA Board - Improved Switching Traffic System and Resistant against failures. **(VTU)**
- ASIC Design and Implementation of Automated Coffee and Tea Brewing System with FPGA Validation. **(VTU)**
- Verilog HDL based Automated Coffee Making Machine with Embedded Interface for Menu Selection - Coffee Brewer with integrated Interface for seamless transaction and processing.
- Android App development for Coffee Making Machine - App based Remote Interfacing.
- Number Plate Recognition By Implementation Of Image Processing In Matlab

ACHIEVEMENTS

- IEEE ICRASET -Journal Paper Published   **(Nov-2023)**
- KISA Cricket Tournament Runner-up   **(Aug-2017)**

ADDITIONAL INFORMATION

Technical Skills: Programming Concepts,Verilog, MATLAB, EPLAN, Networking, Java, C, C++, Linux, Algorithms, Cyber-security & Cryptography methods.
Soft Skills: Communication, Team Work, Ethical, Problem Solving, Conflict Management, Adaptability.
Languages: Fluent in English, Kannada, Hindi. Conversational Proficiency in Tamil, Telugu. Basic Proficiency in French.

Certifications & Training:
- Analog Electronic Circuits - **NPTEL**
- Digital Circuits - **NPTEL**
- Introduction to IoT- **NPTEL**
- Introduction to Embedded System Design - **NPTEL**
- VLSI Design Flow: RTL to GDS - **NPTEL**
- C-based VLSI Design - **NPTEL**
- IEEE-ICRASET-2023 - **AICTE**

The provided information is affirmed to be true to the best of knowledge. Should an opportunity arise, commitment is pledged to exert utmost effort in learning and executing entrusted duties to the fullest extent possible.

Place: Bengaluru (INDIA)   **PUSHKAR D**