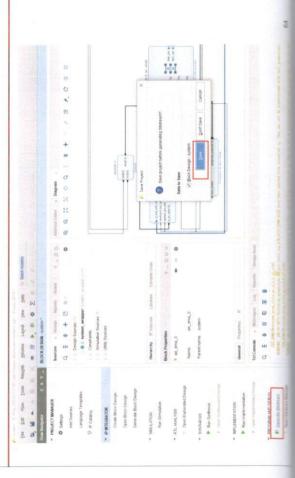
NARLabs Run Validate Design



Run Generate Bitstream

NARLabs



Check Address Editor

MARLabs

BRO B

400

KON

A address

AXI GPIO_0(DIP Switch): 0xA000_0000 AXI GPIO_1(LED): 0xA000_2000 AXI DMA: 0xA000 1000

. 0x(000,0000,0000,000) .

0x(000,0000,0000,000) . 04000,000 JEEF JEEF 04000,0000,000 JEEF 04000,0000,000 DECODE COOP PERFETER · 0±000,0000,000±0 REPORT SOOR STREET FEET . 0x00_X000_1FFF . 0x00_X000_0FFF . 0x00_X000_2FFF Range High Address 5,441,440,540 H40,DOR,HGH \u0000,000,000,000 22G 5,441,440,570 H40,DOR,HGH \u0000,000,000,000 26 0000 0000 0000 00000 0000 0000 0000 00000 0000 0000 0000 000000 02000_0000_0000_0000 000,000,000 MOD SAT SAH DAY SAH DO' MOT BOY ON ONE TON HOU HOU THOU HAVE GOSPI 0 a sq sagn but ... = 2nq_ulta_ps_e_0 = 2nq_ulta_ps_e_0 = 2nq_ulta_ps_e_0 = 2nq_ulta_ps_e_0 Sud utta ps. 6.0 Syduded Address Se # H and office policy = 24, dmg, 0 = 24, gpto, 1 = 24, gpto, 1 6, dma, 0

Export Hardware & Launch SDK

- File → Export → Export Hardware
- A system [E:/Proj_2022/ZCU102_Lab/mpsoc_lab Elle Edit Fjow Iools Reports Close Implemented Design Close Project Add Sources. Launch SDK Constraints S Include bilistream Elle Edit Flow Iools Repgrts Window Layout View b Export Hardware Export IBIS Model Export IIO Ports. File→ Launch SDK Export Palocks. Export

Sport Bitstream File