

# Class Agenda

- Introduction to ZCU102
  - License Setting
  - Vivado 2019.1 operating environment
  - SDK 2019.1 operating environment
- MPSoC system design example(Lab1)
  - Vivado design flow
  - SDK design flow
  - Integrated Logic Analyzer (ILA)
- TSRI Video system for YOLO Application
  - TSRI video demo system
  - AXI DMA wrapper & FIFO interface(YOLO\_TOP)
  - Video data flow
  - Firmware design flow
- TSRI Video system with Sobel Filter
  - Video data flow
  - AXI VDMA
  - Xilinx SDK with Sobel Filter

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# Export IP Invalid Argument / Revision Number Overflow Issue

## January 5th Update:

The patch to address this issue is being tested and we are still expecting a release before the end of this week. In the meantime, a temporary fix is available for those who are in need of an immediate workaround. The following is a manual command setting which has been lightly tested by Xilinx. We recommend waiting for the fully tested patch release if you can.

HLS Users: vitis\_hls or vivado\_hls, all releases

Use export version TCL API or equivalent GUI export IP version field  
HLS TCL Commands

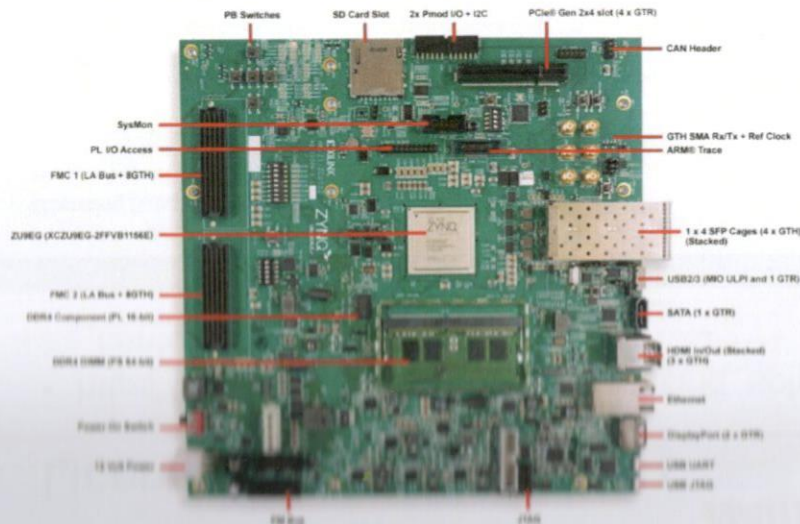
```
1 # For Vivado and Vitis HLS all releases:
2 export_design ... -version 2.0.1; # <major>.<minor>.<revision>
3
4 # For 2018.3 and later releases the recommended equivalent alternative to the above is:
5 config_export -version 2.0.1; # <major>.<minor>.<revision>
```

Please note: version number 2.0.1 is an example. If you have a pre-existing version of the core, you want to choose a version number that indicates a newer version.

[https://support.xilinx.com/s/question/0D52E00006vDlvJSAS/export-ip-invalid-argument-revision-number-overflow-issue?language=en\\_US](https://support.xilinx.com/s/question/0D52E00006vDlvJSAS/export-ip-invalid-argument-revision-number-overflow-issue?language=en_US)

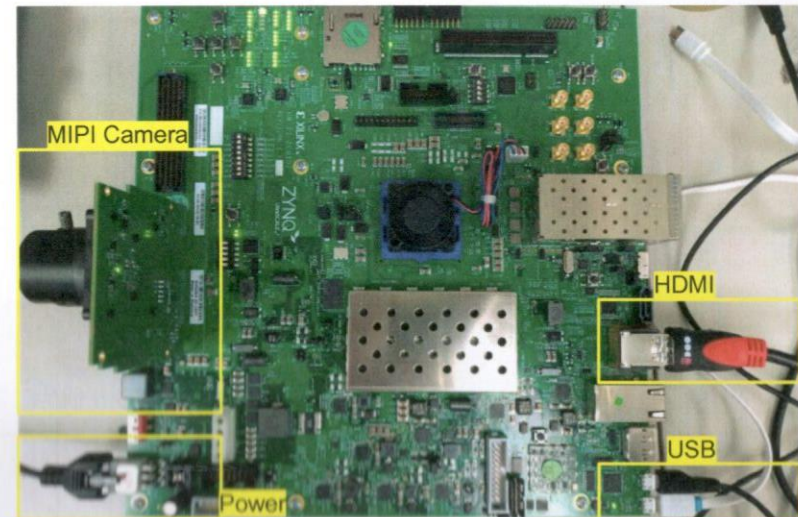
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# ZCU102 EVB



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# Setup ZCU102 EVB



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