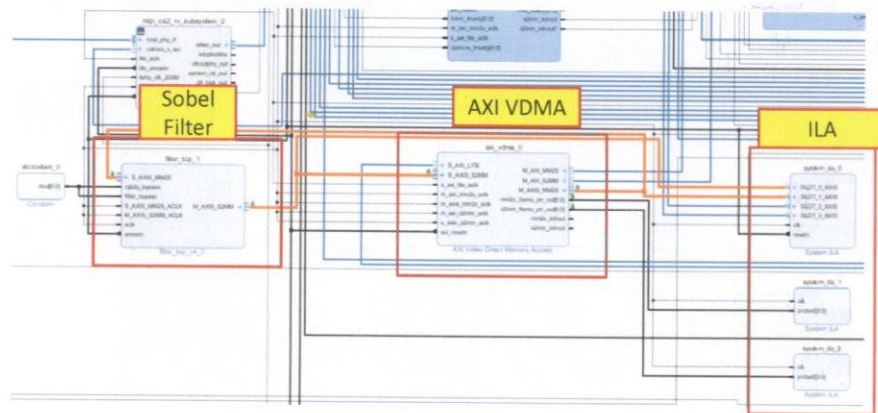
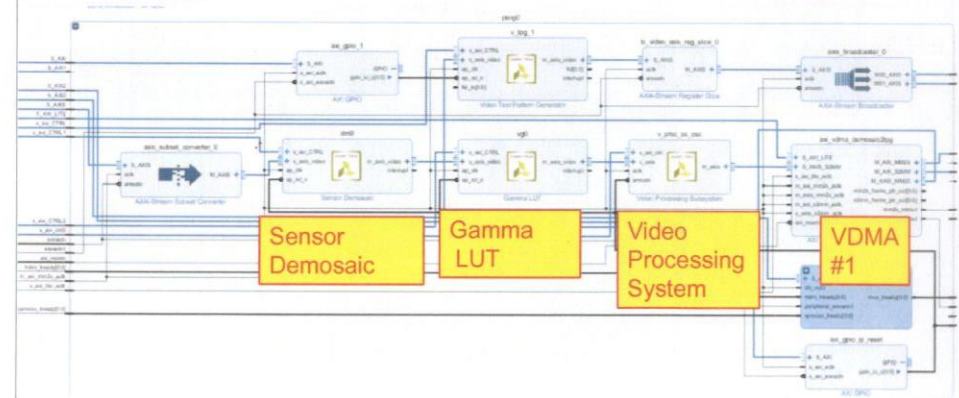


System Block Diagram (Zoom in)



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Video Processing System

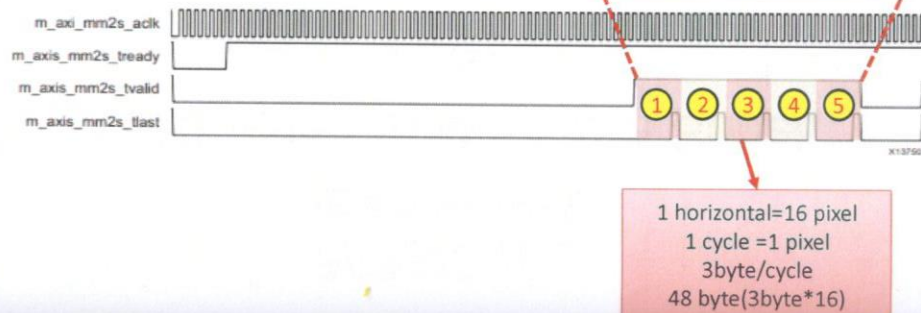


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AXI VDMA#1

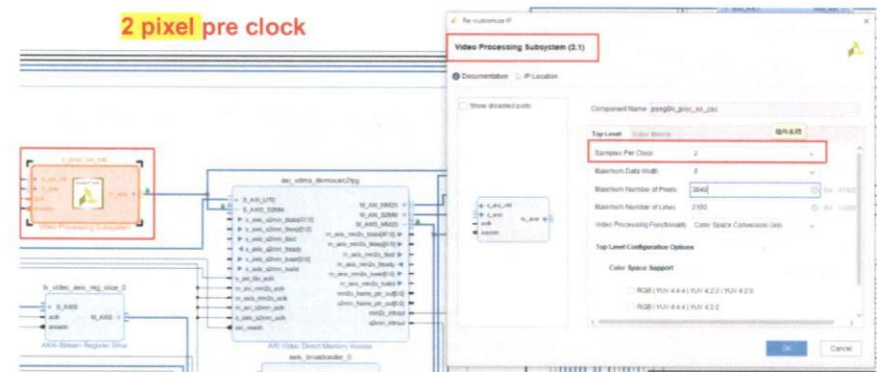
• Example Read (MM2S) Path Timing

- Vertical Size = 5 lines
- Horizontal Size = 16 pixel
- One frame=16x5 pixel



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Video Processing Subsystem



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