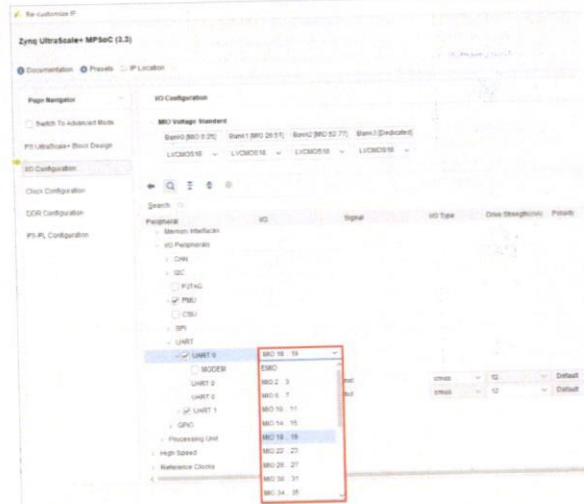


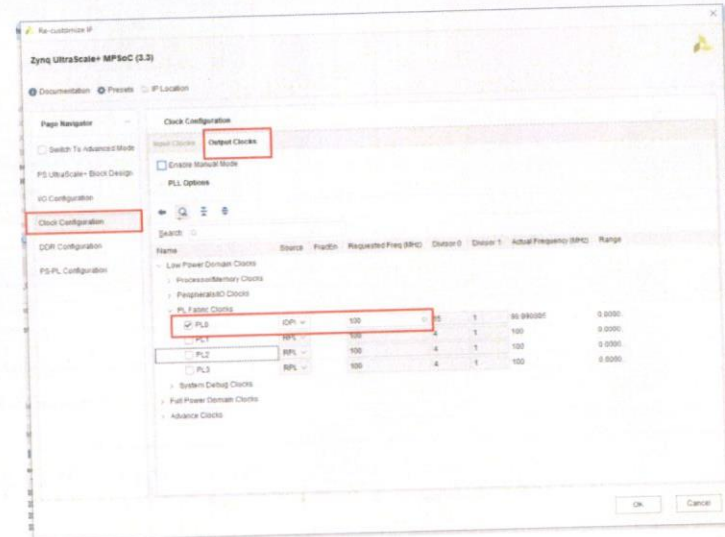
## I/O Configuration

- MIO : Processor I/O pin
- EMIO : FPGA I/O pin



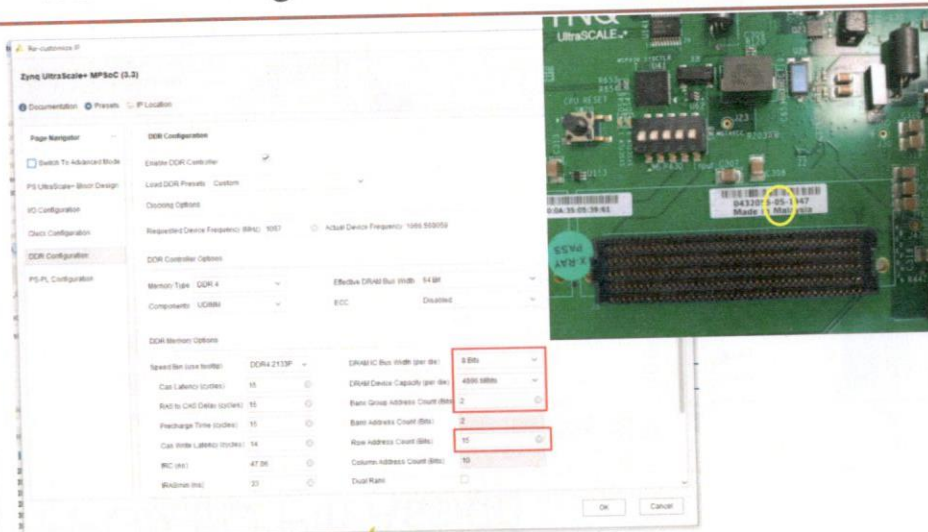
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## Clock Configuration



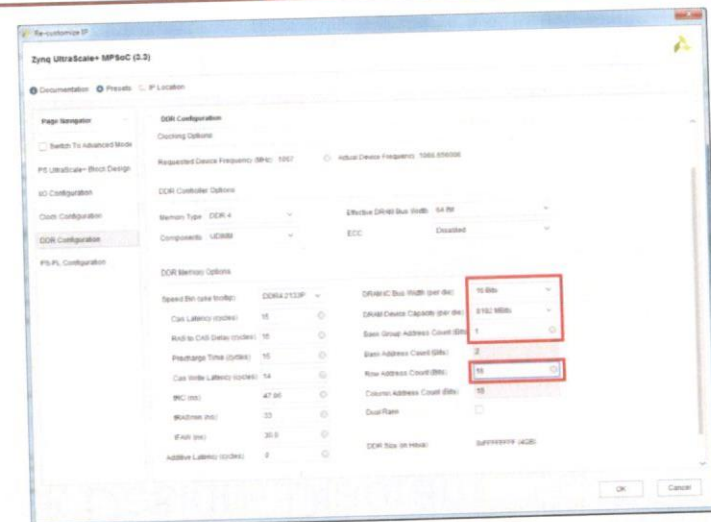
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## DDR Configuration : Board Version 4



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## DDR Configuration : Board Version 5



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