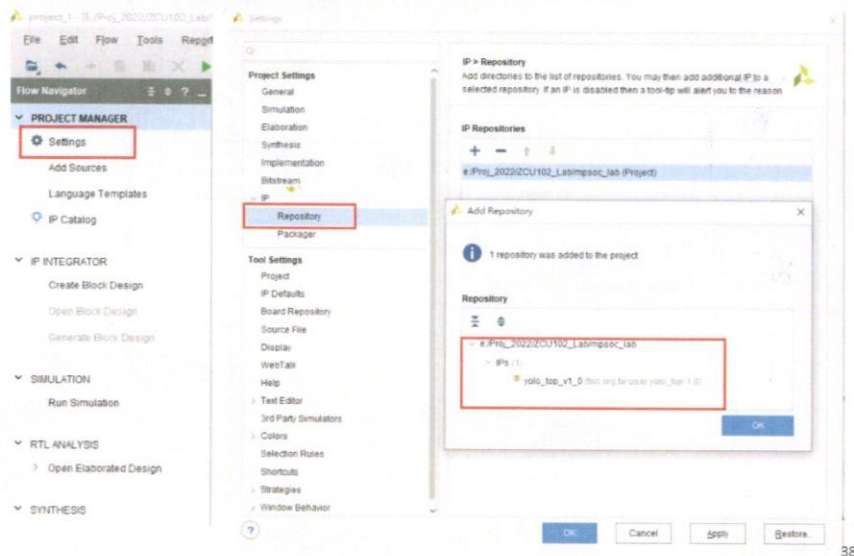
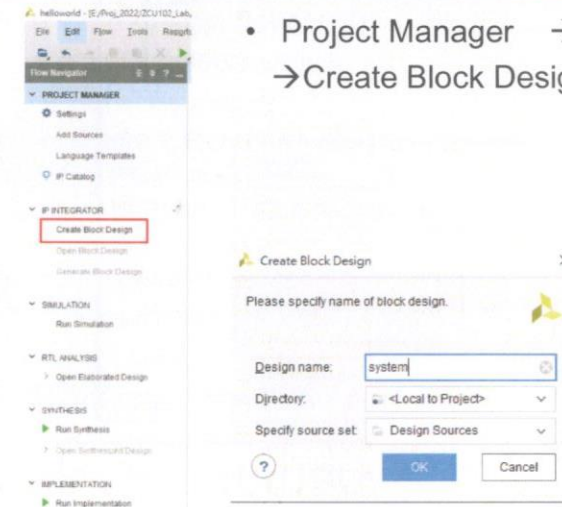


Set IP Repository

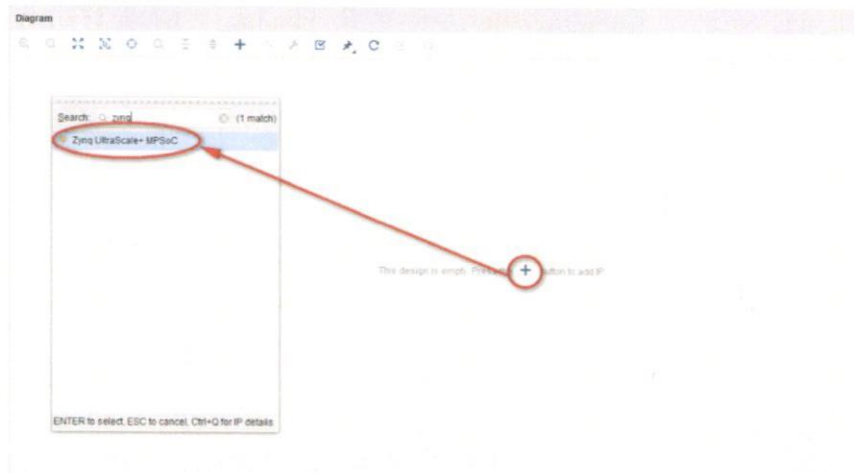


Create Block Design

- Project Manager → IP INTERGATOR
→ Create Block Design



Add Zynq UltraScale+MPSoC



Run Block Automation

