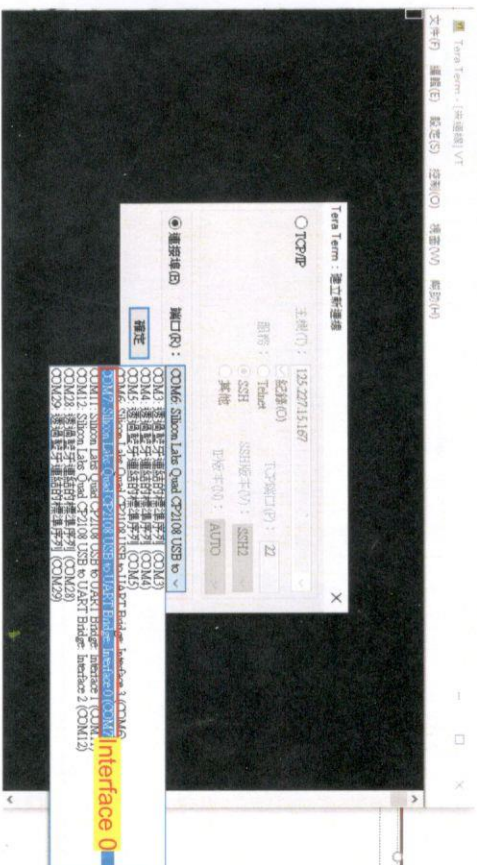


建立Tera Term 連線

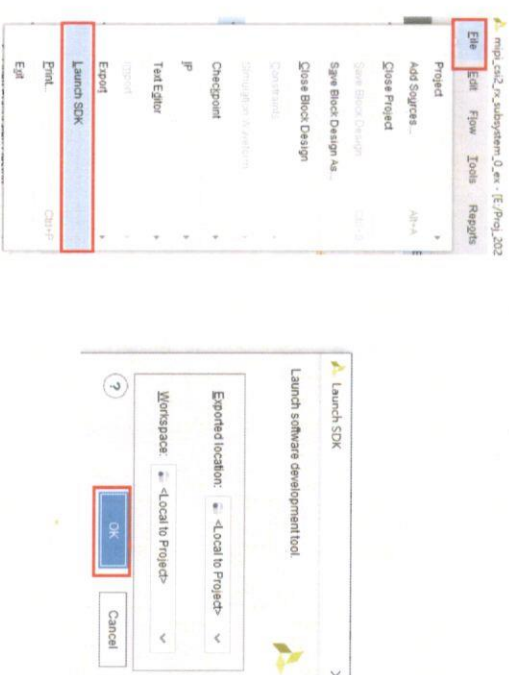
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Launch SDK

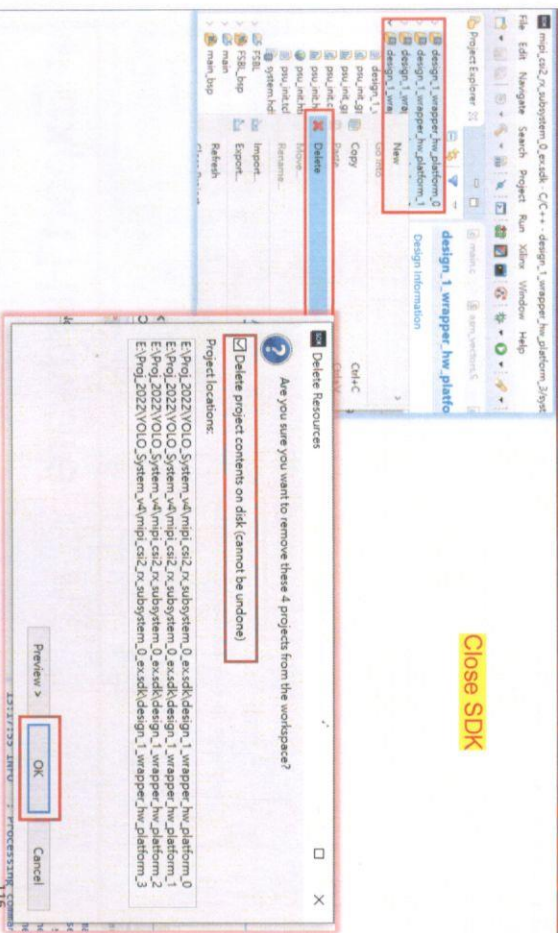
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Delete HW Platform X

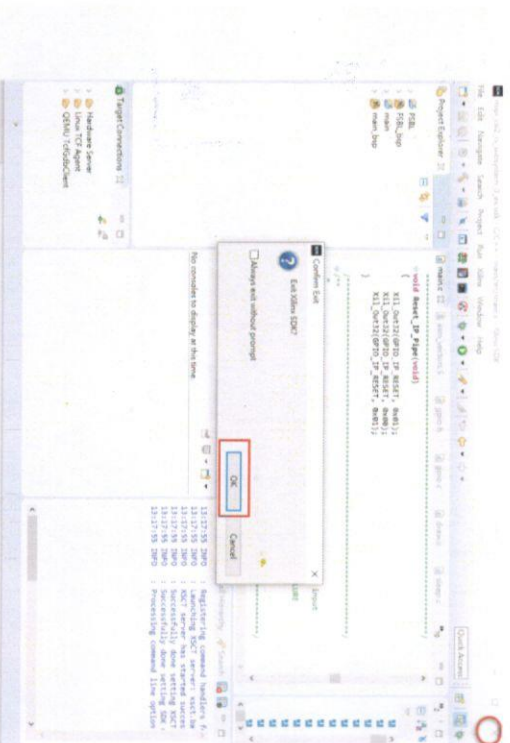
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Close Xilinx SDK

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