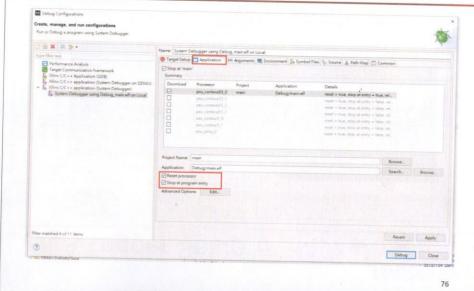
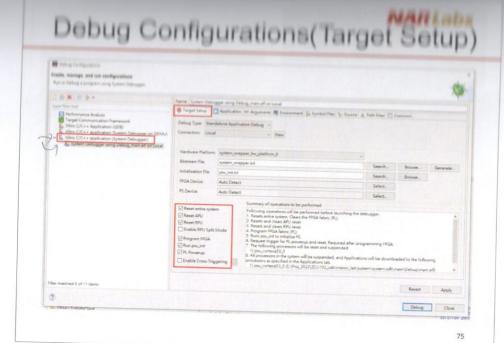
NARLabs Run Configurations Run As →Run Configurations Debug Go Into Open in New Window #define TEST_SIZE 356 u64 Source_TX[TEST_SIZE] + {0}; u64 Source_RX[TEST_SIZE] - {0}; u64 test_data=0; = void AXI_DMA_IP_TEST(void) Delete AXI_DMA_TxDone=0; AXI_DMA_RxDone=0; test_data=0; //demerate lest_pattern for (int i=0;i<TEST_SIZE;i+ **Build Project** Clean Project //Reset EX buffer for (int i=0;i<TEST_SIZE;i+ Close Project Source RX[1]=8: **Build Configurations** Xil_DCacheFlushRange/(UINTF Xil DCacheFlushRange//UINTF Debug As 1 Launch on Hardware (System Debugger) 2 Start Performance Analysis Restore from Local History 3 Launch on Hardware (System Debugger on QEMU) C/C++ Build Settings 4 Launch on Hardware (GDB) Generate Linker Script

Replace With







Open ILA(Vivado)

NARLabs

Flow Navigator→Program and Debug→Open Target→Auto Connect





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