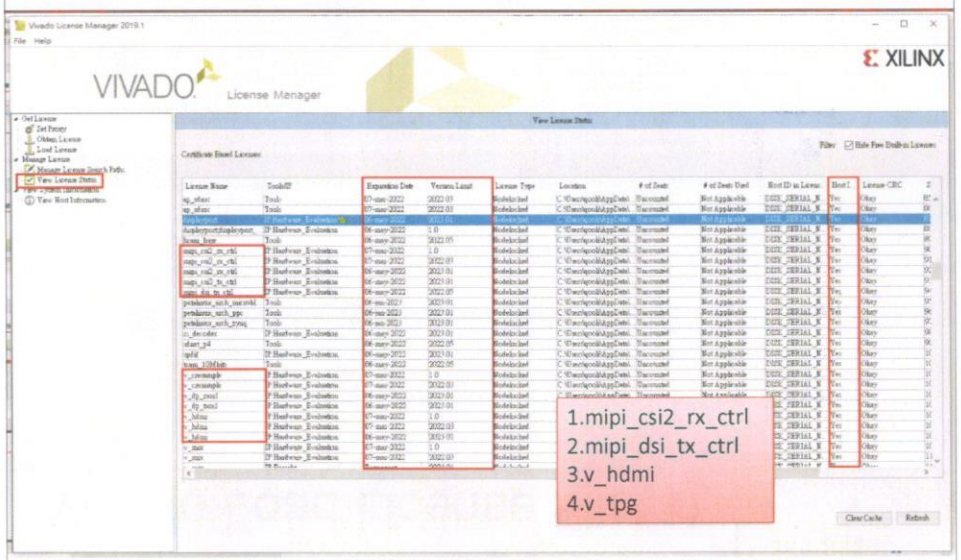
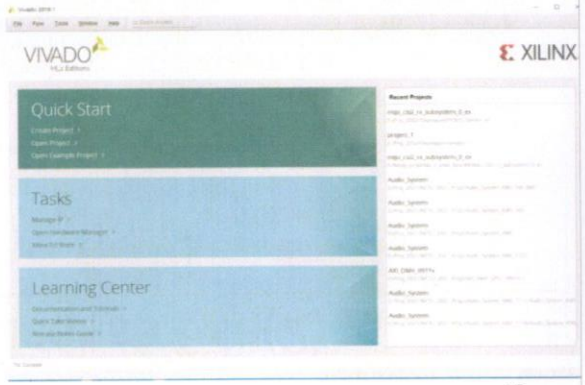
# Check License





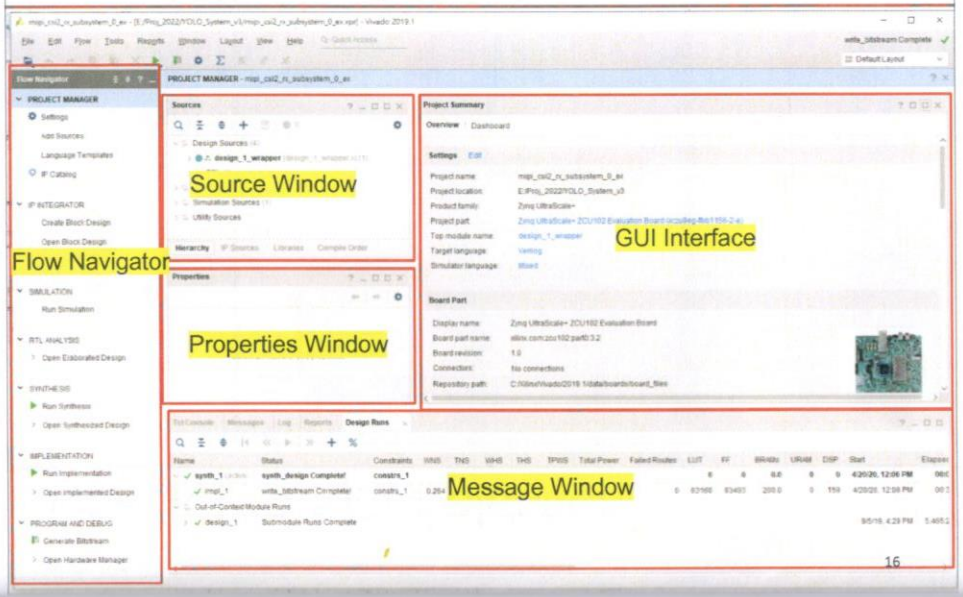

# Create Project

- New Project
  - Project name
  - Project location
  - Design file
  - Constrain file
  - Target platform



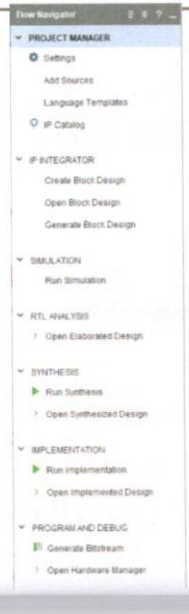



# Vivado 2019.1 Operating Environment






# Flow Navigator



- Projector Manager
  - Design file
  - Constrain file
- IP integrator
  - Block design
- Simulation
  - Run simulation
- RTL analysis
  - Syntax analysis
- Synthesis
  - Syntax analysis
  - Synthesis
- Implementation
  - Placing and routing
- Program and debug
  - Download bitstream file
  - Integrated Logic Analyzer(ILA)