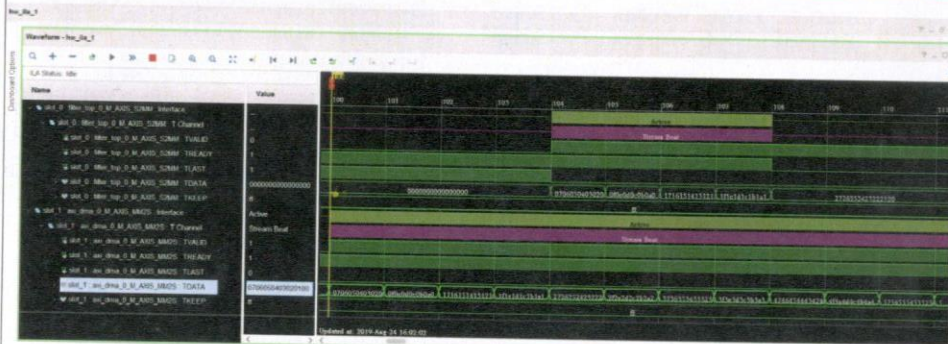


AXI DMA/IP ILA驗證

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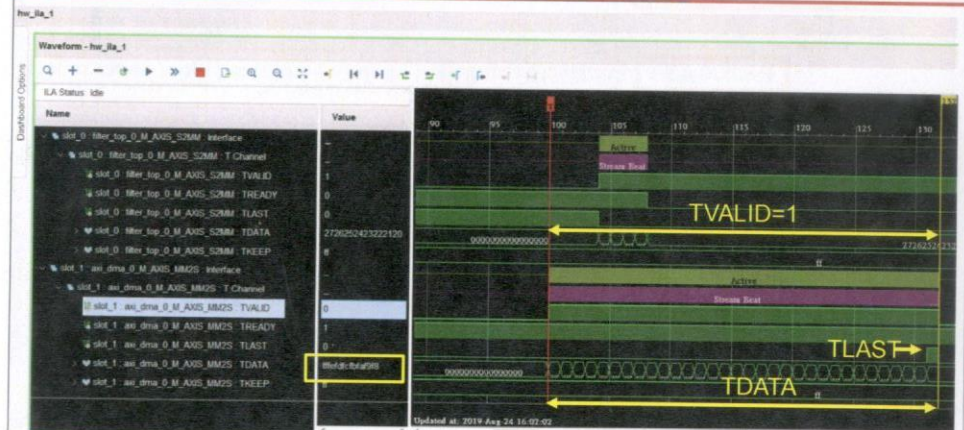


第一筆：07_06_05_04_03_02_01_00 (Bus每次傳送64bits)

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AXI DMA/IP ILA驗證 Cont.

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最後一筆：FF_FE_FD_FC_FB_FA_F9_F8 (Bus每次傳送64bits)

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TSRI Video System for YOLO Lab

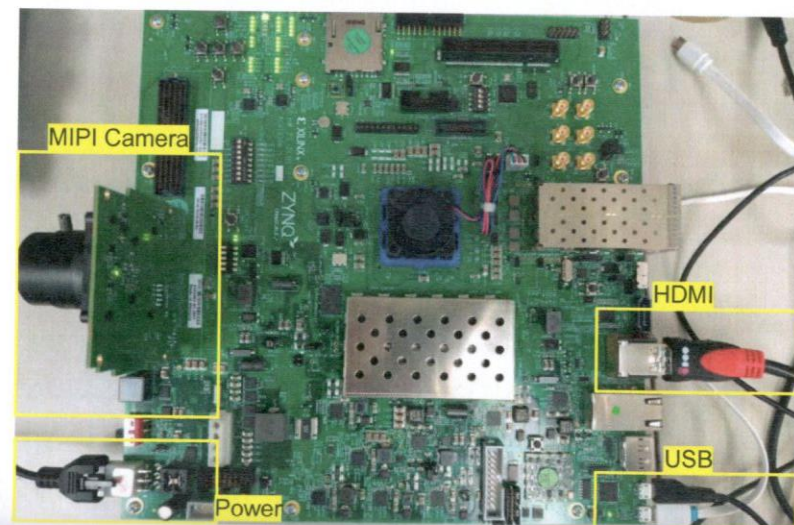
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- Open TSRI video system for YOLO project
- Run SDK

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Setup ZCU102 EVB

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