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zws@niesen:~$ lscpu
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 32
On-line CPU(s) list:   0-31
Thread(s) per core:     2
Core(s) per socket:     8
Socket(s):              2
NUMA node(s):          4
Vendor ID:              GenuineIntel
CPU family:             6
Model:                 85
Model name:             Intel(R) Xeon(R) Gold 6234 CPU @ 3.30GHz
Stepping:               7
CPU MHz:               3830.428
CPU max MHz:           4000.0000
CPU min MHz:           1200.0000
BogoMIPS:              6600.00
Virtualization:         VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              25344K
NUMA node0 CPU(s):     0-2,6,17,18,21,22
NUMA node1 CPU(s):     3-5,7,16,19,20,23
NUMA node2 CPU(s):     8,11,14,15,24,27,30,31
NUMA node3 CPU(s):     9,10,12,13,25,26,28,29
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep
                        se sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc ar
                        _tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
                        cdp_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_e
                        tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
                        _pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm
                        da arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke
zws@niesen:~$
  
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