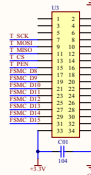


[illegible]

# CPU I/O 模块

The diagram illustrates the internal structure and connections of four CPU I/O modules: PA, PB, PC, and PD. Each module is connected to a central bus and contains a set of input/output lines.

**PA Module:** Contains 8 input lines (PA0-PA7) and 8 output lines (PA8-PA15). The output lines are connected to the central bus via a 16-bit bus structure.

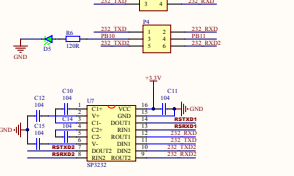
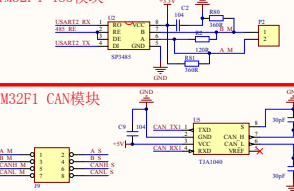
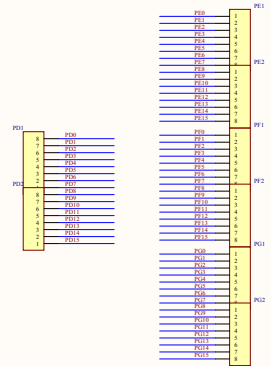
**PB Module:** Contains 8 input lines (PB0-PB7) and 8 output lines (PB8-PB15). The output lines are connected to the central bus via a 16-bit bus structure.

**PC Module:** Contains 8 input lines (PC0-PC7) and 8 output lines (PC8-PC15). The output lines are connected to the central bus via a 16-bit bus structure.

**PD Module:** Contains 8 input lines (PD0-PD7) and 8 output lines (PD8-PD15). The output lines are connected to the central bus via a 16-bit bus structure.

The central bus is a 16-bit bus that connects the modules to the CPU. The bus structure is shown as a 16-bit bus with 16 lines, each labeled with a number from 0 to 15.

The diagram shows the internal structure of the modules and their connections to the central bus. The modules are connected to the bus via a 16-bit bus structure. The bus structure is shown as a 16-bit bus with 16 lines, each labeled with a number from 0 to 15.

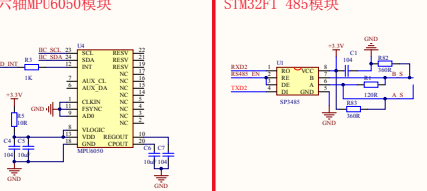
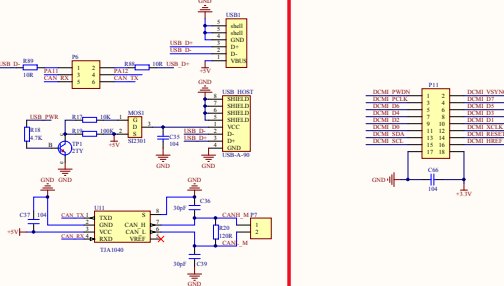
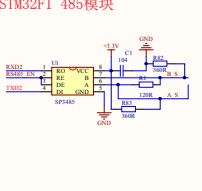
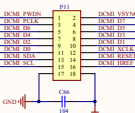


**六轴MPU6050模块**

该图展示了六轴MPU6050模块的引脚连接。模块的VCC（引脚1）连接到5V电源，GND（引脚2）连接到地。SCL（引脚3）和SDA（引脚4）分别连接到I2C总线的SCL和SDA引脚。模块的其他引脚（5-10）通过跳线帽连接到VCC和GND。模块的背面有10个焊点，分别连接到VCC、GND、SCL、SDA、VCC、GND、SCL、SDA、VCC、GND。

**STM32F1 485模块**

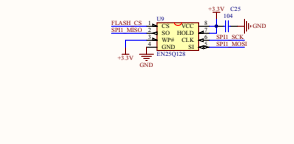
该图展示了STM32F1 485模块的引脚连接。模块的VCC（引脚1）连接到5V电源，GND（引脚2）连接到地。RX+（引脚3）和TX+（引脚4）分别连接到RS485总线的RX+和TX+引脚。模块的其他引脚（5-10）通过跳线帽连接到VCC和GND。模块的背面有10个焊点，分别连接到VCC、GND、RX+、TX+、VCC、GND、RX+、TX+、VCC、GND。

[illegible][illegible]

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249 250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271 272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287 288 289 290 291 292 293 294 295 296 297 298 299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320 321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342 343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364 365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383 384 385 386 387 388 389 390 391 392 393 394 395 396 397 398 399 400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500 501 502 503 504 505 506 507 508 509 510 511 512 513 514 515 516 517 518 519 520 521 522 523 524 
--

[illegible]

## FLASH模块

[illegible]

1.1V

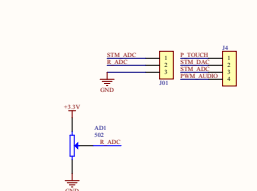
GND

VDD

DATA

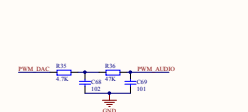
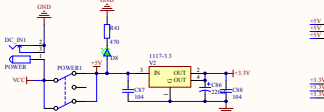
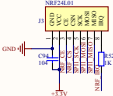
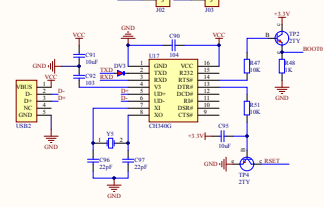
32

DATA



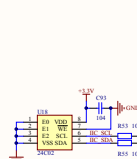
**PWM (DA) 模块**

Pin	Signal	Pin	Signal
1	VCC	17	VCC
2	GND	18	GND
3	VCC	19	VCC
4	GND	20	GND
5	VCC	21	VCC
6	GND	22	GND
7	VCC	23	VCC
8	GND	24	GND
9	VCC		
10	GND		
11	VCC		
12	GND		
13	VCC		
14	GND		
15	VCC		
16	GND		

[illegible][illegible]

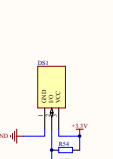
The left diagram illustrates the basic wiring of the DS18B20 sensor. It shows a yellow component labeled 'DS18B20' with three pins: VCC, GND, and DATA. The VCC pin is connected to a 3.3V supply, and the GND pin is connected to ground. A 10K pull-up resistor is connected between the VCC and DATA pins. The DATA pin is also connected to a microcontroller pin labeled 'P1.4'.

The right diagram shows a similar setup for an infrared non-contact thermometer. It features a component labeled 'IR1' with three pins: VCC, GND, and DATA. The VCC pin is connected to a 3.3V supply, and the GND pin is connected to ground. The DATA pin is connected to a microcontroller pin labeled 'P1.4'. A label 'IR' is placed near the component, indicating its function as an infrared thermometer.



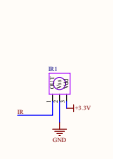
The '按键模块' diagram shows four push buttons (K1, K2, K3, K4) connected to a common ground (GND). Each button has one terminal connected to GND and the other terminal connected to a specific voltage: K1 to +3.3V, K2 to +5V/0V, K3 to +5V, and K4 to +5V.

The '蜂鸣器' diagram shows a buzzer connected to a +3.3V supply. The buzzer's positive terminal is connected to the +3.3V supply through a 100Ω resistor. The negative terminal is connected to GND through a 100Ω resistor.

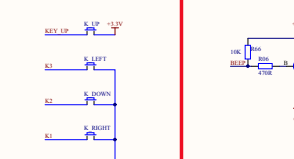


The '按键模块' diagram shows four push buttons (K1, K2, K3, K4) connected to a common ground (GND). Each button has a pull-up resistor connected to +3.3V. The labels for the buttons are K1 UP, K2 LEFT, K3 DOWN, and K4 RIGHT.

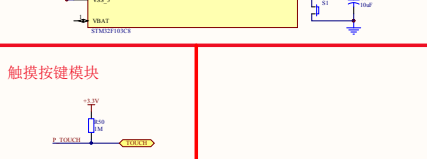
The '蜂鸣器' diagram shows a buzzer connected to a +3.3V supply. The buzzer is represented by a circle with a speaker symbol inside. It is connected to a 10k resistor, which is in turn connected to a 100k resistor. The other end of the 100k resistor is connected to the +3.3V supply. The buzzer is also connected to GND.



The image contains two circuit diagrams. The left diagram, titled '按键模块' (Keypad module), shows a 5V VCC supply connected to the VCC pin of a 4x4 keypad. The GND pin is connected to a common ground. The four columns are labeled K\_UP, K\_LEFT, K\_DOWN, and K\_RIGHT. The right diagram, titled '蜂鸣器' (Buzzer), shows a 5V VCC supply connected to the VCC pin of a buzzer module. The GND pin is connected to a common ground. The buzzer module has a 10K resistor connected to the VCC pin and a 470K resistor connected to the GND pin. The buzzer is connected to the output pin.



触模按键模块



Title		
Size	Number	Revision
A1		
Date	2012-7-31	Sheet of