L1 Cache Address Breakdown Set Index Line Offset Address Tag 47...12 11...6 5...0 4KB Page Address Breakdown PML4E Index | PDPTE Index PDE Index PTE Index Page Offset 47...39 38...30 29...21 20...12 11...0 L2 Cache Address Breakdown Set Index Line Offset Address Tag 47...16 14...6 5...0 L3 Cache Address Breakdown Set Index Line Offset Address Tag 47...16 18...6 5...0

## 2MB Page Address Breakdown

PML4E Index | PDPTE Index PDE Index Page Offset 47...39 38...30 29...21 20...0