

PHILIP WOLFE

pwolfe854@gmail.com · linkedin.com/in/pwolfe854 · github.com/pwolfe8 · 256-701-1047

EXPERIENCE

Georgia Tech Research Institute

- Implemented basic FPGA designs.
- Developed FPGA designs using VHDL, focusing on low-power consumption and high performance.
- Led a team in the design and implementation of complex FPGA solutions using VHDL. Achieved significant improvements in performance and power efficiency, applying modern synthesis techniques and thorough testing methodologies.

Beam Tech

- Implemented basic FPGA designs.
- Developed FPGA designs using VHDL, focusing on low-power consumption and high performance.
- Led a team in the design and implementation of complex FPGA solutions using VHDL. Achieved significant improvements in performance and power efficiency, applying modern synthesis techniques and thorough testing methodologies.

EDUCATION

GEORGIA INSTITUTE OF TECHNOLOGY, School of Electrical and Computer Engineering

Masters of Science in Electrical and Computer Engineering

Bachelor of Science in Computer Engineering with High Honors

Interests: Automation, Controls, Embedded Systems, Machine Learning, FPGAs/Digital Design

SKILLS

Languages: C/C++, Python, Java

Technologies: Git, Docker, AWS

Concepts: Object-Oriented Programming, Machine Learning