# PHILIP WOLFE

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### EXPERIENCE

Job Title, Company Name Month Year - Present

- Bullet point describing your job responsibility or achievement.
- Another bullet point with more information.
- Led a team in the design and implementation of complex FPGA solutions using VHDL. Achieved significant improvements in performance and power efficiency, applying modern synthesis techniques and thorough testing methodologies.

#### **EDUCATION**

## GEORGIA INSTITUTE OF TECHNOLOGY, School of Electrical and Computer Engineering

Masters of Science in Electrical and Computer Engineering

Bachelor of Science in Computer Engineering with High Honors

Interests: Automation, Controls, Embedded Systems, Machine Learning, FPGAs/Digital Design

#### **SKILLS**

Languages: C/C++, Python, Java Technologies: Git, Docker, AWS

Concepts: Object-Oriented Programming, Machine Learning