Philip Wolfe

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Experience

Georgia Tech Research Institute

- Implemented basic FPGA designs.
- Developed FPGA designs using VHDL, focusing on low-power consumption and high performance.
- Led a team in the design and implementation of complex FPGA solutions using VHDL. Achieved significant improvements in performance and power efficiency, applying modern synthesis techniques and thorough testing methodologies.

Beam Tech

- Implemented basic FPGA designs.
- Developed FPGA designs using VHDL, focusing on low-power consumption and high performance.
- Led a team in the design and implementation of complex FPGA solutions using VHDL. Achieved significant improvements in performance and power efficiency, applying modern synthesis techniques and thorough testing methodologies.

Education

GEORGIA INSTITUTE OF TECHNOLOGY, School of Electrical and Computer Engineering
Masters of Science in Electrical and Computer Engineering
Bachelor of Science in Computer Engineering with High Honors

Interests: Automation, Controls, Embedded Systems, Machine Learning, FPGAs/Digital Design

Skills

Languages: C/C++, Python, Java
Technologies: Git, Docker, AWS
Concepts: Object-Oriented Programming, Machine Learning

Publications

[1] Francesco Amato, Chris M. Beaulieu, Aneneth T. Haile, Jingyuan Liang, Kevin M. Mairena, Hiba Murali, George O. Udochukwu, Ikenna C. Uzoije, Philip J. Wolfe and Gregory D. Durgin, "5.8 GHz Energy Harvesting of Space Based Solar Power using Inkjet Printed Circuits on a Transparent Substrate" in 2015 IEEE International Conference on Wireless for Space and Extreme Environments, Orlando, FL, 2015,doi: 10.1109/WiSEE.2015.739105 [link]

[2] Amir Yazdanbakhsh, Hajar Falahati, Philip J. Wolfe, Kambiz Samadi, Nam Sung Kim, Hadi Esmaeilzadeh, "GANAX: A Unified MIMD-SIMD Acceleration for Generative Adversarial Networks" in 45th International Symposium on Computer Architecture (ISCA), 2018, arXiv:1806.01101[cs.DC] [link]