Patrick Landers

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Objective

To apply my knowledge of Computer Engineering principles through co-op employment and further my education through hands on experience.

Skills

Languages: C,C++, Java, VHDL, Python, Assembly

Software: Altera Quartus, ModelSim, MultiSim, Autodesk Inventor **Hardware:** Oscilloscope, Digital Multimeter, Function Generator

Experience

Brooklyn Technical High School / Information Technology

April 2014 - August 2014

Installed OS on computers, added/removed hard drives, made cat-5 ethernet cable, and troubleshooted hardware and software on school computers.

Education

Rochester Institute of Technology /Bachelor of Science in Computer Engineering, expected June 2019

Courses

Digital System Design Circuits I,II Assembly Language
Computer Organization Computer Science I,II Software Engineering
Applied Programming

Brooklyn Technical High School /

Courses

Majored in Aerospace Engineering(PLTW) Computer Integrated Manufacturing(PLTW) Digital Electronics(PLTW)

Projects/Labs

Digital Systems Lab

Various digital logic circuits were analyzed schematically and tested on hardware using an Altera hardware board along with a Spartan-6 FPGA.

Assembly Language Lab

Used an ARM processor and assembly language to perform various tasks such as ciphering/deciphering a message, a game that involves lighting LEDs on a circuit board. The processor would interact with computers through Putty.

Software Engineering

Worked in a team of four developing a website using Django. The purpose of the website was to allow patients and staff of multiple hospitals to interact with each other such as creating appointments or seeing results.

Extra Curricular

Gradebook(Python)

Wrote a program that takes a user input and write it to files, along with reading strings from multiple files. It will then collect the grades and graph them using plotly.