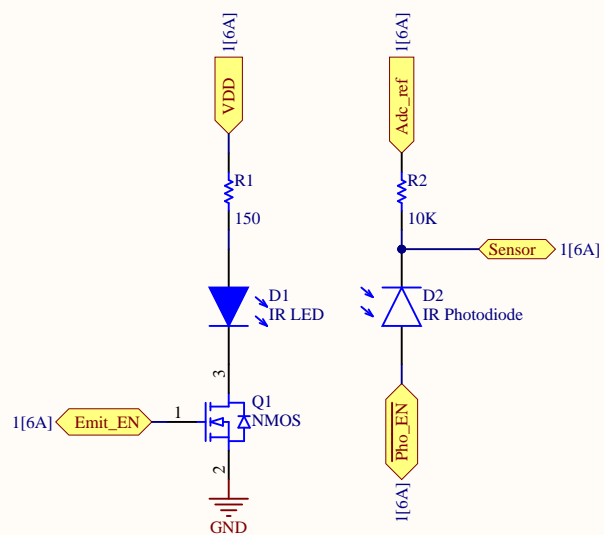
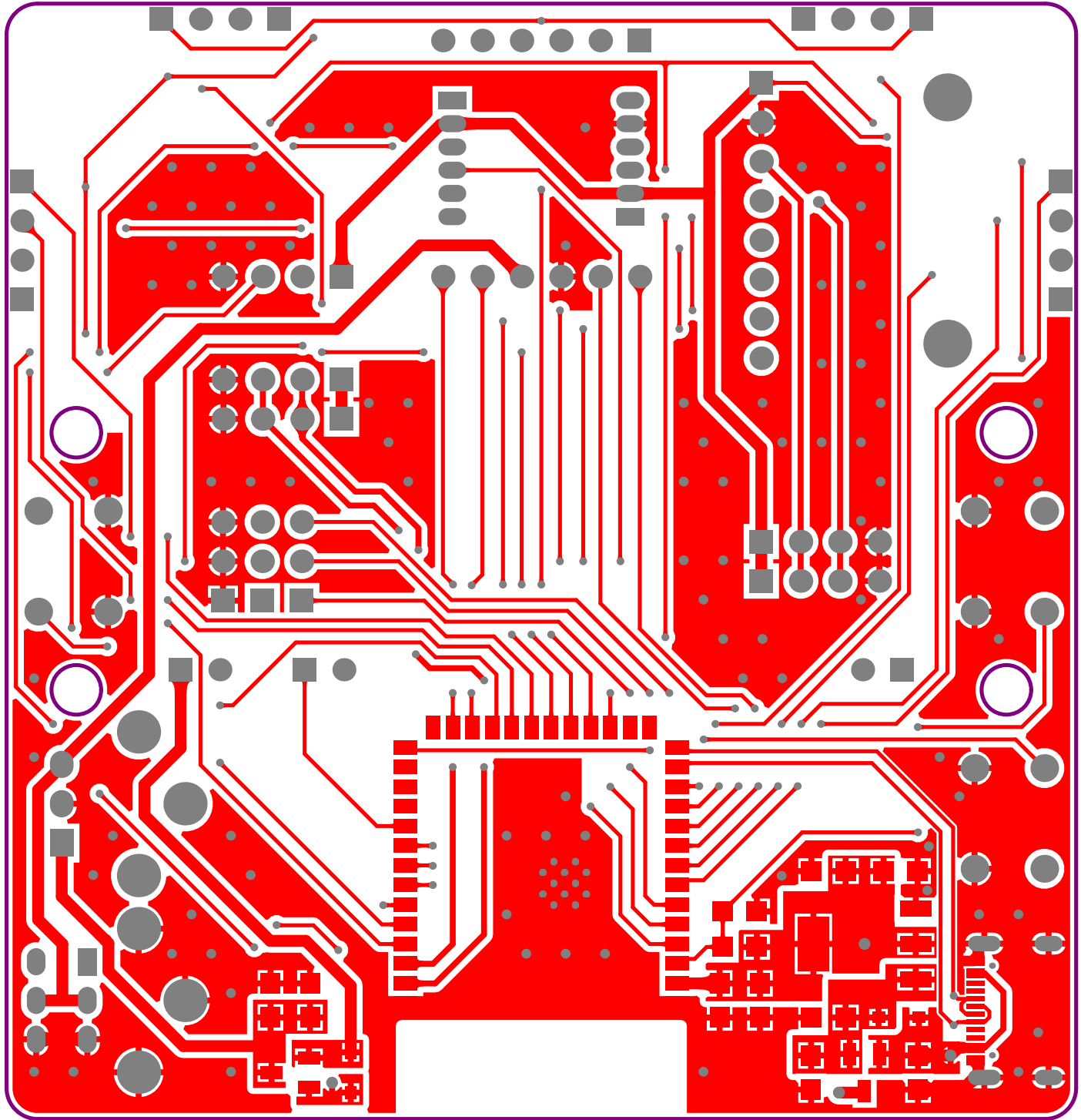
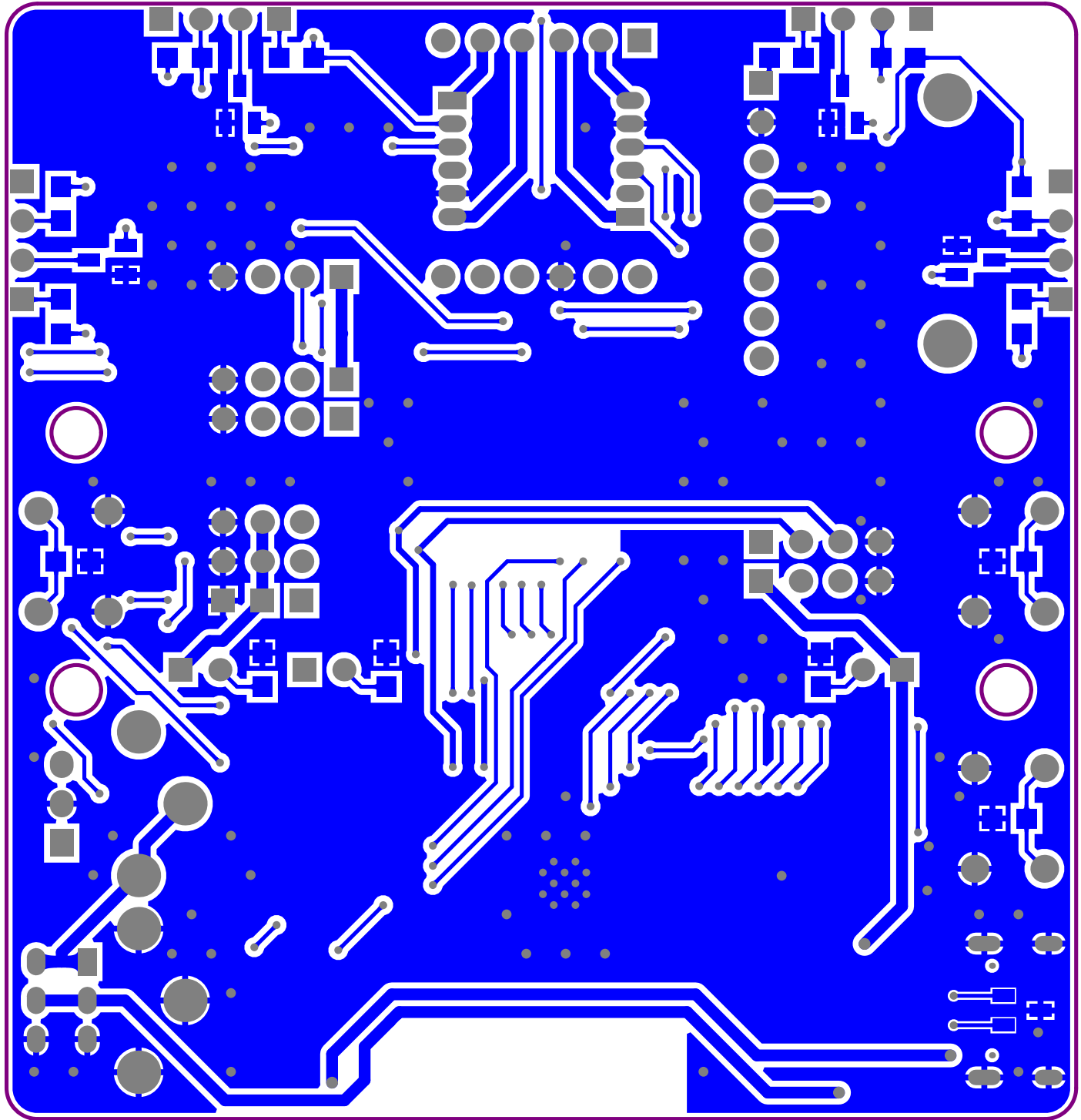


NMOS's max VGStH must be lower than the minimum of VOH
 NMOS's min VGStH must be higher than the maximum of VOL
 ESP32S3 WROOM's $VOH \geq 0.8 * VDD$ $VOL \leq 0.1 * VDD$
 If choose TH package for MOS, change to ZVNL110A/G01N20RE with TO-92 package



Title		
Size	Number	Revision
A4		
Date:	11/15/2023	Sheet of
File:	F:\Altium_Designer_PJ\..IR emit rcv.Sch	Drawn By:







UC Berkeley IEEE

Taihong Xie

1

D2_LIR

1

D1_LIR

1



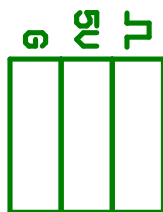
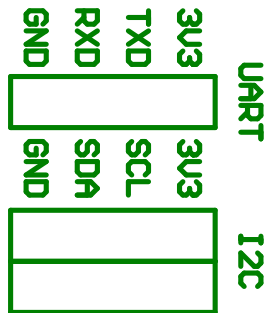
USR



LM7805/5V Mod



OFF



ESP32S3-WROOM-1

C2

C1

Q3

D4

Antenna Area

D1_LFIR

D1_RFIR

D2_RFIR



MPU6050
GY-521

D2_RIR

D1_RIR



RST



BOOT

