INTERFACE CHARACTERISTICS

→UART interface(USB TO UART)

The Host send command and wait for reader return message, the UART parameter as follows:

Baud Rate: 38400 (default)

Data Bits: 8 bitStop Bits: 1 bitParity Bit: none

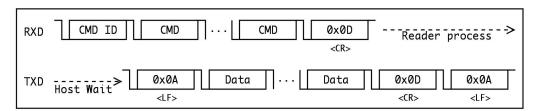


Figure 1 UART Communication

ASCII PROTOCOL COMMANDS

Command and return message is transmitted as **ASCII** format. All command is start with a command character and arguments (if any, in **hexadecimal** units) and stop with a <CR>(0x0D hex), and return message is start with a <LF>(0x0A hex), command first character and stop with a <CR><LF>.

If command is none match, return message will be <LF>X<CR><LF>.

Ex.

PC or Host: <LF>S<CR>

Reader return message: <LF>S01234567<CR><LF>

→ RFID Command Overview

Command*	Return Message**	Description
V	Vxxyy, <message></message>	display reader firmware
	xx: major version number	version
	yy: minor version number	
	<message>: other info.</message>	
S	S01234567	display reader ID
	01234567 is reader ID	
Q	Q <none epc="" or=""></none>	display tag EPC ID
	<none epc="" or=""></none>	
	none: no tag in RF field	
	EPC: PC+EPC+CRC16	
R <bank>,<address>,<length></length></address></bank>	R <none data="" or="" read=""> or</none>	read tag memory data
<bank> memory bank</bank>	<error code=""></error>	
0: reserved	<none data="" or="" read=""></none>	
1: EPC	none: no tag in RF field	
2: TID	<error code=""></error>	
3: USER	0: other error	

<address> start address</address>	3: memory overrun	
0 ~ 3FFF	4: memory locked	
<pre><length> read word length</length></pre>	B: Insufficient power	
1 ~ 1E	F: Non-specific error	
W <bank>,<address>,</address></bank>	W <none <ok="" or="">> or</none>	write data to tag memory
<length>,<data></data></length>	<error code=""></error>	
<bar>bank> memory bank</bar>	<none <ok="" or="">>></none>	
0: reserved	none: no tag in RF field	
1: EPC	<ok>: written ok</ok>	
2: TID	<error code=""></error>	
3: USER	0: other error	
<address> start address</address>	3: memory overrun	
0 ~ 3FFF	4: memory locked	
<length> write words length</length>	B: Insufficient power	
1 ~ 1E	F: Non-specific error	
	Z00~Z1F: words write	
	3Z00~3Z1F: error code	
	and words write	
K <password>,<recom></recom></password>	K <none <ok="" or="">> or</none>	kill tag
<pre><password> kill password</password></pre>	<error code=""></error>	
00000000~FFFFFFF	<none <ok="" or="">></none>	
<recom> recommissioning</recom>	none: no tag in RF field	
0~7	<ok>: kill ok</ok>	
	<error code=""></error>	
	0: other error	
	3: memory overrun	
	4: memory locked	
	B: Insufficient power	
	F: Non-specific error	
L <mask>,<action></action></mask>	L <none <ok="" or="">> or</none>	lock memory
<mask> lock mask</mask>	<error code=""></error>	
000~3FF	<none <ok="" or="">></none>	
<action> lock action</action>	none: no tag in RF field	
000~3FF	<ok>: lock ok</ok>	
	<error code=""></error>	
	0: other error	
	3: memory overrun	
	4: memory locked	
	B: Insufficient power	
	F: Non-specific error	
P <password></password>	P	set access password for R
		•

<pre><password> access password</password></pre>		W L command, one time
00000000~FFFFFFF		use
U	U <none epc="" or=""></none>	Multi-TAG read EPC
	<none epc="" or=""></none>	
	none: no tag in RF field	
	EPC: PC+EPC+CRC16	
G1	G1	Start command logging
G0	G0	End command logging
G2	G2	Run logging commands
		For external TACT switch
		function
T <bank>,<bit address="">,<bit< td=""><td>T</td><td>Select matching tag</td></bit<></bit></bank>	T	Select matching tag
length >, <bit data=""></bit>		
<bank> memory bank</bank>		
0: reserved		
1: EPC		
2: TID		
3: USER		
<pre> <bit address=""> start bit address</bit></pre>		
0~3FFF		
<pre><bit length=""> select bit length</bit></pre>		
1~60		
 select bit mask		
data		
N0,00	N <value></value>	Read/Set RFID Reader
read RFID Reader power		power level
N1, <value></value>	<null></null>	
set RFID Reader power		
(-2~25dBm)		
<value> 00~1B</value>		
N4,00	N <value></value>	Read/Set Frequency Range
read Regulation	<value></value>	
N5, <value></value>	01: US 902~928	
set Regulation	02: TW 922~928	
<value> 01~08</value>	03: CN 920~925	
01: US 902~928	04: CN2 840~845	
02: TW 922~928	05: EU 865~868	
03: CN 920~925	06: JP 916~921	
04: CN2 840~845	07: KR 917~921	
05: EU 865~868	08: VN 918~923	
06: JP 916~921		

07: KR 917~921		
08: VN 918~923		
00. 117 710 723		
N6,00	N <value></value>	get/set GPIO input/output
get GPIO configuration	<value></value>	configuration
N7, <value></value>	4+2+1	- Coming with the second secon
set GPIO configuration	4: pin10 out	
<pre><value>mask and setting</value></pre>	2: pin11 out	
mask: first digi 4+2+1	1: pin14 out	
4: pin10	1. piii i out	
2: pin11		
1: pin14		
setting: second digi 4+2+1		
4: pin10 out		
2: pin11 out		
1: pin14 out		
1. piii 14 out		
N8,00	N <value></value>	read/write GPIO pins
read GPIO pins	<value></value>	1
N9, <value></value>	4+2+1	
write GPIO pins	4: pin10 high level	
<pre><value>mask and setting</value></pre>	2: pin11 high level	
mask: first digi 4+2+1	1: pin14 high level	
4: pin10	1.	
2: pin11		
1: pin14		
setting: second digi 4+2+1		
4: pin10 high		
2: pin11 high		
1: pin14 high		
UR:	U <epc>,R<data> or</data></epc>	Multi-Band data read with
U <slot q="">,</slot>	<error code=""></error>	EPC for multi-Tag read
R <band>,<address>,</address></band>	EPC= PC+EPC+CRC16	
<length></length>	DATA= read data	
Slot Q: 0~10	Error code:	
<bar> <bank> memory bank</bank></bar>	0: other error	
0: reserved	3: memory overrun	
1: EPC	4: memory locked	
2: TID	B: Insufficient power	
3: USER	F: Non-specific error	
<address> start word address</address>		
	1	ı

$0 \sim 3FFF$		
<pre><length> read word length</length></pre>		
1 ~ 1E		
QR:	Q <epc>,R<data> or</data></epc>	Multi-Band data read with
Q,	<error code=""></error>	EPC for single-Tag read
R <band>,<address>,<length></length></address></band>		8 8
<bar> bank> memory bank</bar>	EPC= PC+EPC+CRC16	
0: reserved	DATA= read data	
1: EPC	Error code:	
2: TID	0: other error	
3: USER	3: memory overrun	
<address> start word address</address>	4: memory locked	
$0 \sim 3FFF$	B: Insufficient power	
<pre><length> read word length</length></pre>	F: Non-specific error	
1 ~ 1E	-	
NA, <value></value>	N <value></value>	Setting UART Baud Rate.
setting UART Baud Rate	<value></value>	After getting the
<value></value>	0: 4800	reply,Baud Rate will be
0: 4800	1: 9600	changed
1: 9600	2: 14400	
2: 14400	3: 19200	
3: 19200	4: 38400	
4: 38400	5: 57600	
5: 57600	6: 115200	
6: 115200	7: 230400	
7: 230400		

^{*}command is start with <LF> stop with <CR>

Example:

1. Read TID memory bank, start address at 0, read 4 words length, TID data is 0x1234567890

Host send: <LF>R2,0,4<CR>

Hex format: 0A 52 32 2C 30 2C 34 0D

Reader message: <LF>R123456789ABCDEF0<CR><LF>

Hex format: 0A 52 31 32 33 34 35 36 37 38 39 41 42 43 44 45 46 30 0D 0A

2. Write USER memory bank, start address at 12, write 2 word length, write data is 0xAAAABBBB

Host send: <LF>W3,C,2,AAAABBBB<CR>

Hex format: 0A 57 33 2C 43 2C 32 2C 41 41 41 41 42 42 42 42 0D

Reader message: <LF>W<OK><CR><LF>

Hex format: 0A 57 3C 4F 4B 3E 0D 0A

^{*}Return Message is start with <LF> stop with <CR><LF>