| 指令格式 | [31:26] | [25:21] | [20:16] | [15:11] | [10:6] | [5:0] | [15:0] | 异常 |
|--------------------|---------|---------|---------|---------|--------|--------|--------|------|
| ADD rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100000 | | 整型溢出 |
| ADDI rt, rs, imm | 001000 | rs | rt | | | | imm | 整型溢出 |
| ADDU rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100001 | | |
| ADDIU rt, rs, imm | 001001 | rs | rt | | | | imm | |
| SUB rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100010 | | 整型溢出 |
| SUBU rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100011 | | |
| SLT rd, rs, rt | 000000 | rs | rt | rd | 00000 | 101010 | | |
| SLTI rt, rs, imm | 001010 | rs | rt | | | | imm | |
| SLTU rd, rs, rt | 000000 | rs | rt | rd | 00000 | 101011 | | |
| SLTIU rt, rs, imm | 001011 | rs | rt | | | | imm | |
| DIV rs, rt | 000000 | rs | rt | 00000 | 00000 | 011010 | | |
| DIVU rs, rt | 000000 | rs | rt | 00000 | 00000 | 011011 | | |
| MULT rs, rt | 000000 | rs | rt | 00000 | 00000 | 011000 | | |
| MULTU rs, rt | 000000 | rs | rt | 00000 | 00000 | 011001 | | |
| AND rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100100 | | |
| ANDI rt, rs, imm | 001100 | rs | rt | | | | imm | |
| LUI rt, imm | 001111 | 00000 | rt | | | | imm | |
| NOR rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100111 | | |
| OR rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100101 | | |
| ORI rt, rs, imm | 001101 | rs | rt | | | | imm | |
| XOR rd, rs, rt | 000000 | rs | rt | rd | 00000 | 100110 | | |
| XORI rt, rs, imm | 001110 | rs | rt | | | | imm | |
| SLLV rd, rt, rs | 000000 | rs | rt | rd | 00000 | 000100 | | |
| SLL rd, rt, sa | 000000 | 00000 | rt | rd | sa | 000000 | | |
| SRAV rd, rt, rs | 000000 | rs | rt | rd | 00000 | 000111 | | |
| SRA rd, rt, sa | 000000 | 00000 | rt | rd | sa | 000011 | | |
| SRLV rs, rt, rd | 000000 | rs | rt | rd | 00000 | 000110 | | |
| SRL rd, rt, sa | 000000 | 00000 | rt | rd | sa | 000010 | | |
| BEQ rs, rt, offset | 000110 | rs | rt | | | | offset | |

| 指令格式 | [31:26] | [25:21] | [20:16] | [15:11] | [10:6] | [5:0] | [15:0] | 异常 |
|----------------------|---------|---------|---------|---------|--------|-------------|--------|------|
| BNE rs, rt, offset | 000101 | rs | rt | | | | offset | |
| BGEZ rs, offset | 000001 | rs | 00001 | | | | offset | |
| BGTZ rs, offset | 000111 | rs | 00000 | | | | offset | |
| BLEZ rs, offset | 000110 | rs | 00000 | | | | offset | |
| BLTZ rs, offset | 000001 | rs | 00000 | | | | offset | |
| BGEZAL rs, offset | 000001 | rs | 10001 | | | | offset | |
| BLTZAL rs, offset | 000001 | rs | 10000 | | | | offset | |
| J target | 000010 | | | | | | | |
| JAL target | 000011 | | | | | | | |
| JR rs | 000000 | rs | 00000 | 00000 | 00000 | 001000 | | |
| JALR rd, rs | 000000 | rs | 00000 | rd | 00000 | 001001 | | |
| MFHI rd | 000000 | 00000 | 00000 | rd | 00000 | 010000 | | |
| MFLO rd | 000000 | 00000 | 00000 | rd | 00000 | 010010 | | |
| MTHI rs | 000000 | rs | 00000 | 00000 | 00000 | 010001 | | |
| MTLO rs | 000000 | rs | 00000 | 00000 | 00000 | 010011 | | |
| BREAK | 000000 | code | code | code | code | 001101 | | 断点 |
| SYSCALL | 000000 | code | code | code | code | 001100 | | 系统调用 |
| LB rt, offset(base) | 100000 | base | rt | | | | offset | |
| LBU rt, offset(base) | 100100 | base | rt | | | | offset | |
| LH rt, offset(base) | 100001 | base | rt | | | | offset | |
| LHU rt, offset(base) | 100101 | base | rt | | | | offset | |
| LW rt, offset(base) | 100011 | base | rt | | | | offset | |
| SB rt, offset(base) | 101000 | base | rt | | | | offset | |
| SH rt, offset(base) | 101001 | base | rt | | | | offset | |
| SW rt, offset(base) | 101011 | base | rt | | | | offset | |
| ERET | 010000 | 10000 | 00000 | 00000 | 00000 | 011000 | | |
| MFC0 rt, rd, sel | 010000 | 00000 | rt | rd | 00000 | {3'b0, sel} | | |
| MTC0 rt, rd, sel | 010000 | 00100 | rt | rd | 00000 | {3'b0, sel} | | |