

NOZOMI-3 UMA SVT LOGIC SCHEMATICS



NZM3I-6
VER 6.06
Nov/25/2010

BASE LOGIC :NZM3 UMA SIT
VER 5.11
OCT/15/2010

1.TITLE PAGE
2.EC HISTORY
3.CPU(1/8) : DMI/EDP/PEG/FDI
4.CPU(2/8) : CLK/MISC/JTAG
5.CPU(3/8) : DDR3 CHANNEL- A
6.CPU(4/8) : DDR3 CHANNEL- B
7.CPU(5/8) : PROCESSOR POWER
8.CPU(6/8) : GRAPHICS POWER
9.CPU(7/8) : GND
10.CPU(8/8) : CFG/RESERVED
11.XDP CONNECTOR
12.DDR3 SO DIMM CHANNEL-A
13.DDR3 SO DIMM CHANNEL-B
14.DDR3 DECOUPLING
15.BLANK
16.BLANK
17.BLANK
18.BLANK
19.BLANK
20.BLANK
21.BLANK
22.BLANK
23.PCH(1/9) : HDA/JTAG/SPI/SATA
24.PCH(2/9) : PCI-E/SMBUS/CLK
25.PCH(3/9) : DMI/FDI/PM
26.PCH(4/9) : LVDS/CRT/DDI
27.PCH(5/9) : PCI/USB/NVRAM
28.PCH(6/9) : GPIO/NCTF/RSVD
29.PCH(7/9) : POWER
30.PCH(8/9) : POWER
31.PCH(9/9) : GND
32.LCD CONNECTOR
33.RGB SWITCH
34.EXT CRT INTERFACE

35.DISPLAY PORT CONNECTOR
36.DOCK DISPLAY PORT
37.DISPLAY PORT MUX
38.RTC BATTERY
39.SATA HDD CONN
40.SATA BAY I/F CONN
41.ESATA CONNECTOR
42.USB POWER/CONN
43.AUDIO CX20672-21Z
44.AUDIO CONNECTOR
45.AUDIO JACK SENSE
46.AUDIO EXT MIC I/F
47.AUDIO SPEAKER
48.AUDIO BEEP
49.MDC CONNECTOR
50.GBE LEWISVILLE
51.GBE LAN SWITCH
52.GBE MAGNETICS
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54.PCIE MINI CARD SLOT
55.1394/MEDIA CARD CONTROLLER
56.MEDIA CARD INTERFACE
57.EXPRESS CARD/SMART CARD I/F
58.SLOT POWER CONTROL
59.SPI FLASH
60.DOCKING CONNECTOR
61.H8S/2113 (1/2)
62.H8S/2113 (2/2)
63.KEYBOARD CONNECTOR
64.TOUCH PAD CONNECTOR
65.WIRELESS DISABLE SW
66.FAN CONNECTOR
67.G-SENSOR
68.THINKER-1
69.TPM

70.THERMAL SENSOR
71.EEPROM/SMBUS SW
72.DC-IN
73.BATTERY INPUT
74.BATTERY CHARGER(BQ24742)
75.CHARGE SELECTOR
76.BATTERY MONITOR
77.POWER SEQUENCE
78.DC/DC VCC5M/VCC3M(TPS51220ARSNR)
79.DC/DC VCCCPUCORE(VT1316/VT1317)
80.BLANK
81.DC/DC VCCGFXCORE_I(VT1317)
82.VCCCPUCORE DECOUPLING
83.BLANK
84.DC/DC VCC1R05B_VTT(VT356)
85.DC/DC VCC1R05AMT(VT356)
86.DC/DC VCC1R5A(VT357)
87.DC/DC VCC0R75B(MAX1510)
88.BLANK
89.DC/DC VCC1R8B(TPS62060)
90.DC/DC VCCSA(VT355)
91.DC/DC RINKAN-2
92.LOAD SW LAN
93.BLANK
94.MEPWRG
95.LOAD SW B
96.LOAD SW VCC5MUBAY
97.LOAD SW WAN & WLAN
98.PTH FOR SCREW HOLES
99.BLANK

EC HISTORY

NOZOMI-3 PRE-DV :BASE LOGIC NZM1 EXT 512MB SIT VER.7.03 12/01/2009

VER.0.01 12/10/2009 APPLIED PDV_EC001
VER.0.02 12/14/2009 APPLIED PDV_EC002-012
VER.0.03 12/15/2009 APPLIED PDV_EC013-025
VER.0.04 12/16/2009 APPLIED PDV_EC026-033
VER.0.05 12/17/2009 APPLIED PDV_EC034-038
VER.0.06 12/18/2009 APPLIED PDV_EC039-041,043-45,047-054
VER.0.07 12/21/2009 APPLIED PDV_EC042,046,055-066
VER.0.08 12/22/2009 APPLIED PDV_EC067-080
VER.0.09 12/24/2009 APPLIED PDV_EC081-085
VER.0.10 12/25/2009 APPLIED PDV_EC086-089
VER.0.11 12/28/2009 APPLIED PDV_EC090-095
VER.0.12 01/06/2010 APPLIED PDV_EC096-112
VER.0.13 01/07/2010 APPLIED PDV_EC113-120
VER.0.14 01/08/2010 APPLIED PDV_EC121
VER.0.15 01/12/2010 APPLIED PDV_EC122
VER.0.16 01/13/2010 APPLIED PDV_EC123-125
VER.0.17 01/14/2010 APPLIED PDV_EC126-128,130
VER.0.18 01/15/2010 APPLIED PDV_EC131-133
VER.0.19 01/18/2010 APPLIED PDV_EC134-137

VER.0.20 01/19/2010 APPLIED PDV_EC138
VER.0.21 01/20/2010 APPLIED PDV_EC139,140
VER.0.22 01/21/2010 APPLIED PDV_EC141-146
VER.0.23 01/22/2010 APPLIED PDV_EC147,148
VER.0.24 01/25/2010 APPLIED PDV_EC149-160
VER.0.25 01/26/2010 APPLIED PDV_EC162-168
VER.0.26 01/27/2010 APPLIED PDV_EC169-176
VER.0.27 01/28/2010 APPLIED PDV_EC177-179
VER.0.28 01/29/2010 APPLIED PDV_EC180
VER.0.29 02/01/2010 APPLIED PDV_EC181-189
VER.0.30 02/02/2010 APPLIED PDV_EC190-193
VER.0.31 02/03/2010 APPLIED PDV_EC194-197
VER.0.32 02/04/2010 APPLIED PDV_EC198-202

NOZOMI-3 SDV :BASE LOGIC NZM3 PRE-DV VER.0.32 02/04/2010

VER.1.00 02/05/2010 APPLIED SDV_EC001-004
VER.1.01 02/08/2010 APPLIED SDV_EC005-007
VER.1.02 02/09/2010 APPLIED SDV_EC010
VER.1.03 02/10/2010 APPLIED SDV_EC011-013
VER.1.04 02/15/2010 APPLIED SDV_EC014
VER.1.05 02/16/2010 APPLIED SDV_EC015-019
VER.1.06 02/17/2010 APPLIED SDV_EC020-022
VER.1.07 02/18/2010 APPLIED SDV_EC024,025,027
VER.1.08 02/19/2010 APPLIED SDV_EC028,030-032
VER.1.09 02/22/2010 APPLIED SDV_EC033-035
VER.1.10 02/23/2010 APPLIED SDV_EC036-038

VER.1.11 03/18/2010 APPLIED SDV_EC045,046
VER.1.12 03/19/2010 APPLIED SDV_EC040-043,CNV_EC061-066
VER.1.13 03/22/2010 APPLIED SDV_EC048-052
VER.1.14 03/24/2010 APPLIED SDV_EC053-059, ECR001-003
VER.1.15 03/26/2010 APPLIED SDV_EC061
VER.1.16 04/1/2010 APPLIED SDV_EC065-075, except SDV_EC069, SDV_EC076-079
VER.1.17 04/6/2010 APPLIED SDV_EC080-084, ECR004, ECR008 and ECR009
VER.1.18 04/8/2010 APPLIED SDV_EC085-091, SDV_ECR010-013
VER.1.19 04/13/2010 APPLIED SDV_EC092-094, change PCB footprint of all resistors from xxx to xxx-R, SDV_EC103 and EC047
VER.1.20 04/16/2010 APPLIED SDV_EC105-110 and ECR023, ECR026 and ECR027
VER.1.21 04/21/2010 APPLIED ECR028

NOZOM-3 UMA SDV: BASE LOGIC NZM3 SWG SDV VER 1.21 04/21/2010

VER.1.22 04/26/2010 APPLIED EC111-113, EC115-117 and assembly options for UMA planar

NOZOMI-3 UMA SDV2 :BASE LOGIC NZM3 UMA SDV VER.1.22 04/26/2010

VER.2.00 04/27/2010 APPLIED NZM3_UMA_SDV2_EC003-007
VER.2.01 04/29/2010 APPLIED NZM3_UMA_SDV2_EC008-018
VER.2.02 05/04/2010 APPLIED NZM3_UMA_SDV2_EC019-036
VER.2.03 05/12/2010 APPLIED NZM3_UMA_SDV2_EC037-038
VER.2.04 05/13/2010 APPLIED NZM3_UMA_SDV2_EC039
VER.2.05 05/21/2010 APPLIED NZM3_UMA_SDV2_EC040, EC041, EC044 and NZM3_UMA_SDV2_ECR001-002
VER.2.06 05/25/2010 APPLIED NZM3_UMA_SDV2_EC045, EC046, EC047, EC050, EC056, EC058, EC059, EC060 and EC062
VER.2.07 06/02/2010 APPLIED NZM3_UMA_SDV2_EC064-EC067
VER.2.08 06/03/2010 APPLIED NZM3_UMA_SDV2_EC068-EC070

NOZOMI-3 UMA FVT2:BASE LOGIC NZM3 UMA SDV2 VER.2.08 06/03/2010

VER.3.00 06/21/2010 APPLIED NZM3_UMA_FVT2_EC001-EC004, EC007-EC014, EC016-EC020, EC022 and EC024
VER.3.01 06/25/2010 APPLIED NZM3_UMA_FVT2_EC026, EC027, EC029, EC030, EC031 and EC033.
VER.3.02 07/02/2010 APPLIED NZM3_UMA_FVT2_EC036, EC037, EC039 and EC042
VER.3.03 07/05/2010 APPLIED NZM3_UMA_FVT2_EC044-EC050, EC052-EC055 and EC057
VER.3.04 07/05/2010 APPLIED NZM3_UMA_FVT2_EC059, EC062-EC065
VER.3.05 07/05/2010 APPLIED NZM3_UMA_FVT2_EC068-EC071
VER.3.06 07/09/2010 APPLIED NZM3_UMA_FVT2_EC072-EC081
VER.3.07 07/19/2010 APPLIED NZM3_UMA_FVT2_EC082-EC084
VER.3.08 07/20/2010 Rename SA_DIMM_VREFDQ as SA_DIMM_VREFDQ
Rename SB_DIMM_VREFDQ as SB_DIMM_VREFDQ
Shift page title 87-99
VER.3.09 07/23/2010 APPLIED NZM3_UMA_FVT2_EC085

NOZOMI-3 UMA MSIT:BASE LOGIC NZM3 UMA FVT2 VER.3.09 07/23/2010

VER.4.00 07/26/2010 APPLIED NZM3_UMA_MSIT_EC001, EC002, EC007 and EC008
VER.4.01 07/29/2010 APPLIED NZM3_UMA_MSIT_EC009-019
VER.4.02 08/02/2010 APPLIED NZM3_UMA_MSIT_EC020, EC023, EC024 and EC025
VER.4.03 08/04/2010 APPLIED NZM3_UMA_MSIT_EC028
VER.4.04 08/09/2010 APPLIED NZM3_UMA_MSIT_EC029-036 and EC039
VER.4.05 08/11/2010 APPLIED NZM3_UMA_MSIT_EC042-044
VER.4.06 08/13/2010 APPLIED NZM3_UMA_MSIT_EC045-048
VER.4.07 08/17/2010 APPLIED NZM3_UMA_MSIT_EC050
VER.4.08 08/23/2010 APPLIED NZM3_UMA_MSIT_EC051

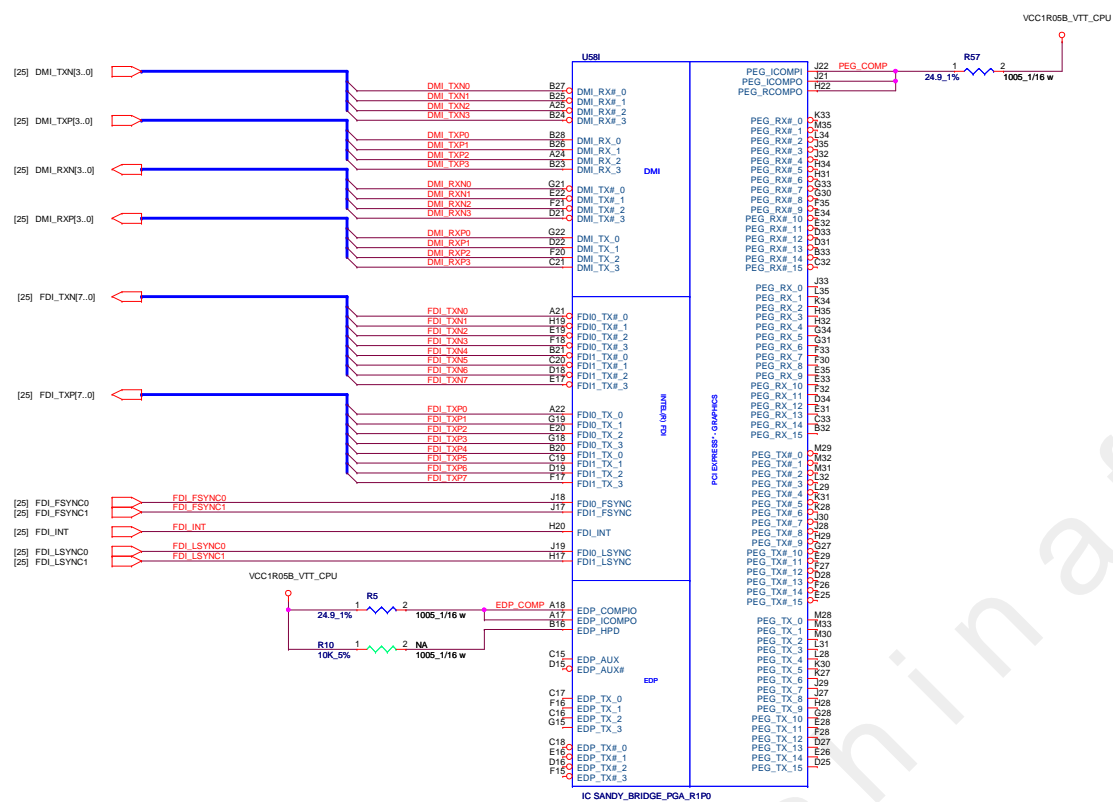
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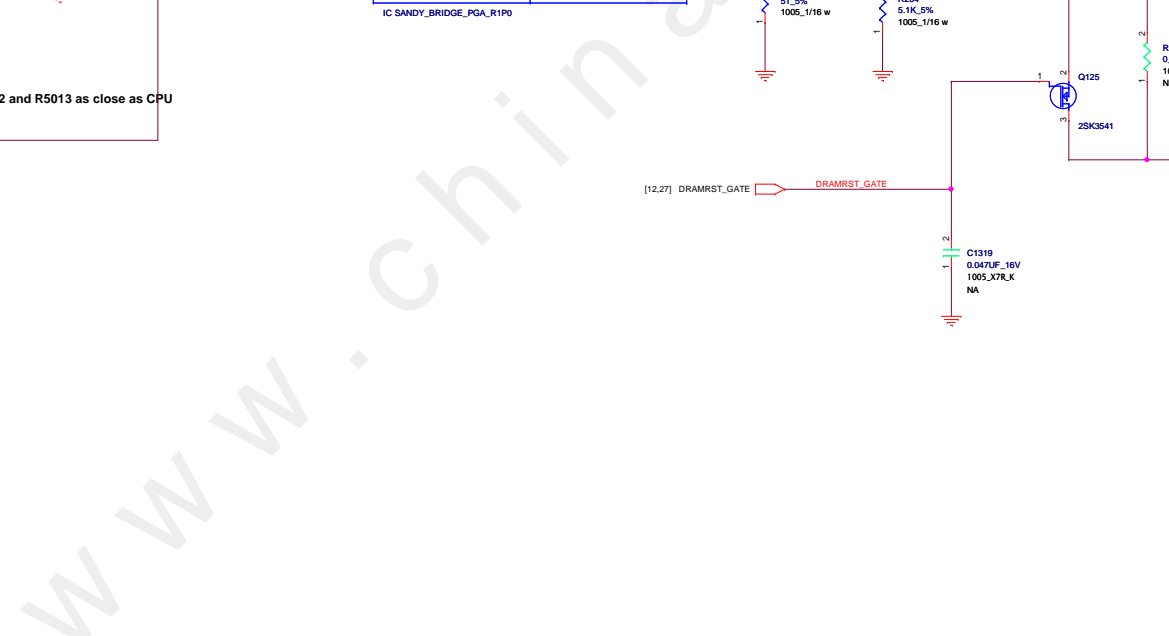
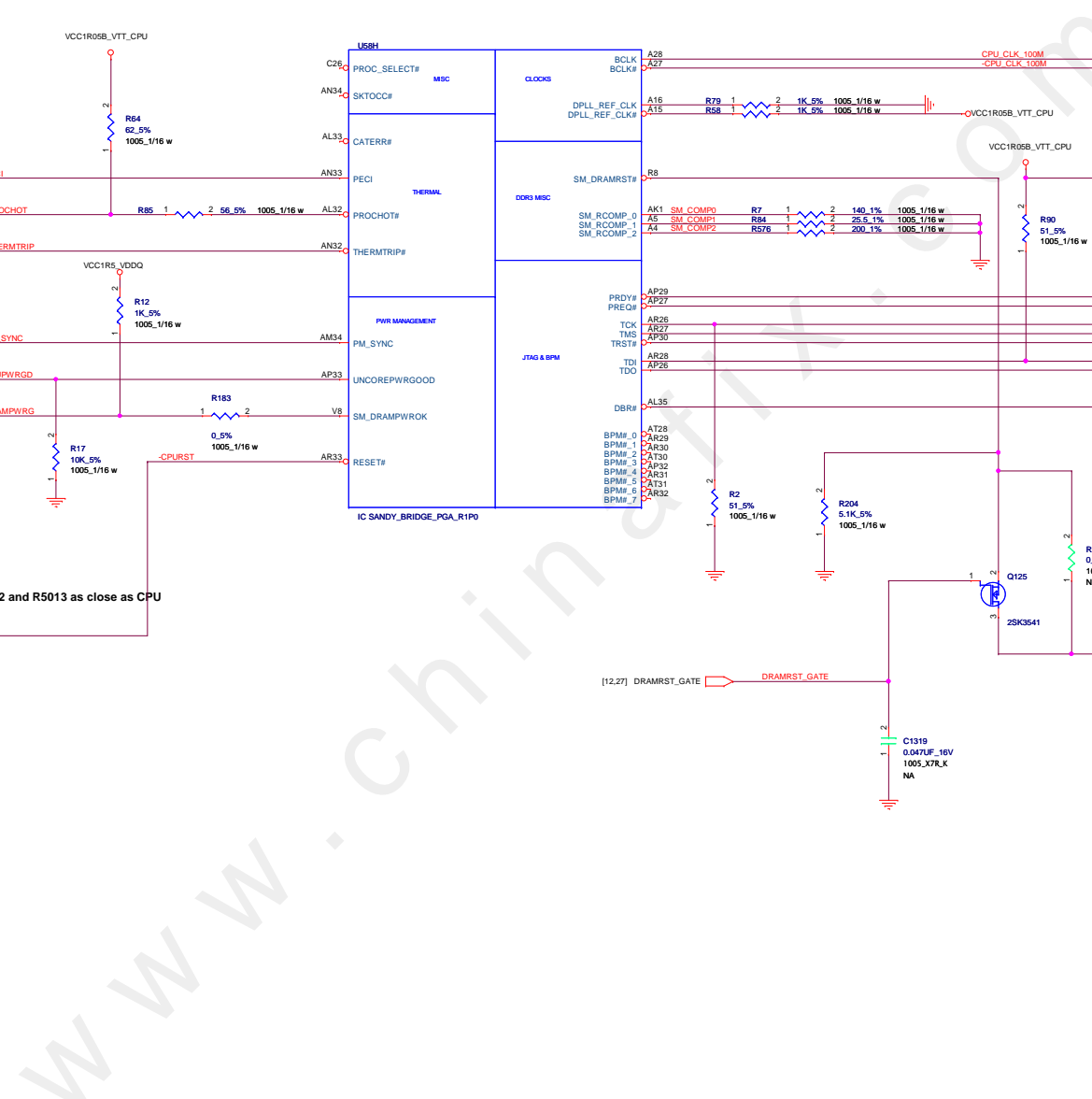
VER.5.00 08/31/2010 APPLIED NZM3_UMA_SIT_EC001, EC002 and EC004
VER.5.01 09/03/2010 APPLIED NZM3_UMA_SIT_EC005-EC008
VER.5.02 09/08/2010 APPLIED NZM3_UMA_SIT_EC009-EC013
VER.5.03 09/14/2010 APPLIED NZM3_UMA_SIT_EC014-EC017
VER.5.04 09/20/2010 APPLIED NZM3_UMA_SIT_EC019-EC021 and EC023
VER.5.05 09/23/2010 APPLIED NZM3_UMA_SIT_EC026, EC028, EC029 and EC030
VER.5.06 09/27/2010 APPLIED NZM3_UMA_SIT_EC031-EC037
VER.5.07 09/29/2010 APPLIED NZM3_UMA_SIT_EC040
VER.5.08 10/01/2010 APPLIED NZM3_UMA_SIT_EC041-EC044
VER.5.09 10/04/2010 APPLIED NZM3_UMA_SIT_EC045-EC050
VER.5.10 10/06/2010 Delete NPTH6, Add NUT3 on page 98
VER.5.11 10/15/2010 APPLIED NZM3_UMA_SIT_EC051-EC053

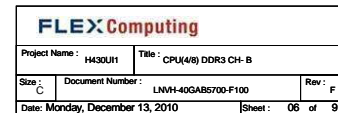
NOZOMI-3 UMA SVT:BASE LOGIC NZM3 UMA SIT VER.5.11 10/15/2010

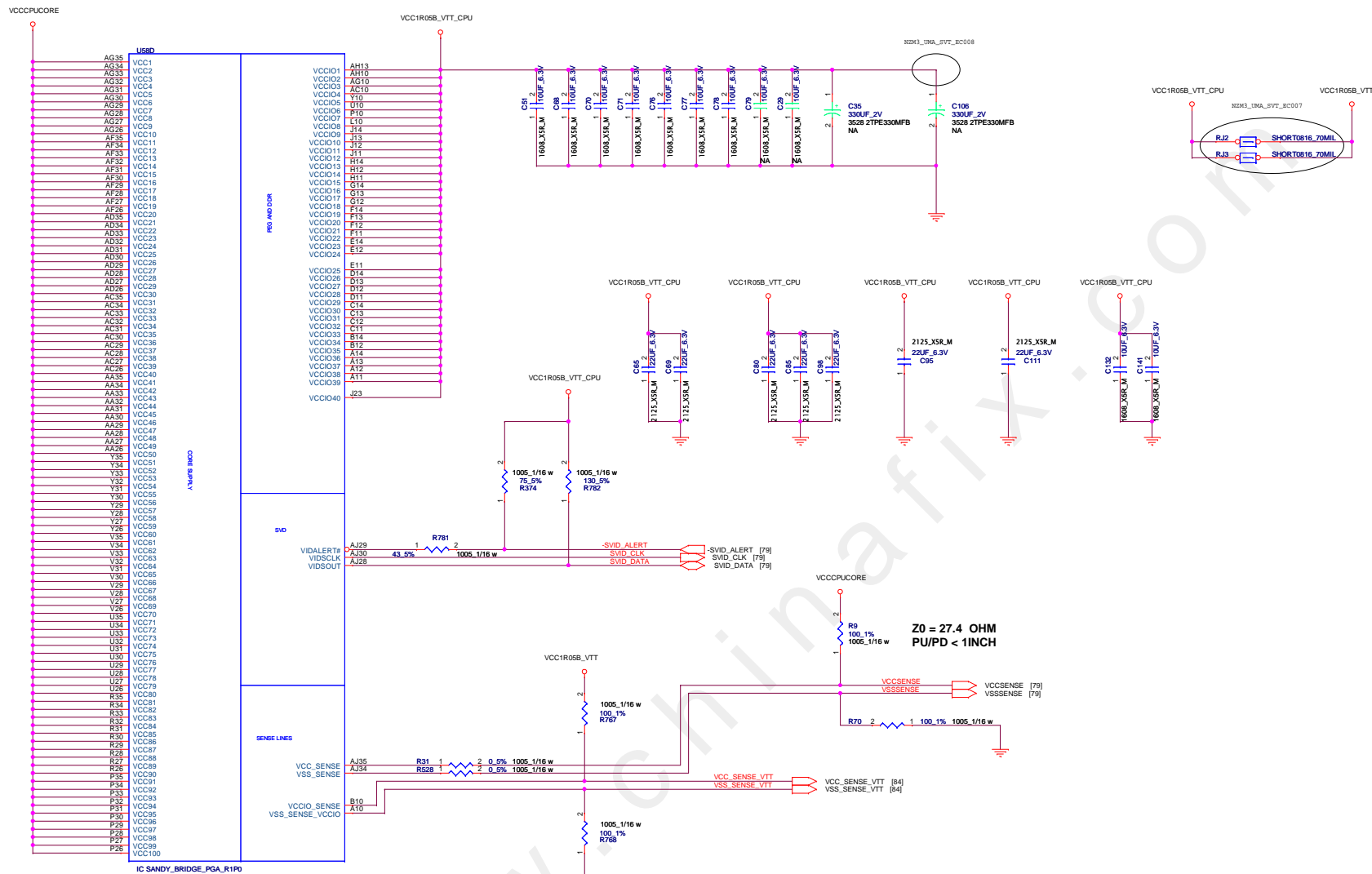
VER.6.00 10/21/2010 APPLIED NZM3_UMA_SVT_EC001, EC002, EC004, EC005 and EC006
VER.6.01 10/26/2010 APPLIED NZM3_UMA_SVT_EC007, EC008 and EC009
VER.6.02 11/02/2010 APPLIED NZM3_UMA_SVT_EC010, EC011 and EC012
VER.6.03 11/08/2010 APPLIED NZM3_UMA_SVT_EC016 and EC017
VER.6.04 11/12/2010 APPLIED NZM3_UMA_SVT_EC019 and EC020
VER.6.05 11/17/2010 APPLIED NZM3_UMA_SVT_EC022
VER.6.06 11/22/2010 APPLIED NZM3_UMA_SVT_EC023

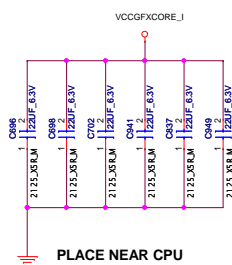




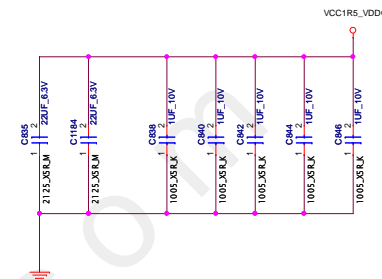
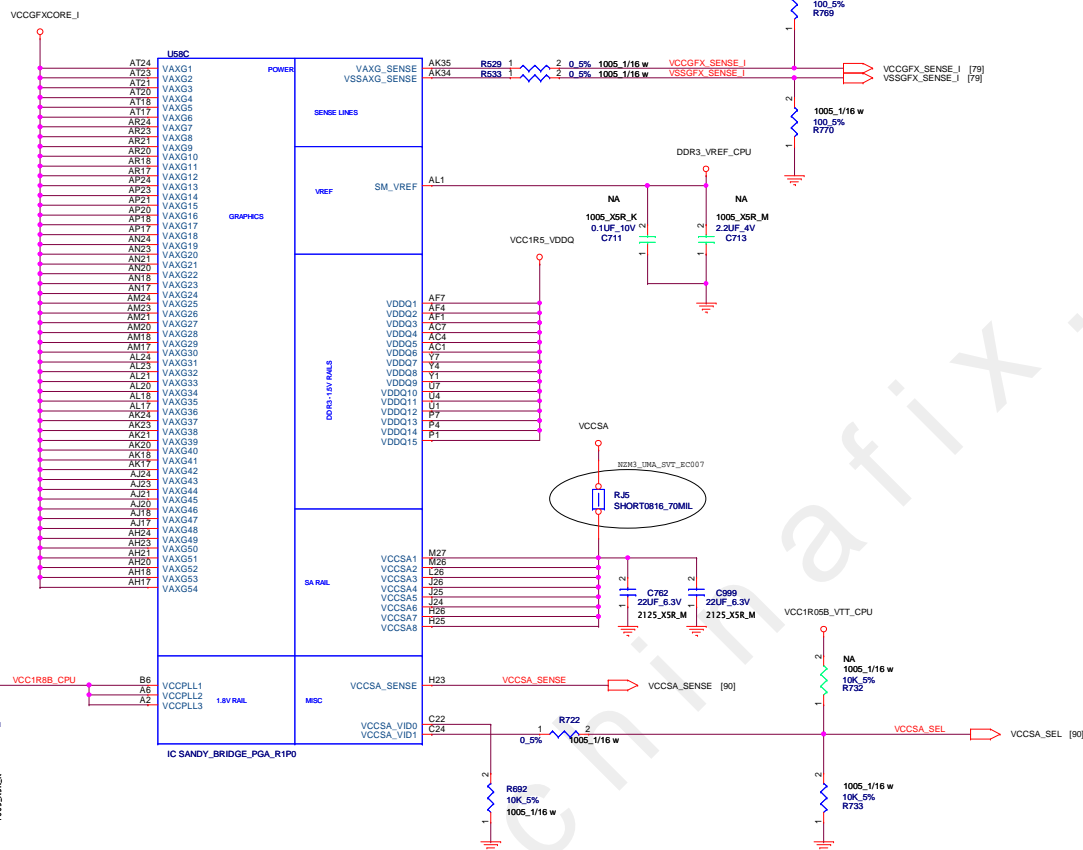
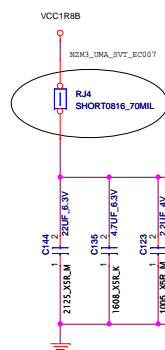


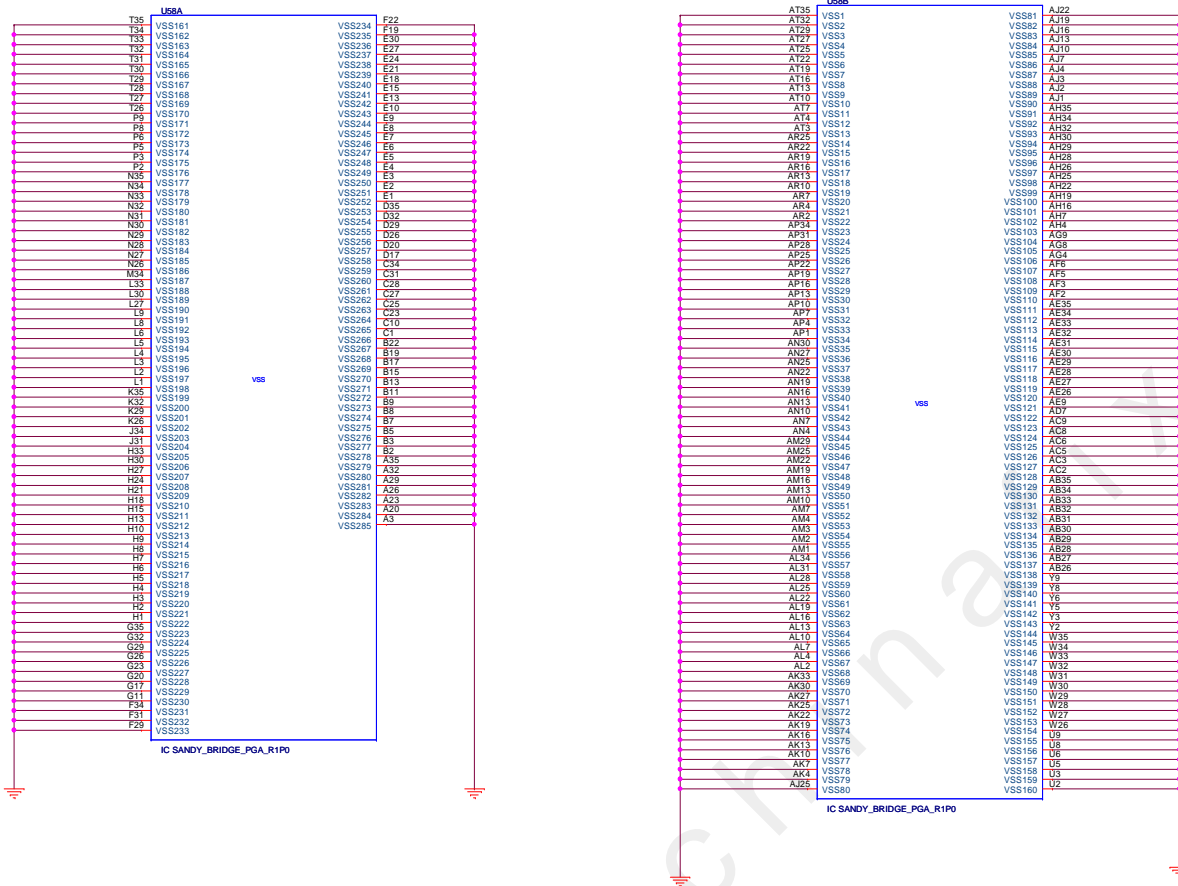






PLACE NEAR CPU





```
CFG2 PEG LAN REVERSAL
1 -NO ASM : NORMAL
0 -ASM : RESVERSE

CFG4 DISPLAY PORT PRESENCE
1 -NO ASM : DISABLE
0 -ASM : ENABLE

CFG[6 : 5] PEG BIFURCATION CONFIG
00 = 1 x 8, 2 x 4 PCI Express
01 = reserved
10 = 2 x 8 PCI Express
11 = 1 x 16 PCI Express

CFG7 PEG DEFER TRAINING
1 -NO ASM :PEG TRAIN IMMEDIATELY FOLLOWING XXRESETB DEASSERTION
0 -ASM : PEG WAIT FOR BIOS FOR TRAINING
```

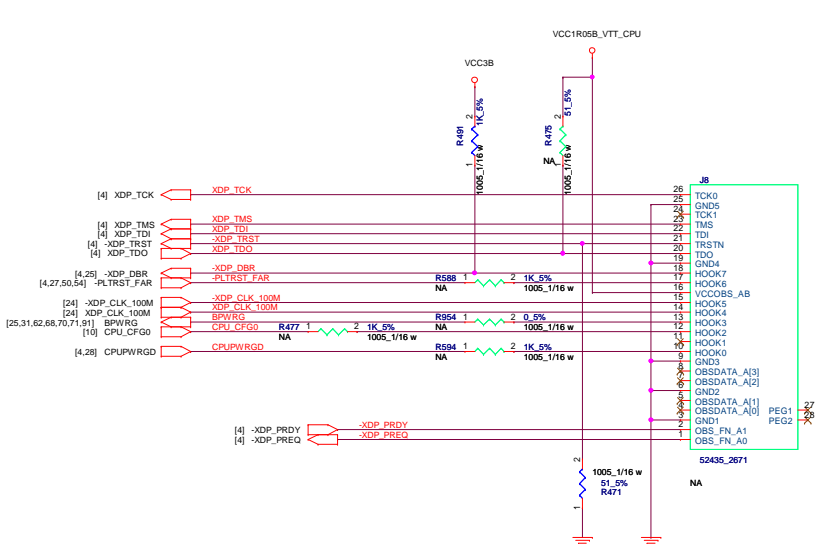


TABLE NOTE: J8 "ASM" FOR SDV1 ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
CFG0	R477	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
BPWRG	R954	ASM	NO ASM
	J8	ASM	NO ASM

LOGIC

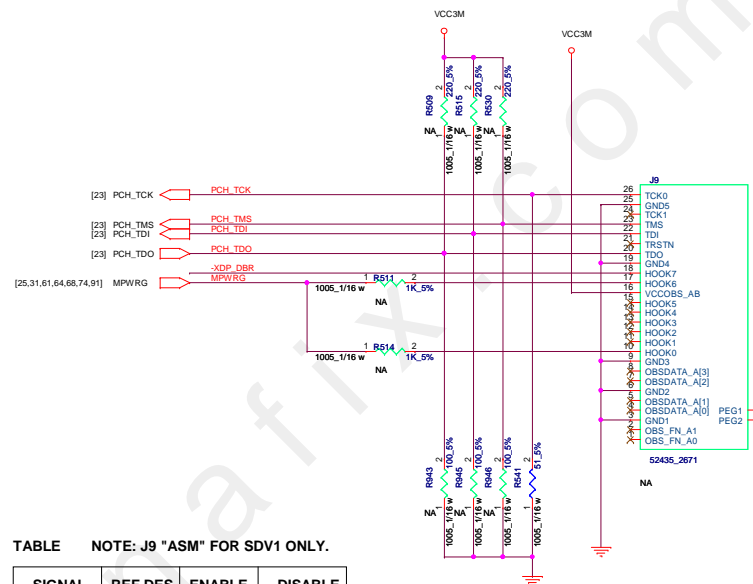
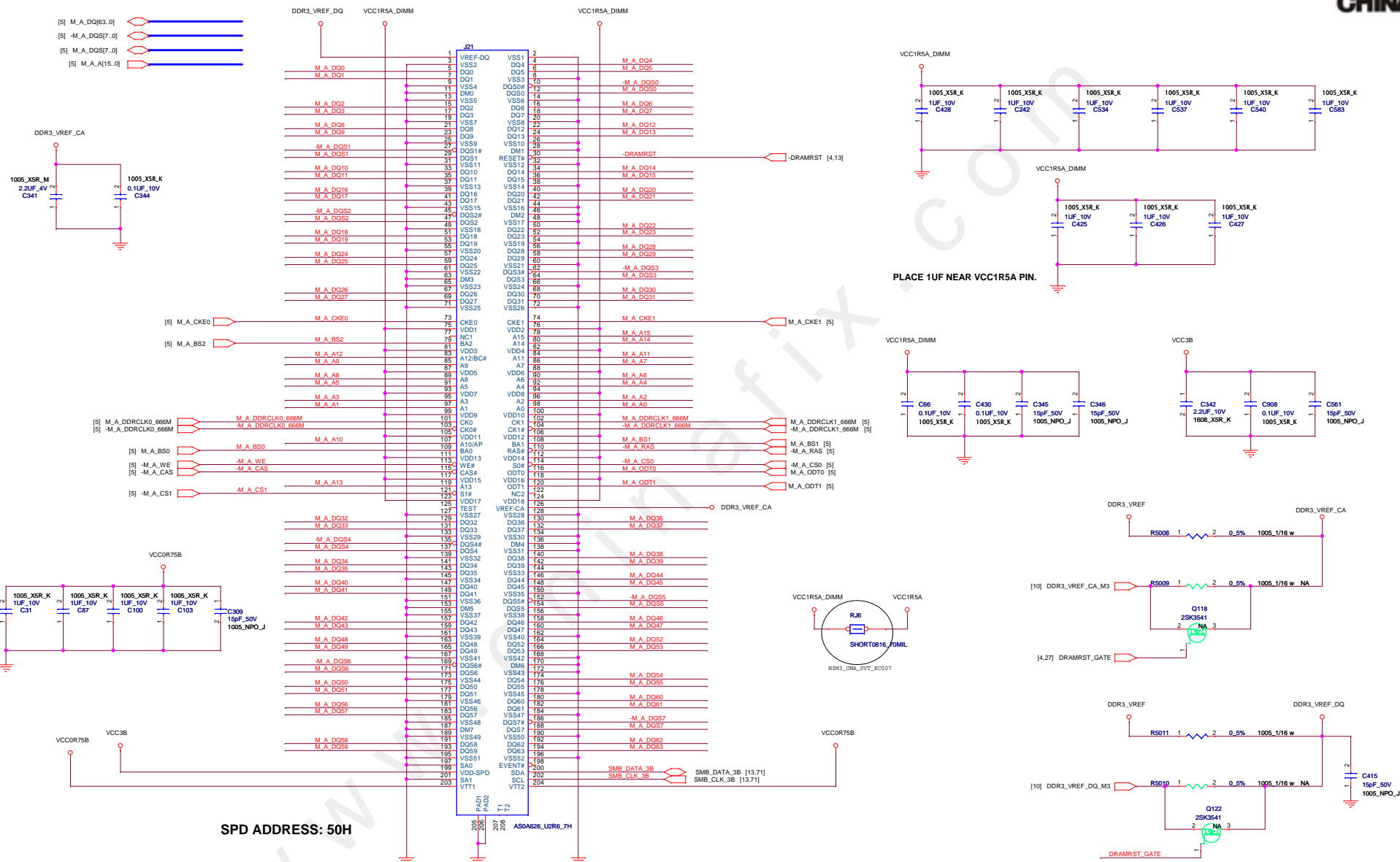
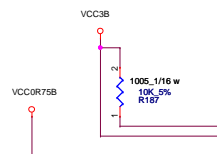
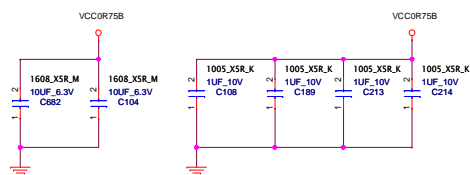
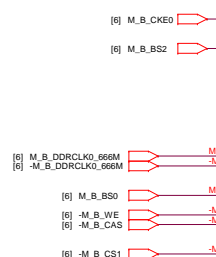
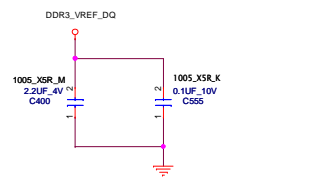
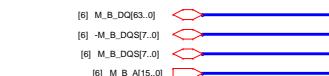


TABLE NOTE: J9 "ASM" FOR SDV1 ONLY.

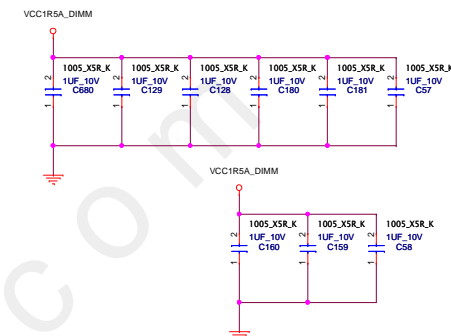
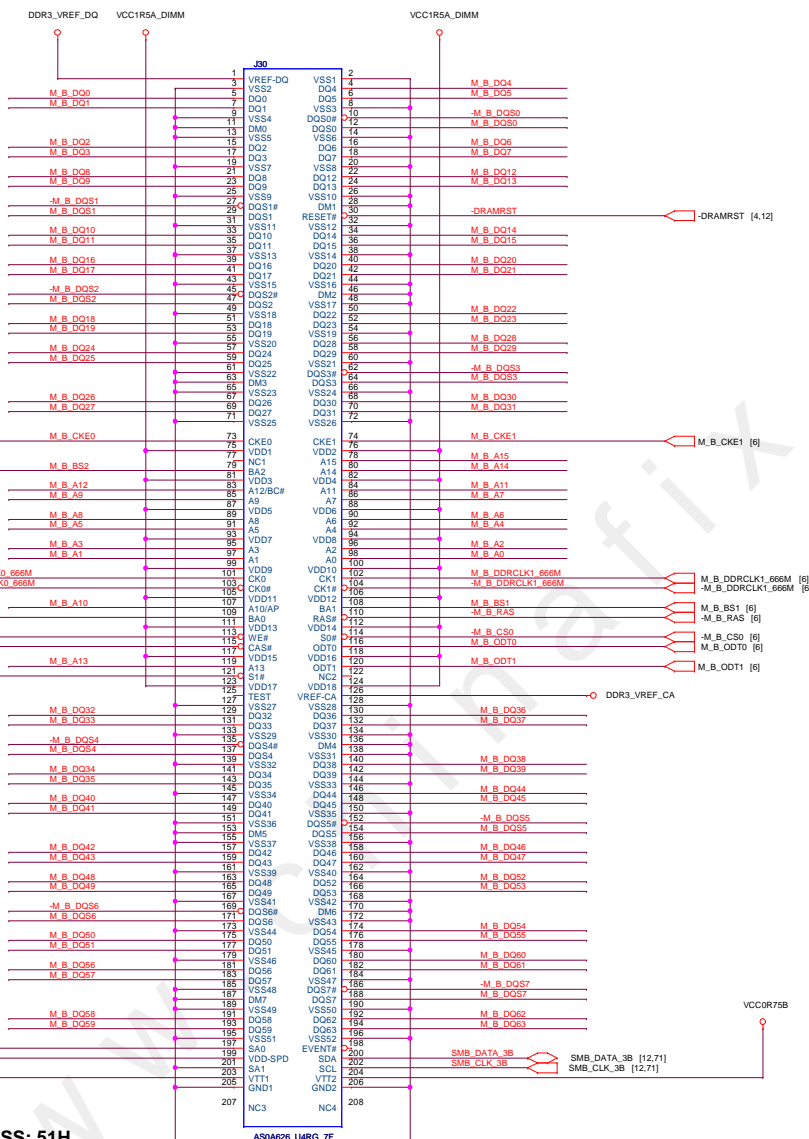
SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	200	NO ASM
	R943	100	NO ASM
TMS	R530	220	NO ASM
	R946	100	NO ASM
TDI	R515	220	NO ASM
	R945	100	NO ASM
TCK	R541	51	51
MPWRG	R511	ASM	NO ASM
	R514	ASM	NO ASM
	J9	ASM	NO ASM

LOGIC

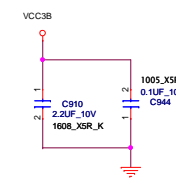
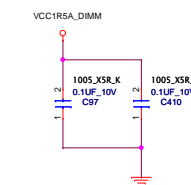


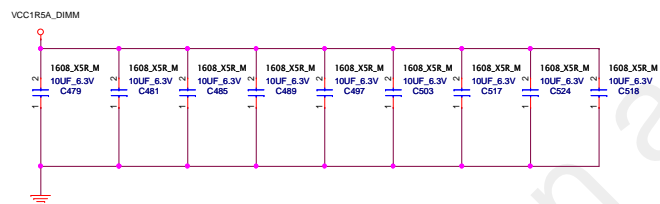
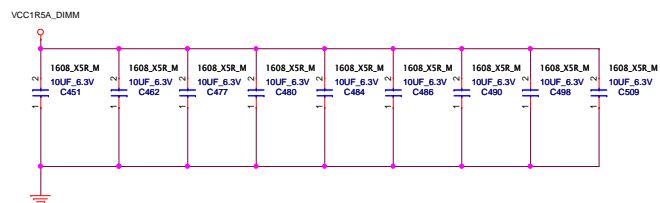
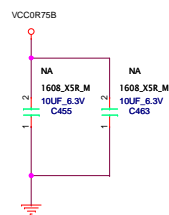


SPD ADDRESS: 51H



PLACE 1UF NEAR VCC1R5A PIN







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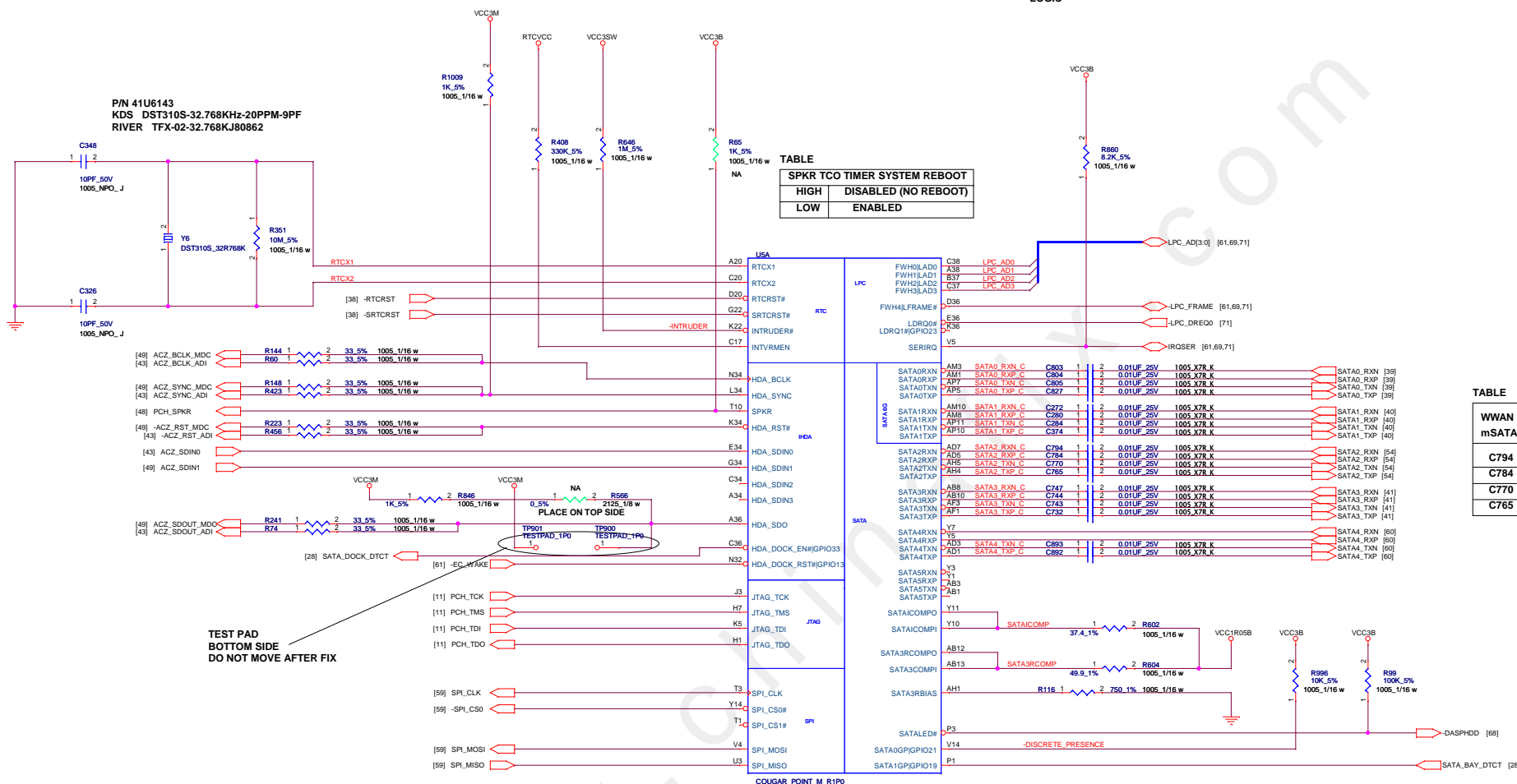


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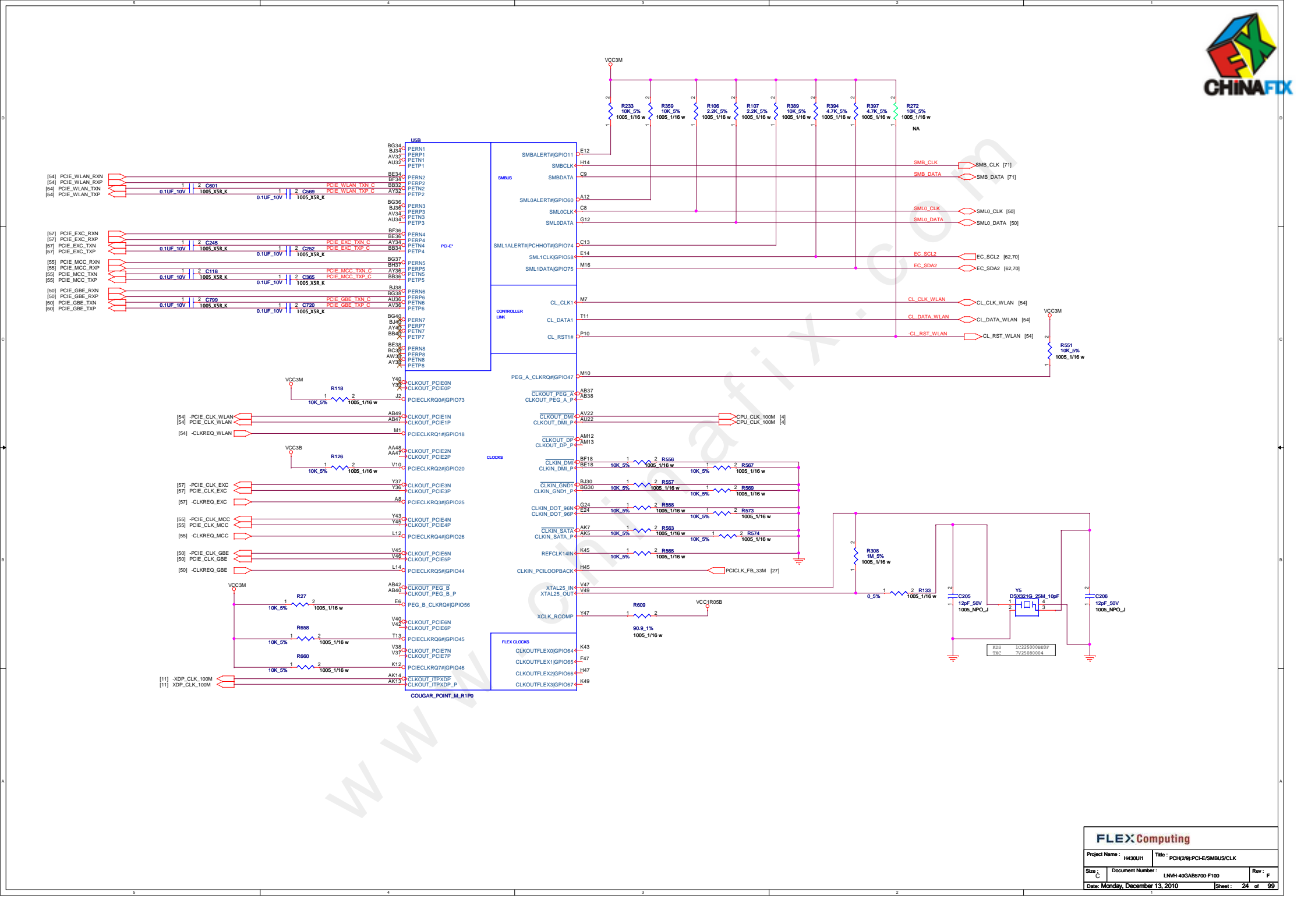
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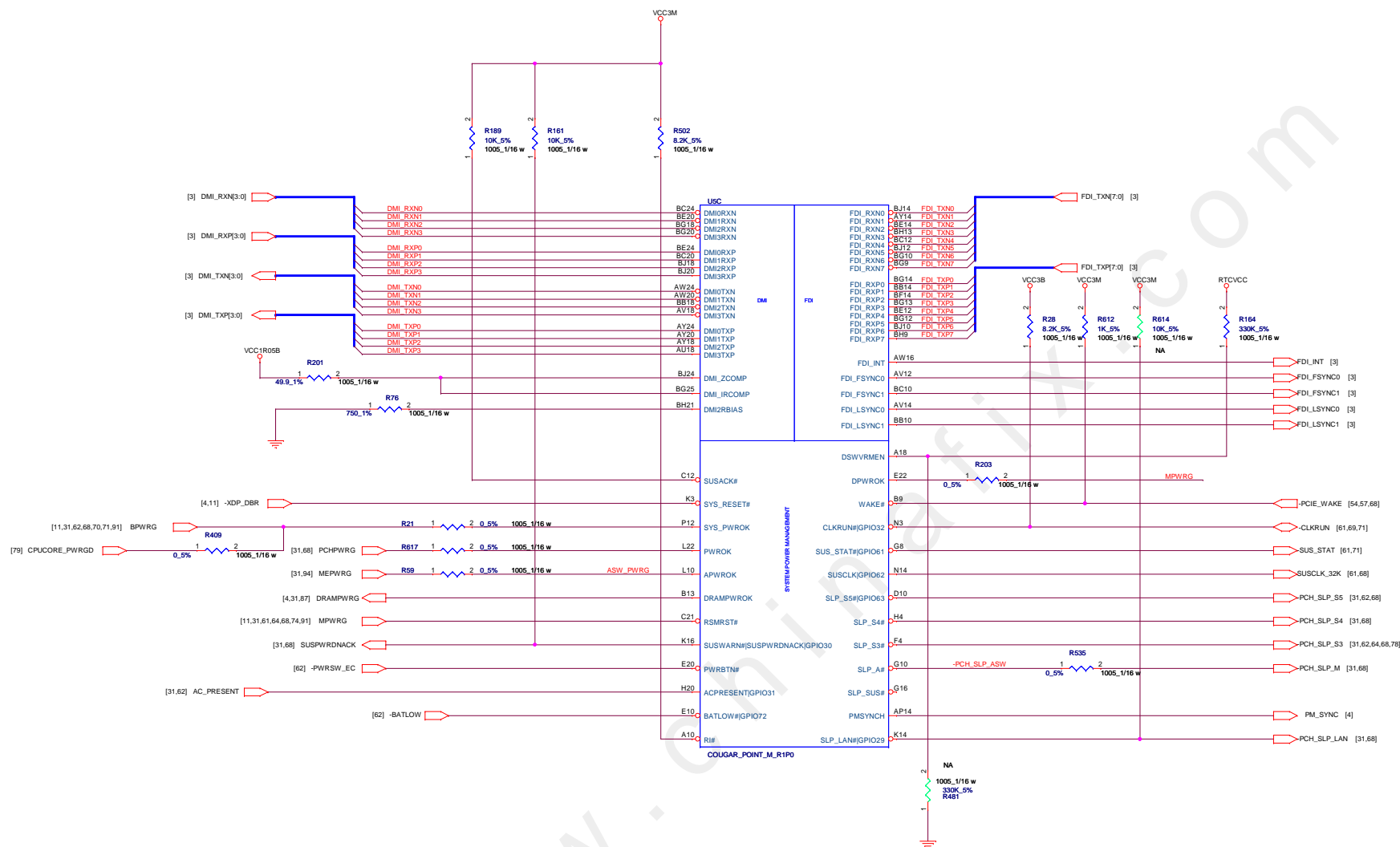
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RPAT	YES	YES	NO
U5	QM67	HM67	HM65

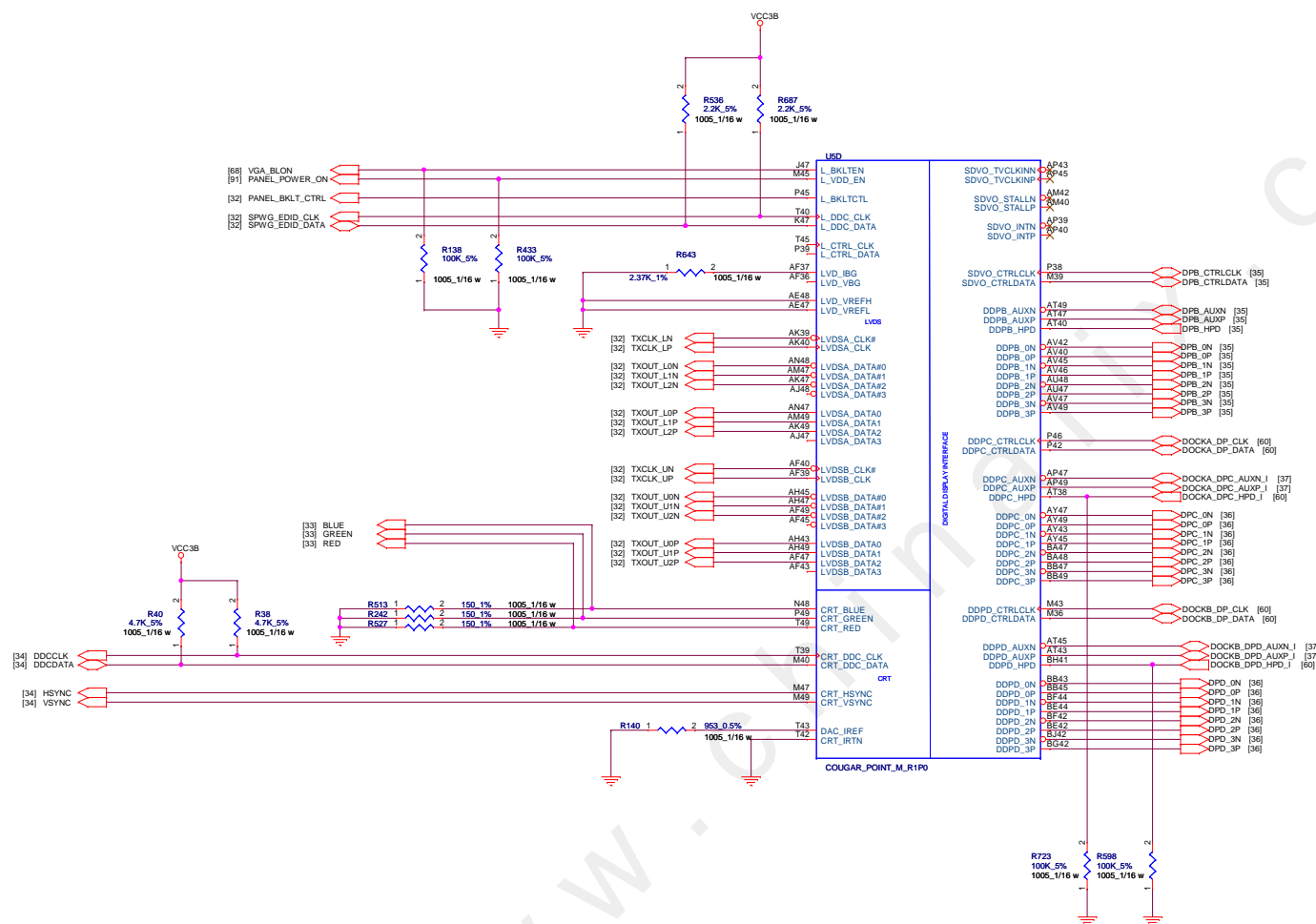
LOGIC

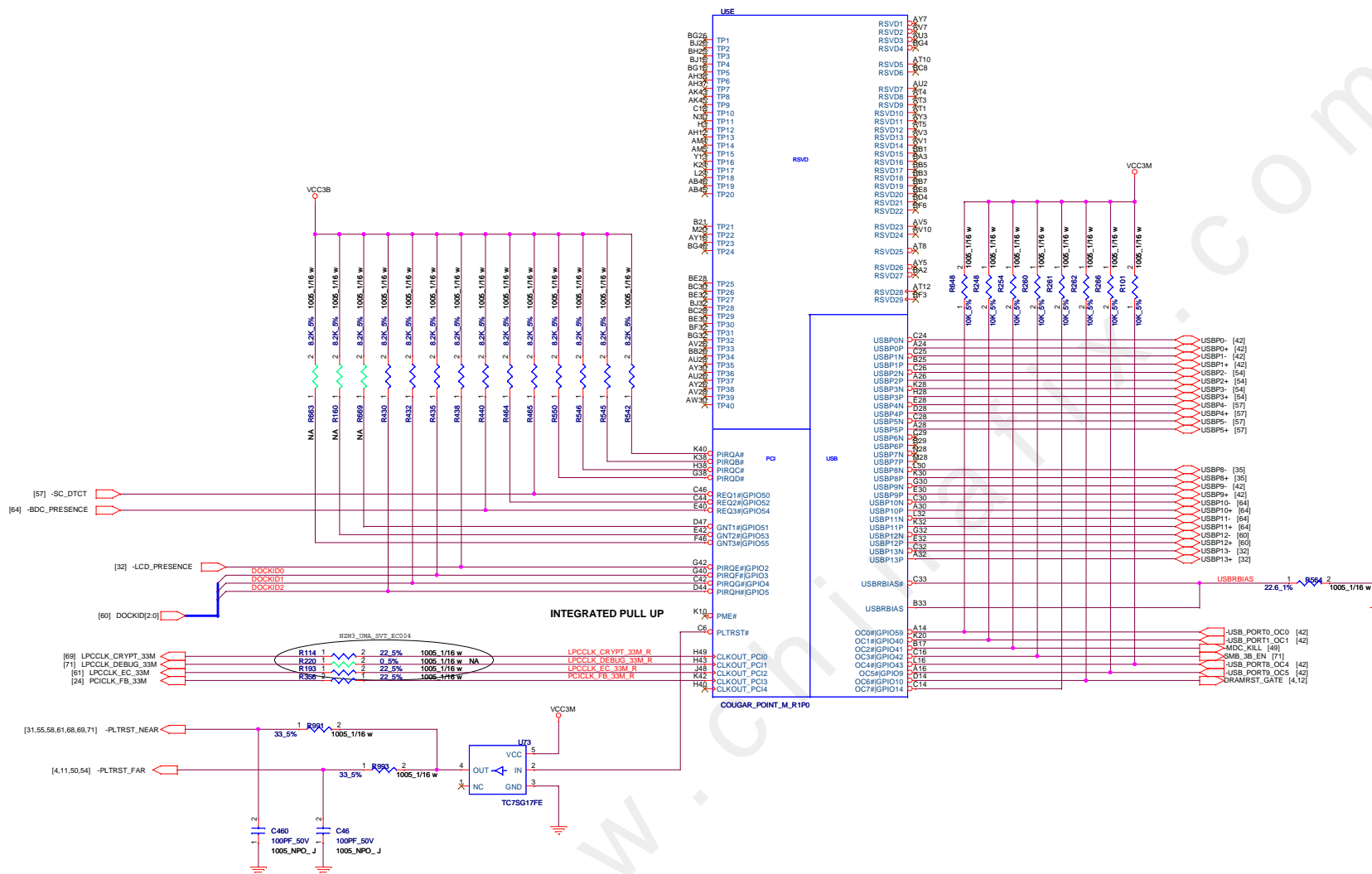


TEST PAD
BOTTOM SIDE
DO NOT MOVE AFTER FIX

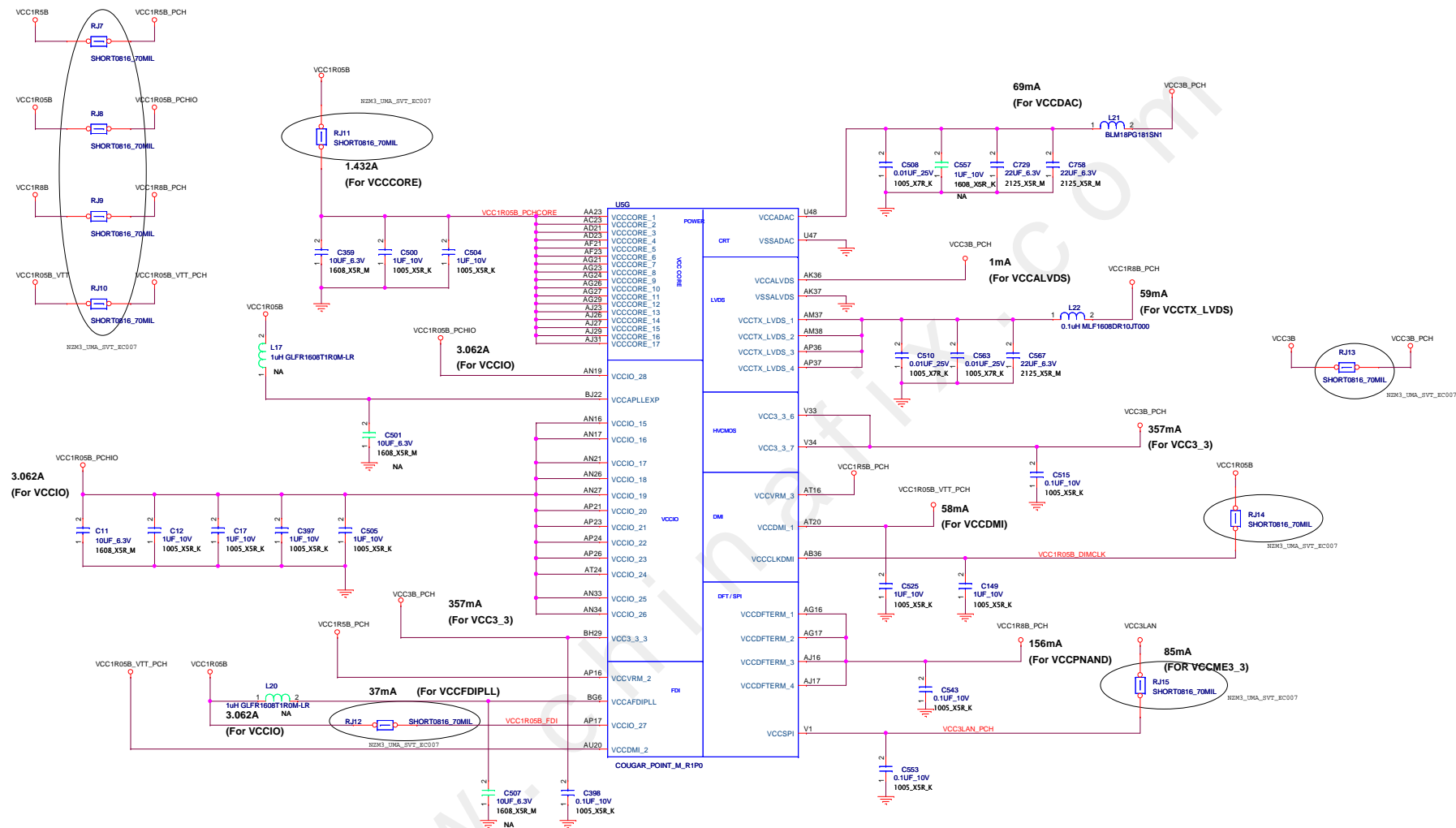


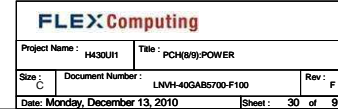




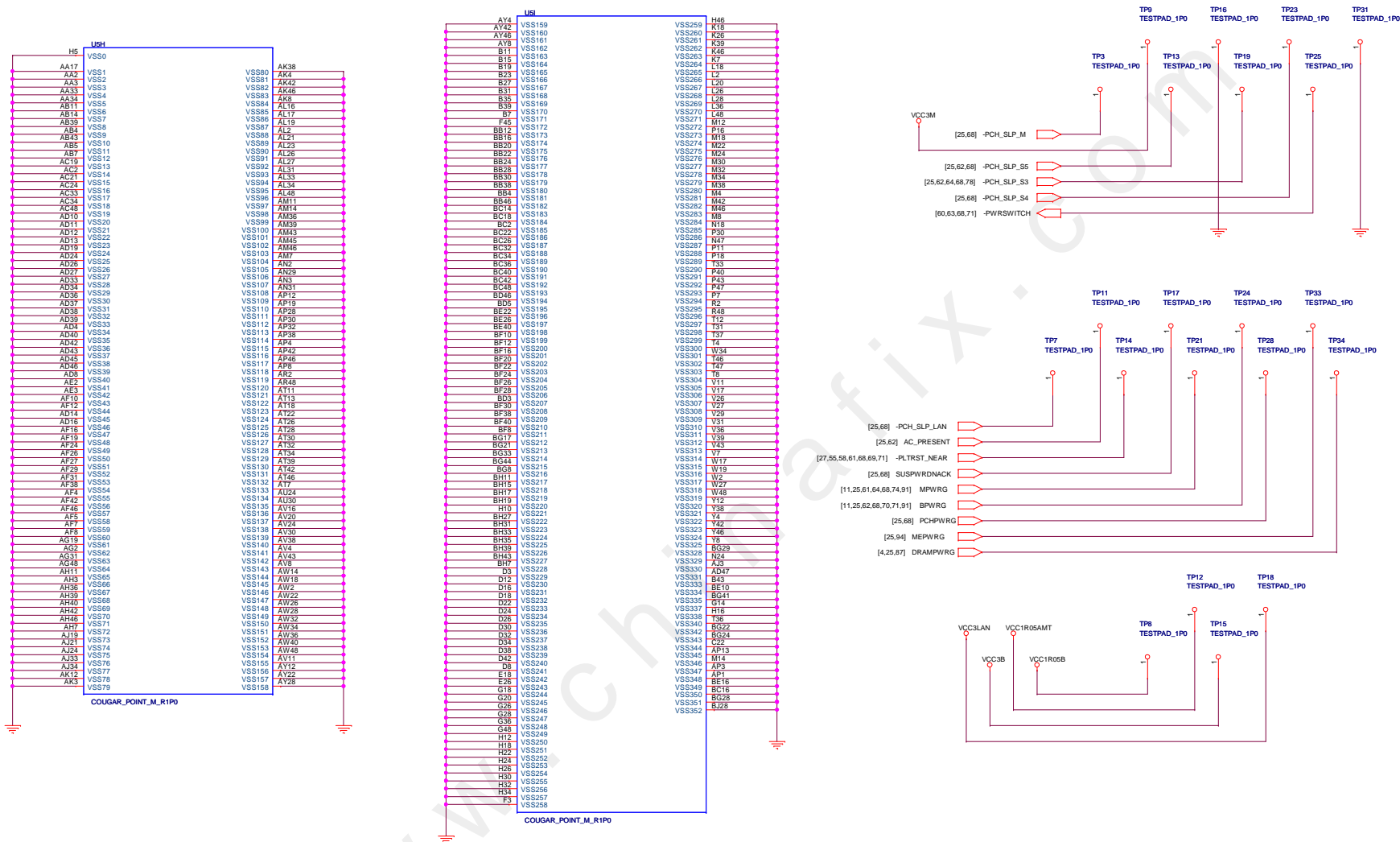


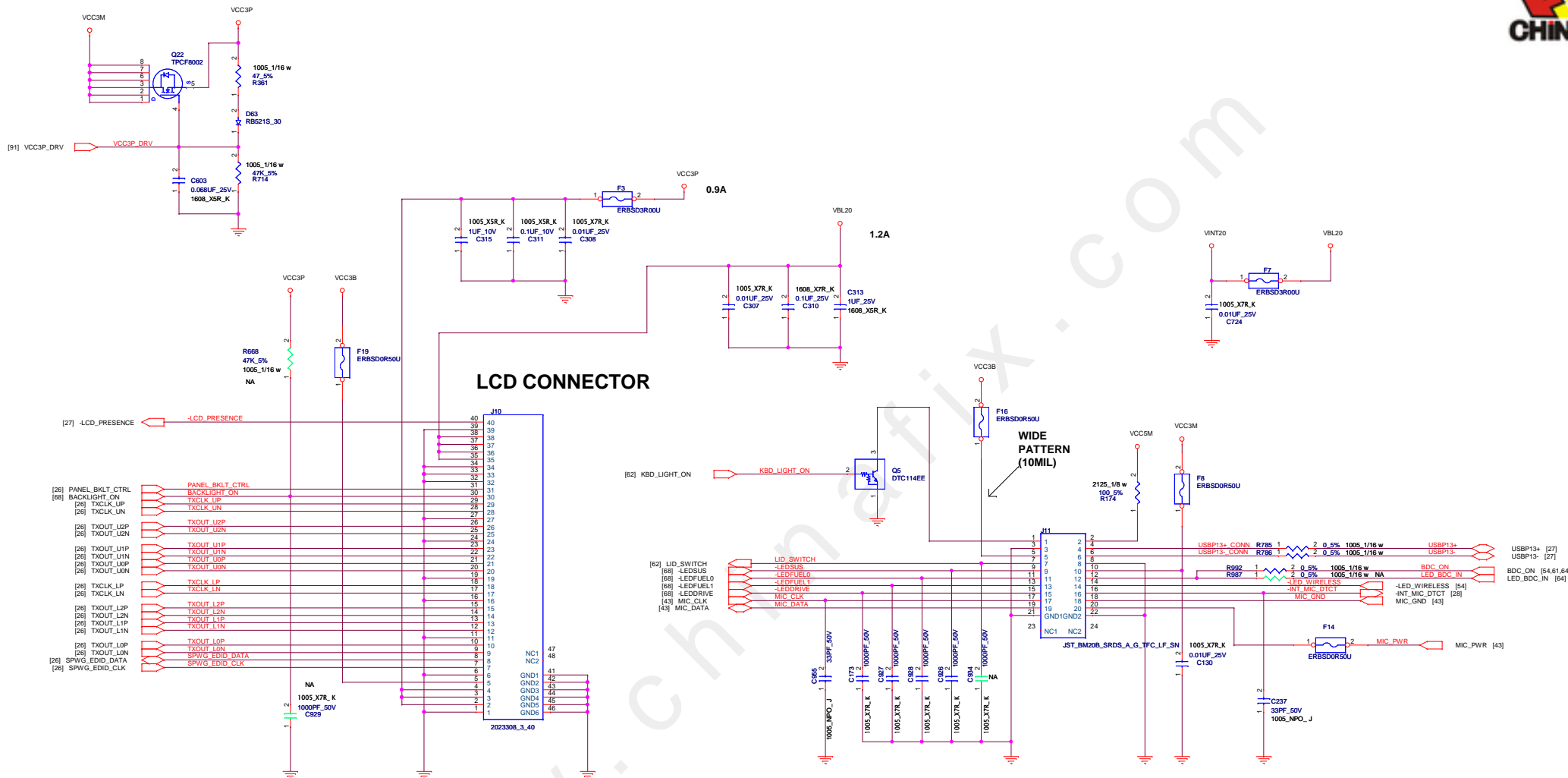
USB PORT TO	
0	SYSTEM PORT 4
1	SYSTEM PORT 2
2	HALF MINICARD (WLAN)
3	FULL MINICARD (WWAN)
4	SMART CARD SLOT
5	EXPRESS CARD SLOT
6	RESERVED
7	RESERVED
8	SYSTEM PORT 3
9	SYSTEM PORT 1
10	FPR (TOUCH PAD)
11	BLUETOOTH (TOUCH PAD)
12	DOCKING
13	USB CAMERA (LCD)

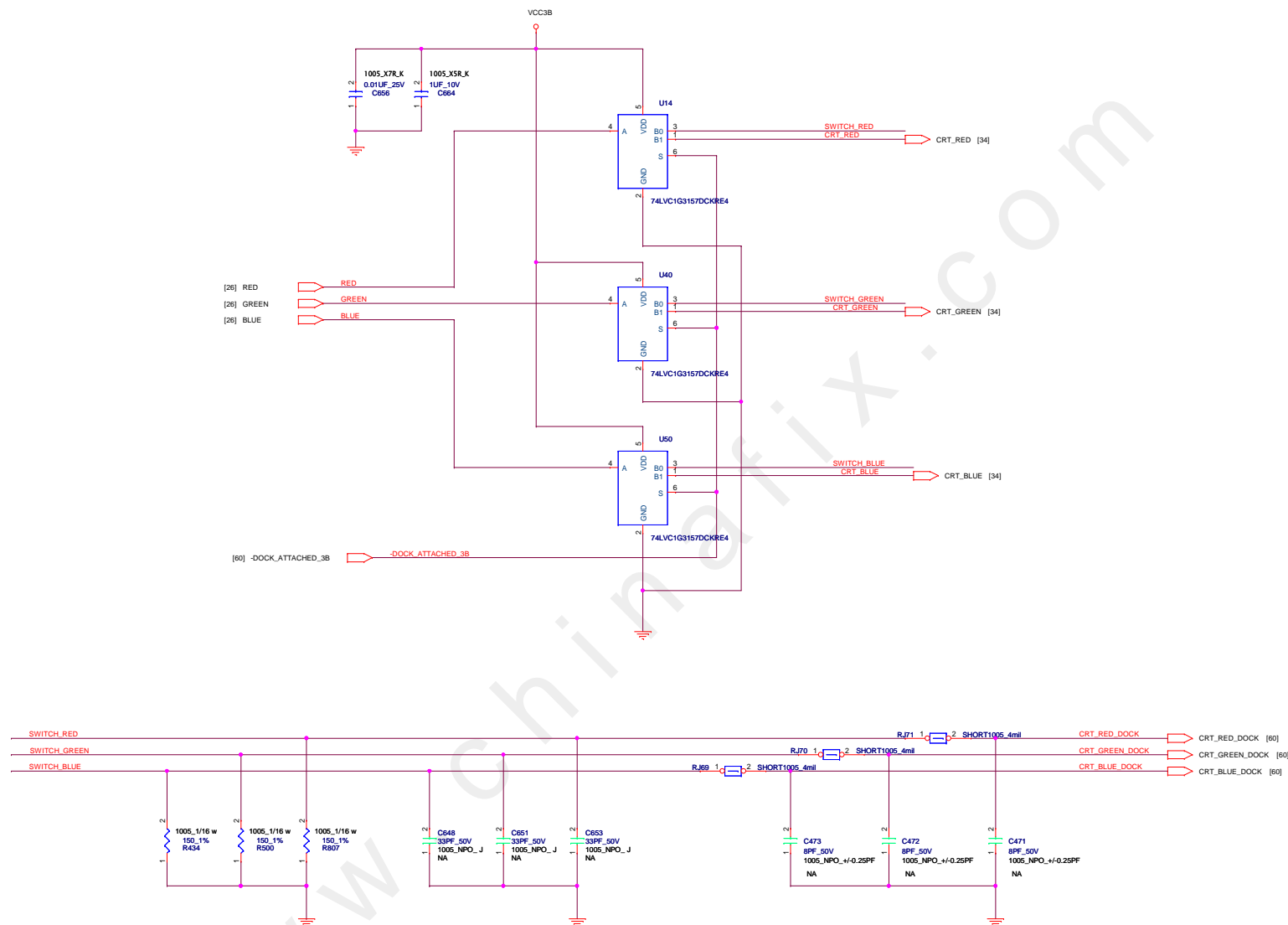


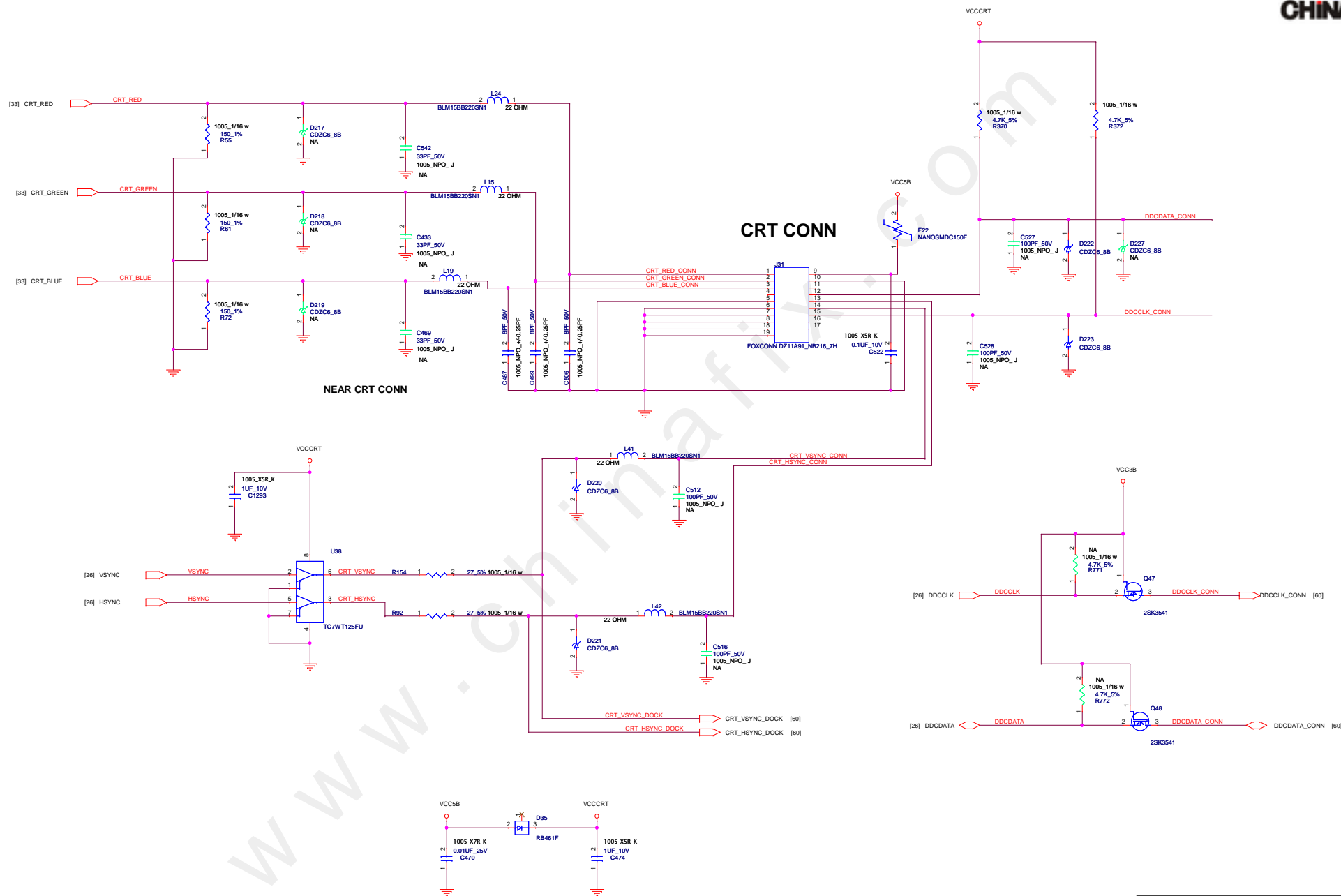


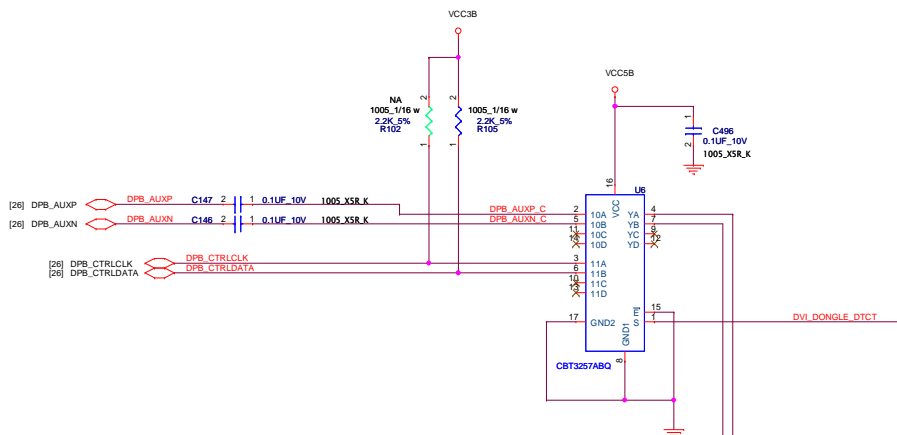
TEST PAD FOR METS/APS



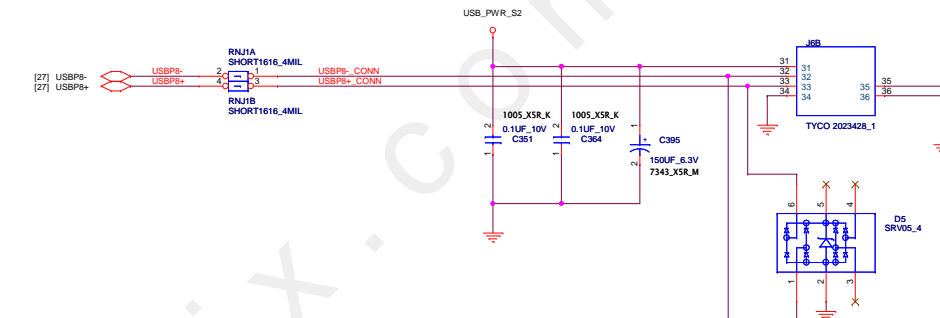
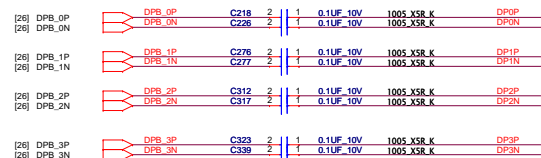




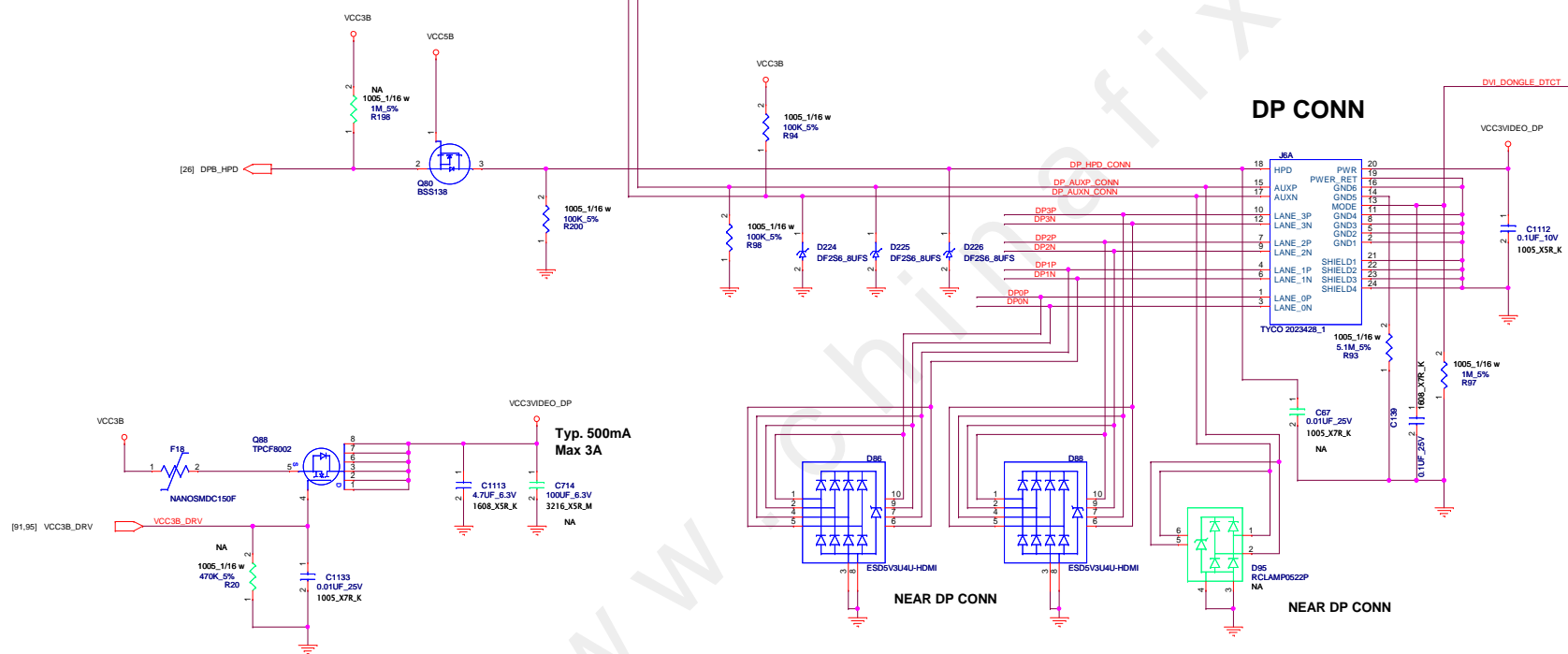




FOR SYSTEM DP NEAR DP CONN



DP CONN

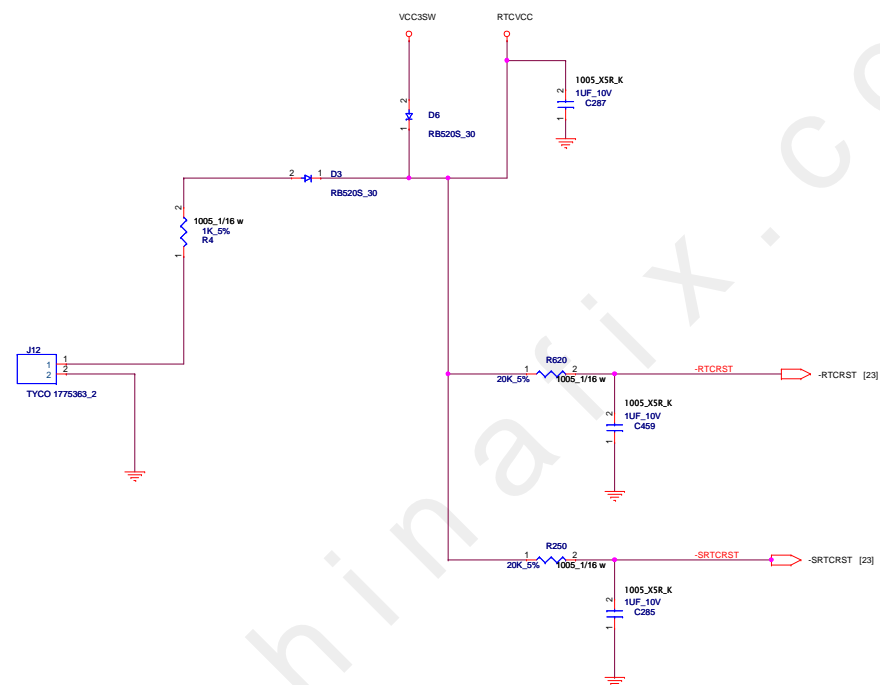


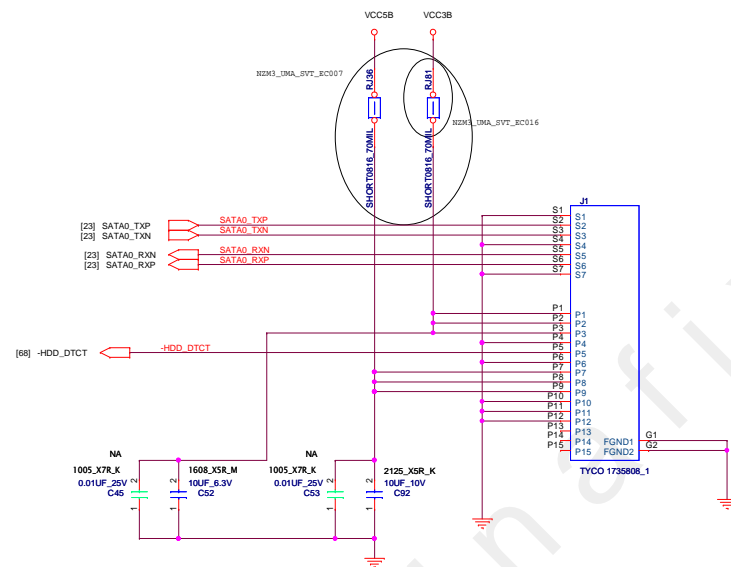
NEAR DP CONN

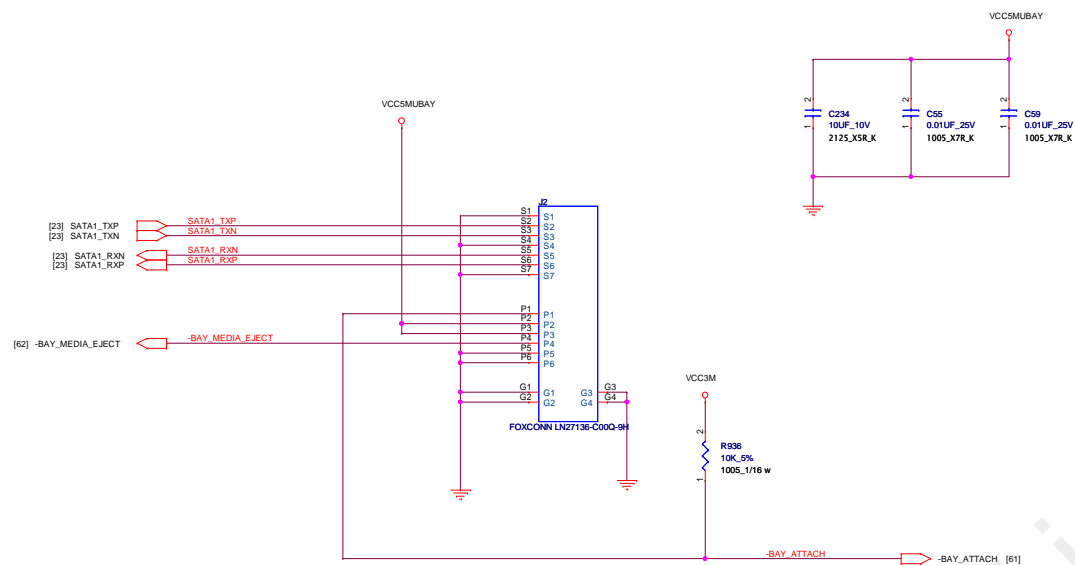
NEAR DP CONN

NEAR DOCK CONN									
[26] DPC_3N	DPC_3N	C873	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP3N	DOCKA_DP3N [60]
[26] DPC_3P	DPC_3P	C869	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP3P	DOCKA_DP3P [60]
[26] DPC_2N	DPC_2N	C807	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP2N	DOCKA_DP2N [60]
[26] DPC_2P	DPC_2P	C797	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP2P	DOCKA_DP2P [60]
[26] DPC_1N	DPC_1N	C788	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP1N	DOCKA_DP1N [60]
[26] DPC_1P	DPC_1P	C809	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP1P	DOCKA_DP1P [60]
[26] DPC_0N	DPC_0N	C798	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP0N	DOCKA_DP0N [60]
[26] DPC_0P	DPC_0P	C808	2	1	0.1UF	10V	1005.X5R.K	DOCKA_DP0P	DOCKA_DP0P [60]
NEAR DOCK CONN									
[26] DPD_3N	DPD_3N	C874	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP3N	DOCKB_DP3N [60]
[26] DPD_3P	DPD_3P	C875	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP3P	DOCKB_DP3P [60]
[26] DPD_2N	DPD_2N	C819	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP2N	DOCKB_DP2N [60]
[26] DPD_2P	DPD_2P	C811	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP2P	DOCKB_DP2P [60]
[26] DPD_1N	DPD_1N	C810	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP1N	DOCKB_DP1N [60]
[26] DPD_1P	DPD_1P	C876	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP1P	DOCKB_DP1P [60]
[26] DPD_0N	DPD_0N	C828	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP0N	DOCKB_DP0N [60]
[26] DPD_0P	DPD_0P	C838	2	1	0.1UF	10V	1005.X5R.K	DOCKB_DP0P	DOCKB_DP0P [60]

[26]	DOCKA_DPC_AUXP_I	DOCKA_DPC_AUXP_I	C841	2	1	0.1UF 10V	1005 X5R K	DOCKA_AUXP	DOCKA_AUXP	[60]
[26]	DOCKA_DPC_AUXN_I	DOCKA_DPC_AUXN_I	C843	2	1	0.1UF 10V	1005 X5R K	DOCKA_AUXN	DOCKA_AUXN	[60]
[26]	DOCKB_DPD_AUXP_I	DOCKB_DPD_AUXP_I	C883	2	1	0.1UF 10V	1005 X5R K	DOCKB_AUXP	DOCKB_AUXP	[60]
[26]	DOCKB_DPD_AUXN_I	DOCKB_DPD_AUXN_I	C887	2	1	0.1UF 10V	1005 X5R K	DOCKB_AUXN	DOCKB_AUXN	[60]







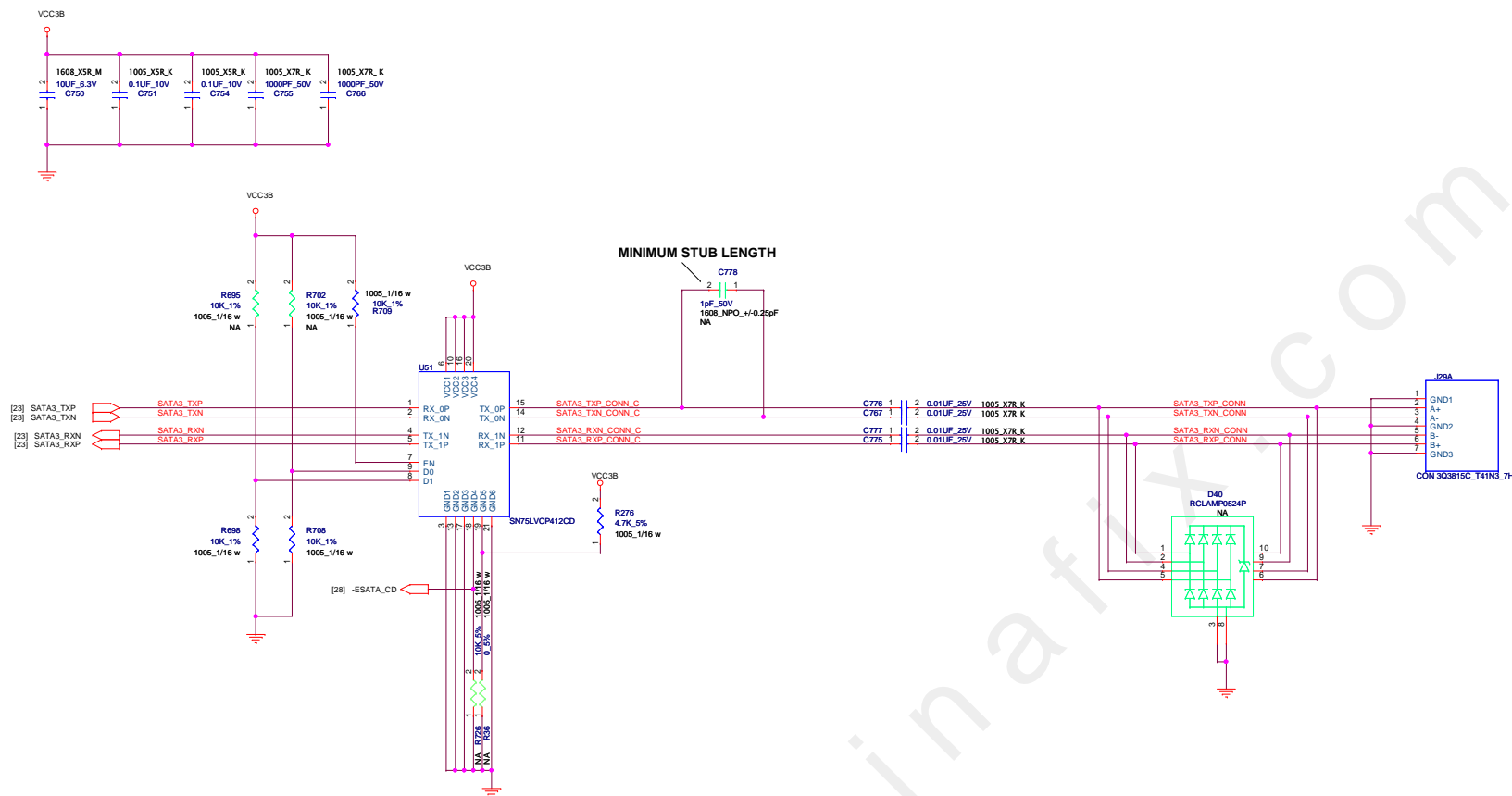
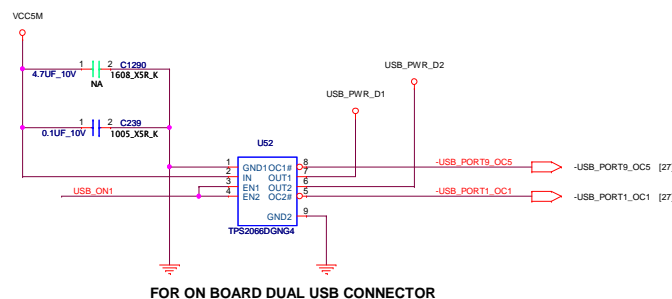
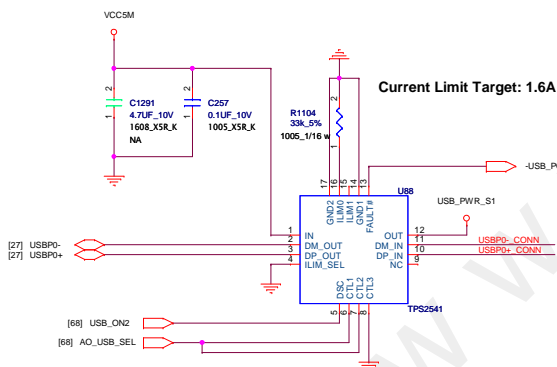
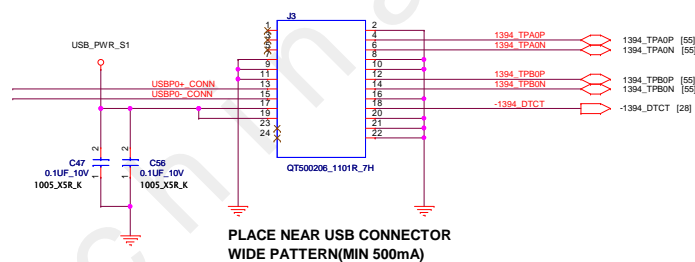
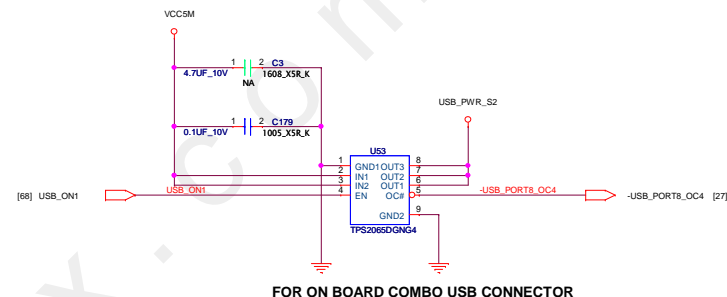
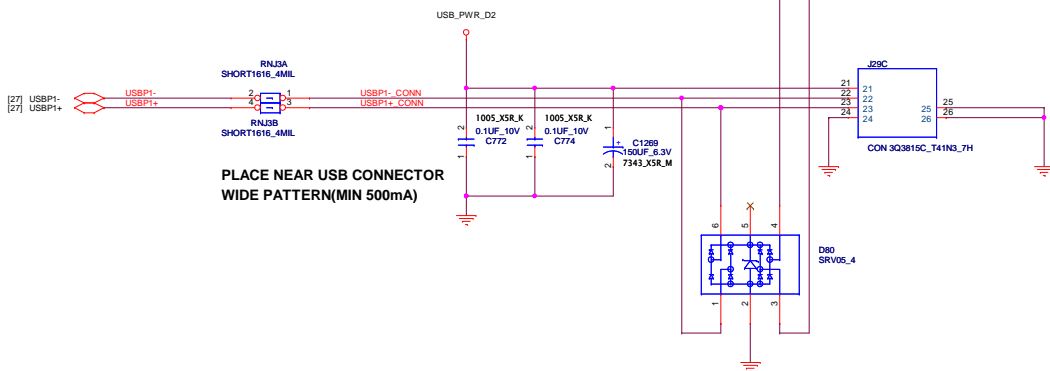
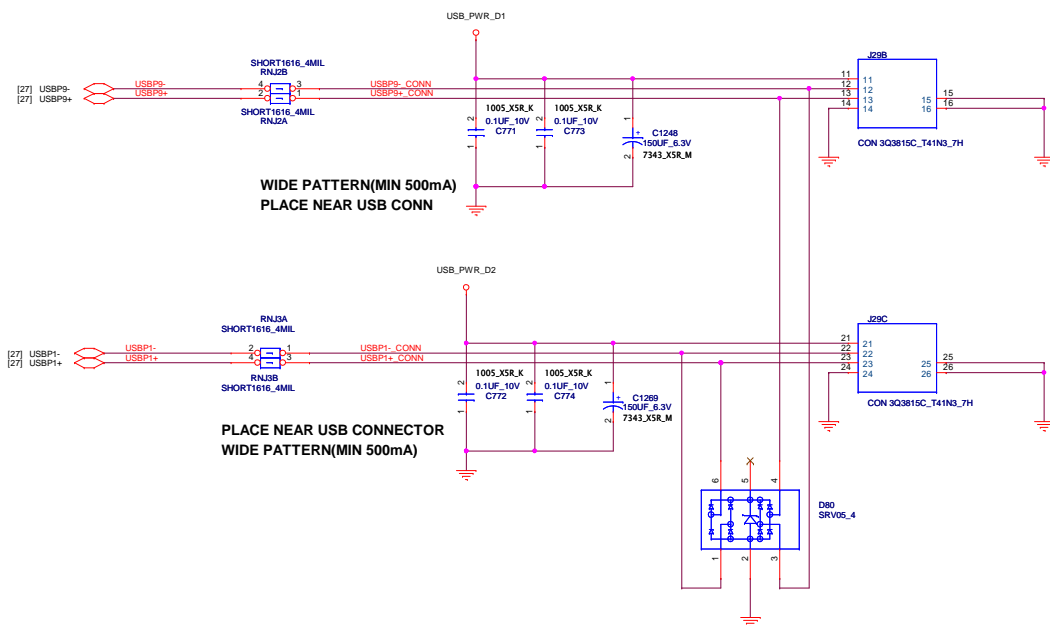


TABLE
ESATA REDRIVER CHANNEL BOOST ENABLE OPTION

EN	D0	D1	CH-0	CH-1
0	X	X	STANDBY	STANDBY
1	0	0	STANDARD	STANDARD
1	1	0	BOOST	STANDARD
1	0	1	STANDARD	BOOST
1	1	1	BOOST	BOOST

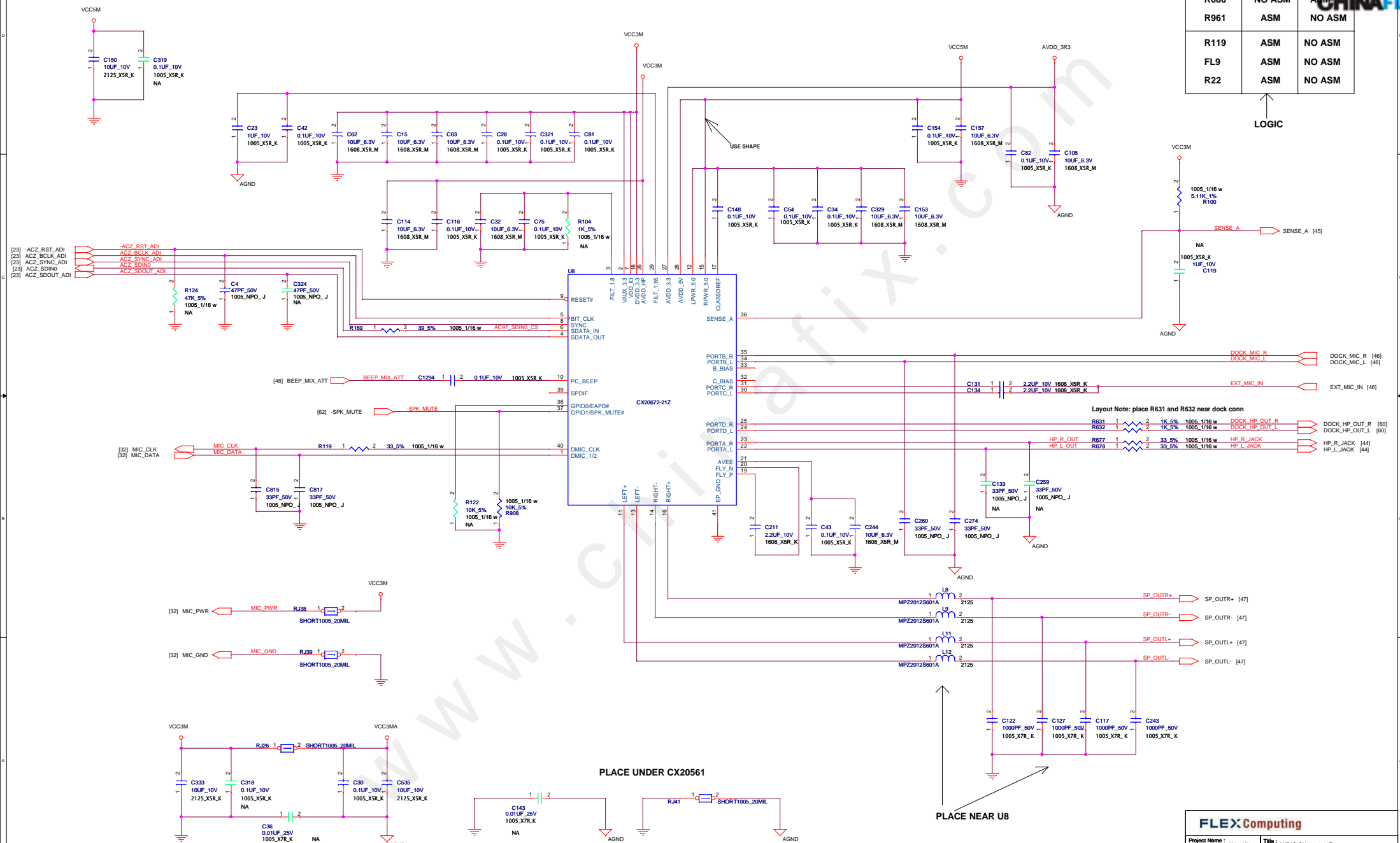
← LOGIC





	ENABLE	DISABLE
R686	NO ASM	ASM
R961	ASM	NO ASM
R119	ASM	NO ASM
FL9	ASM	NO ASM
R22	ASM	NO ASM

LOGIC

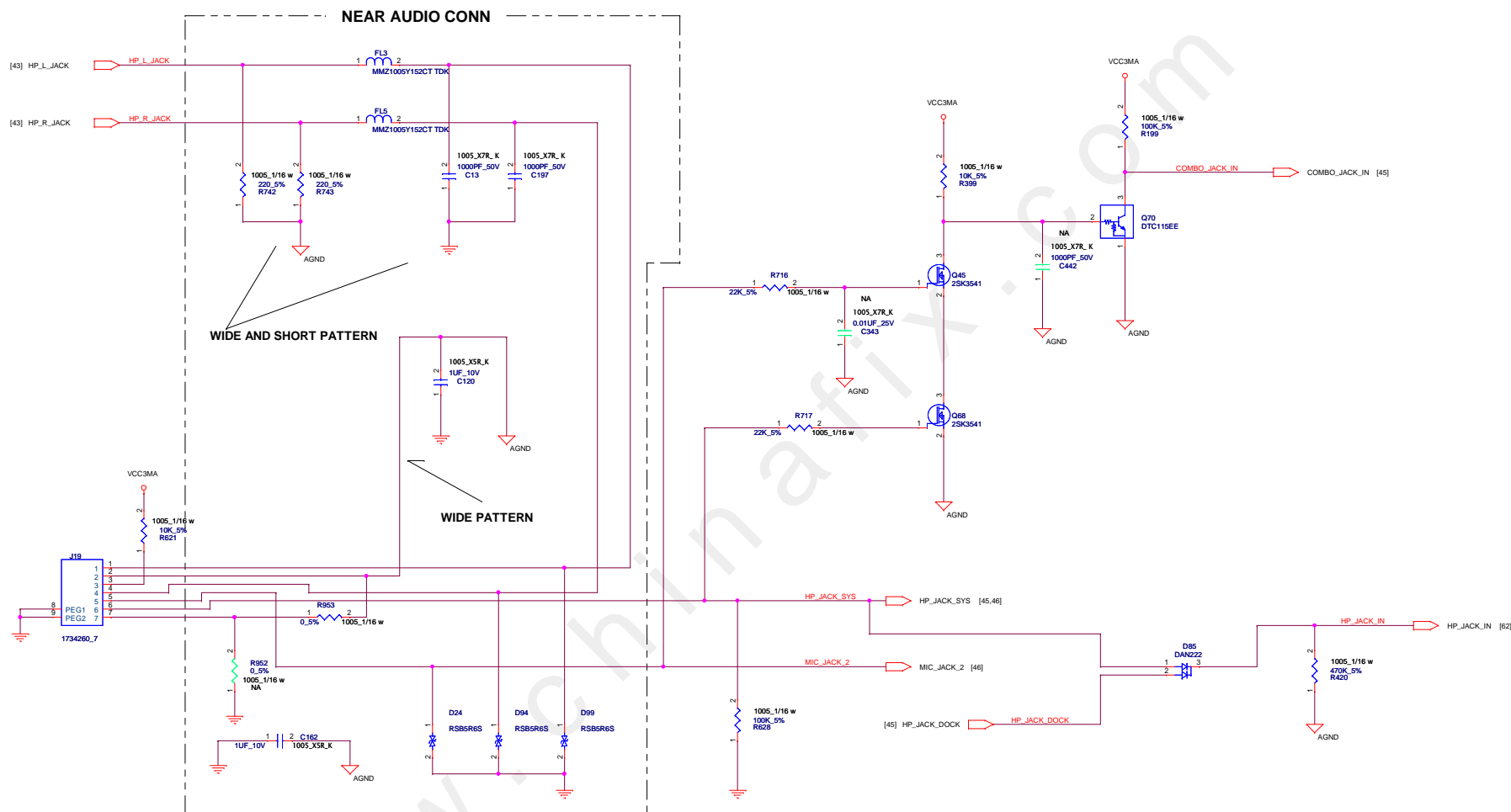


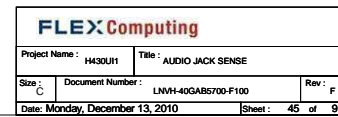
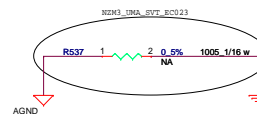
FLEX Computing

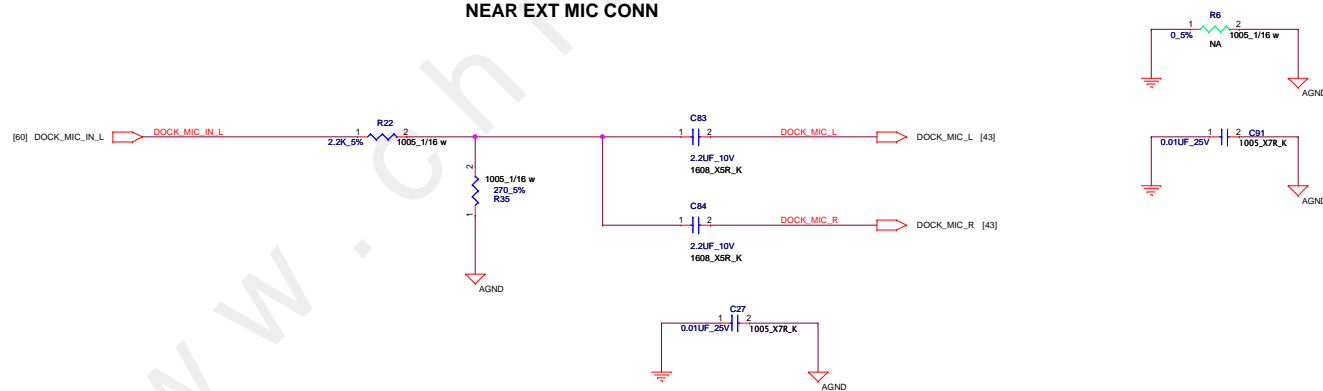
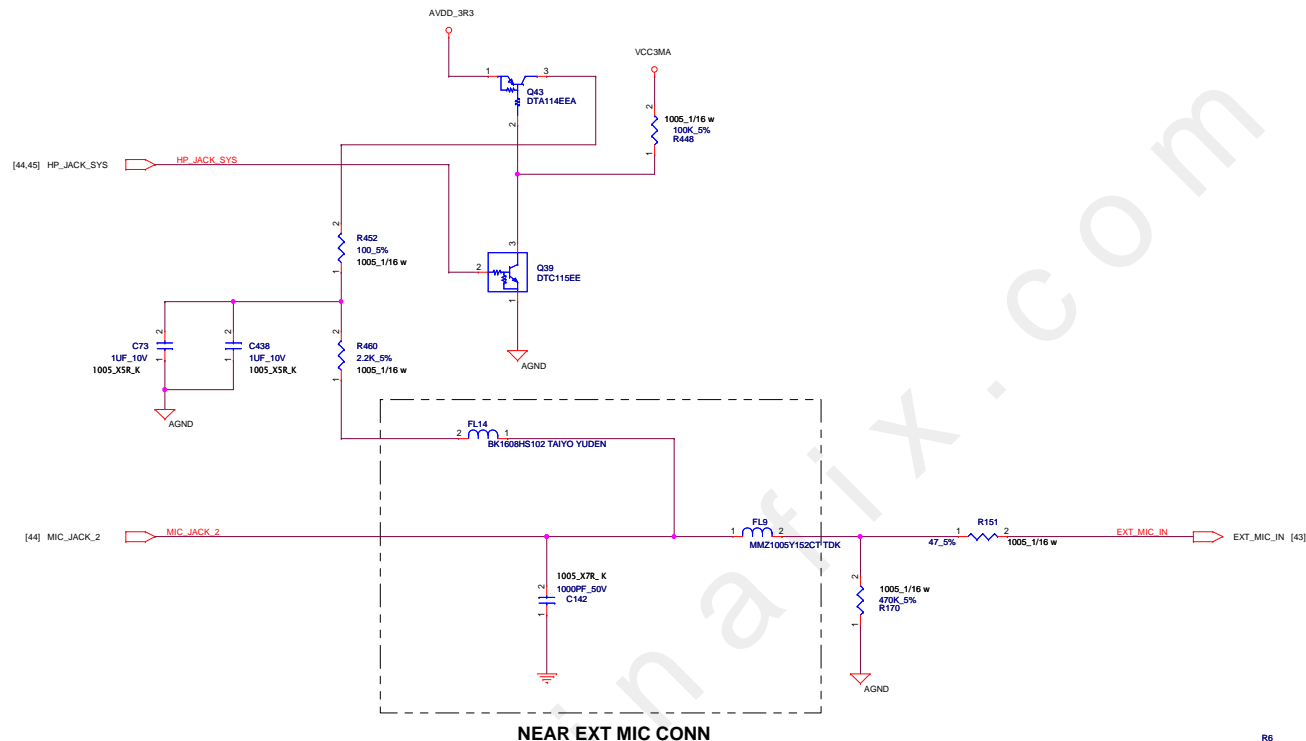
Project Name :	H430U11	Title :	AUDIO CX20672-21Z
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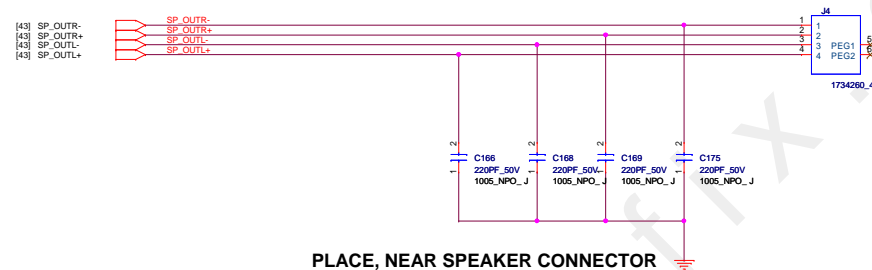
Size :	Document Number :
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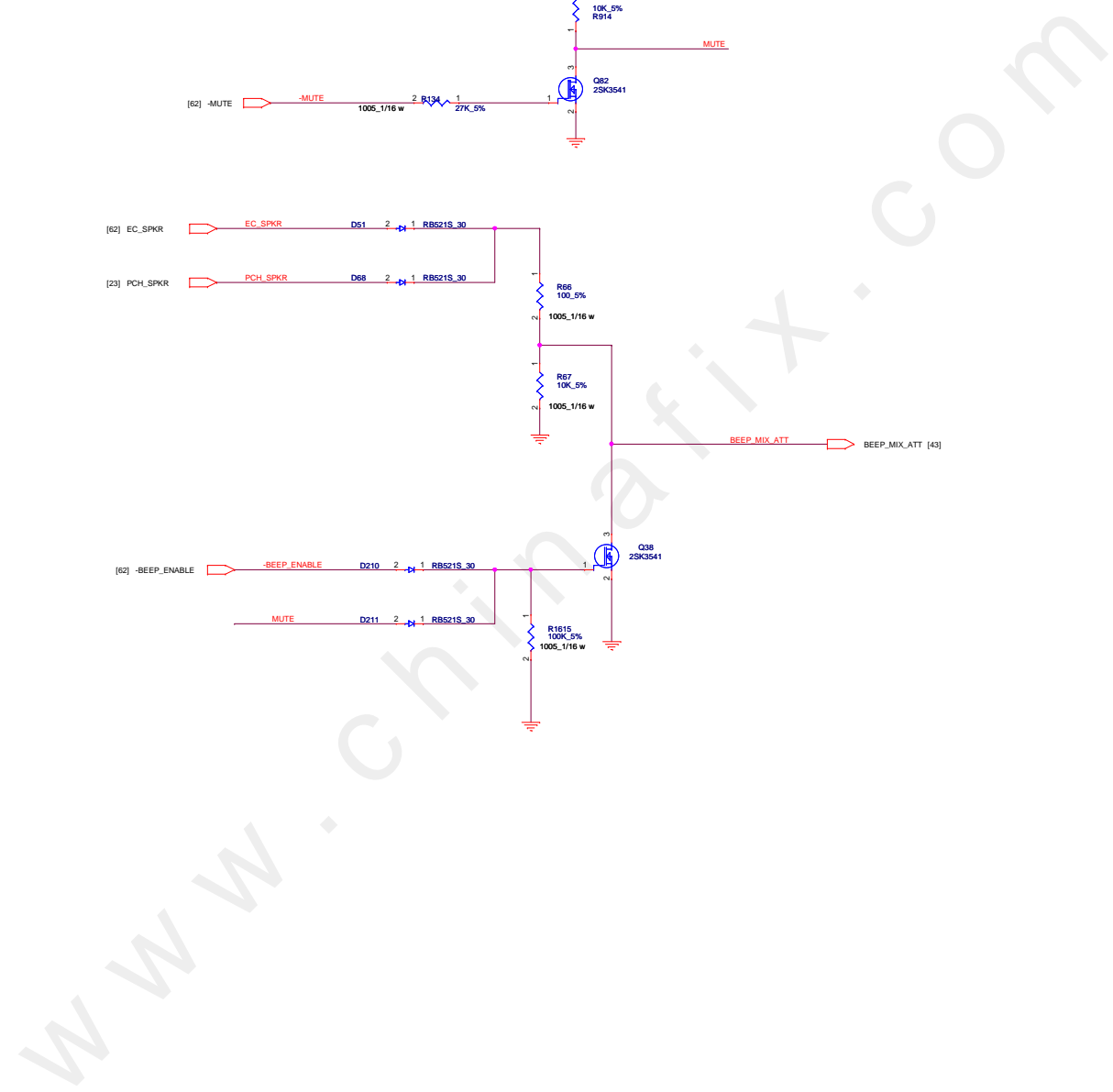
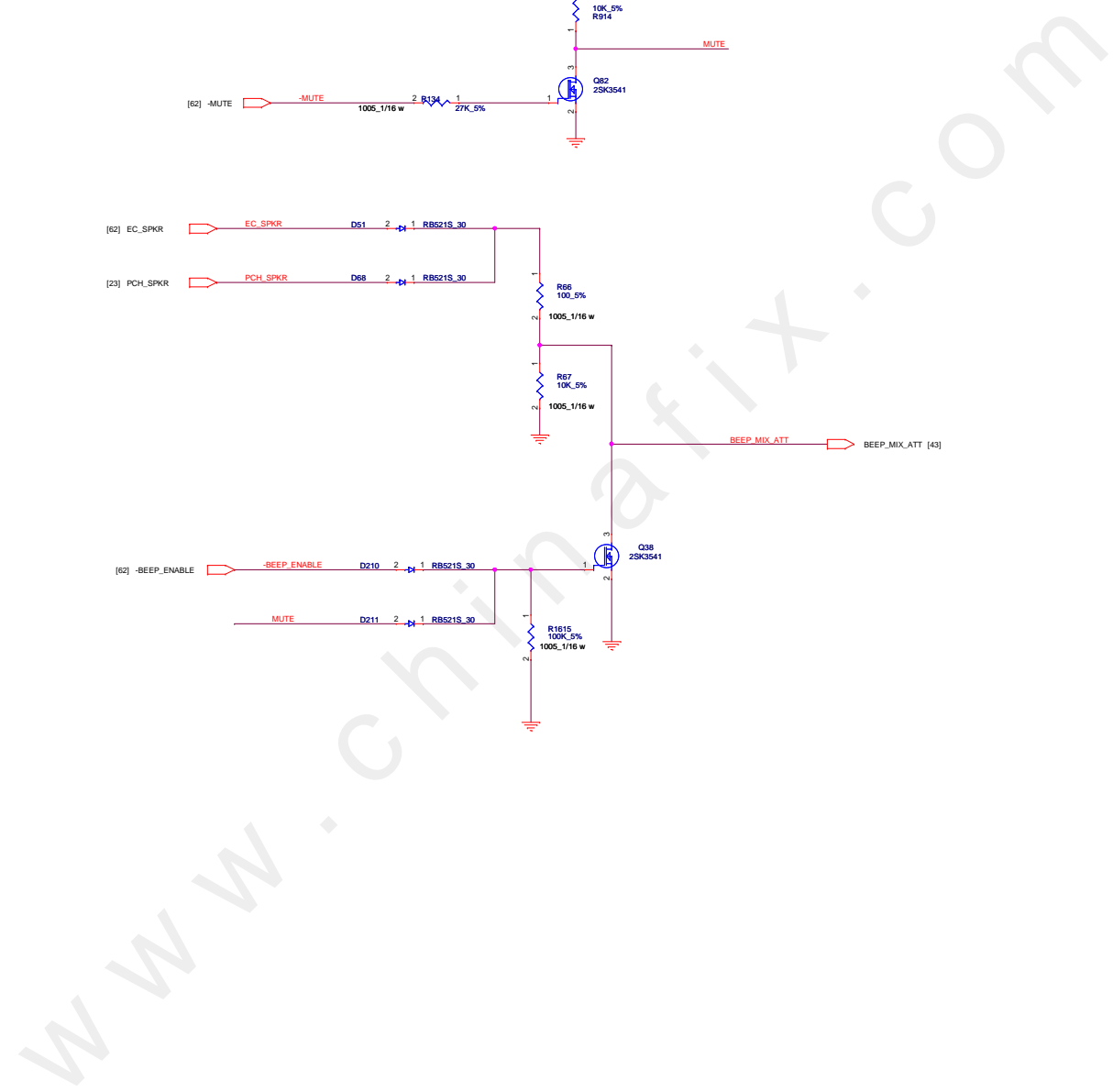
C	LNWH-40GABS700-F100	F
Date: Monday, December 13, 2010		Sheet : 43 of 99

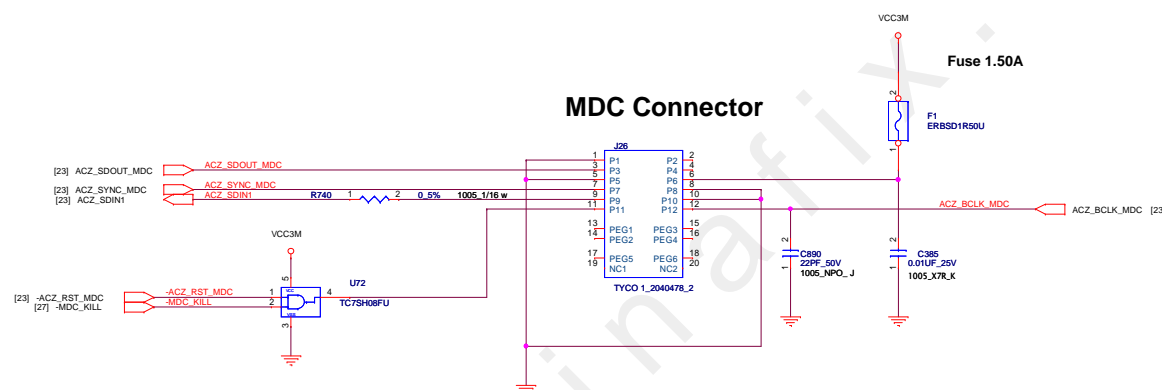


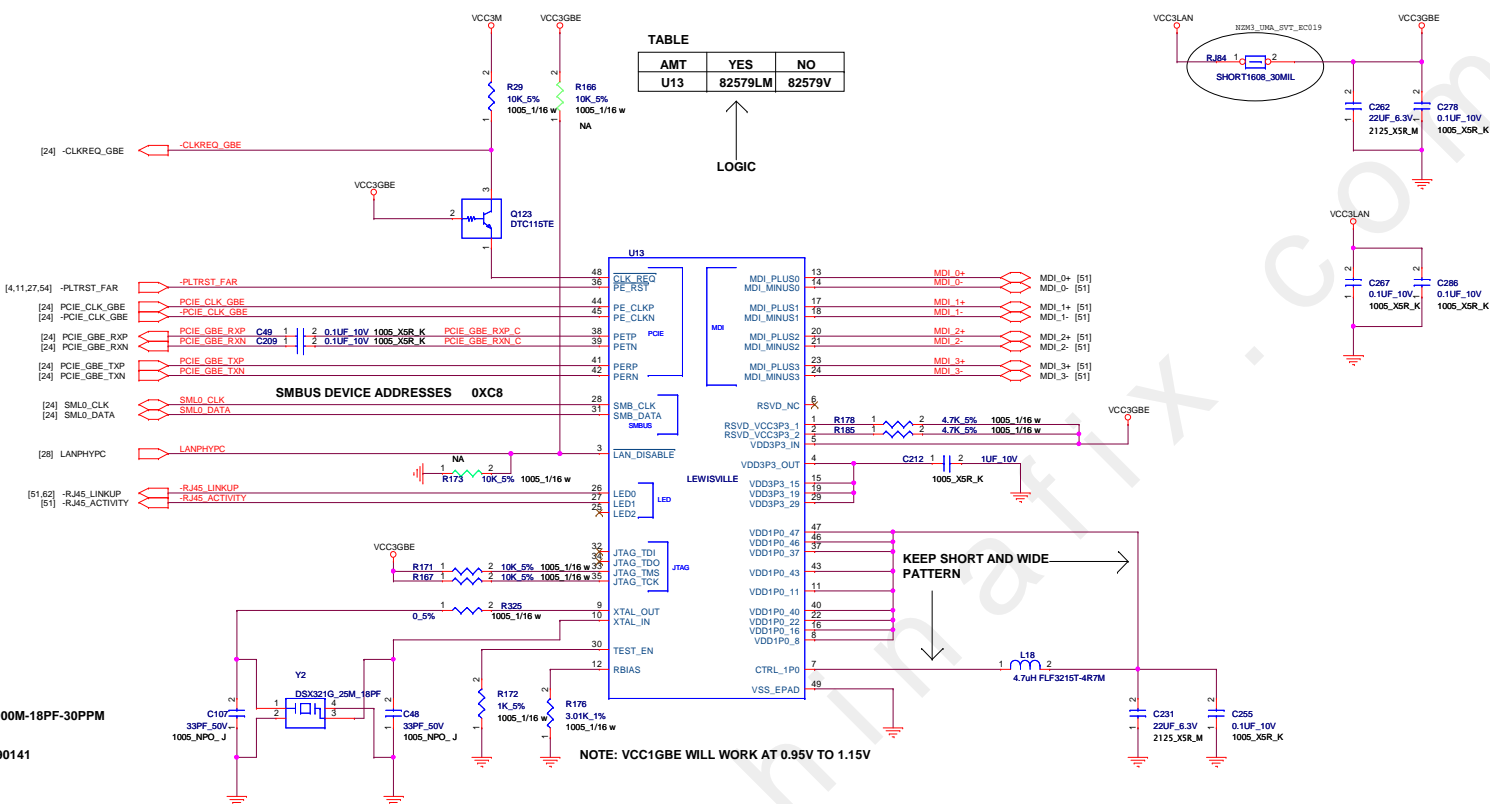




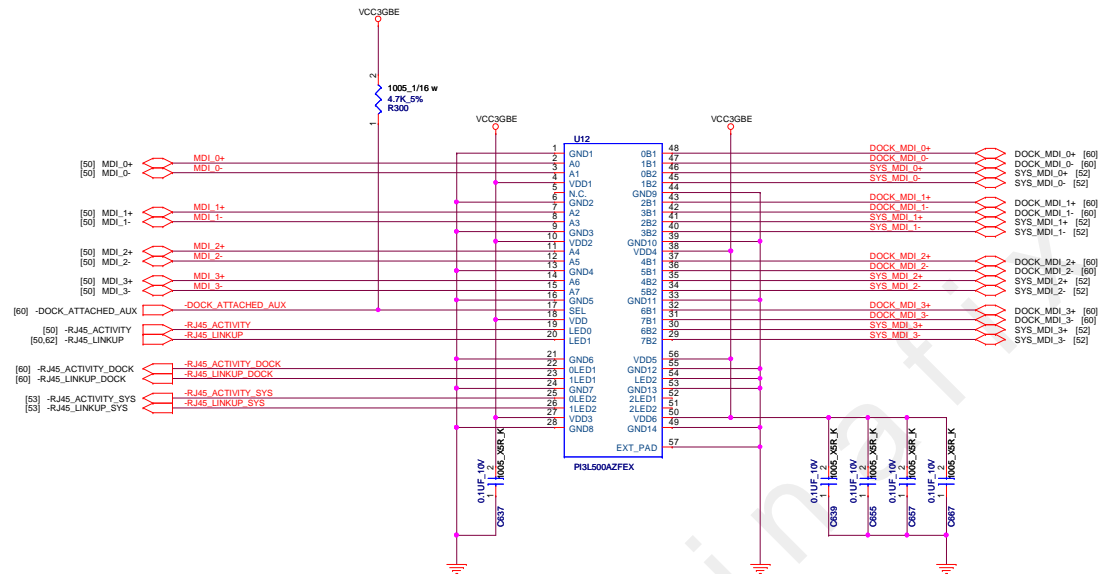








P/N 41U6141
KDS DSX321G-25.000M-18PF-30PPM
TXC 7V25020001
RIVER FCX-04-25M.J90141



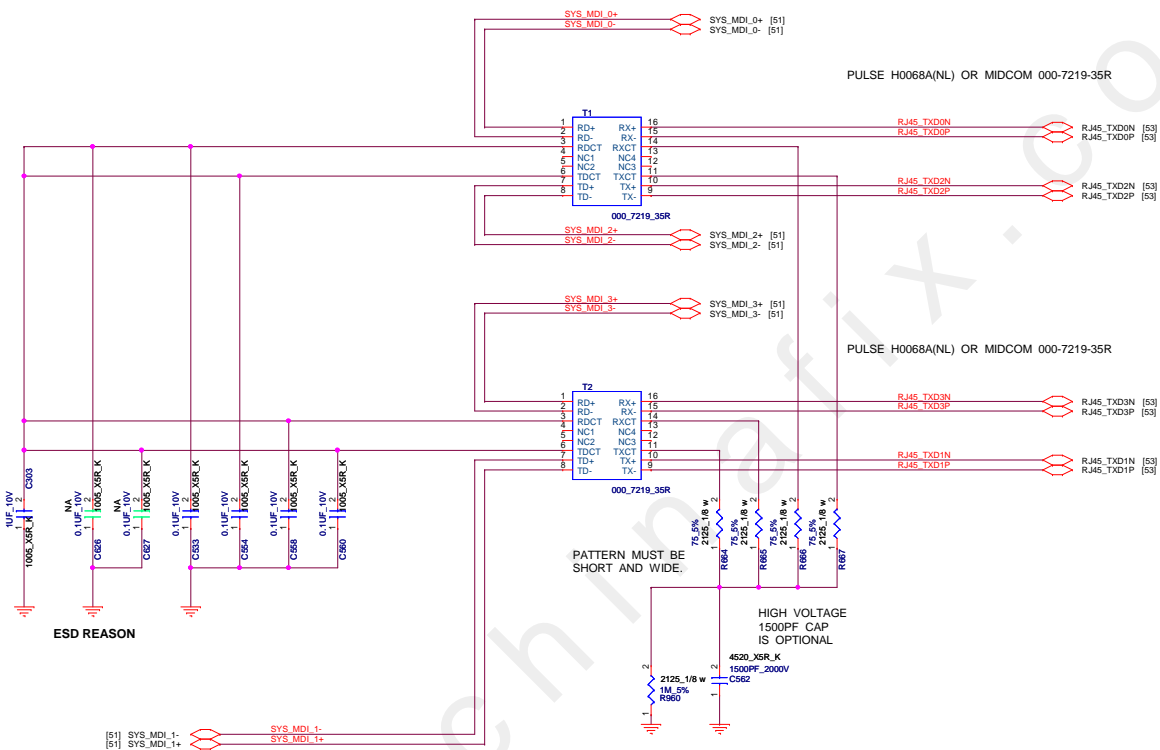
THE WIDTH OF THESE TRACE SHOULD
BE WIDER THAN 35MIL TO PREVENT
VOLTAGE DROP.

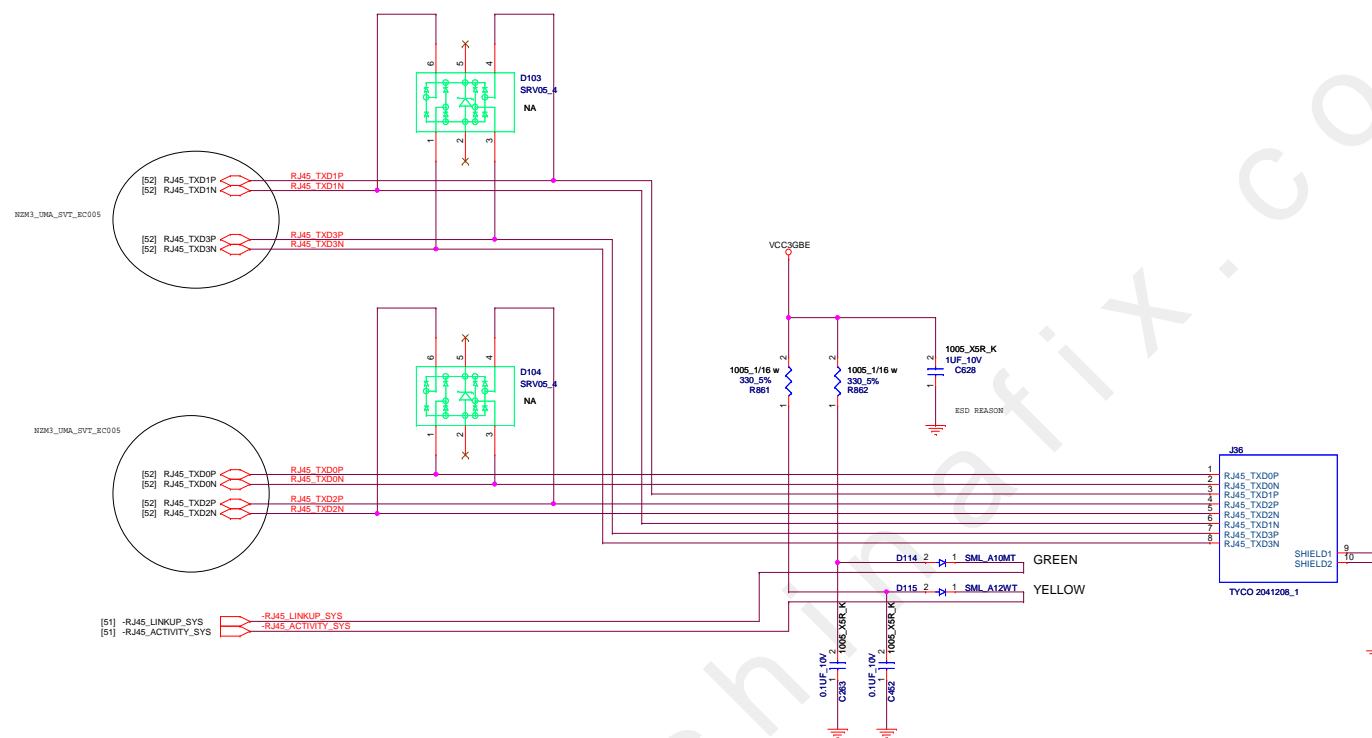
C303
SHOULD BE PLACED AS CLOSE
TO MAGNETICS AS POSSIBLE.

ESD REASON

PATTERN MUST BE SHORT AND WIDE.

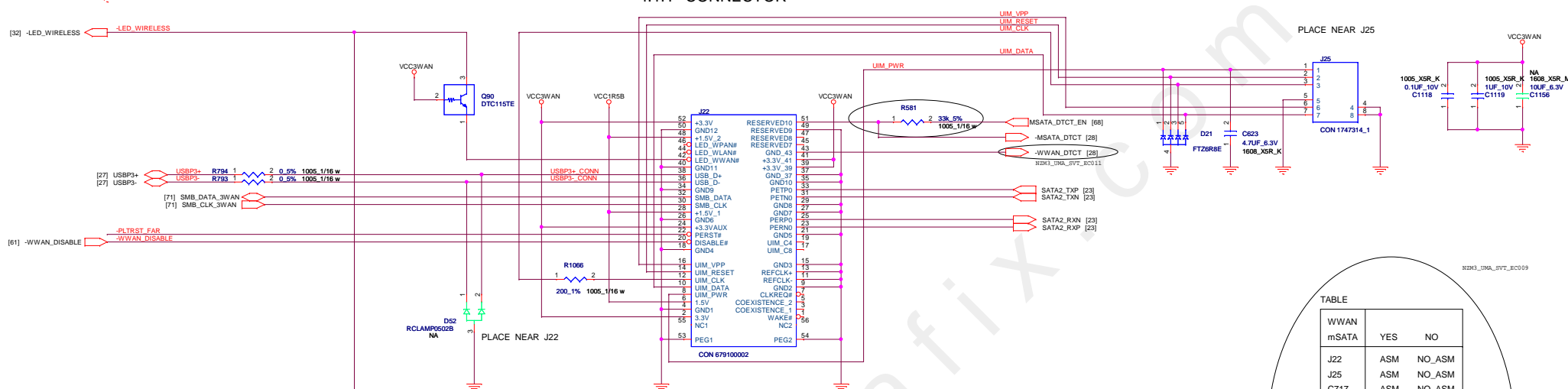
HIGH VOLTAGE
1500PF CAP
IS OPTIONAL





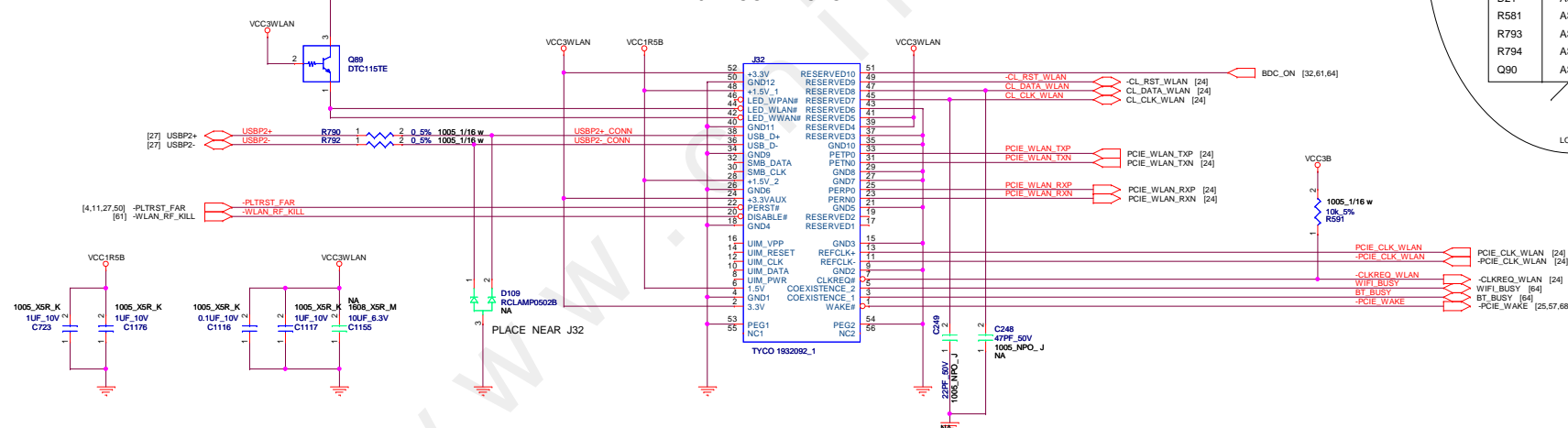
FULL MINI CARD FOR WWAN

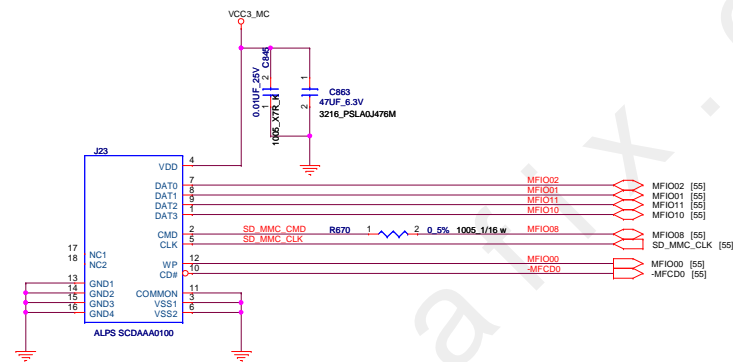
4.1H CONNECTOR

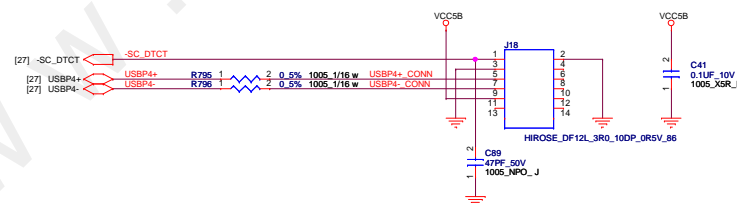
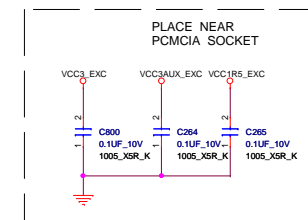
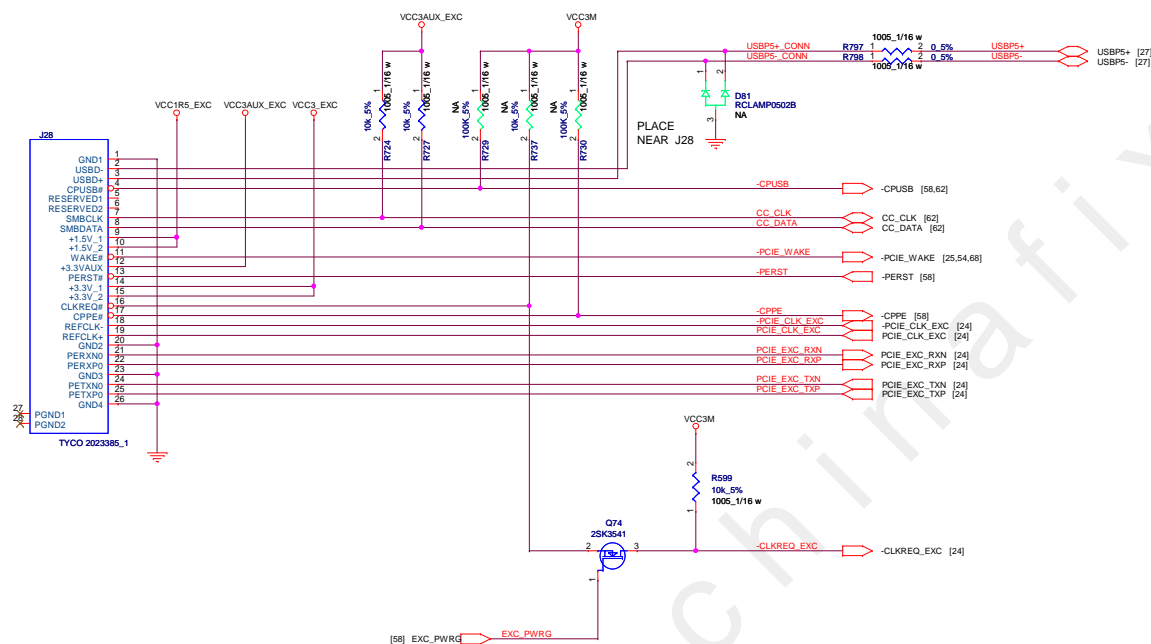


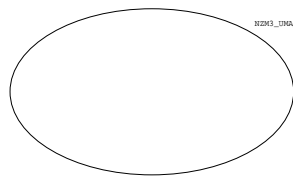
HALF MINI CARD FOR WLAN

7.0H CONNECTOR







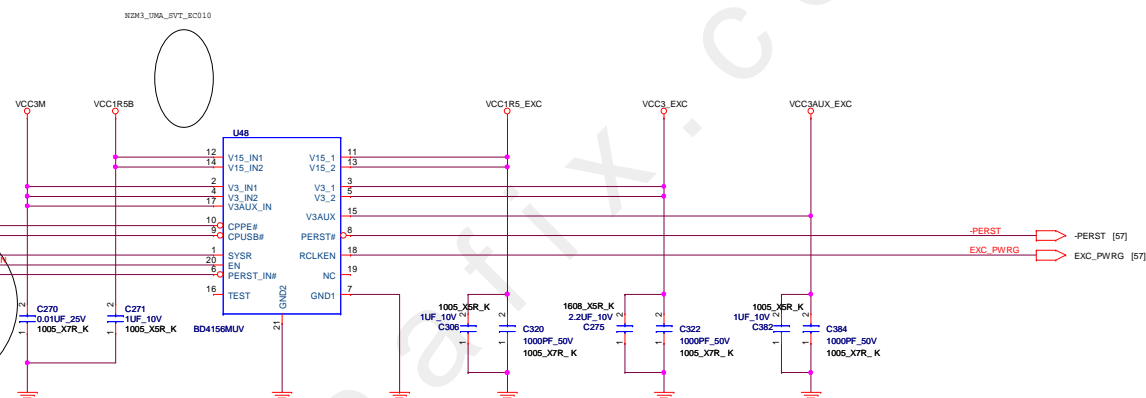


N2K3_UNA_SVT_EC010

N2K3_UNA_SVT_EC010

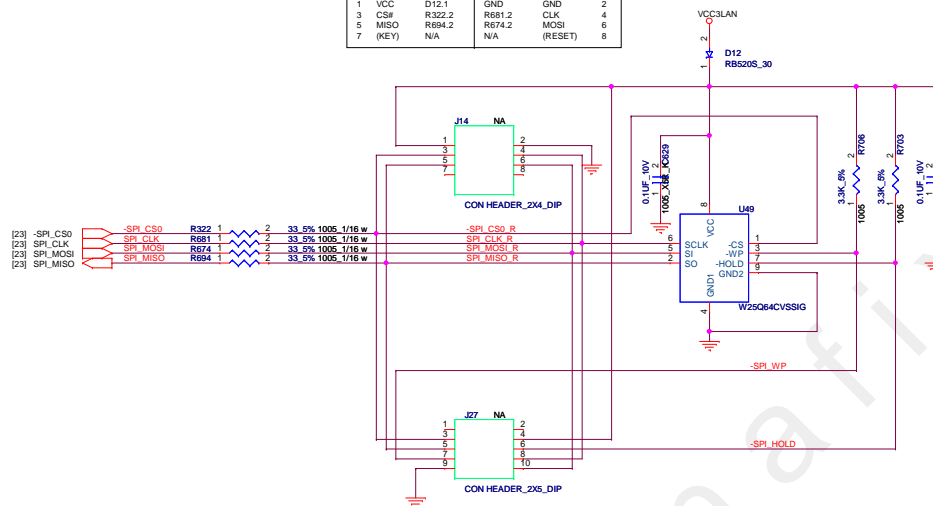
[37] -CPPE
[57,62] -CPUSB
[71,77,84,87,91,95] B_ON
[61] -EXC_PWR_SHDN
[77,31,55,61,68,69,71] -PLTRST_NEAR

N2K3_UNA_SVT_EC010



TABLE

SF100 PIN HEADER INTERFACE (TOP VIEW)							
1	VCC	D12.1	GND	GND	2		
3	CS#	R322.2	R681.2	CLK	4		
5	MISO	R694.2	R674.2	MOSI	6		
7	(KEY)	N/A	N/A	(RESET)	8		

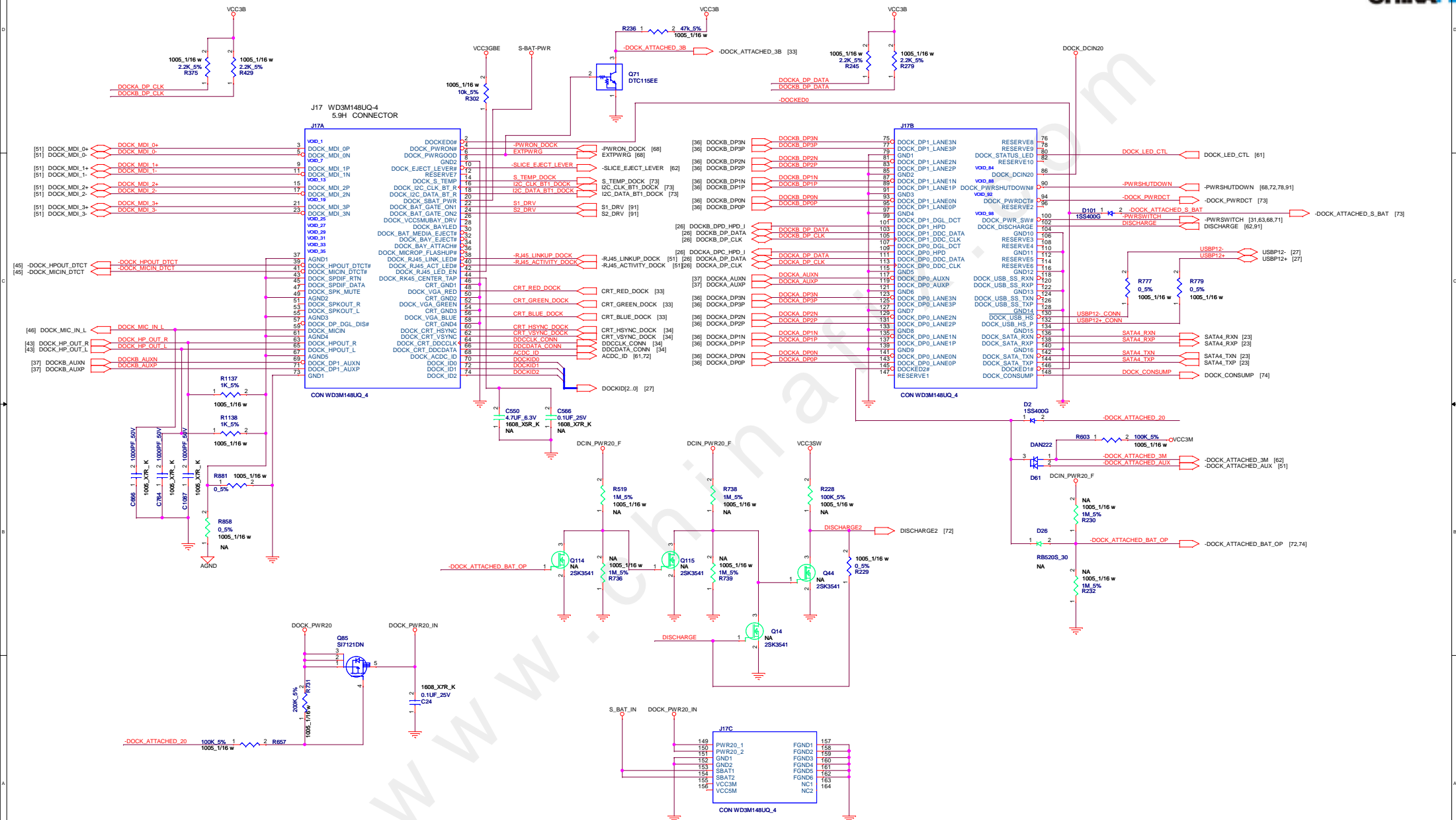


8MB SOIC8
MACRONIX MX25L6436EM2I-10G
MACRONIX MX25L6406EM2I-12G
WINBOND W25Q64CVSSIG

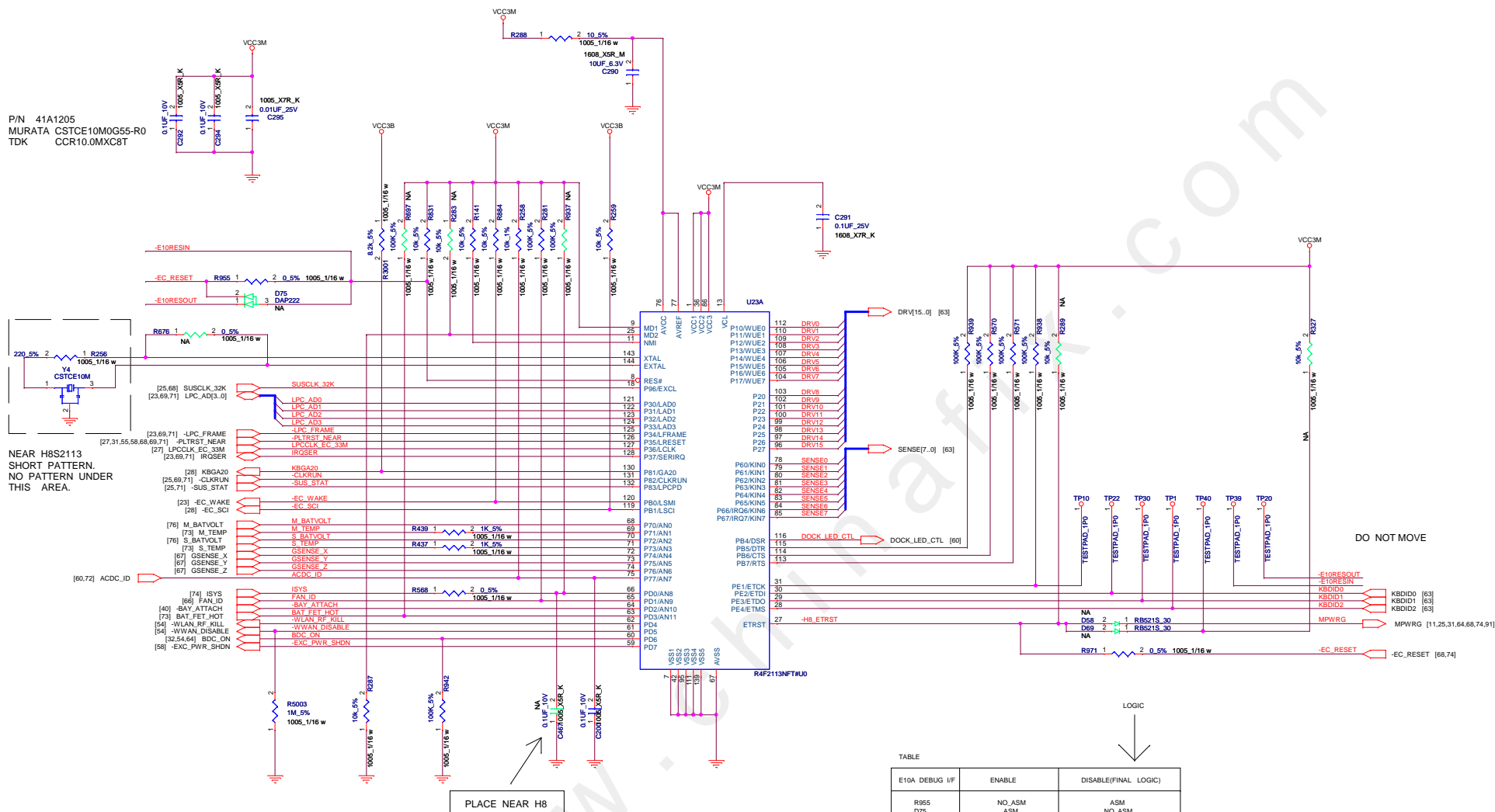
8MB WSON8
NUMONYX M25PX64-VMD6TG

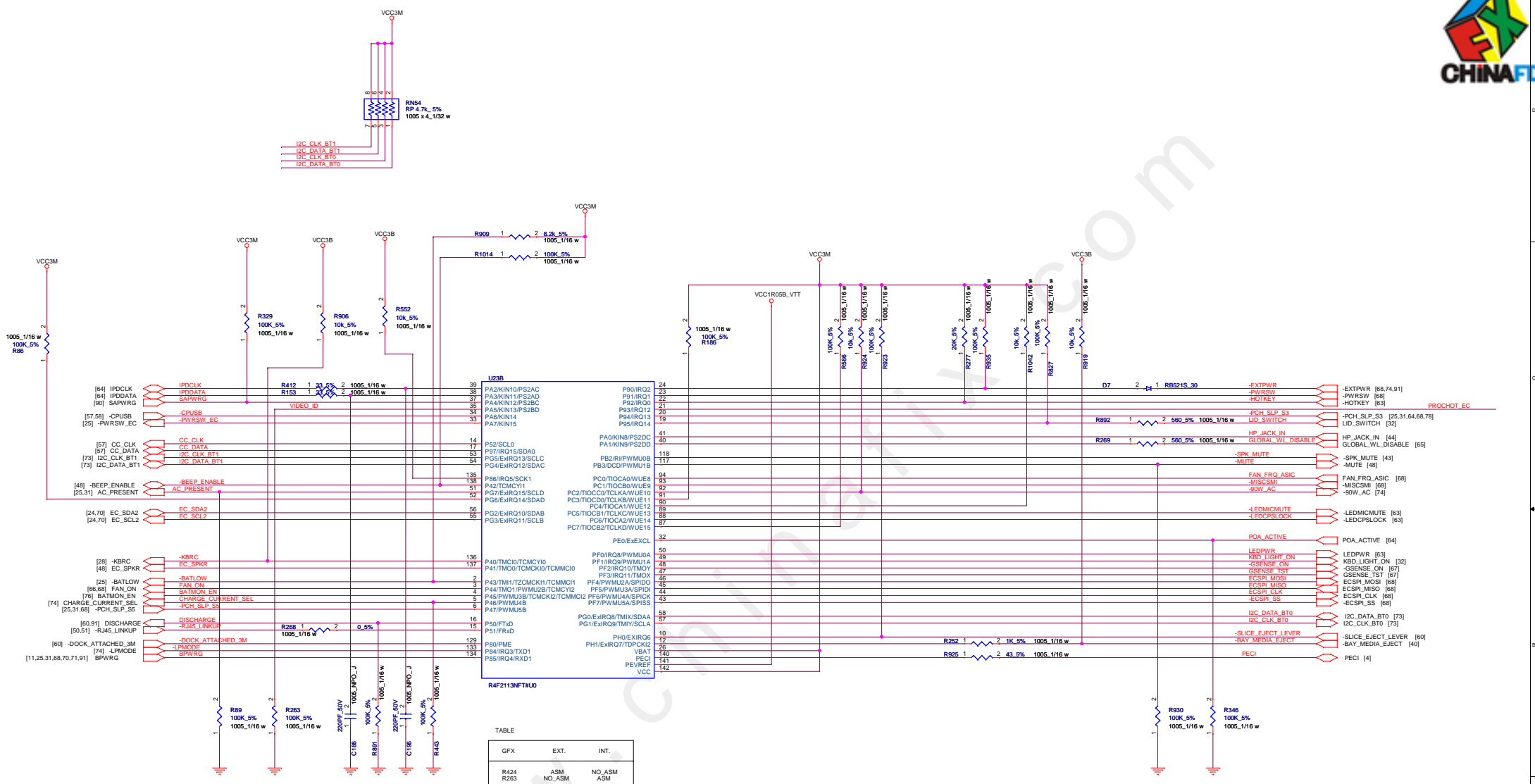
TABLE

EM100 PIN HEADER INTERFACE (TOP VIEW)							
1	(HOLD1#)	(CS1#)	2				
3	CS0#	VCC	4				
5	MISO	HOLD0#	6				
7	WP0#	CLK	8				
9	GND	MOSI	10				



P/N 41A1205
MURATA CSTCE10M0G55-R0
TDK CCR10.0MXC8T

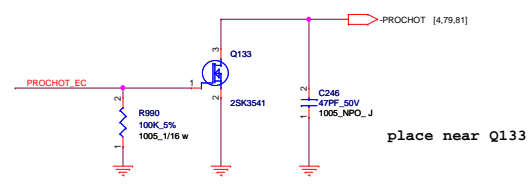


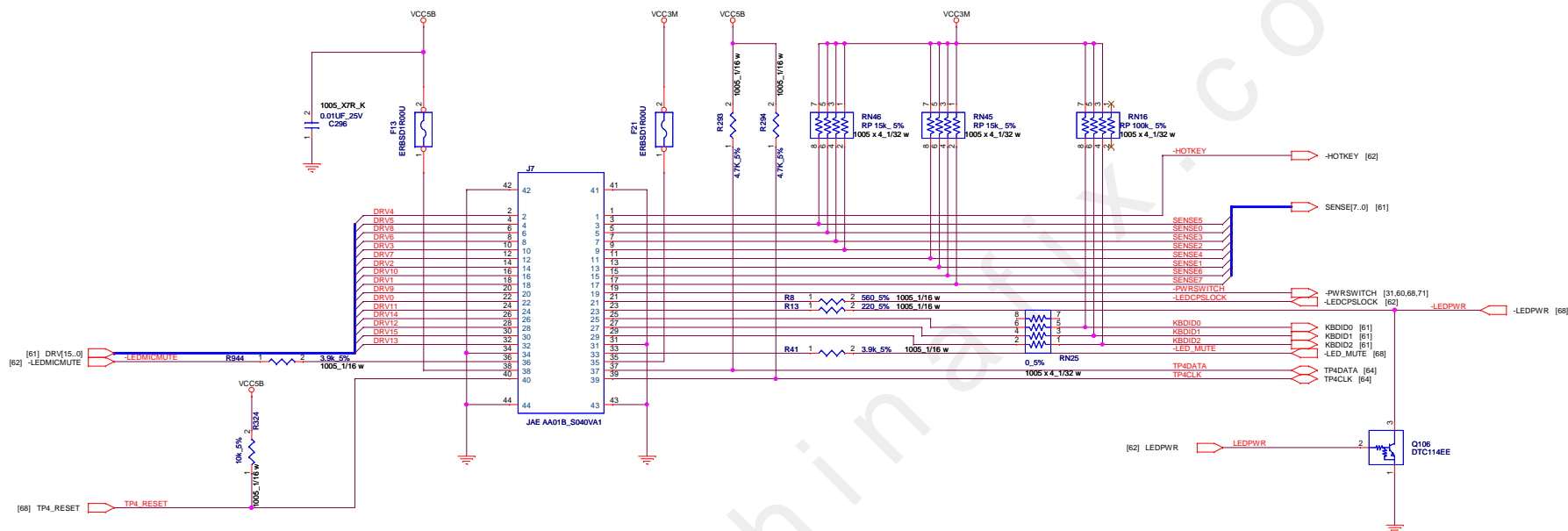


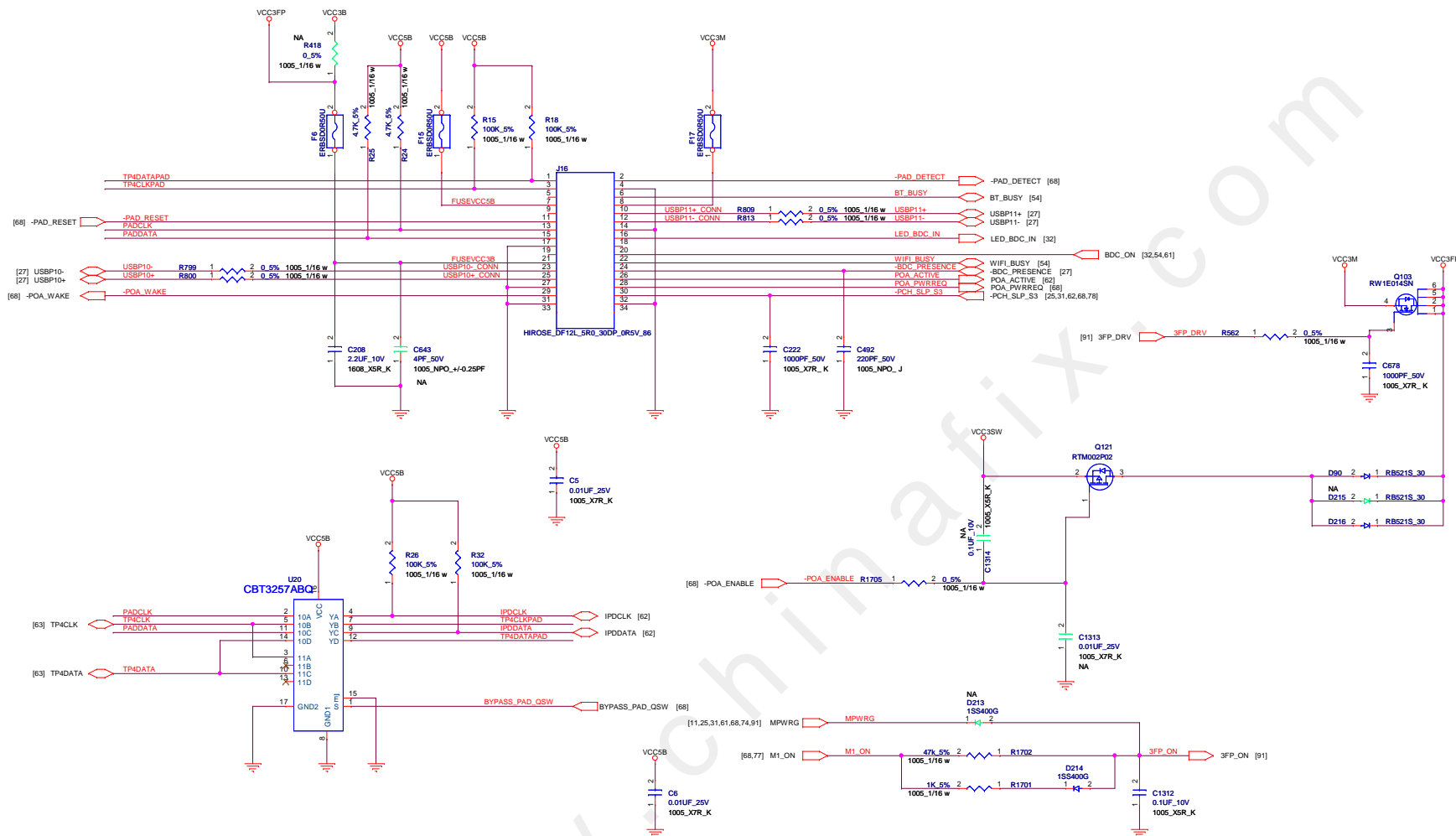
TABLE

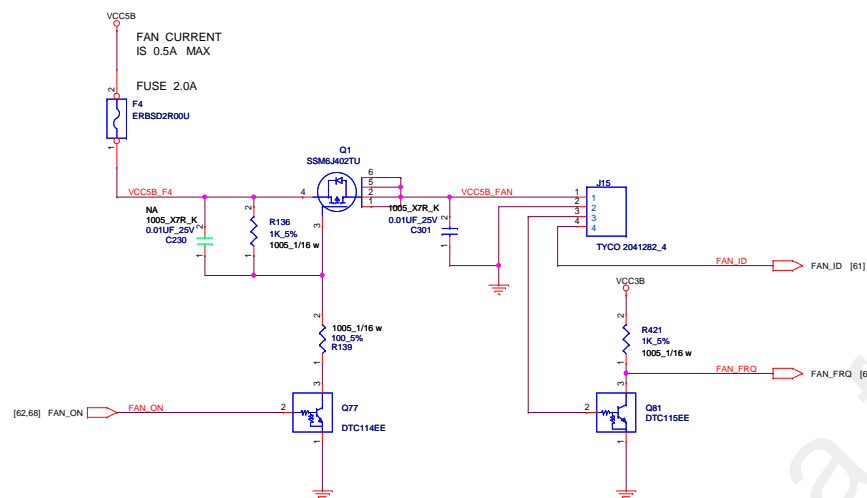
	EXT.	INT.
R424	ASM	NO_ASM
R263	NO_ASM	NO_ASM

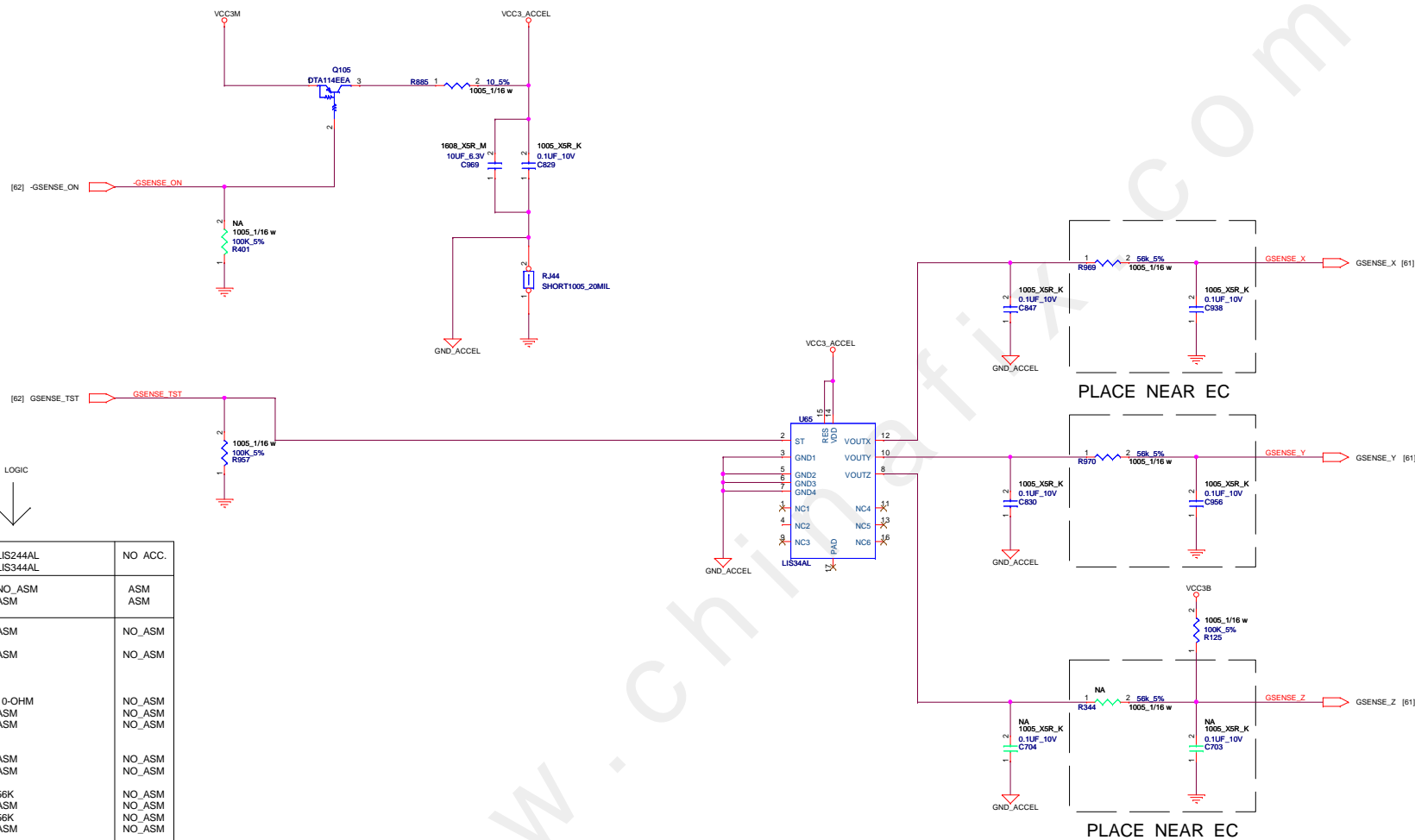
LOGIC





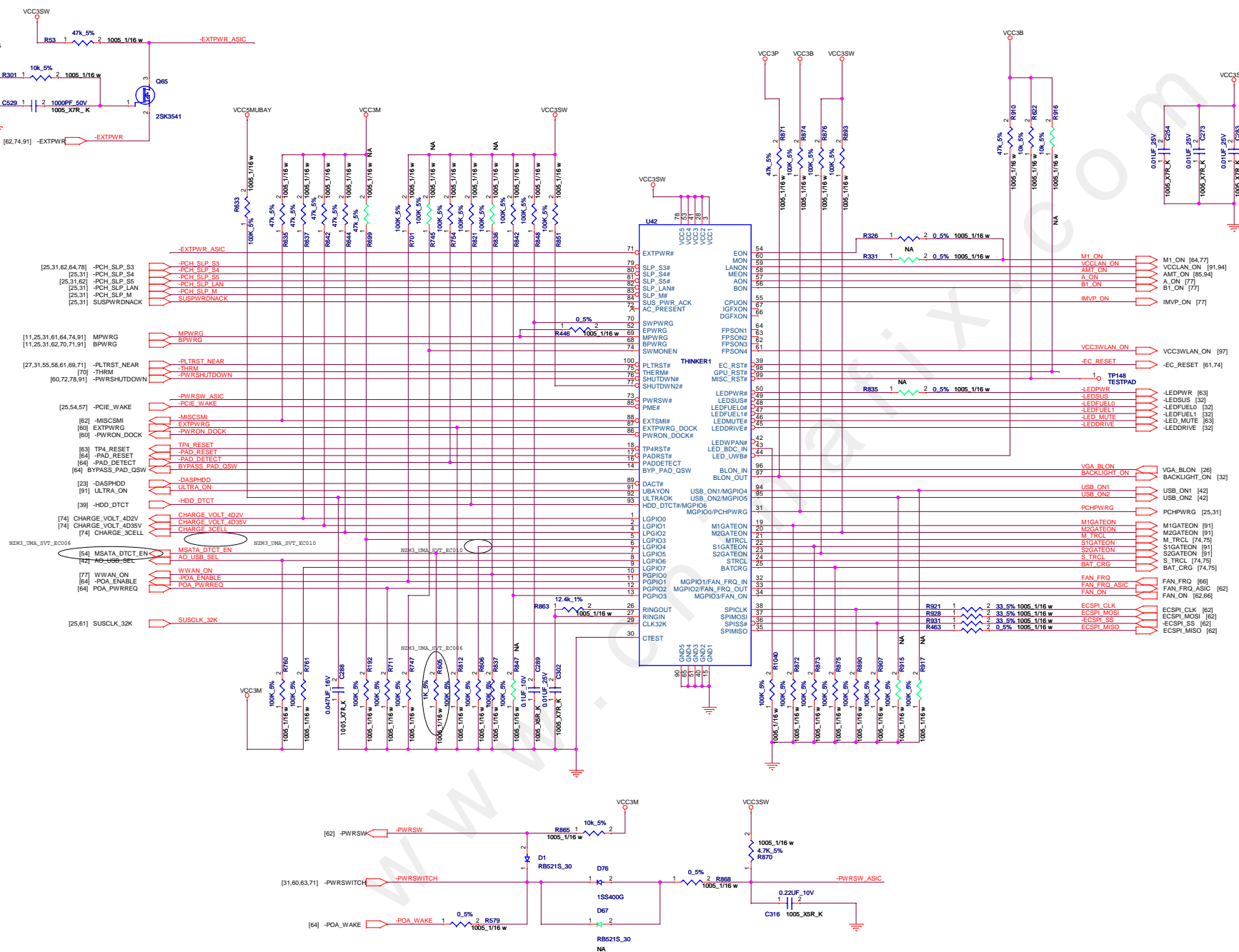


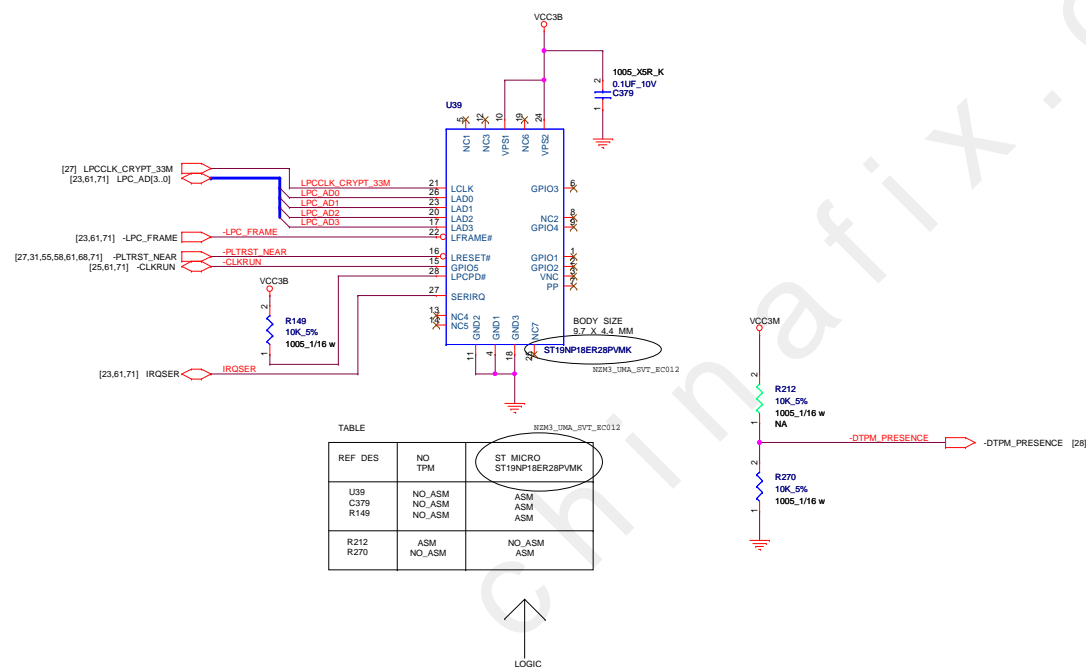


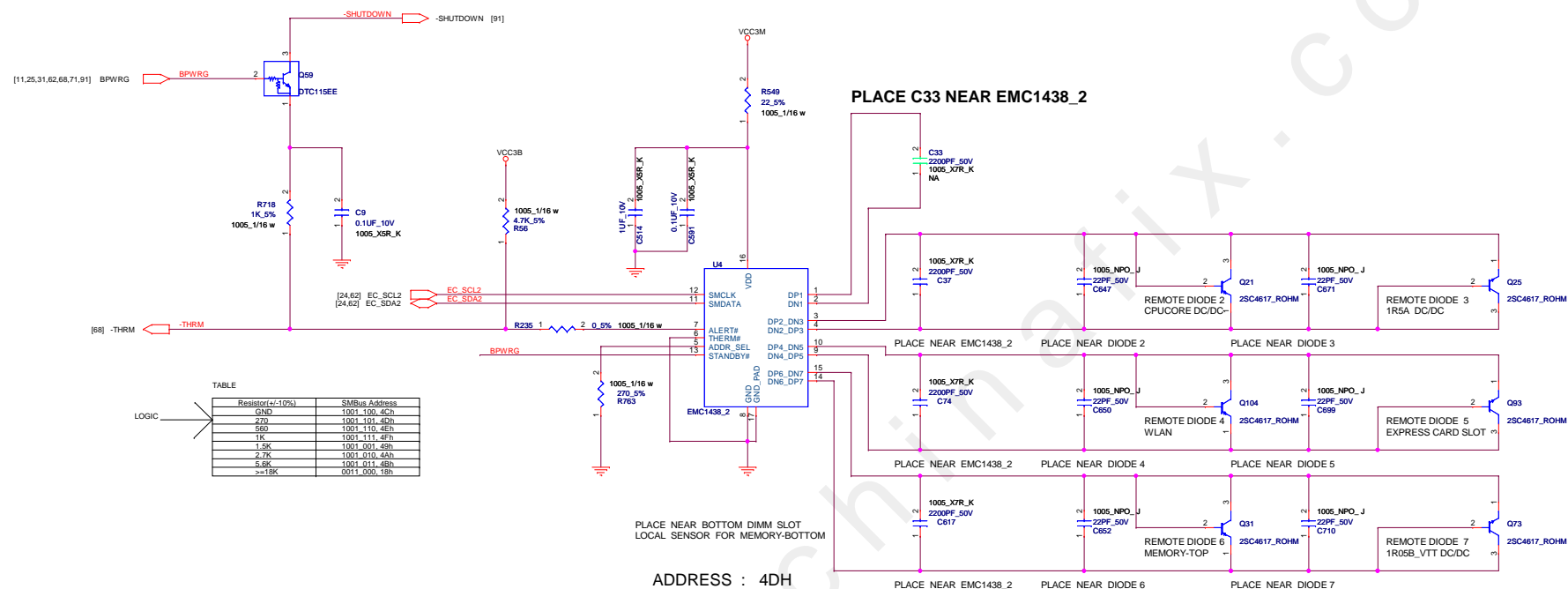


TABLE

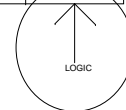
	LIS244AL LIS344AL	NO ACC.
R401 R957	NO_ASM ASM	ASM ASM
U65 Q105	ASM ASM	NO_ASM NO_ASM
R885 C829 C969	10-OHM ASM ASM	NO_ASM NO_ASM NO_ASM
C830 C847	ASM ASM	NO_ASM NO_ASM
R969 C938 R970 C956	56K ASM 56K ASM	NO_ASM NO_ASM NO_ASM NO_ASM
C704 R344 C703	NO_ASM NO_ASM NO_ASM	NO_ASM NO_ASM NO_ASM
R125	ASM	ASM





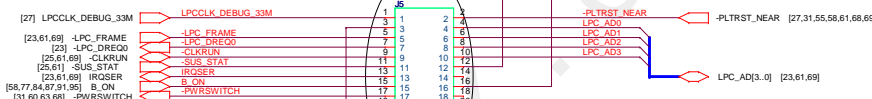


REF	DES	ENABLE	DISABLE
J5	ASM	NO_ASM	
R220	ASM	NO_ASM	



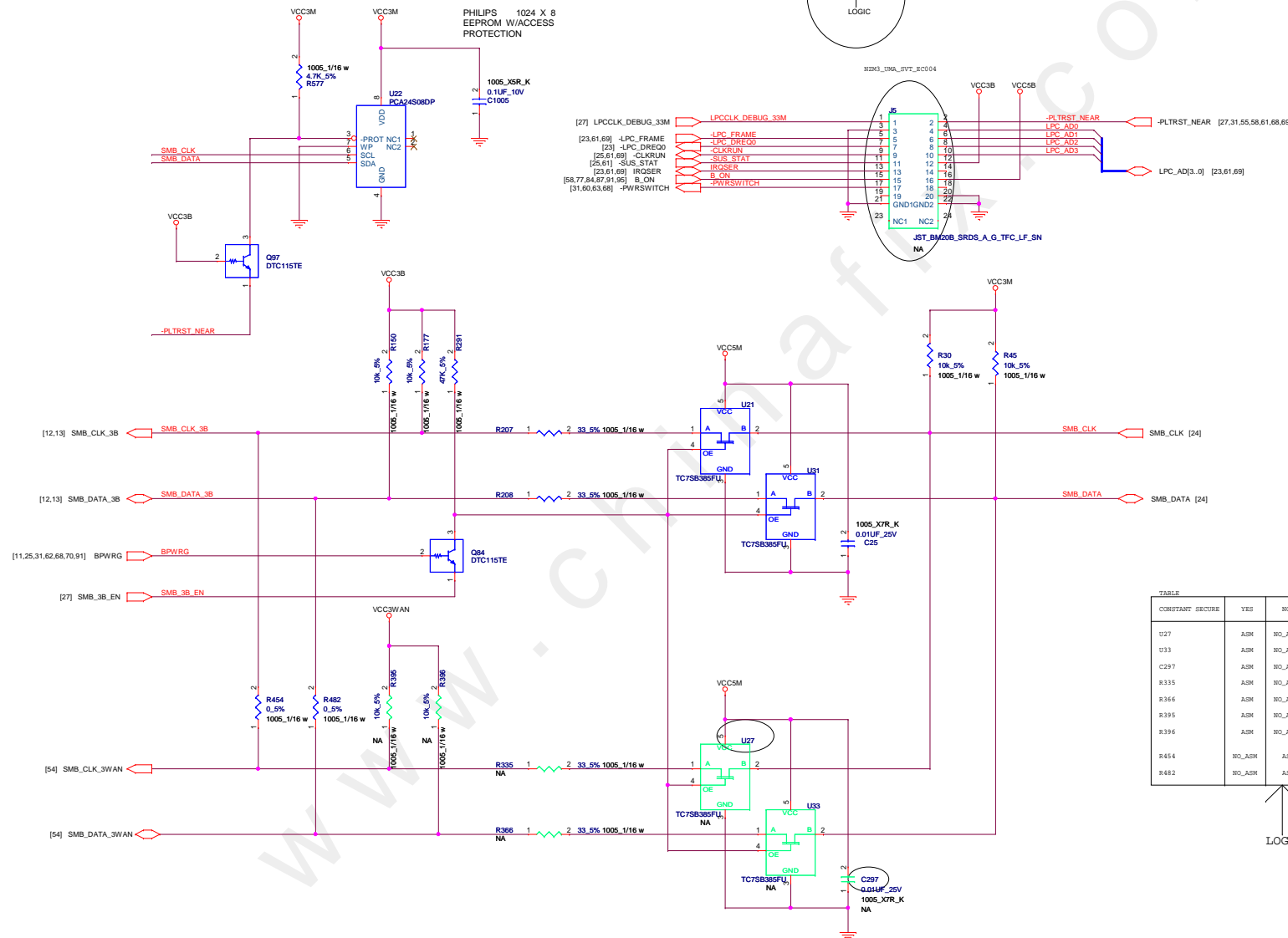
NRX3_UMA_SVT_EC004

NRX3_UMA_SVT_EC004



JST_BM20B_SRD5_A_G_TFC_LF_SN

NA



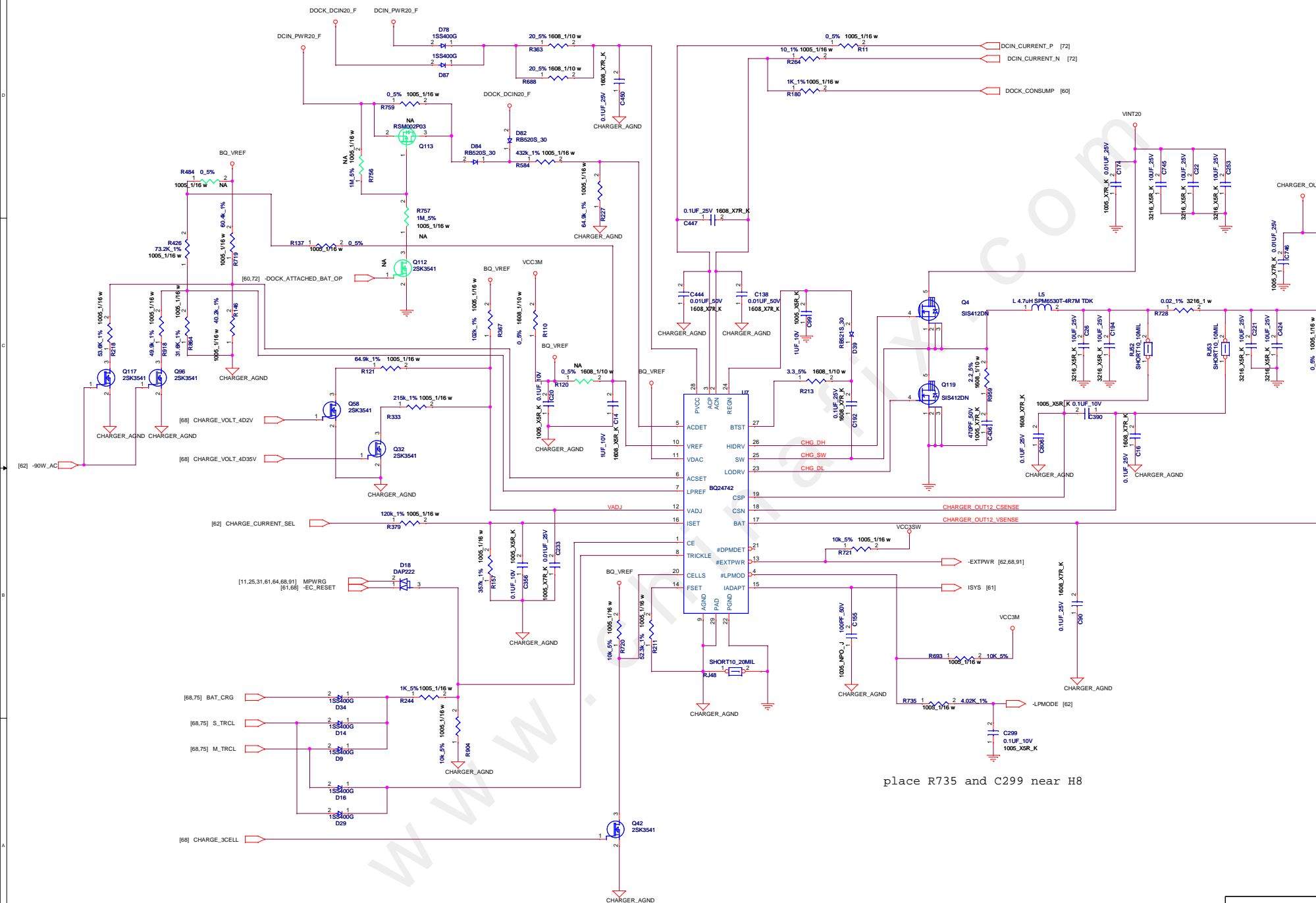
CONSTANT	SECURE	YES	NO
U27	ASM	NO_ASM	
U33	ASM	NO_ASM	
C297	ASM	NO_ASM	
R335	ASM	NO_ASM	
R366	ASM	NO_ASM	
R395	ASM	NO_ASM	
R396	ASM	NO_ASM	
R454	NO_ASM	ASM	
R482	NO_ASM	ASM	

LOGIC

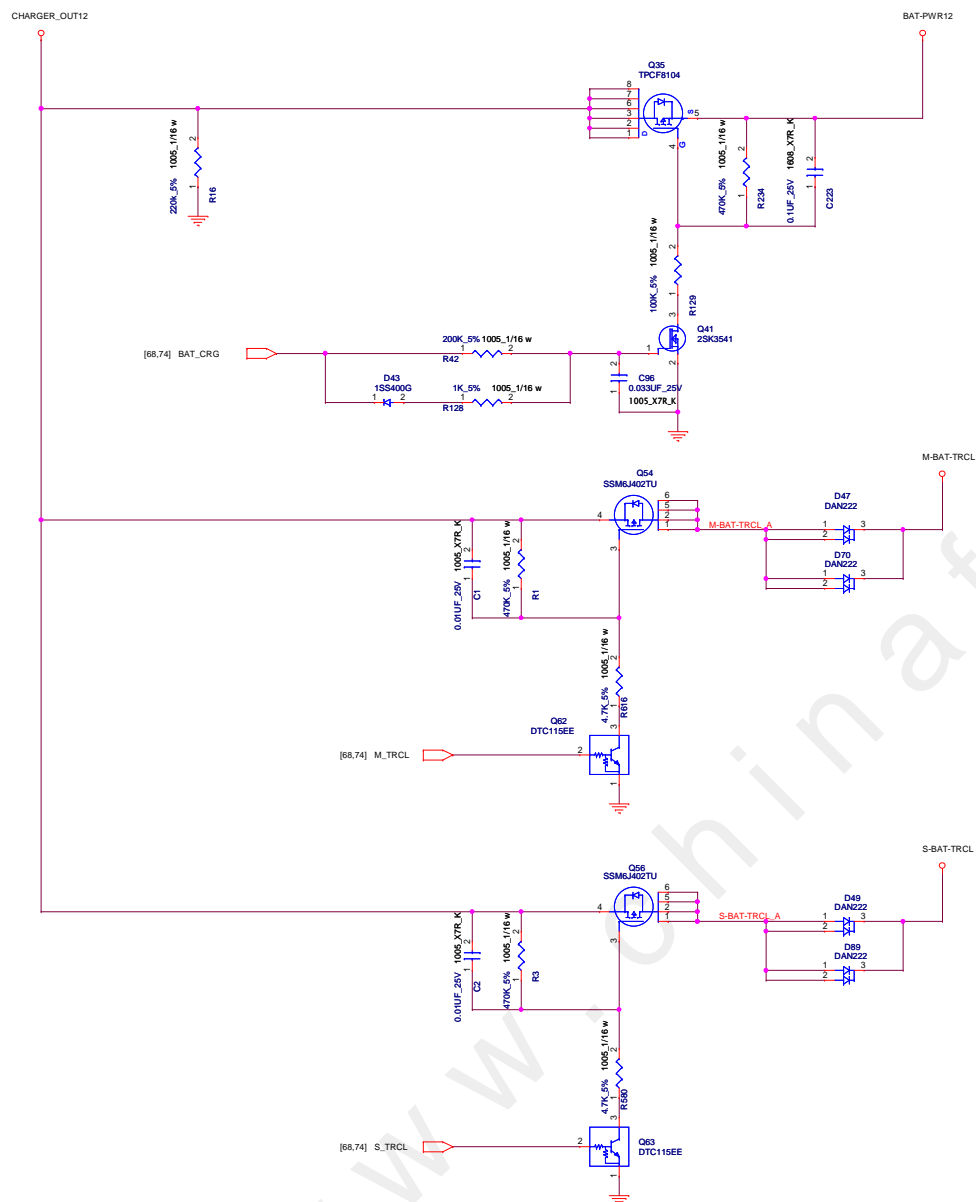


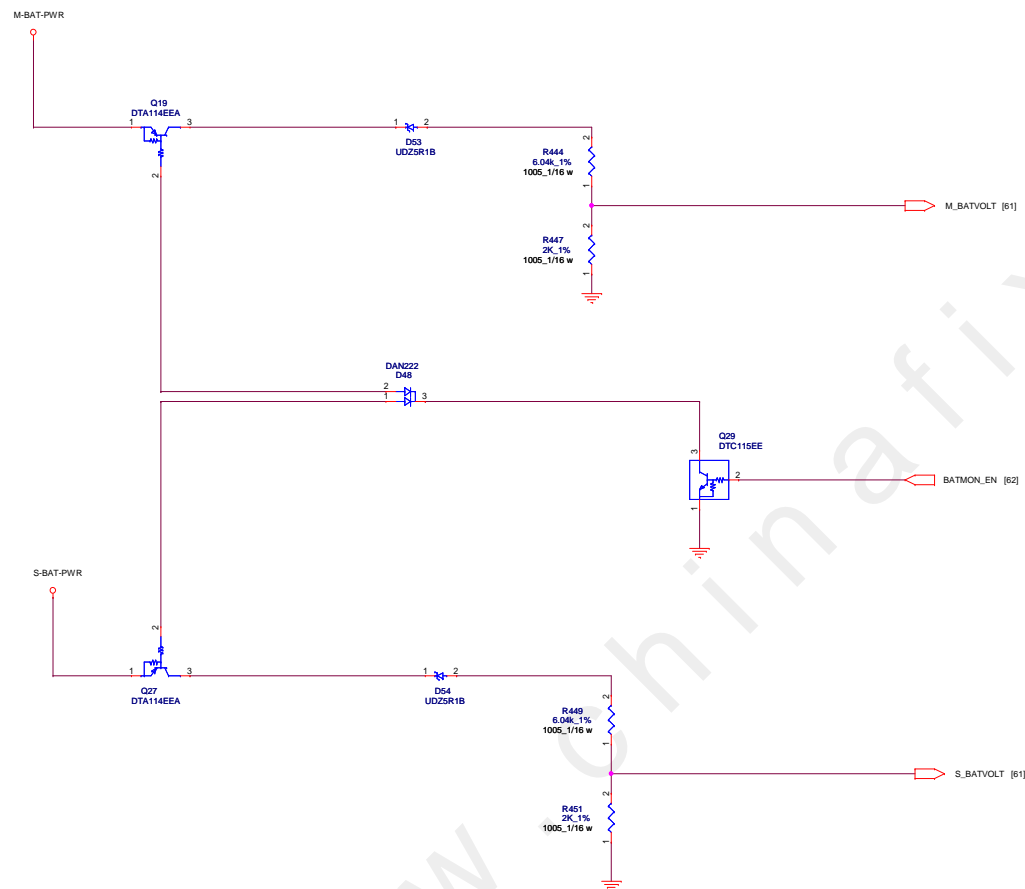
PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

LOGIC




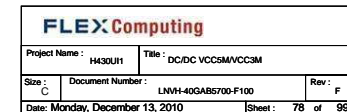
place R735 and C299 near H8

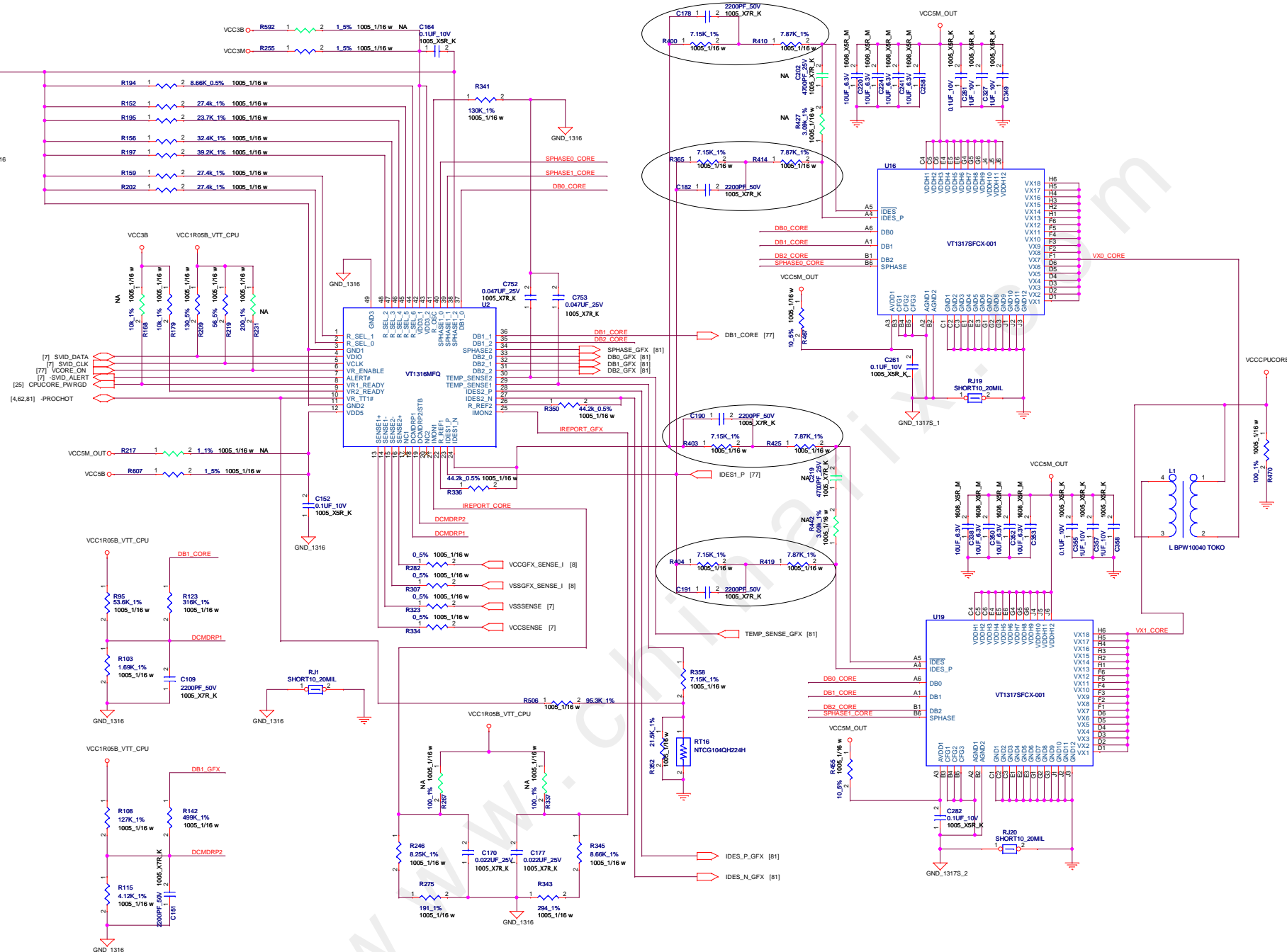




CONSTANT SECURE	YES	NO
D73	ASM	NO-ASM

			
Project Name : H430U11		Title : POWER SEQUENCE	
Size : C	Document Number : LNVH-40GAB5700-F100		Rev : F
Date: Monday, December 13, 2010		Sheet: 77 of 9	

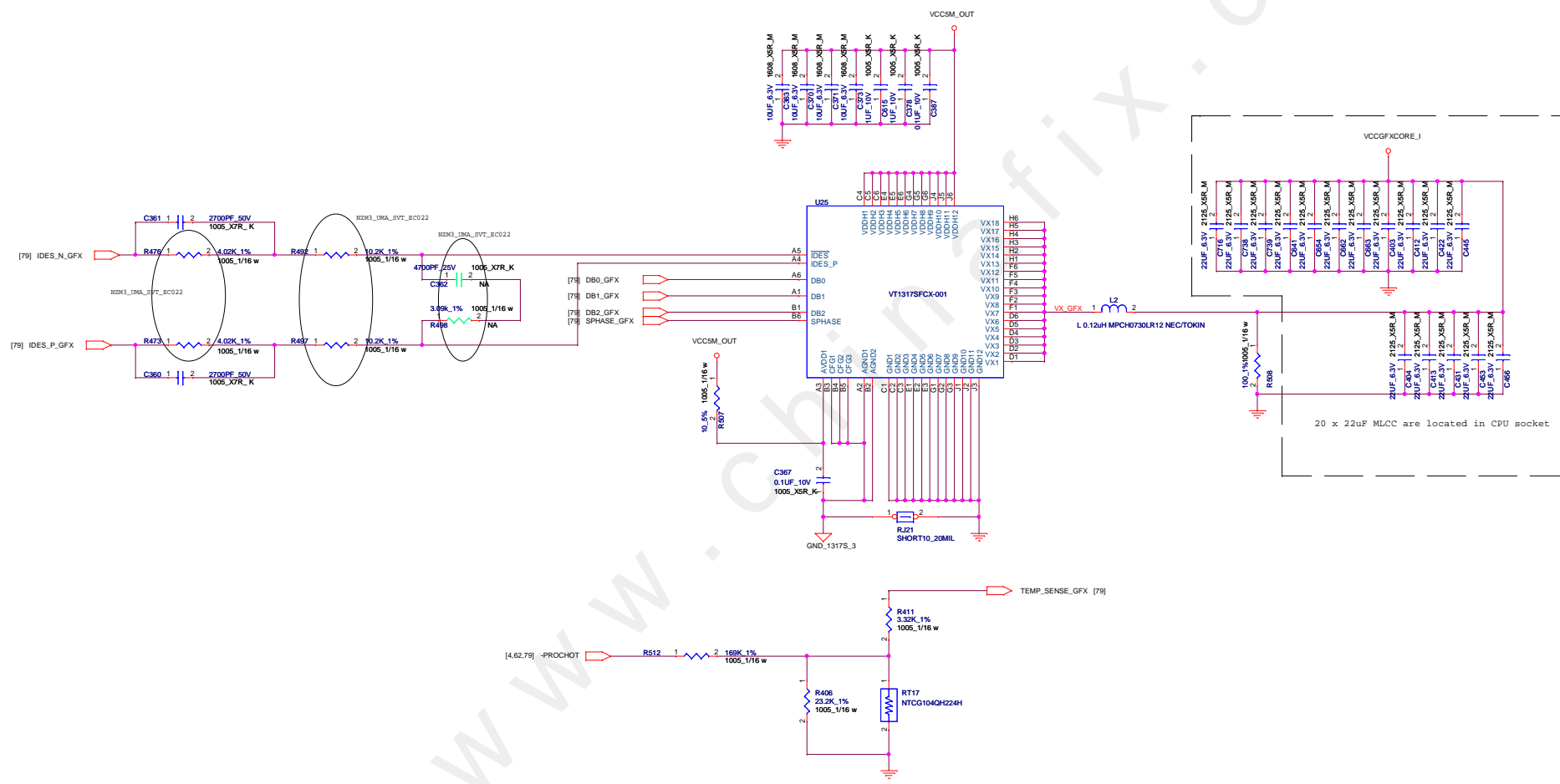


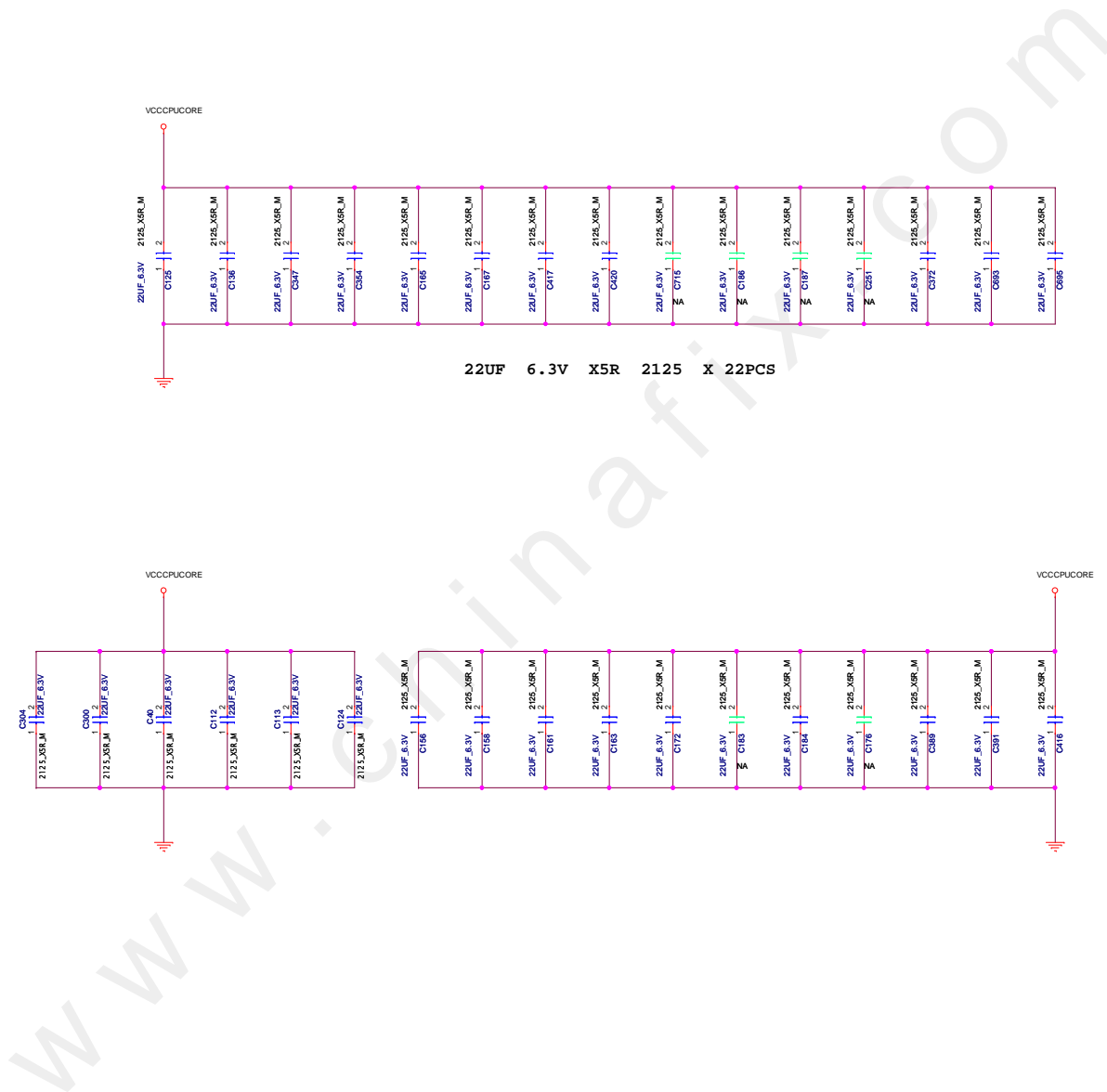




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FLEX Computing		
Project Name : H43DUH1		Title : BLANK
Size : C	Document Number : LNVH-40GAB5700-F100	Rev : F
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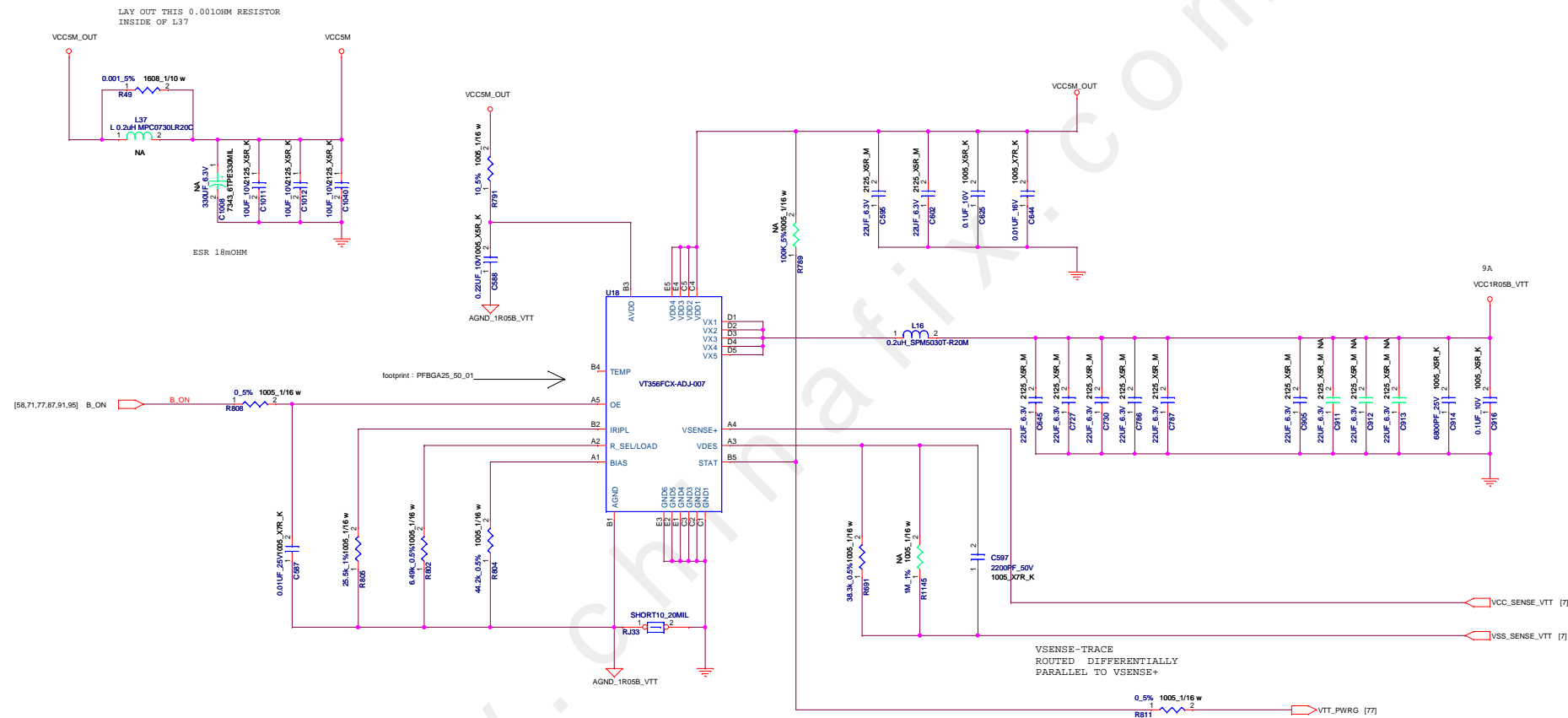




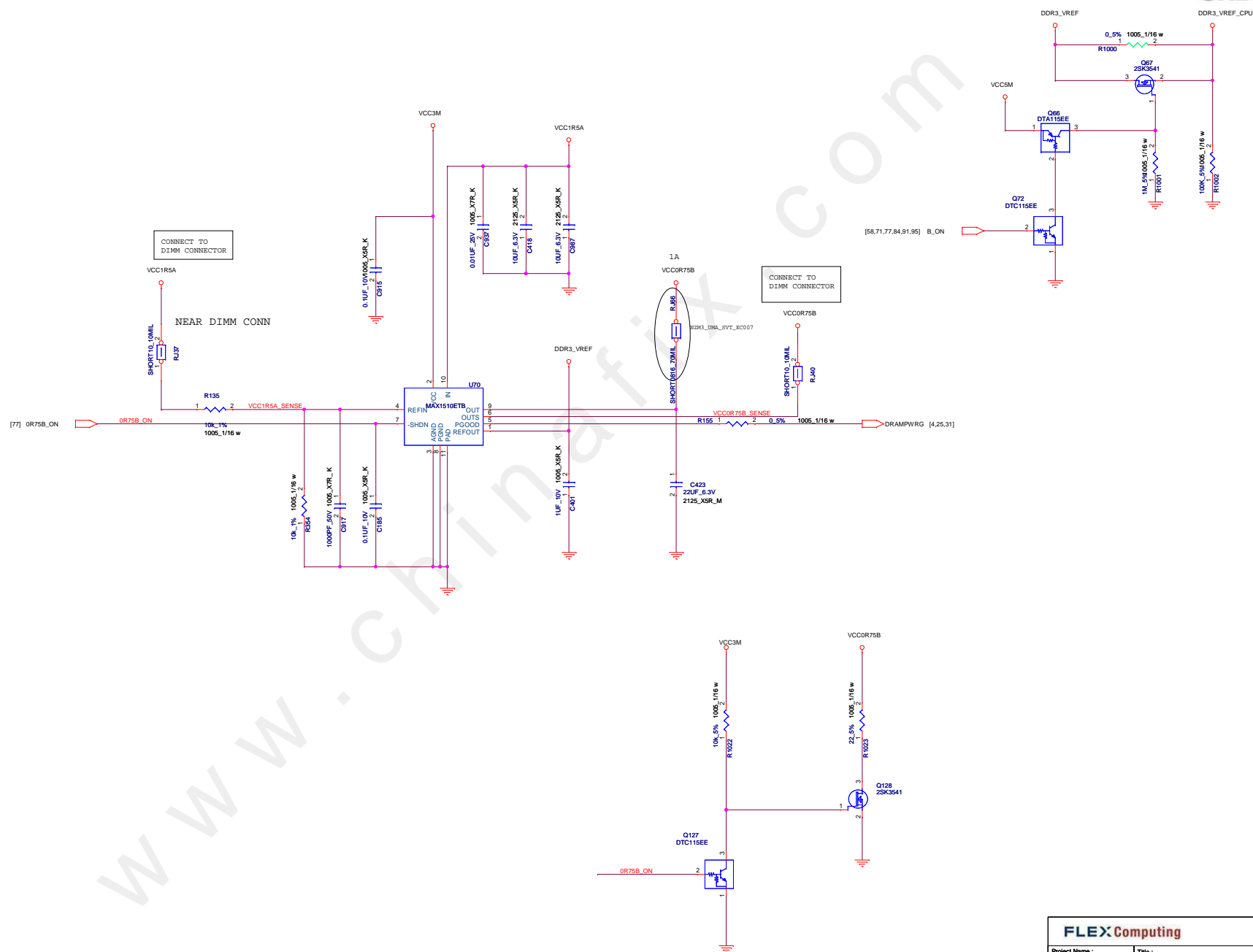


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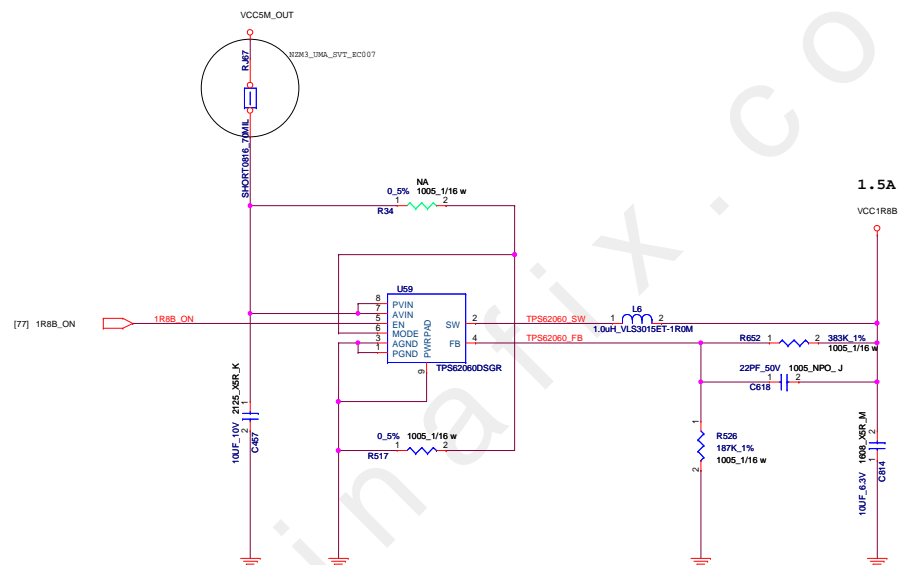
```
1.10V R691 = 40.2K
1.05V R691 = 38.3K
VOUT = 1.212*R691/44.2K
```

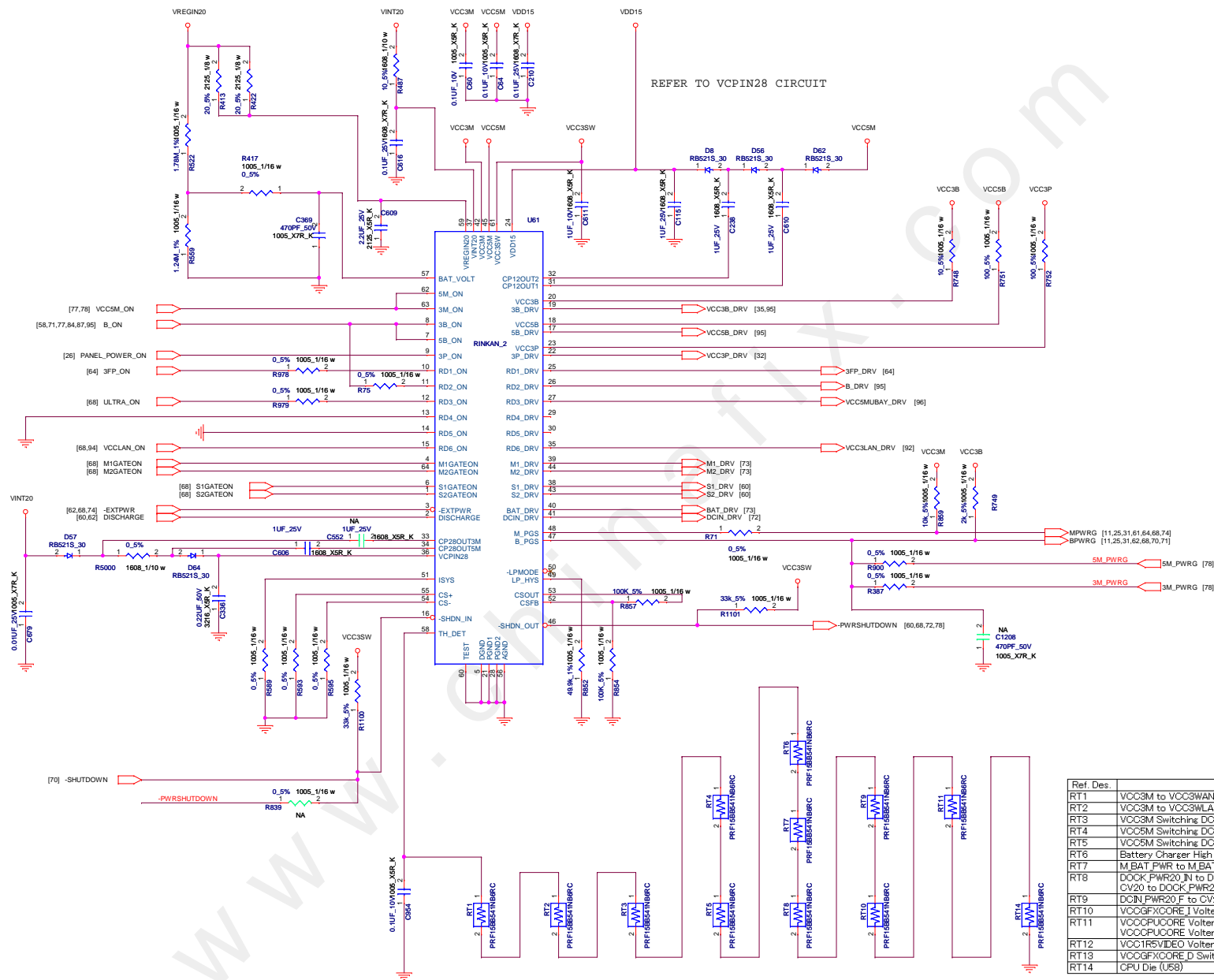





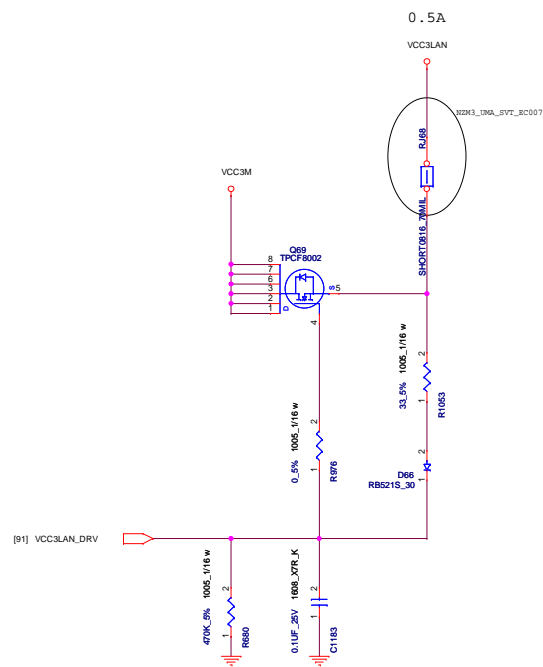
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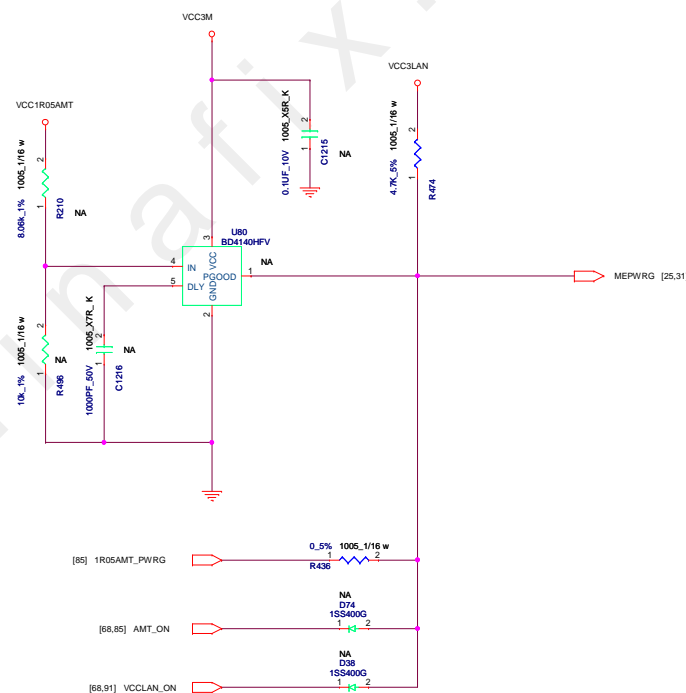
Ref. Des.	Target
RT1	VCC3M to VCC3WAN Load Switch (Q94)
RT2	VCC3M to VCC3WAN Load Switch (Q100)
RT3	VCC3M Switching DC/DC High/Low Side FET (Q18/Q17)
RT4	VCC5M Switching DC/DC High Side FET (Q2/Q16)
RT5	VCC5M Switching DC/DC Low Side FET (Q6/Q46)
RT6	Battery Charger High Side FET (Q4)
RT7	M.BAT_PWR to M.BAT_PWR_A FET (Q8)
RT8	DOCK_PWR20_IN to DOCK_PWR20 FET (Q85)
RT9	DOCK_PWR20_F to CV20 FET (Q36)
RT10	DOCK_PWR20_F to CV20 FET (Q9)
RT11	VCC3MUCORE Volterra Driver IC VT1317S Phase 1 (U16)
RT12	VCC3MUCORE Volterra Driver IC VT1317S Phase 2 (U19)
RT13	VCC3MUCORE Volterra Driver IC VT1317S Phase 2 (U19)
RT14	CPU Die (U58)

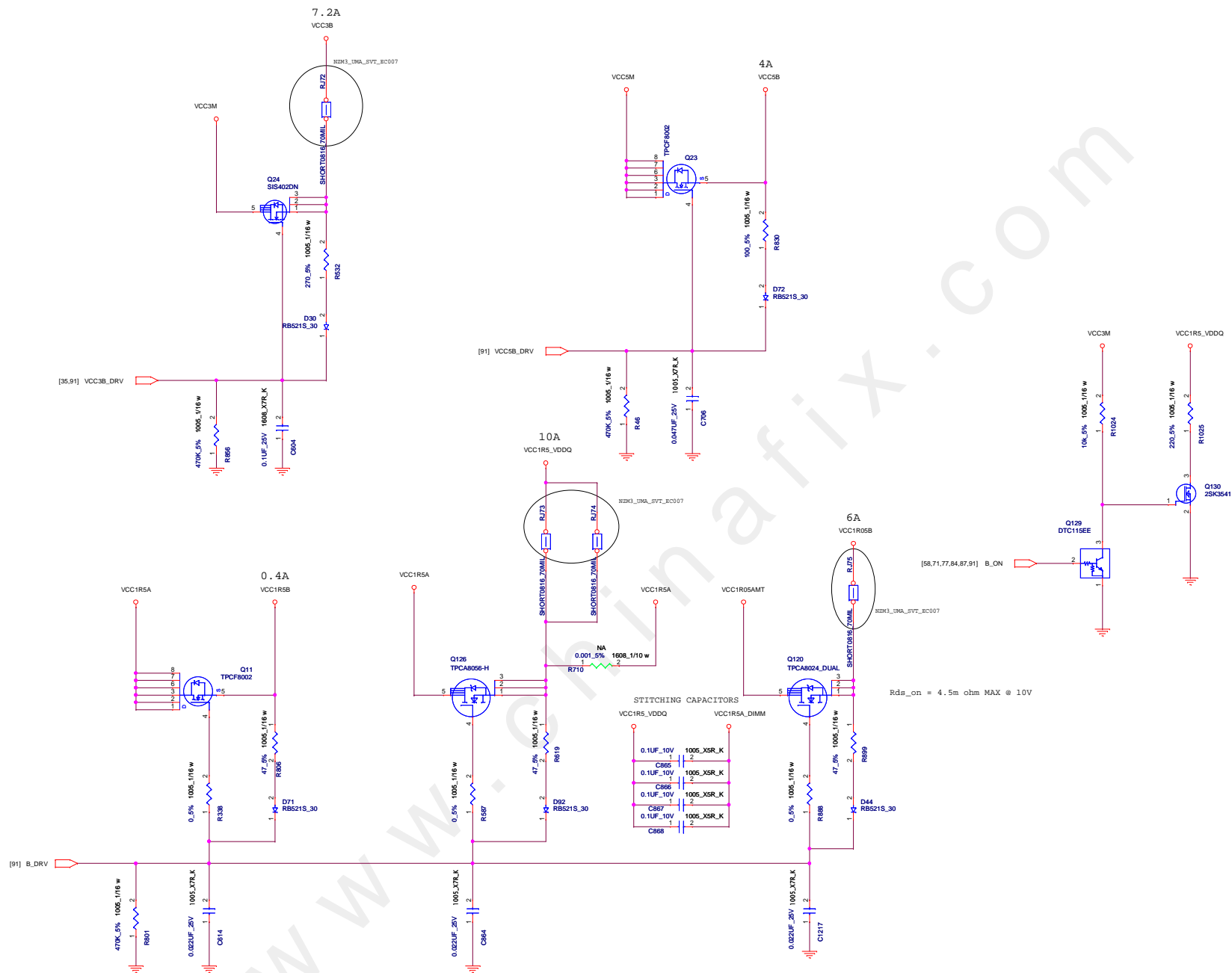


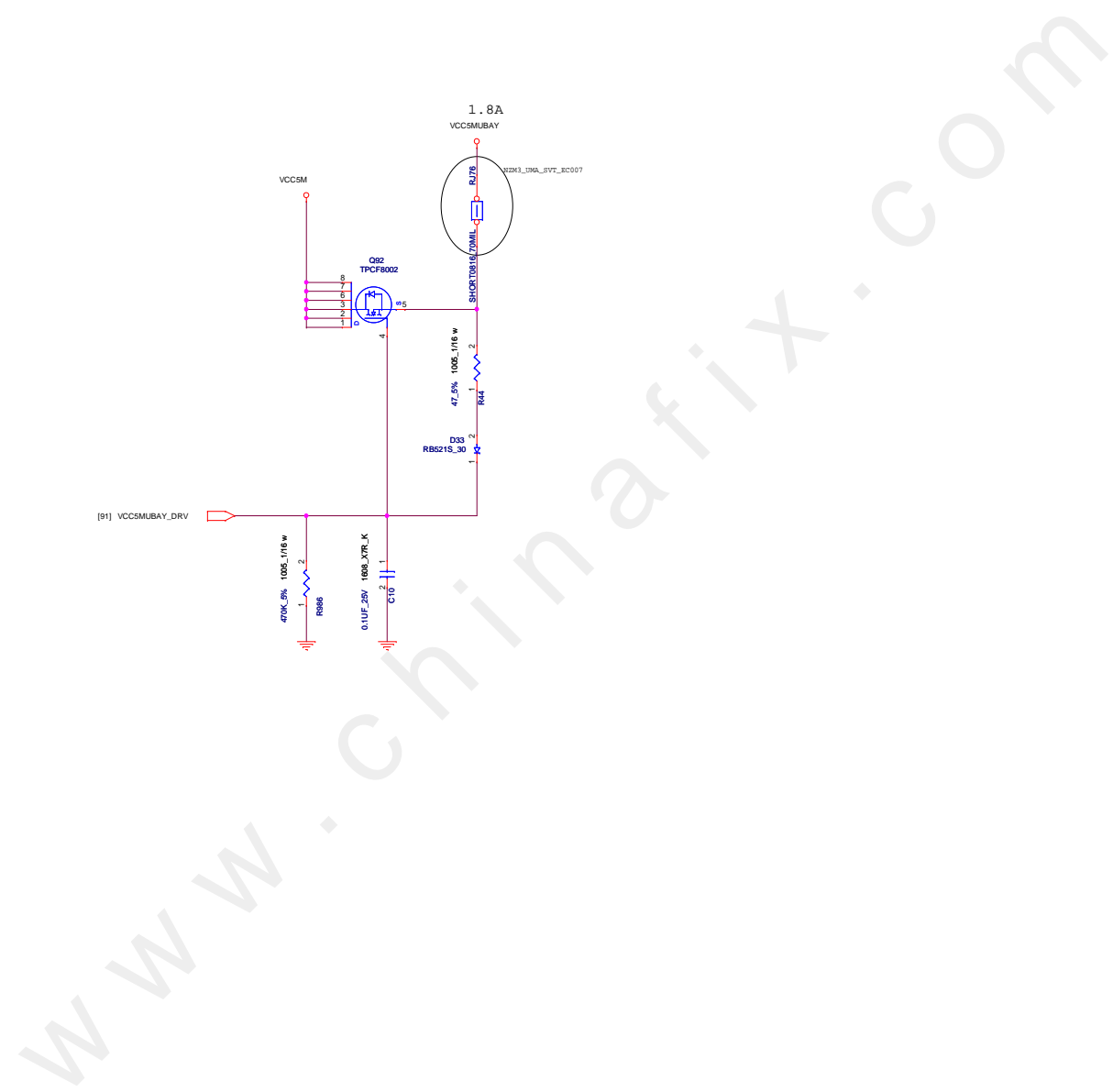


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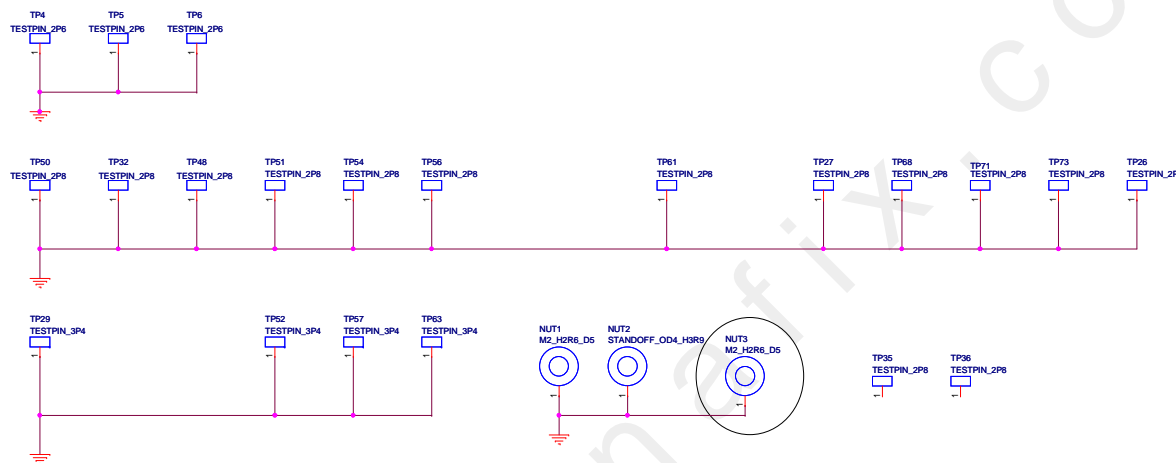
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PTH FOR SCREW HOLE



NPTH



FID
Board Area

FD1
NC, NO CONNECT TO ANY.

FD2
NC, NO CONNECT TO ANY.

FD3
NC, NO CONNECT TO ANY.

FD4
NC, NO CONNECT TO ANY.

FD5
NC, NO CONNECT TO ANY.

FD6
NC, NO CONNECT TO ANY.

LAYER_MARK1
10_LAYERS
LAYER_1-10

FID
Component Area

CF1
NC, NO CONNECT TO ANY.

CF2
NC, NO CONNECT TO ANY.

CF3
NC, NO CONNECT TO ANY.

CF4
NC, NO CONNECT TO ANY.

CF5
NC, NO CONNECT TO ANY.

CF6
NC, NO CONNECT TO ANY.

CF7
NC, NO CONNECT TO ANY.

CF8
NC, NO CONNECT TO ANY.

CF9
NC, NO CONNECT TO ANY.

CF10
NC, NO CONNECT TO ANY.

CF11
NC, NO CONNECT TO ANY.

CF12
NC, NO CONNECT TO ANY.

CF13
NC, NO CONNECT TO ANY.

CF14
NC, NO CONNECT TO ANY.

CF15
NC, NO CONNECT TO ANY.

CF16
NC, NO CONNECT TO ANY.

CF17
NC, NO CONNECT TO ANY.

CF18
NC, NO CONNECT TO ANY.



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