MODEL NAME: VBW11

PROJECT CODE: ANRVBW0100

PCB NO: DA8000WN000 LA-9984P M/B

DA40001FP00 LS-9102P USB/B DA40001FR00 LS-9104P ODD/B

DA40001G400 LS-9105P POWER BUTTON/B **DA40001FQ00 LS-9106P TP BUTTON/B**

Dell / Compal Confidential

Schematic Document

Intel Shark Bay ULT OAK Mainstream2 UMA/DIS AMD Venus XT

2013-05-17 Rev: 1.0

X76@: 76 level 46@: 46 level

@: Nopop component

CONN@: Connector component

XDP@: XDP function UMA@: Only for UMA DIS@: Only for Discrete

VENUS@: VENUS Pro, VENUS XT

VENUSXT@: VENUS XT VENUSPRO@: VENUS Pro

@VENUS@: VENUS nopop component

EMI@: EMI parts

@EMI@: Reserve EMI parts

ESD@: ESD parts RF@: RF parts

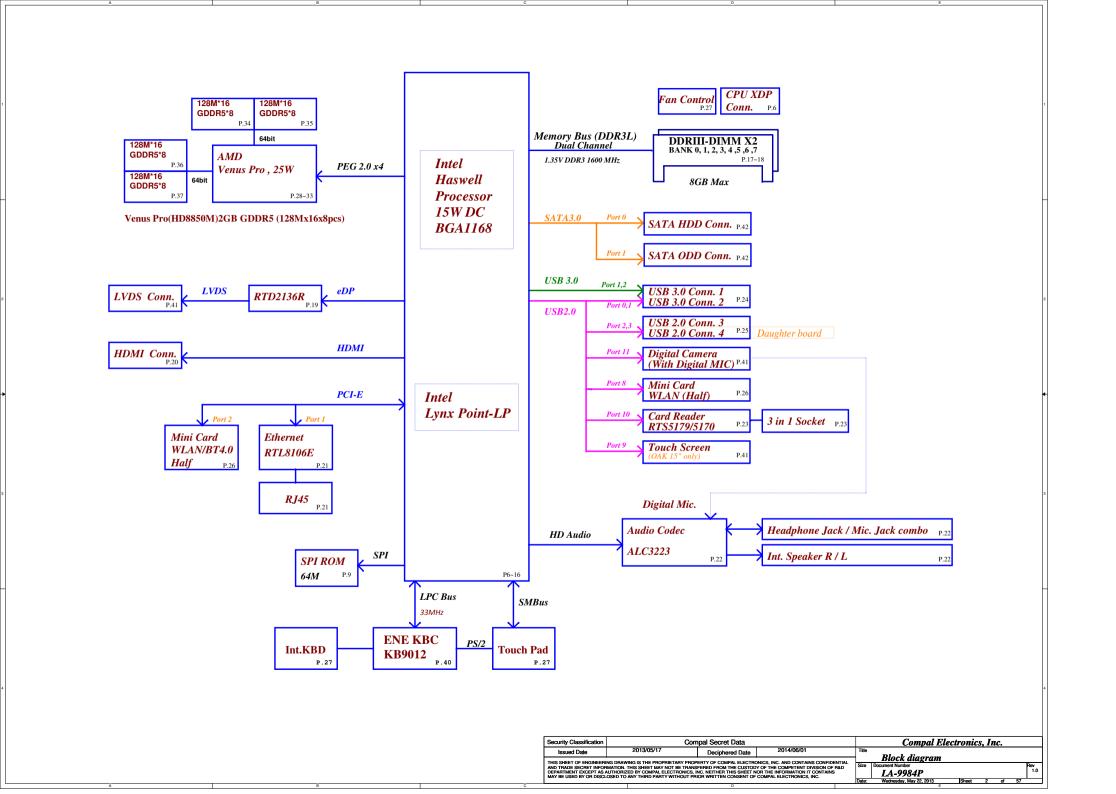
BOM config

UMA: UMA@,EMI@,ESD@,RF@,XDP@

DIS VENUS: VENUS@, VENUSXT@, DIS@, EMI@, ESD@, RF@, XDP@

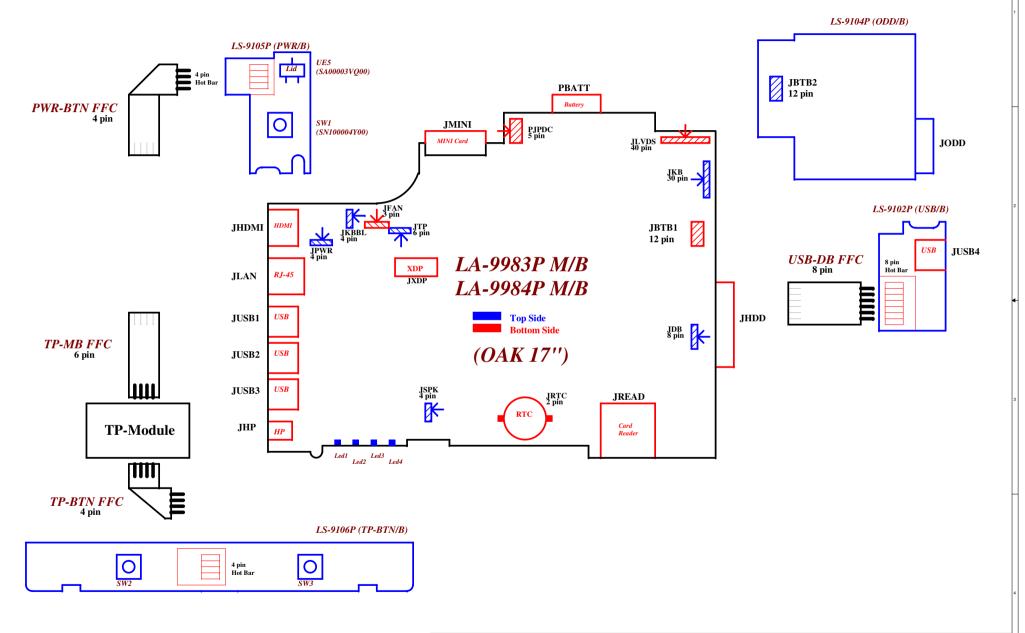


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Project Code: VAW10 / VAW11 File Name: LA-9983P / LA-9984P



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Board ID Table for AD channel

Vcc	3.3V +/- 1%				
Ra	100K +/- 1%				
Board ID	Rb	V_{AD_BID} min	V _{AD_BID} typ	V _{AD_BID} max	EC AD3
0	0	0.000V	0.000V	0.300V	0x00 - 0x0B
1	12K +/- 1%	0.347V	0.354V	0.360V	0x0C - 0x1C
2	15K +/- 1%	0.423V	0.430V	0.438V	0x1D - 0x26
3	20K +/- 1%	0.541V	0.550V	0.559V	0x27 - 0x30
4	27K +/- 1%	0.691V	0.702V	0.713V	0x31 - 0x3B
5	33K +/- 1%	0.807V	0.819V	0.831V	0x3C - 0x46
6	43K +/- 1%	0.978V	0.992V	1.006V	0x47 - 0x54
7	56K +/- 1%	1.169V	1.185V	1.200V	0x55 - 0x64
8	75K +/- 1%	1.398V	1.414V	1.430V	0x65 - 0x76
9	100K +/- 1%	1.634V	1.650V	1.667V	0x77 - 0x87
10	130K +/- 1%	1.849V	1.865V	1.881V	0x88 - 0x96
11	160K +/- 1%	2.015V	2.031V	2.046V	0x97 - 0xA3
12	200K +/- 1%	2.185V	2.200V	2.215V	0xA4 - 0xAD
13	240K +/- 1%	2.316V	2.329V	2.343V	0xAE - 0xB7
14	270K +/- 1%	2.395V	2.408V	2.421V	0xB8 - 0xC0
15	330K +/- 1%	2.521V	2.533V	2.544V	0xC1 - 0xC9
16	430K +/- 1%	2.667V	2.677V	2.687V	0xCA - 0xD3
17	560K +/- 1%	2.791V	2.800V	2.808V	0xD4 - 0xDC
18	750K +/- 1%	2.905V	2.912V	2.919V	0xDD - 0xE6
19	NC	3.000V	3.300V	3.300V	0xE7 - 0xFF

SMBUS Control Table

	SOURCE	BATT	Charger	RTD2136S	VGA	DDR3L	XDP	WLAN mini card	Touch pad		
EC_SMB_CK1 EC_SMB_DA1	KB9012	V	V								
EC_SMB_CK2 EC_SMB_DA2	KB9012			V	V					\leftarrow	ì
SMBCLK SMBDATA	ULT					V	V	V	V		Link
SML0CLK SML0DATA	ULT										
SML1CLK SML1DATA	ULT									\leftarrow	l

Board ID TABLE

ID	P	CB Revis	ion	
10	UMA	Sun XT	VenusPro	VenusXT
0	SSI&A02			
1		SSI&A02		
3			SSI&A02	
				SSI&A02
4	PT			
5		PT		
6			PT	
7				PT
8	ST			
9		ST		
10			ST	
11				ST
12	XB			
13		XB		
14			XB	
15				XB
16	A01			
17		A01		
18			A01	
19				A01

Symbol Note:

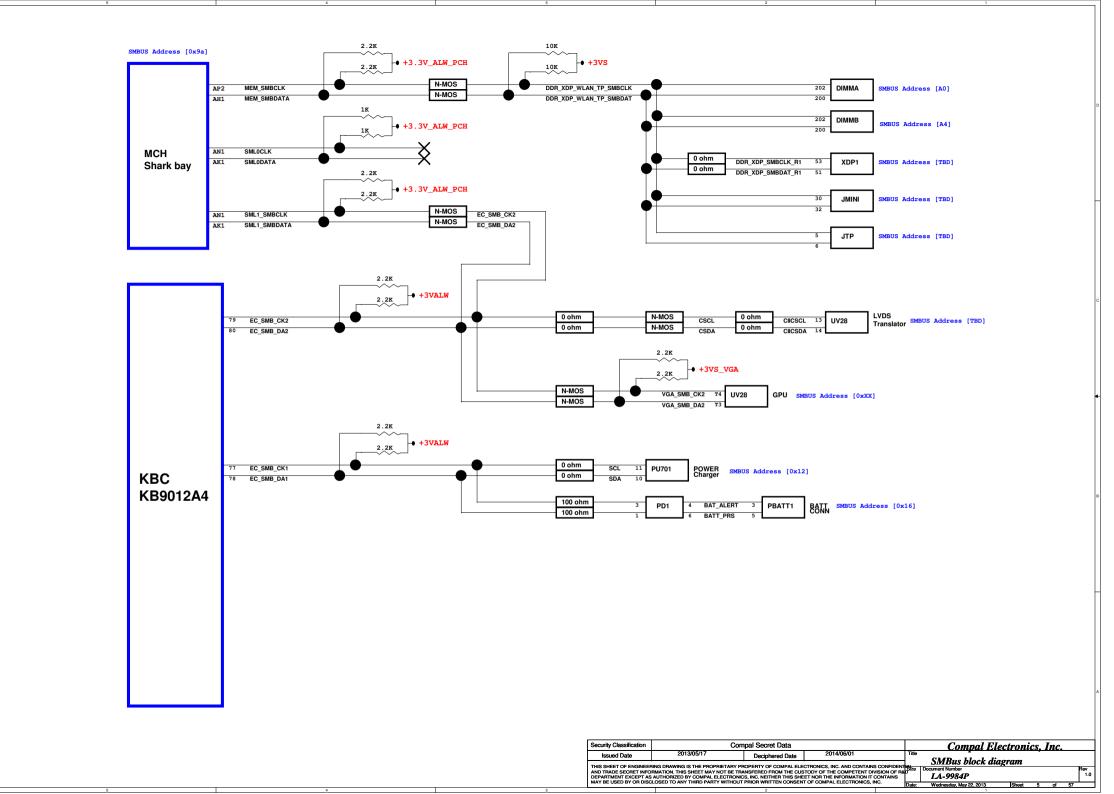


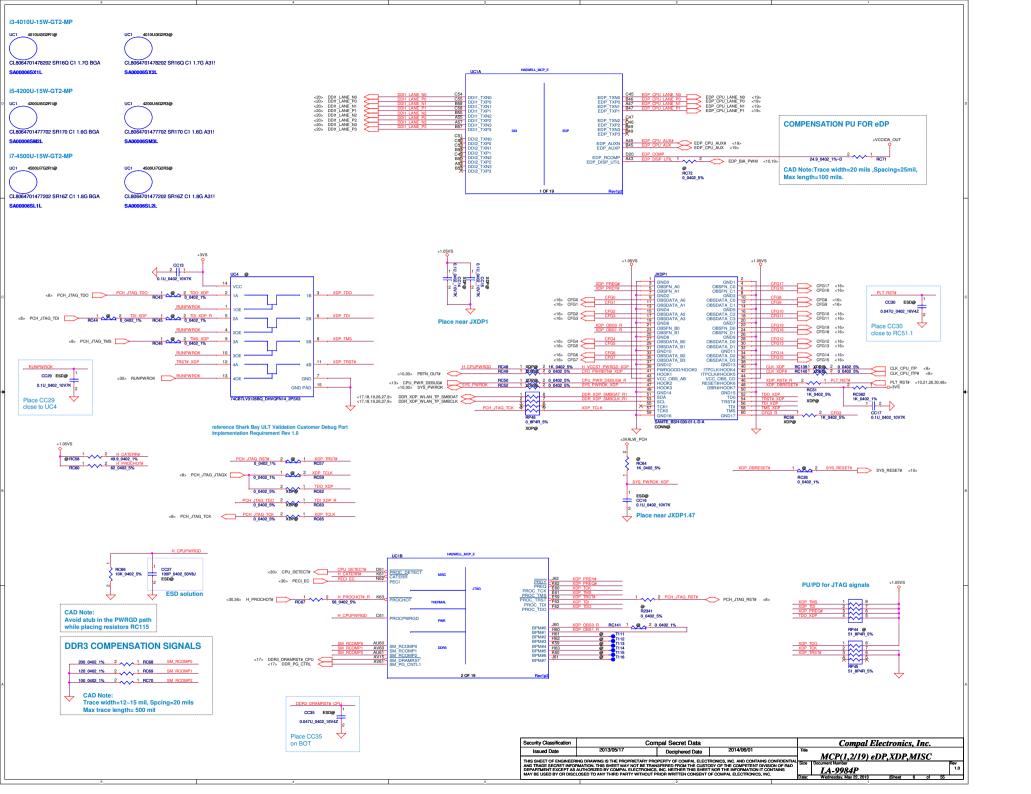
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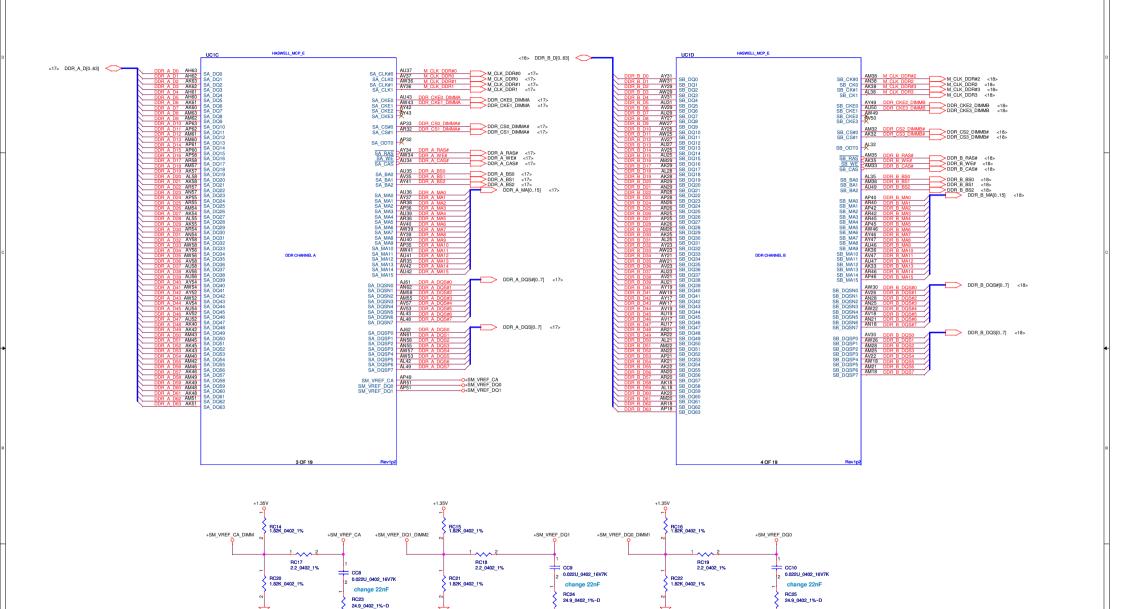
CLOC	CK SIGNAL
CLKOUT_PCIE0	
CLKOUT_PCIE1	
CLKOUT_PCIE2	10/100 LAN
CLKOUT_PCIE3	MINI Card (WLAN)
CLKOUT_PCIE4	dGPU
CLKOUT_PCIE5	

		USB3.0
	Port1	USB connector 2
	Port2	USB connector 1
	Port3	
	Port4	
		USB2.0
	Port0	USB connector 2
	Port1	USB connector 1
	Port2	USB connector 3
	Port3	USB connector 4 (DB)
	Port4	MINI Card (WLAN)
	Port5	Touch Screen Panel
ULT	Port6	Card Reader
	Port7	Camera
		PCI EXPRESS
	Lane 1	
	Lane 2	
	Lane 3	10/100 LAN
	Lane 4	MINI Card (WLAN)
	Lane 5	PEG (N14P)
	Lane 6	PEG (N14P)
		SATA
	SATA0	HDD
	SATA1	ODD
	SATA2	
	SATA3	

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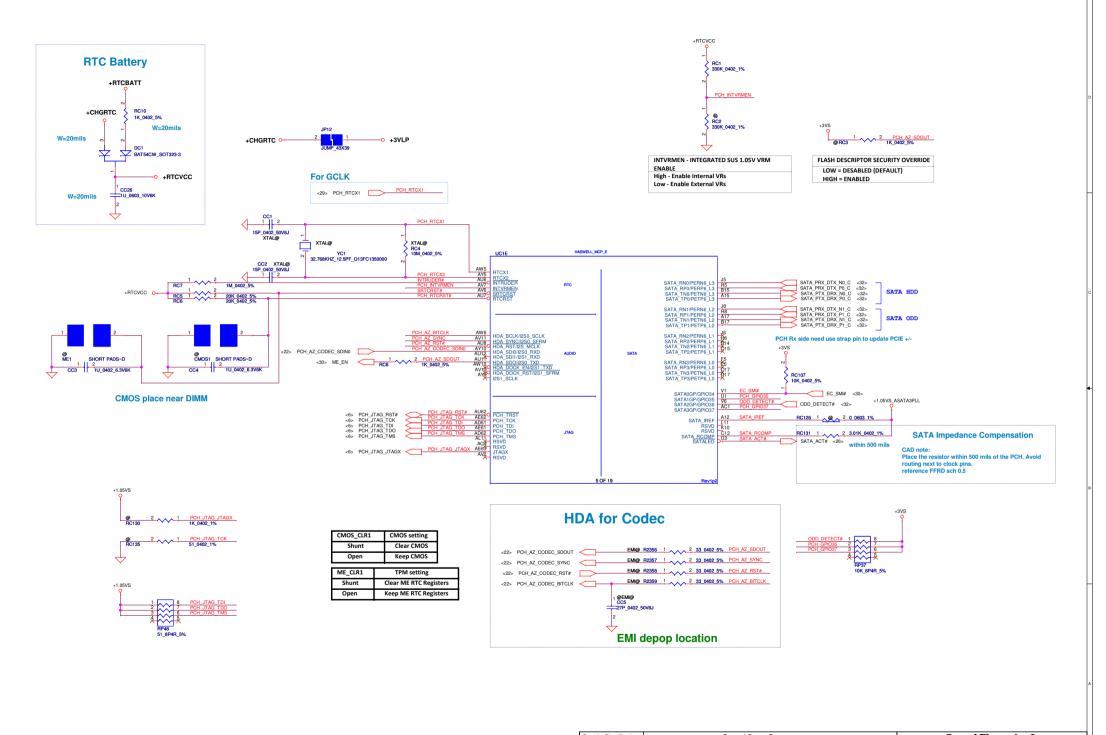


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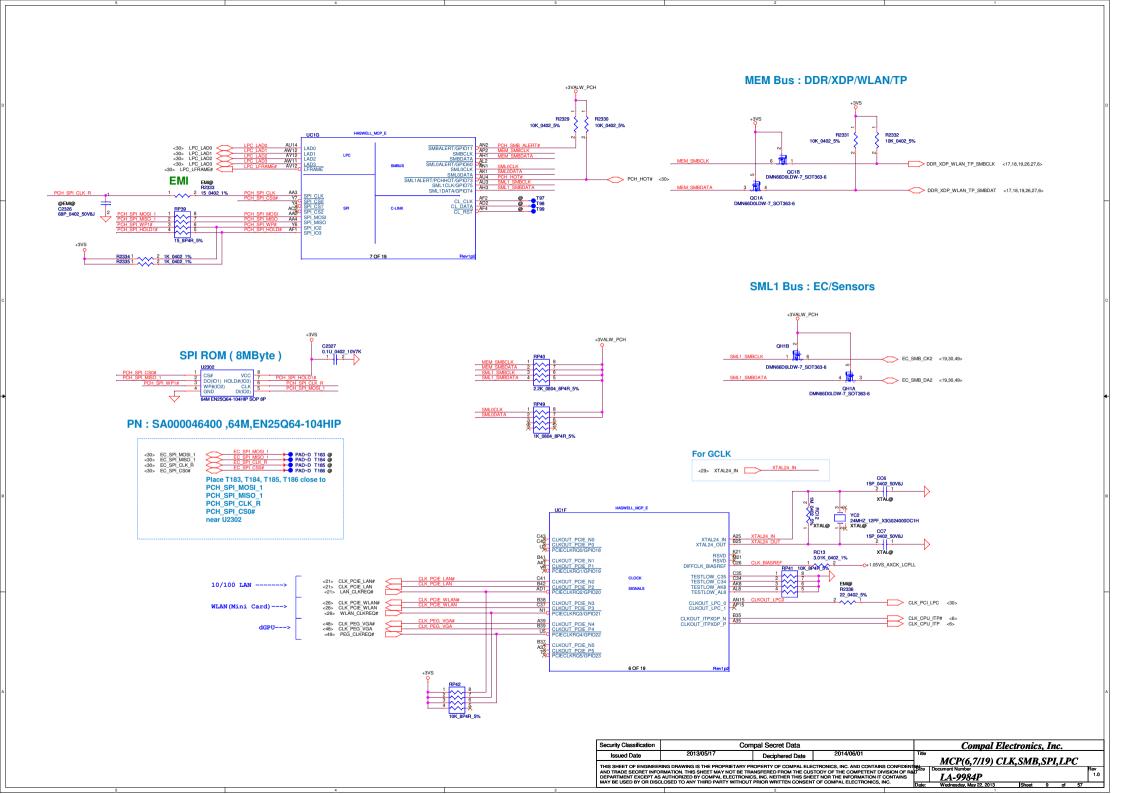
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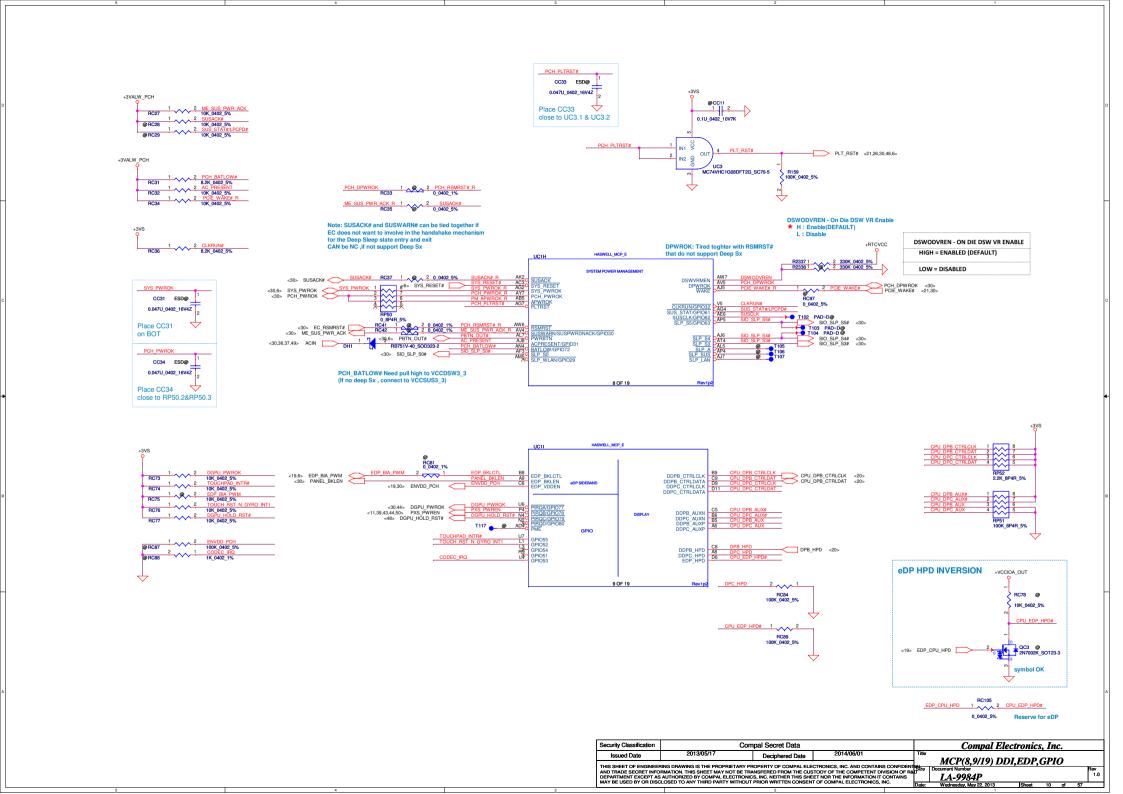
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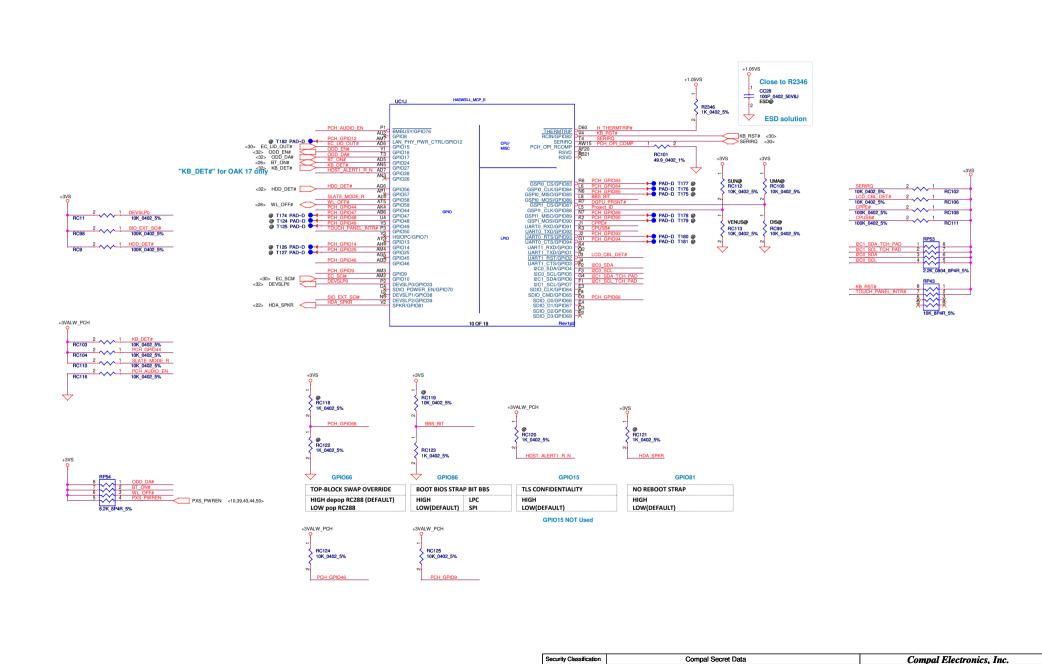
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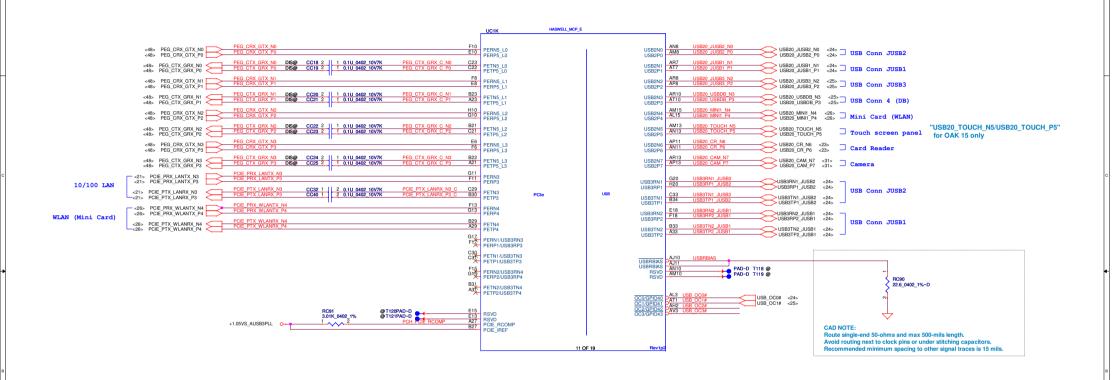
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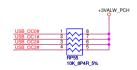
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LA-9984P

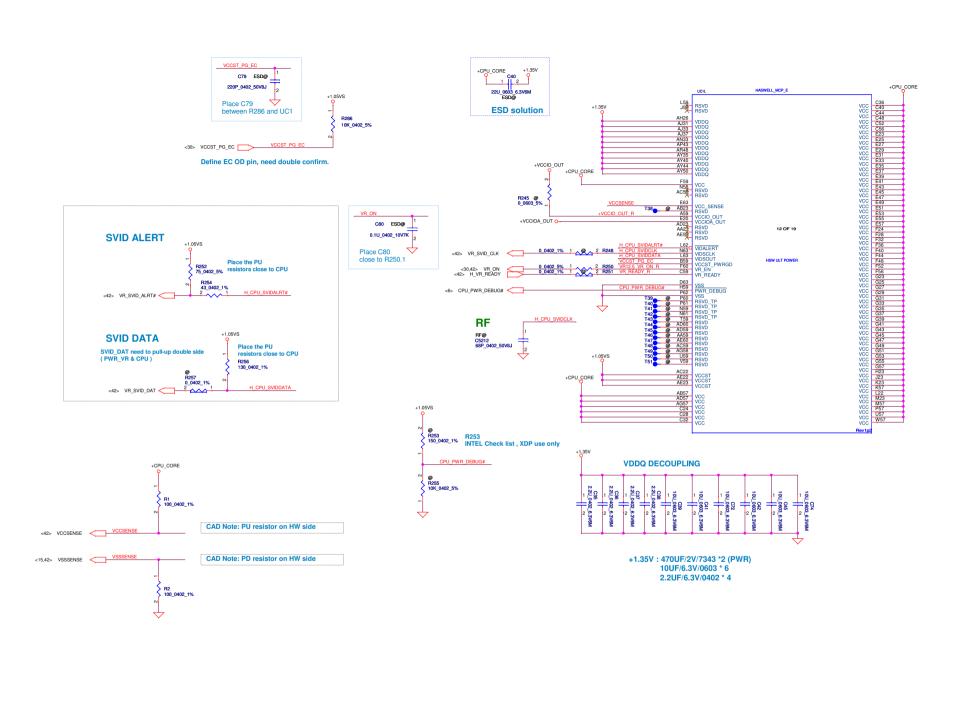
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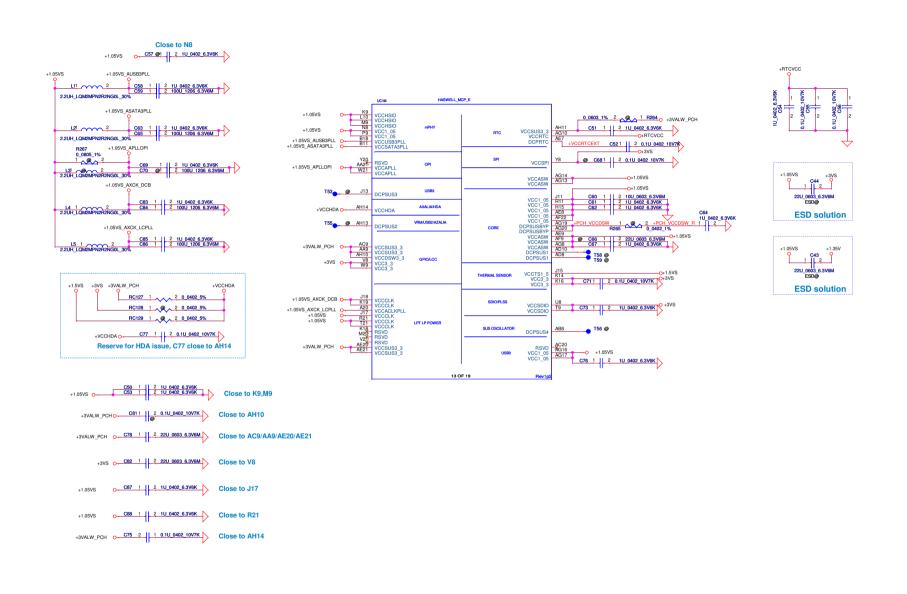




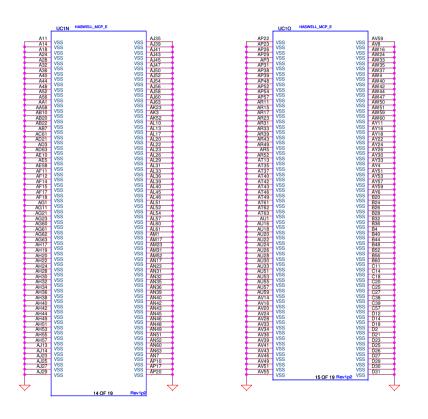
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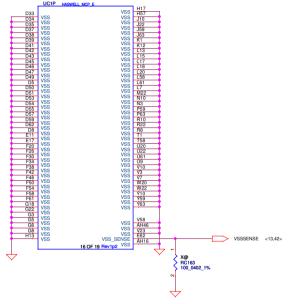


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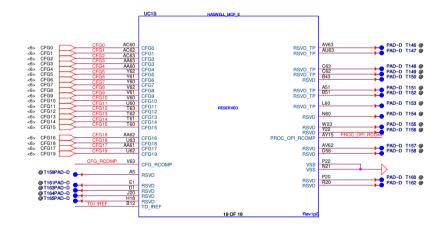


CAD Note: RC163 SHOULD BE PLACED CLOSE TO CPU

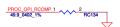
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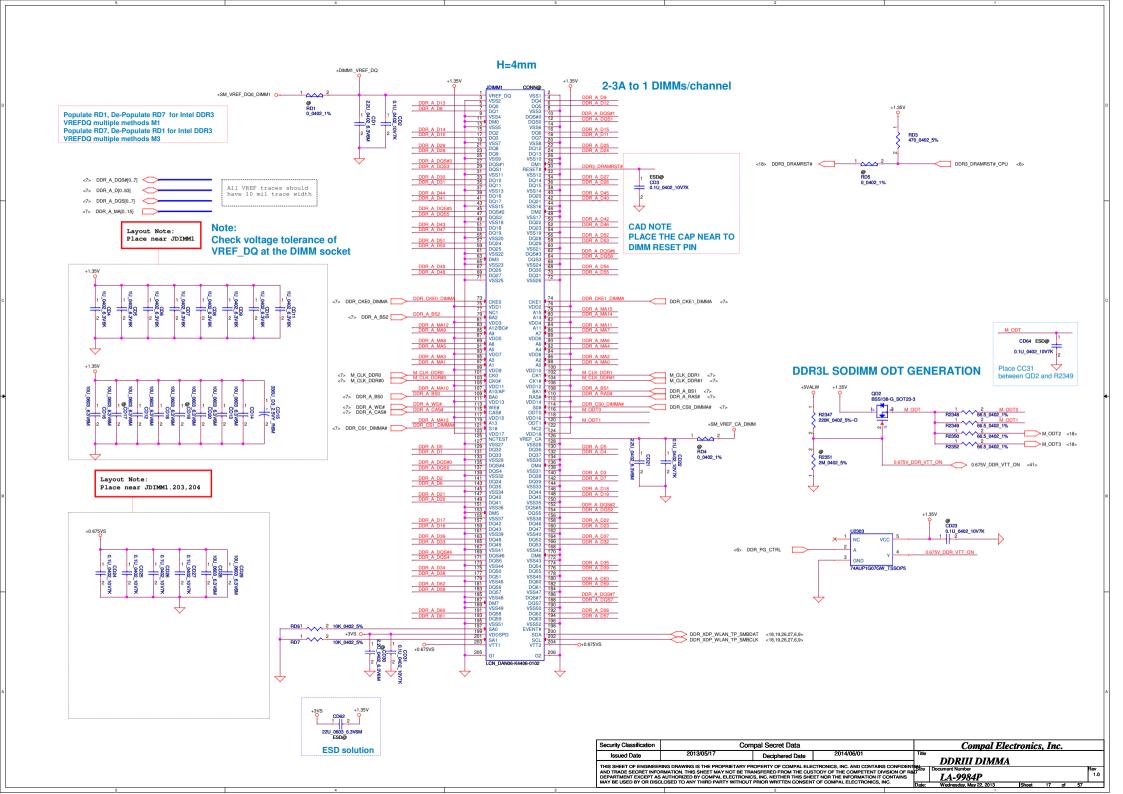


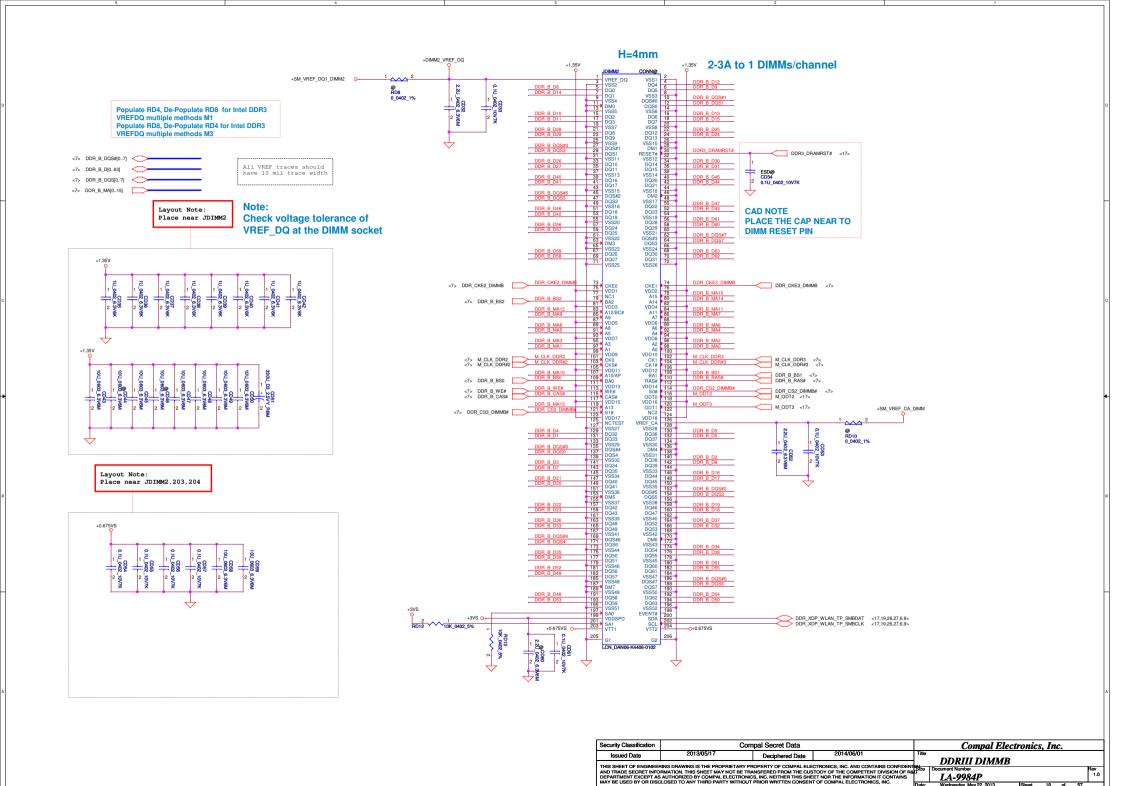
CFG STRAPS for CPU

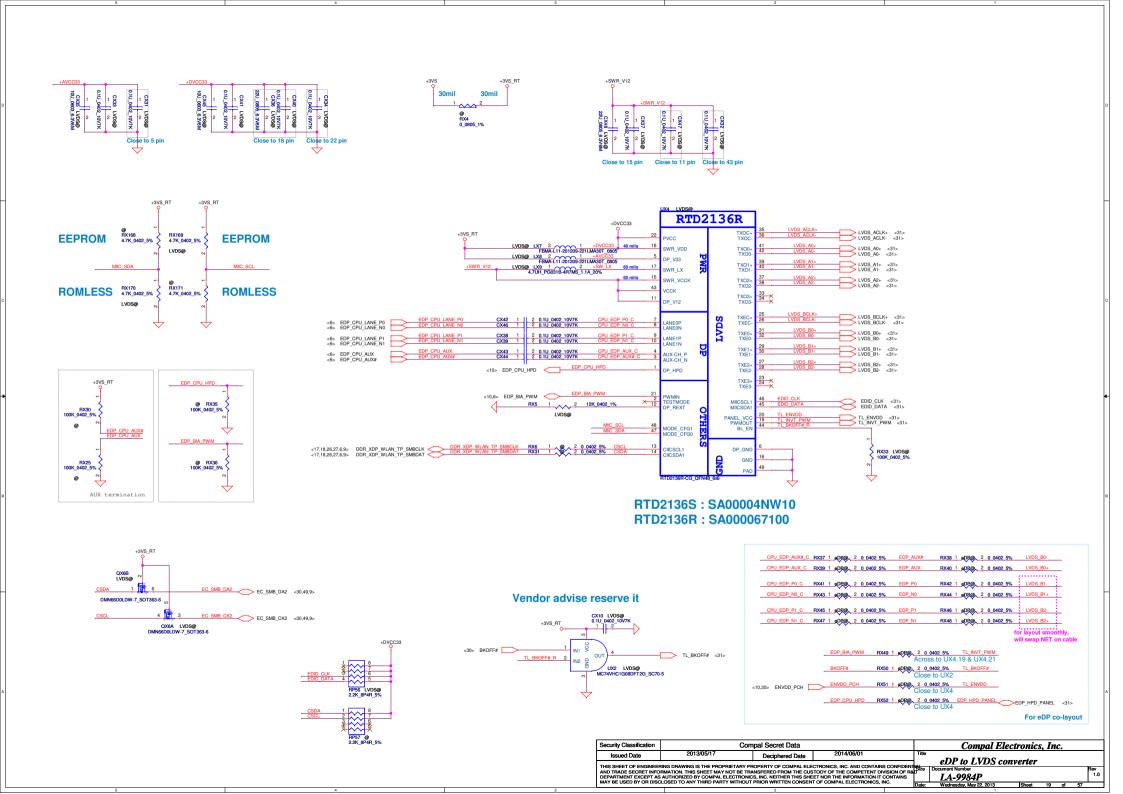


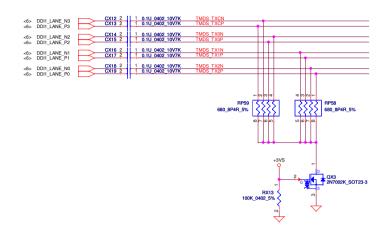
	Display Port Presence Strap
CFG4	1: Disabled; No Physical Display Port attached to Embedded Display Port
	0: Enabled; An external Display Port device is connected to the Embedded Display Port

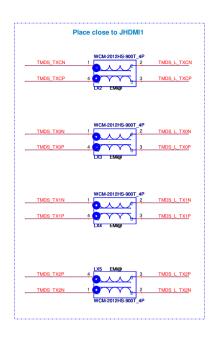
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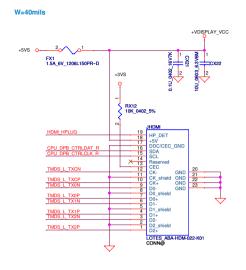


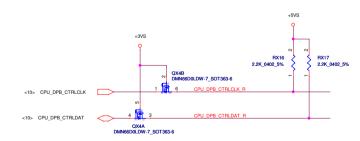


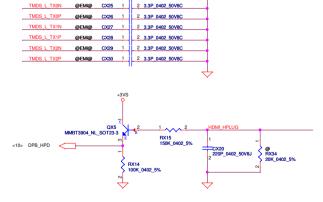








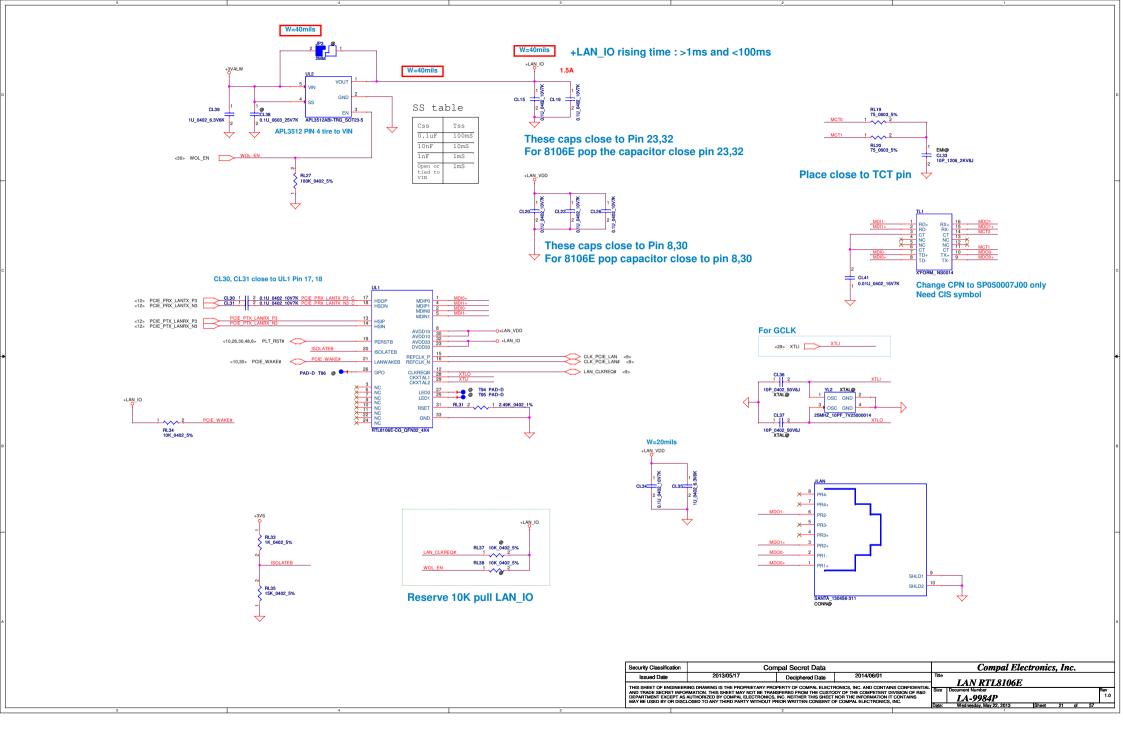


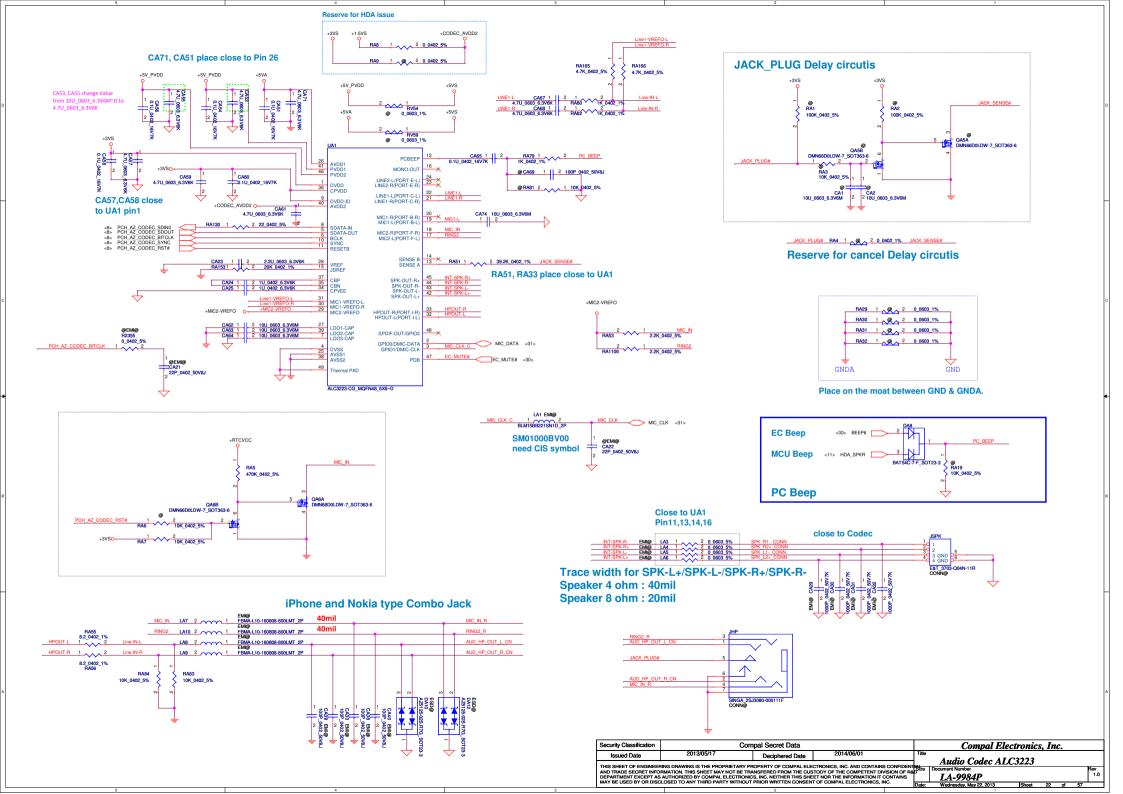


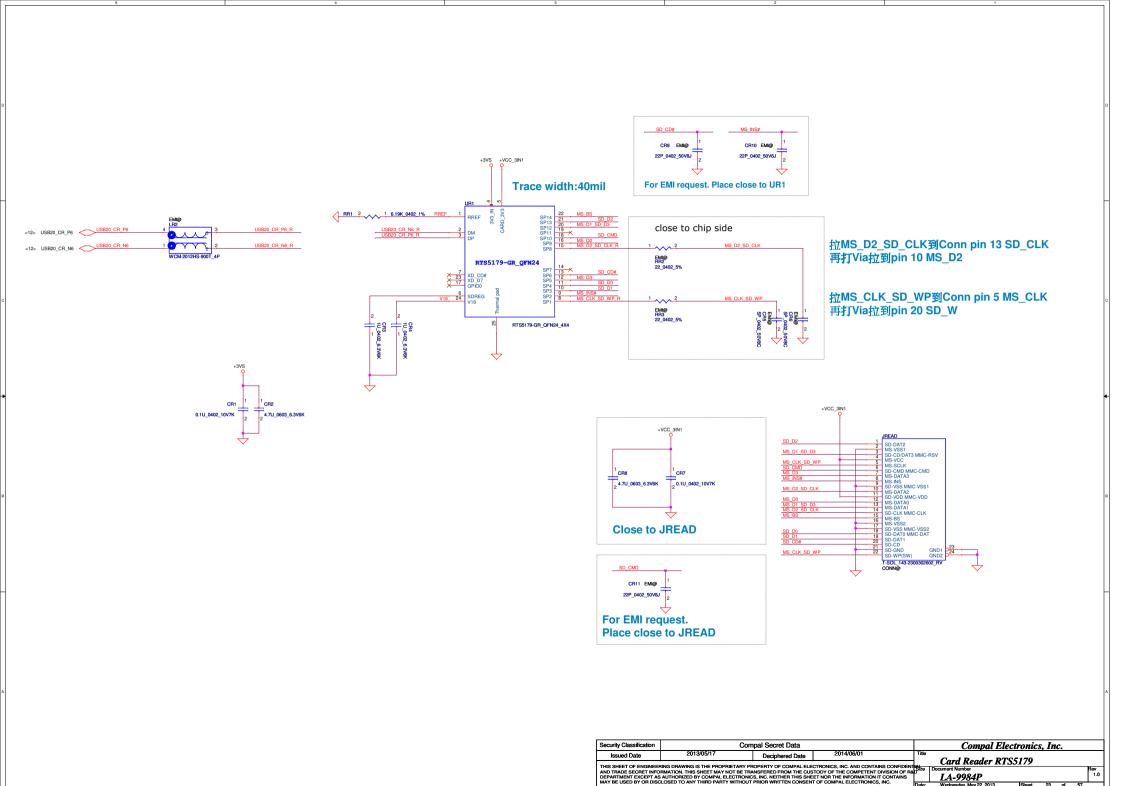
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 CX23
 1
 2 3.3P 0402 50V8C

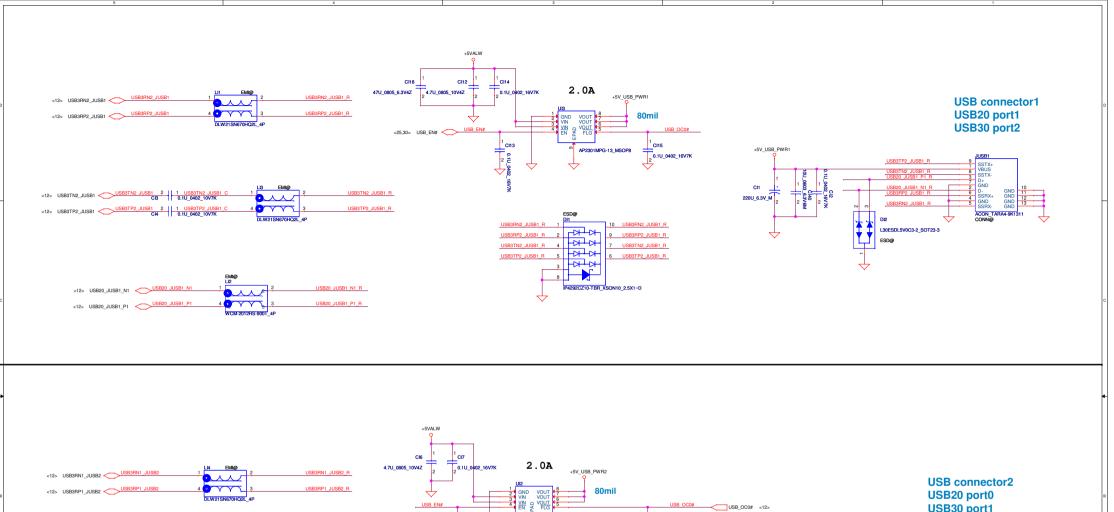
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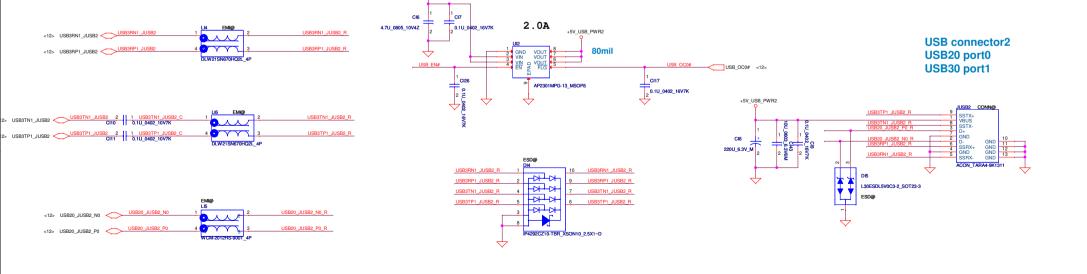
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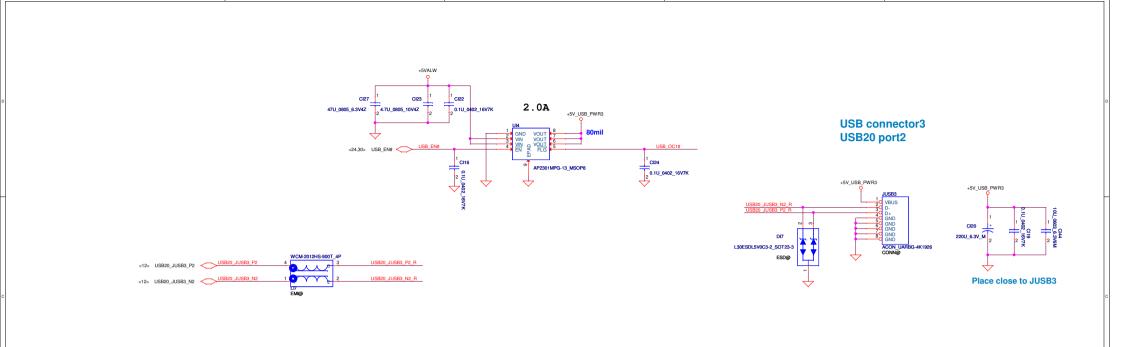


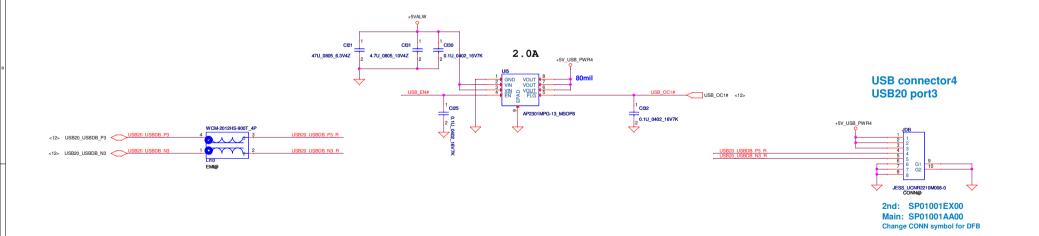




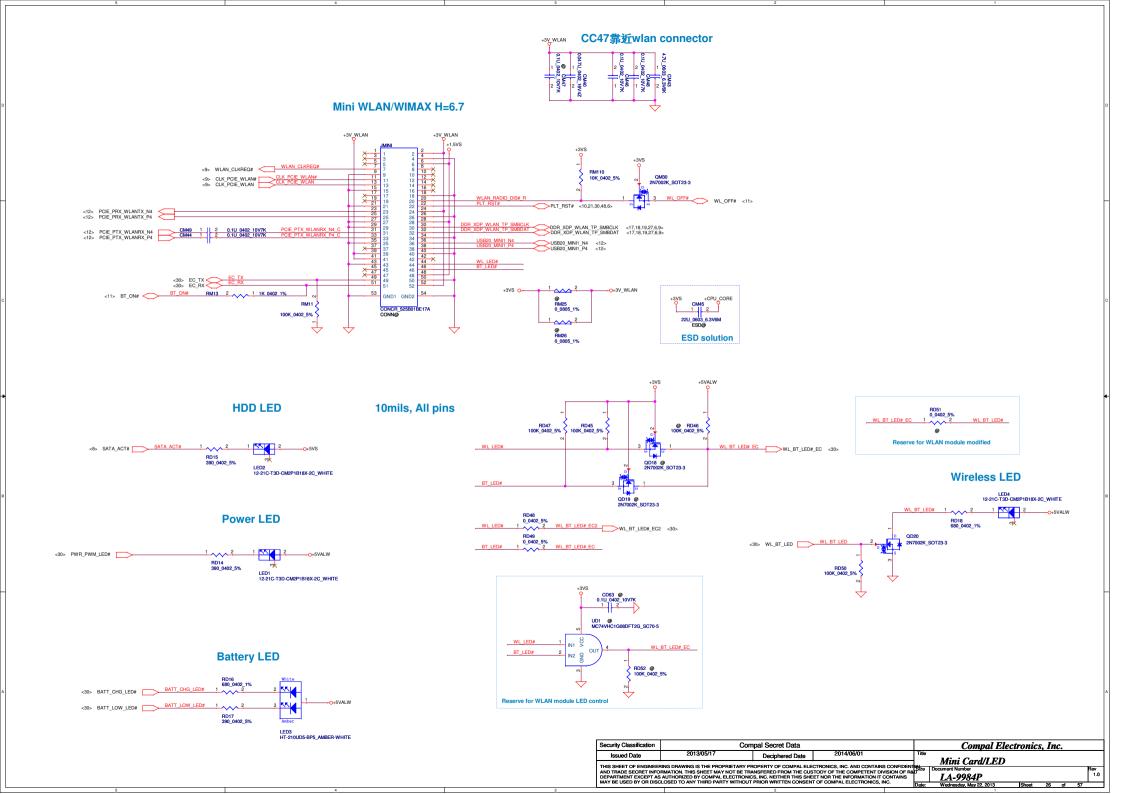


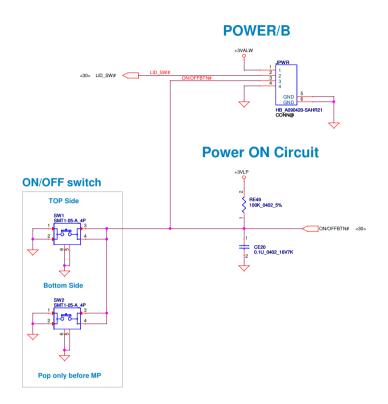
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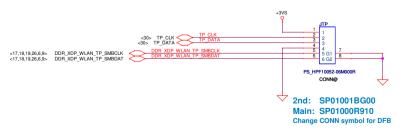


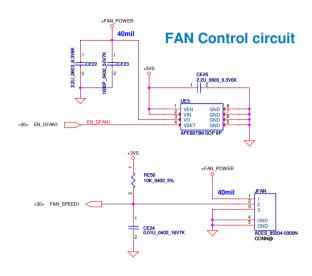
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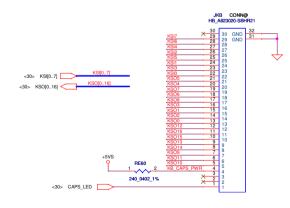


Touch pad



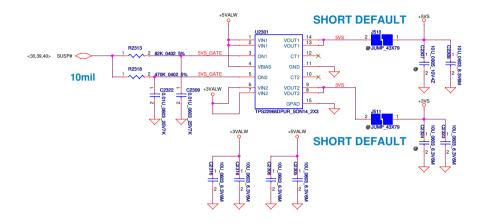


INT_KBD Connector

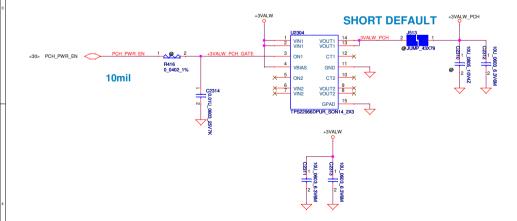


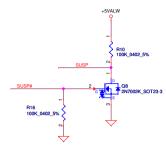
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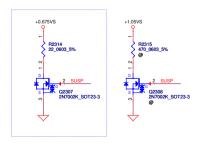
+5VS and +3VS switch



+3VALW_PCH switch

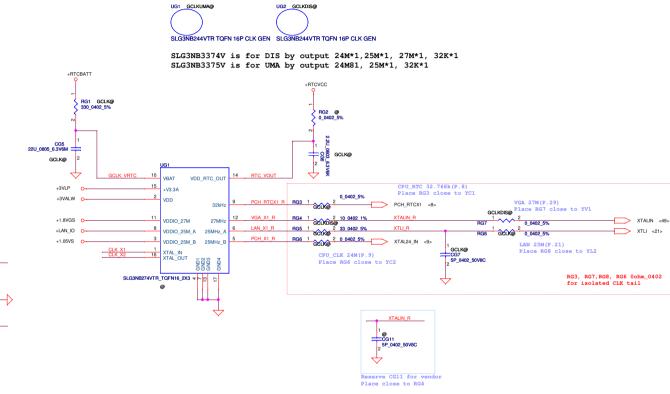


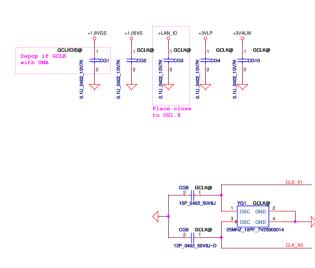


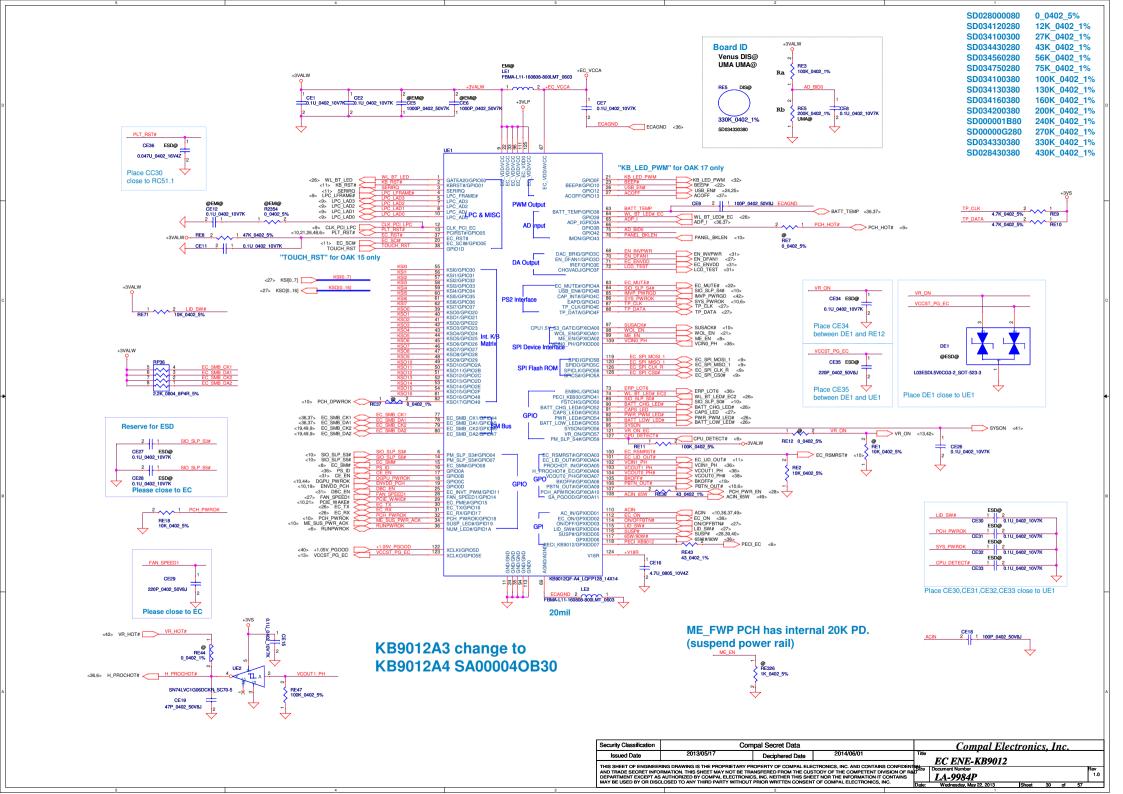


For Intel S3 Power Reduction

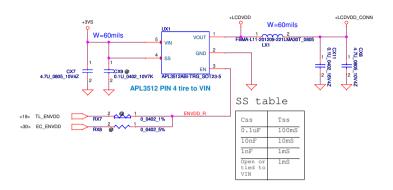
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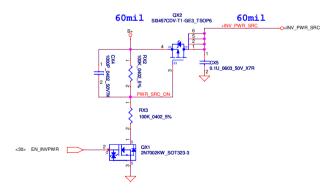




LCD PWR CTRL



LCD backlight PWR CTRL

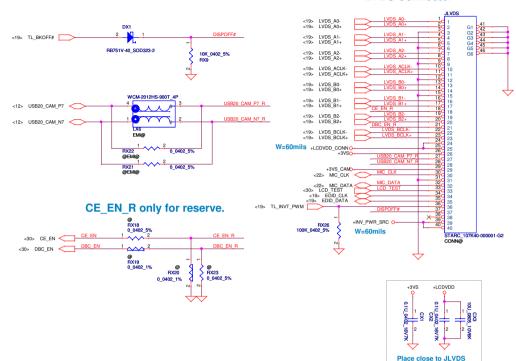


Webcam PWR CTRL

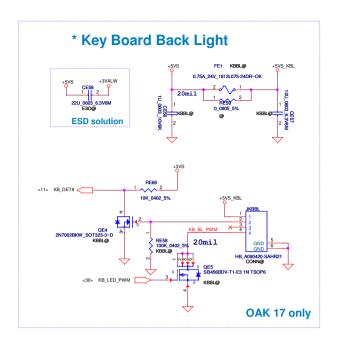




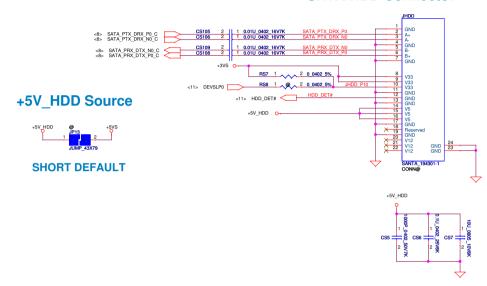
LVDS Connector



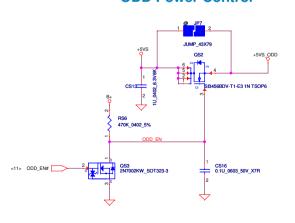
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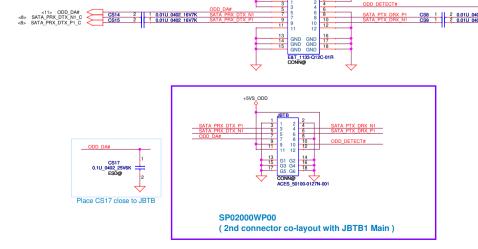


SATA HDD Connector

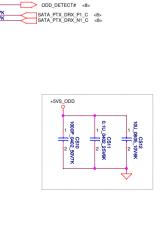




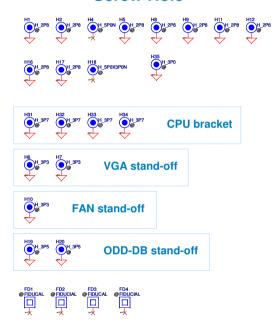


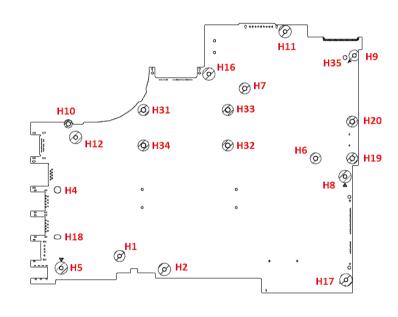


ODD BTB Connector



Screw Hole

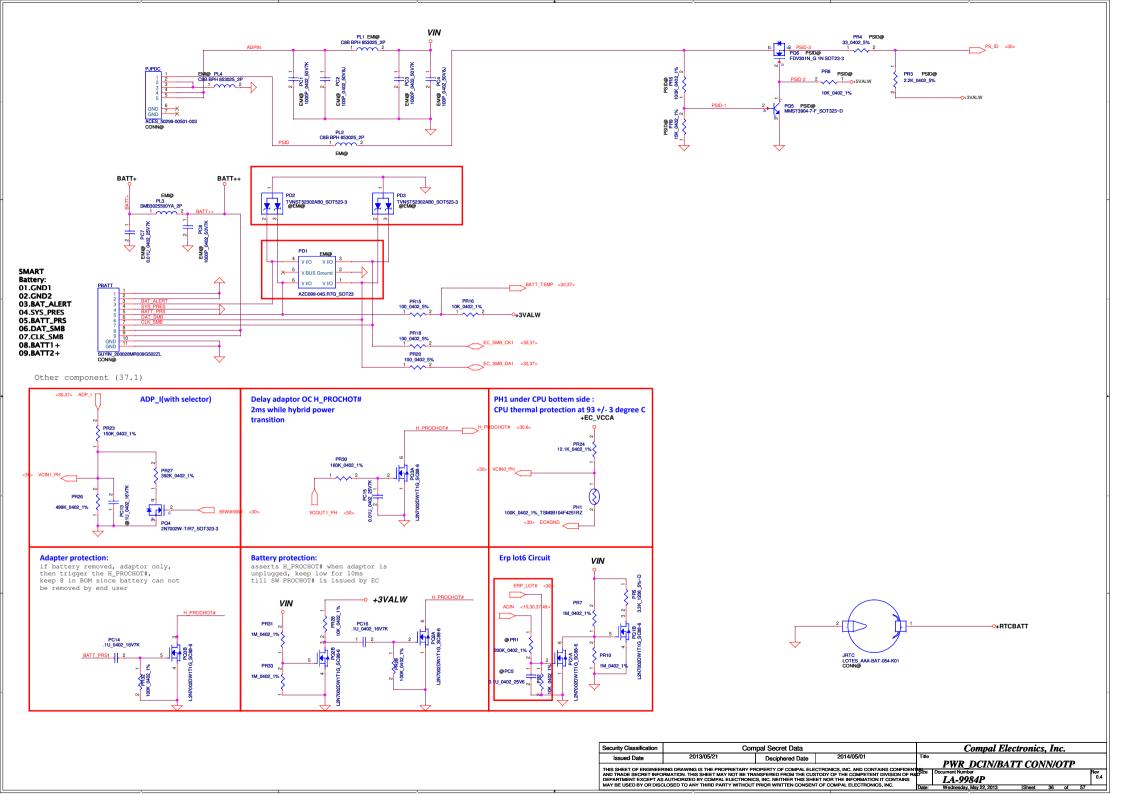


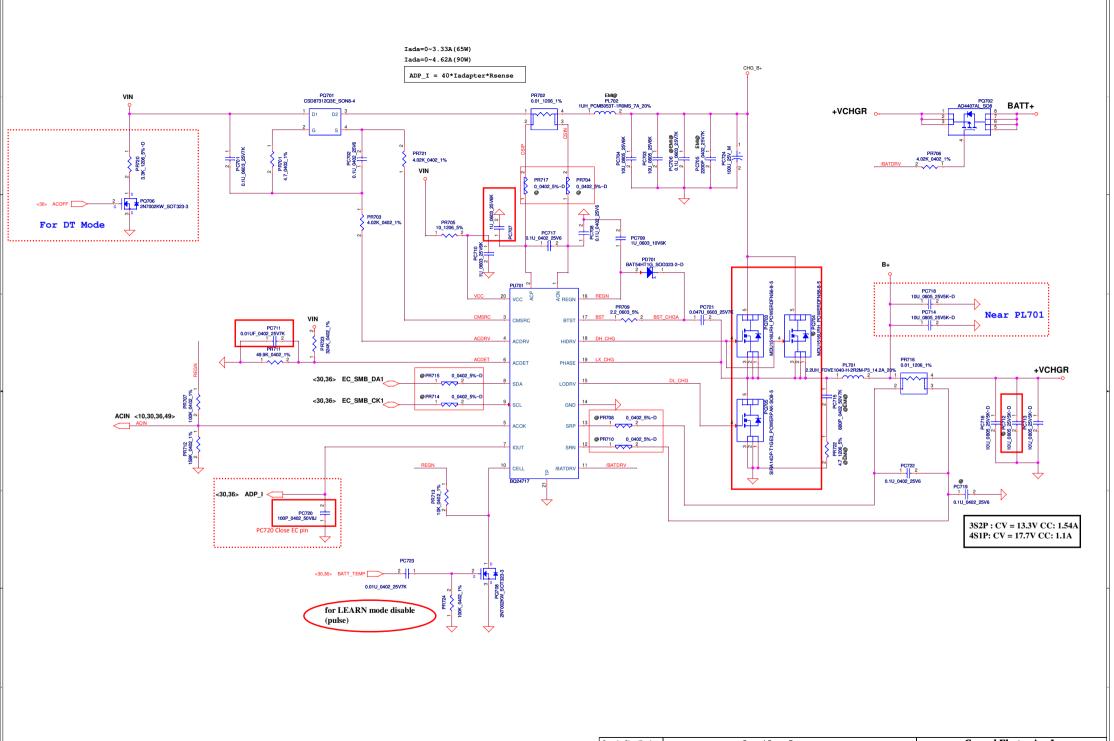


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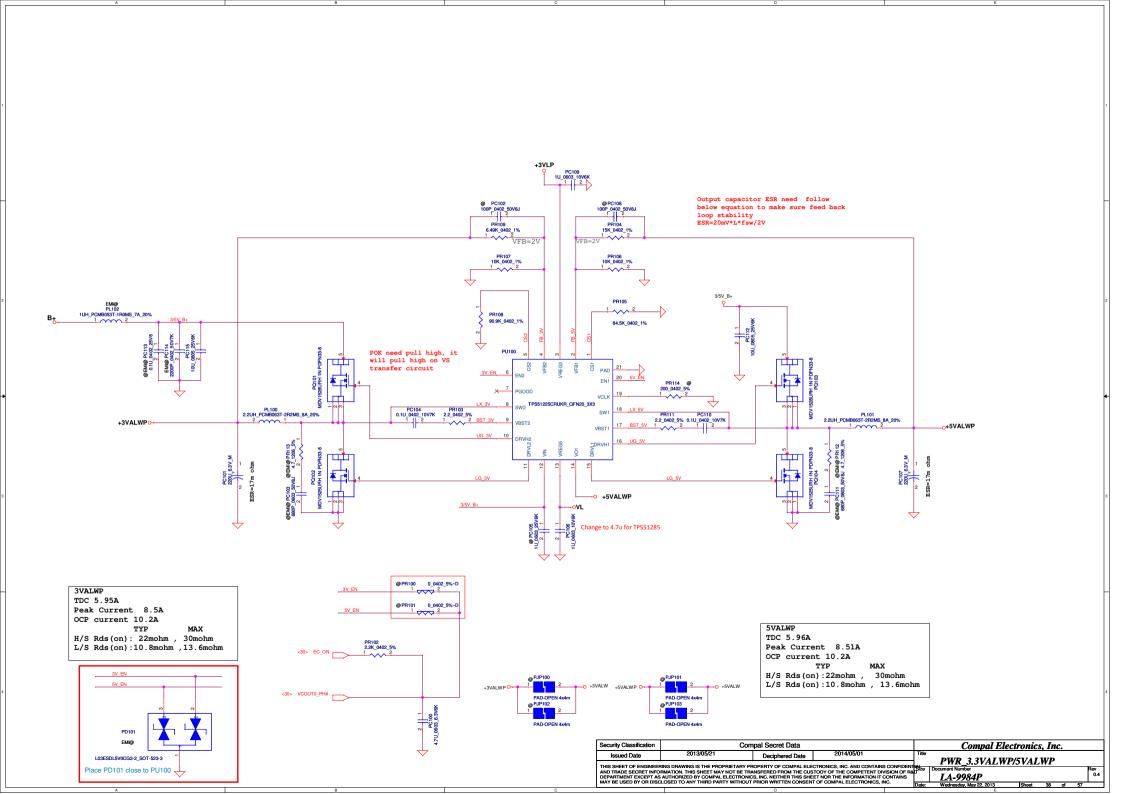
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Item	Page#	Title	Date	Request Owner	Issue Description		Solution Description		Rev.
1	34	Card Reader	2012/04/27	HW	The Card reader USB signal is incorrect.	SWAP UR1 USB signal 1	P/N		0.2
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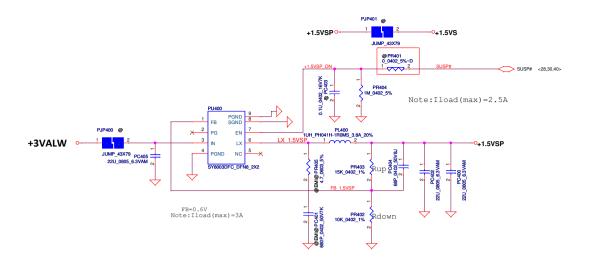
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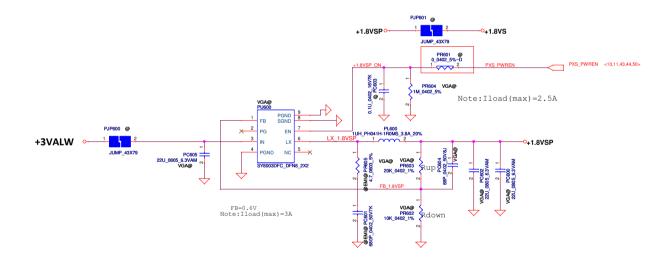




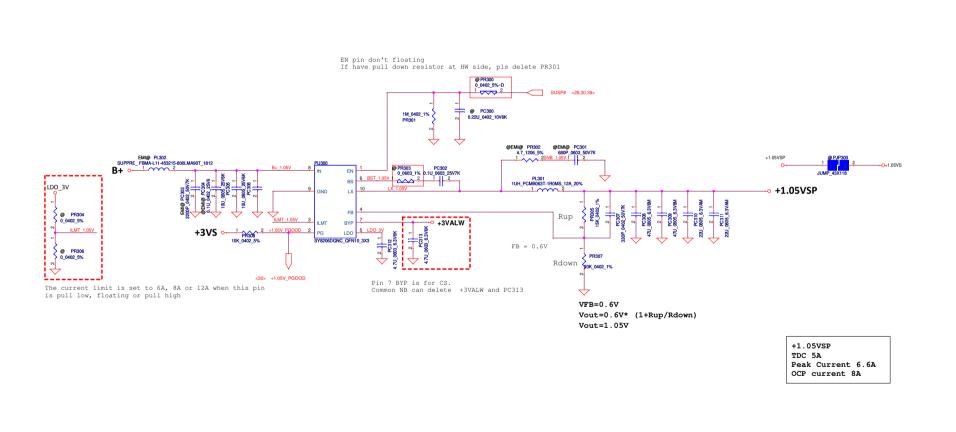
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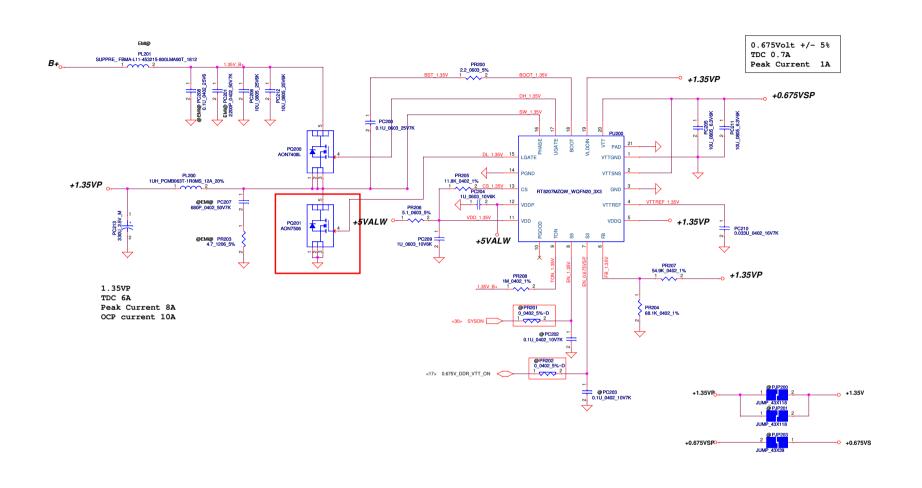
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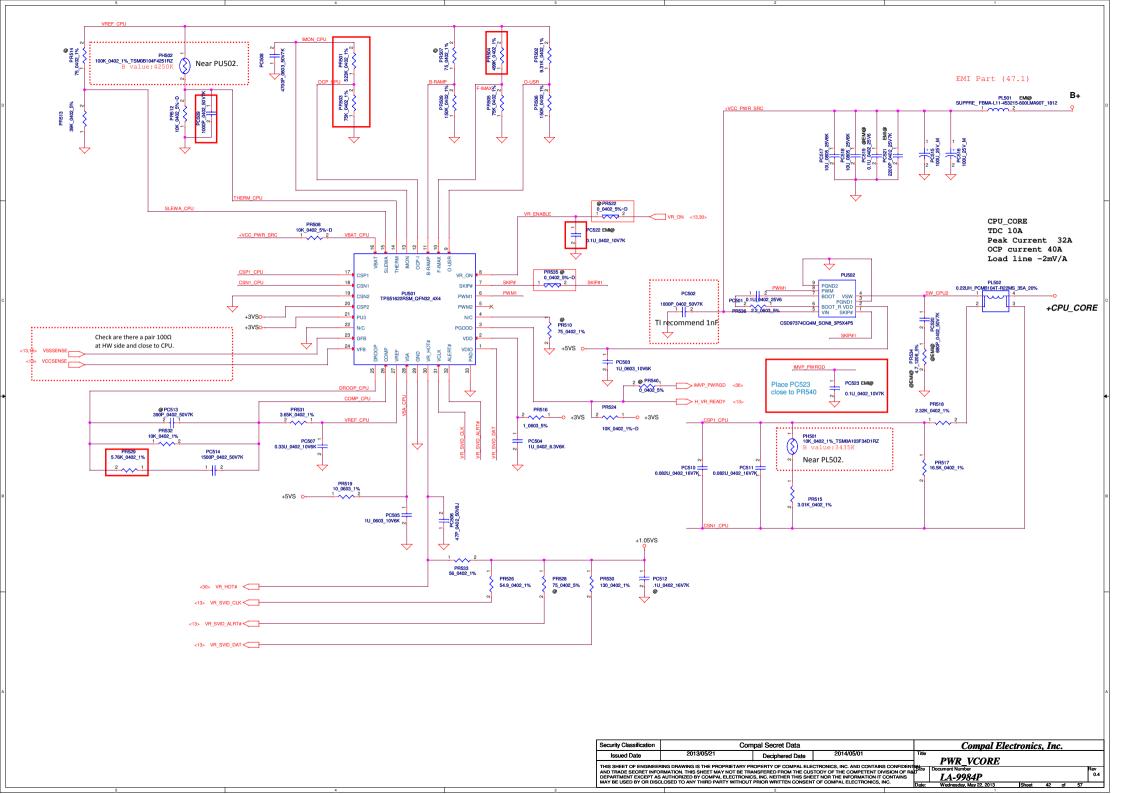
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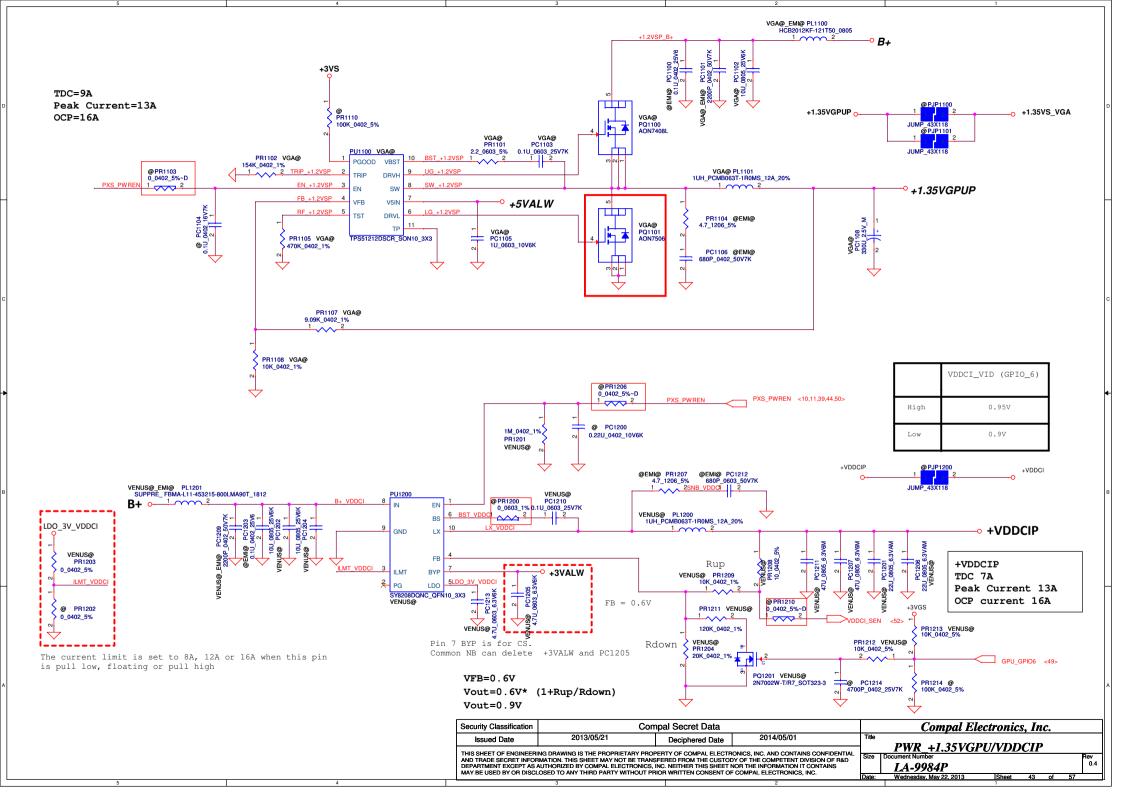
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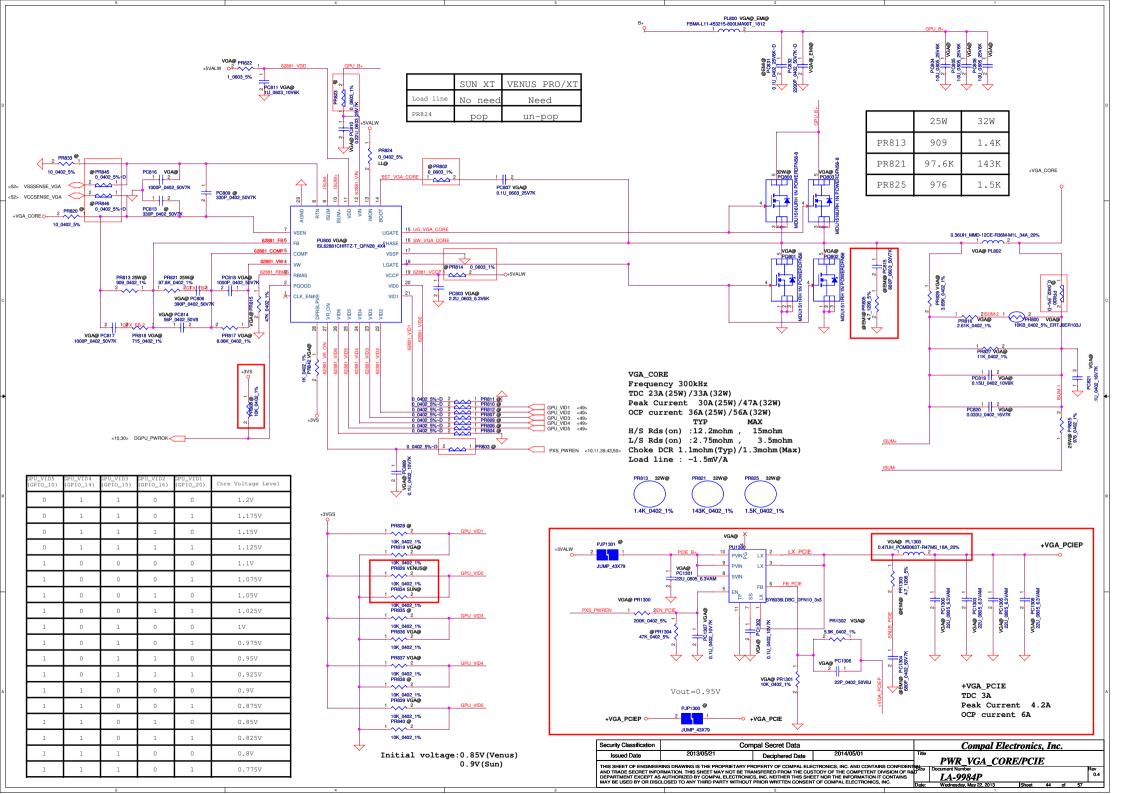
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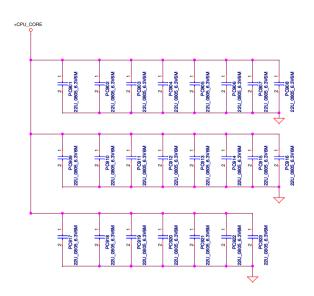


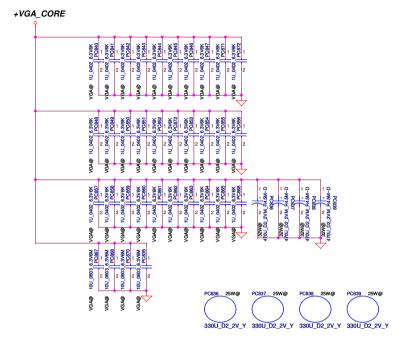
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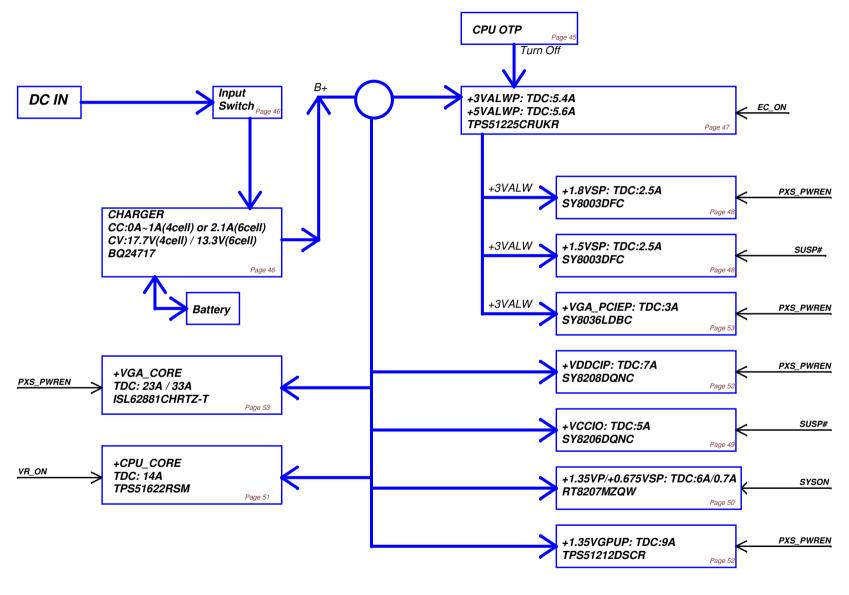






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Power block



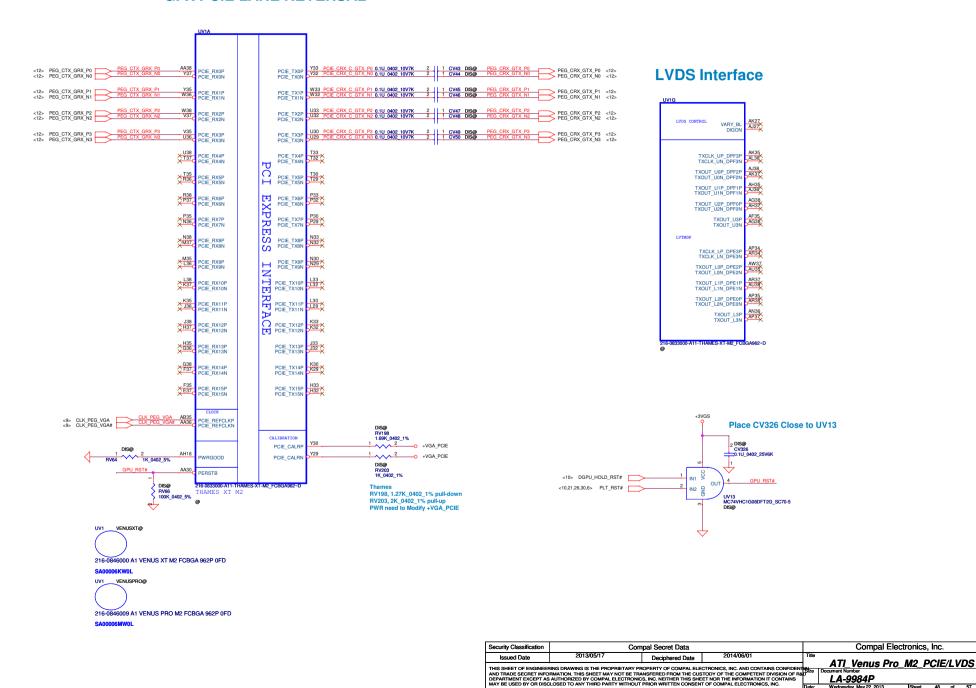
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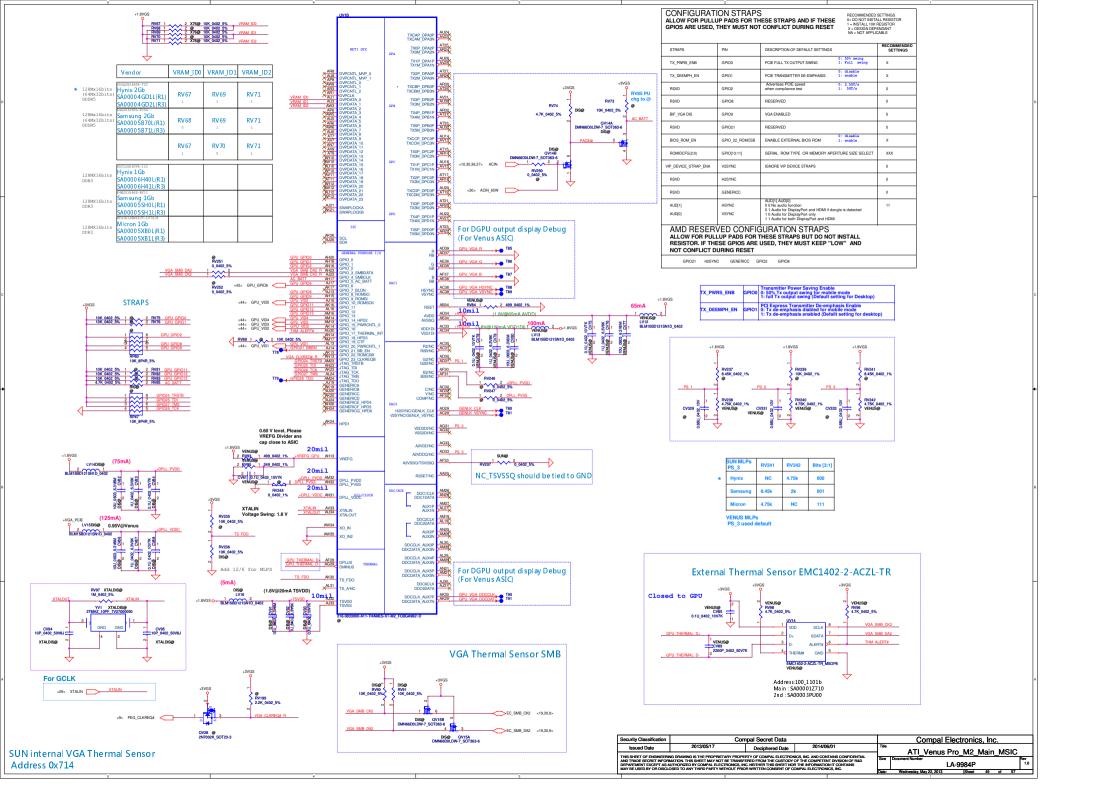
Version Change List (P. I. R. List) Page 1

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	37	CHARGER	13/01/30	Morris	adjust design parameter from vendor recommend	delete PD702 change PC712 to unpop change PC707 from 0.1uF_0402 to 1uF_0603 change PC707 from 0.1uF to 100pF change PC720 from 0.1uF to 100pF change PC720 from 0.00pF to 0.01uF change PC711 from 1000pF to 0.01uF change PQ705 from SB00000SD00 to SB00000WY00	0.3
2	42	VCORE	13/01/30	Morris	adjust design parameter from vendor recommend	change PC509 from 0.1uF to 1000pF change PR529 from 3.83K to 5.76K change PR504 from 523K to 499K	0.3
3	36	DCIN/BATT CONN/OTP	13/01/30	Morris	change from ESD request	change PD1 from SC300002E00 to SC300001G00	0.3
4	38	3.3VALWP/5VALWP	13/02/01	Morris	add ESD diode from ESD request	add PD101(SCA00002A00)	0.3
5	42	VCORE	13/02/21	Morris	adjust design parameter from fine tune result	change PR501 from 422K to 523K change PR503 from 56K to 75K	0.3
6	44	VGA_CORE/PCIE	13/02/21	Morris	unpop from EE request	unpop PR808	0.3
7	44	VGA_CORE/PCIE	13/03/05	Morris	adjust output voltage from vender request	unpop PR826 and pop PR834 (only for Sun XT)	0.3
8	37 38 39 40 41 42	CHARGER 3.3VALWP/5VALWP 1.5VSP/1.8VSP +VCCIO +1.35VP/0.675VSP VCORE	13/03/28	Morris	verify function ok, so delete 0 ohm to short	unpop PR100, PR101, PR201, PR202, PR300, PR303, PR401, PR522, PR535, PR704, PR708, PR710, PR714, PR715, PR717	0.4
9	36	DCIN/BATT CONN/OTP	13/04/09	Morris	design change for solve issue	unpop PR1 and PC5	0.4
10	41 43	+1.35VP/0.675VSP +1.35VGPU/VDDCI	13/04/09	Morris	part shortage issue	change PQ201 and PQ1101 from SB00000T600 to SB000010A00	0.4
11	39 43 44	1.5VSP/1.8VSP +1.35VGPU/VDDCI VGA_CORE/PCIE	13/04/09	Morris	verify function ok, so delete 0 ohm to short	unpop PR601,PR802,PR803,PR814,PR823,PR830,PR845,PR846, PR1103,PR1200,PR1206,PR1210	0.4
12	43	+1.35VGPU/VDDCI	13/04/09	Morris	unpop VDDCI parts from vendor recommend and EEverify ok only for Sun XT	unpop PL1200, PL1201, PU1200, PQ1201, PR1201, PR1203, PR1204, PR1208, PR1209, PR1211, PR1212, PR1213, PC1201, PC1202, PC1204, PC1205, PC1206, PC1207, PC1209, PC1210, PC1211, PC1213 (only for Sun XT)	0.4
13	44	VGA_CORE/PCIE	13/04/12	Morris	part shortage issue	change PL1300 from SH00000GQ00 to SH00000PK00	0.4
14	36	DCIN/BATT CONN/OTP	13/04/12	Morris	customer request	add PR2 10Kohm	0.4
15	42	VCORE	13/04/15	Morris	EMI request	pop PC522 and add PC523 0.1uF	0.4

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GFX PCIE LANE REVERSAL





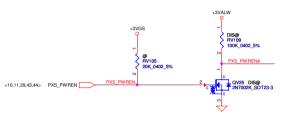
PX_MODE=1 for Normal Operation
PX_MODE=0 for BACO mode to shut down power rails expcept VDDR3,PCIE_VDDC and 1.8V rail

Note:

PX4.0 +VGA_CORE, VDDCI, +1.5VGS ON PX4.0 +3VGS, +1.0VGS, +1.8VGS OFF

+1.35V_MEM_GFX

PX5.0 +3VGS,+VGA_CORE,VDDCI,+1.5VGV,+1.0VGS,+1.8VGS OFF

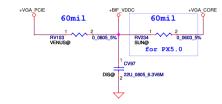


for PX4.0 and PX5.0

Power seguence of Sun XT, Venus Pro, Venus XT +3VGS +VGA_CORE +VDDCI(+VGA_PCIE)

+1.0VGS +1.8VGS <20ms

Switch circuits in BACO desingns for Thames/Seymour only 55mA@1.0V, in BACO mode



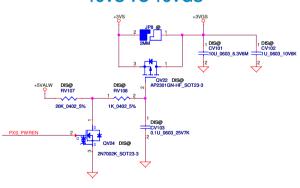
+1.8VS TO +1.8VGS



+1.35VS_VGA TO +1.35V_MEM_GFX



+3VS TO +3VGS



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