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Scala3-14CRV

CPU : IVY Bridge PGA

Chip Set : Intel Panther Point

GFX : NVIDIA N13M-GS/GE

Remarks : Chief River Platform

Model Name : Scala3-14CRV

PBA Name : MAIN Dual

PCB Code : BA41-02092A(GCE)

BA41-02093A(HST) BA41-02094A(PTC)

BA41-02098A (GBM)

Dev. Step : MP

Revision : 1.1

T.R. Date : 2012.06.14

Design	CHECK	APPROVAL
Tao Xiaofeng	Chu XF	BL LEE

■ Owner: SEC Mobile R & D Signature:

sheet01 : Cover sheet27 : LVDS/CAMERA sheet02 : BLOCK DIAGRAM sheet28 : CRT

sheet03: BOARD INFORMATION sheet29: HDMI

sheet04: POWER DIAGRAM sheet30: Audio - ALC269 sheet05: CLOCK DISTRIBUTION sheet31: Audio - HP/MIC

sheet06 : Green Clock sheet32 : LAN (Realtek RTL8111E) sheet07 : Thermal Sensor sheet33 : 4-in-1 card (AU6437)

sheet08 : CPU sheet34 : WLAN

sheet09 : CPU sheet35 : SATA interface sheet10 : CPU sheet36 : MICOM / BIOS

sheet11 : CPU sheet37 : MICOM Glue Logic (KBD / TP)

sheet12: DDR3 SODIMM #0 sheet38: LED for 14" & for 15" sheet13: DDR3 SODIMM #1 sheet39: USB connector sheet40: Power - charger

sheet14: PCH-CougarPoint sheet15: PCH-CougarPoint sheet16: PCH-CougarPoint sheet16: PCH-CougarPoint sheet40: Power - charger sheet41: Power - P3.3V_AUX / P5.0V_AUX sheet42: Power - P1.05V (Chipset Power)

sheet17: PCH-CougarPoint sheet43: Power - P0.8V (Chipset Power)

sheet18: PCH-CougarPoint sheet44: Power - DDR3 sheet19: N12P-GVR sheet45: Power - CPU VRM sheet20: N12P-GVR sheet46: Power - EGFX CORE

sheet21: N12P-GVR sheet47: Power - Switched Power 1 sheet22: N12P-GVR sheet48: Power - Switched Power 2 sheet23: N12P-GVR sheet49: Power - Power Discharge sheet24: GFX memory sheet50: Mount Hole / Thermal Hole

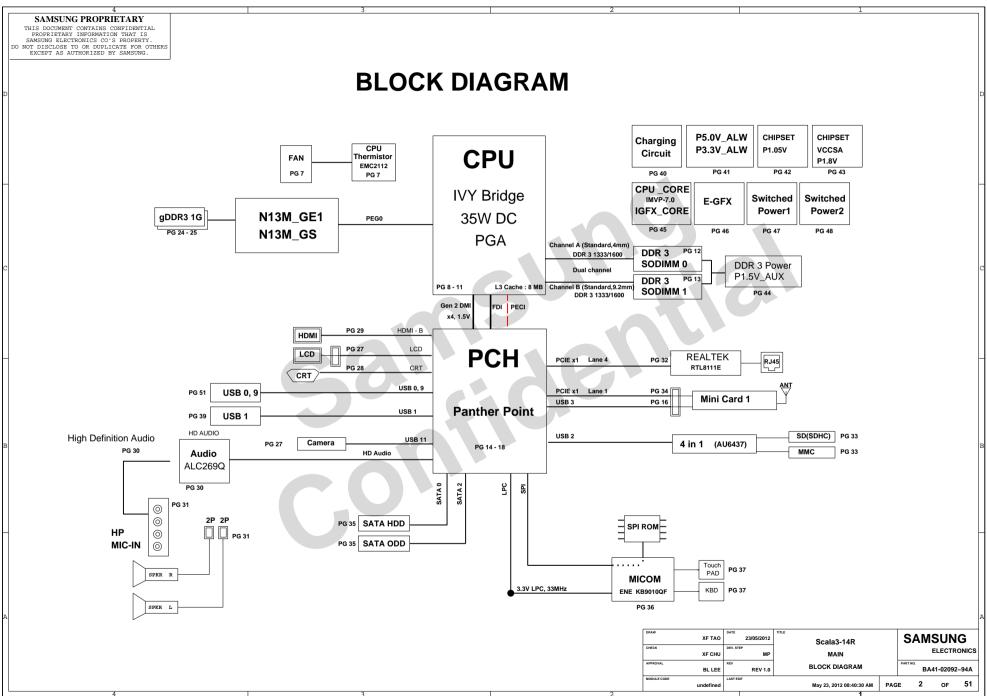
sheet25 : GFX memory sheet51 : USB Sub B'D

sheet26: STRAP

DRAW	XF TAO	11/06/2012	Scala3-14R		SA	١M	SUN	G
CHECK	XF CHU	DEV. STEP MP	MAIN				ELECTR	ONICS
APPROVAL	BL LEE	REV 1.1	COVER		PART NO.	ВА	41-02092	2~94A
MODULE CODE		LAST EDIT	May 23, 2012 08:40:30 AM	PAG	E	1	OF	51

D/mentor/CPD9/Scala3_14R_ADV

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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

,	Voltage Rails	Active in
VDC P3.3V_MICOM P5.0V_STB P5.0V_ALW P3.3V_ALW P12.0V_ALW	Primary DC system power supply (7 to 21V) 3.3V always power rail (for Micom) 5.0V always power rail 5.0V always power rail 3.3V always power rail 12.0V always power rail	S4 - S5
P5.0V_AUX P3.3V_AUX P1.5V_AUX	5.0V switched on power rail (off in S4-S5) 3.3V switched on power rail (off in S4-S5) 1.5V power rail for DDR3 (off in S4-S5)	S3
P5.0V P3.3V P1.8V P1.5V P0.75V P0.8V	5.0V switched power rail (off in S3-S5) 3.3V switched power rail (off in S3-S5) 1.8V switched power rail (off in S3-S5) 1.5V switched power rail (off in S3-S5) 0.75V power rail for DDR3 (off in S3-S5) 0.8V switched power rail (off in S3-S5)	SO
VCC_CORE EGFX_CORE P1.05V (VCCP)	Core Voltage for CPU Core Voltage for GPU VCC for COUGARPOINT	SO
P3.3V_D P1.5V_D P1.05V_D	3.3V descrete power rail for N12X 1.5V descrete power rail for N12X 1.05V descrete power rail for N12X	

ι	JSB PORT Assign	PCI E	xpress Assign
PORT#	ASSIGNED TO	PORT#	ASSIGNED TO
0 1 2 3 4 5 6 7 8 9 10 11 12 13	SYSTEM PORT 0 SYSTEM PORT 1 Min IPCI Express Multi Memory Card Controller SYSTEM PORT 2 NC C C C C C C C C C C C C C C C C C C C	1 2 3 4 5 6 7 8	Mini Card 1 (WLAN) NC NC NC LAN CONTROLLER NC NC NC

SATA PORT Assign				
PORT #	ASSIGNED TO			
0	HDD			
1	NC			
2	ODD			
3	NC			
4	NC			
5	NC			

o. y o.c	ıl / Oscillator		
TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	HM65	Real Time Clock
Crystal	25MHz	LAN	Intel LAN
Crystal	25MHz	GREENCLK	GREENCLK
Crystal	25MHz	HM65	INTEL
Crystal	27MHz	N12P-GVR	NVIDIA

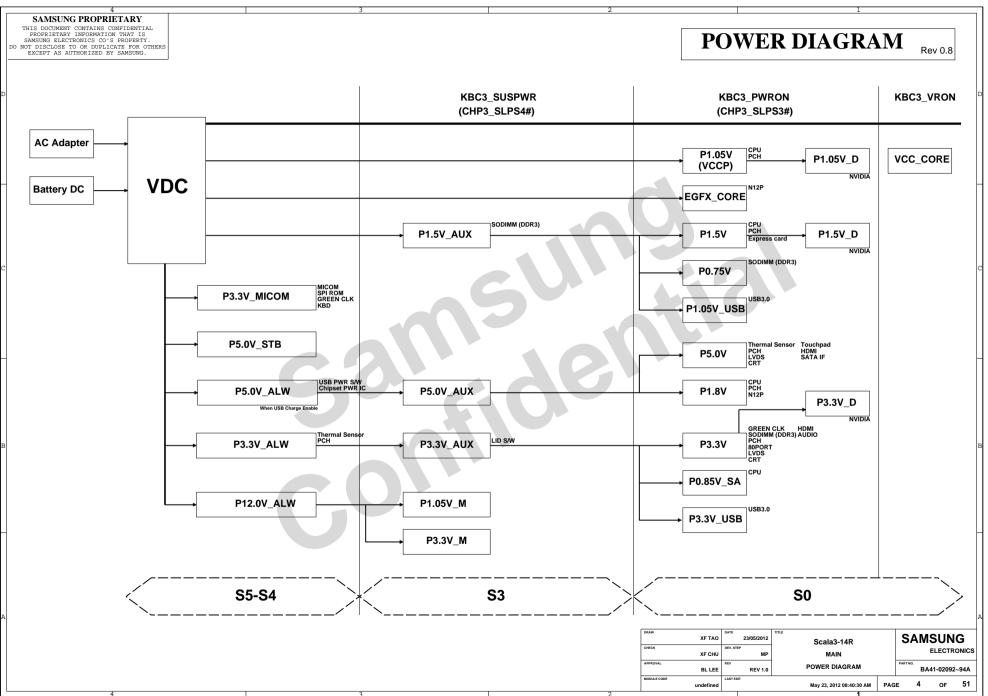
LCD Pannel Detect (TBD)							
Devices		Resolution	PANNEL_DETECT_	_0			

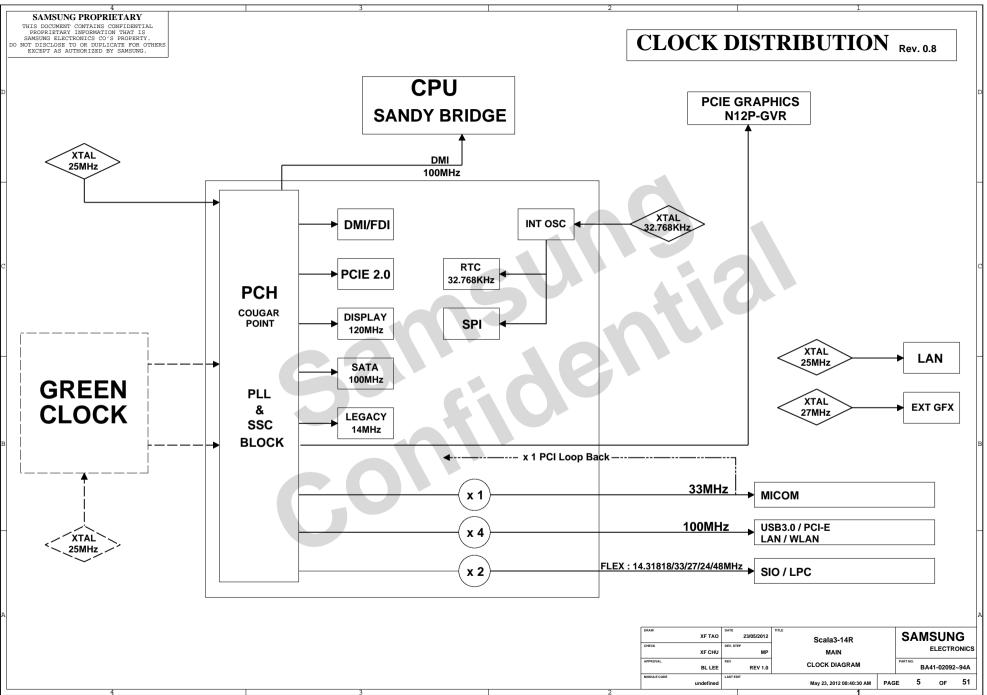
I ² C / SMB Address						
Devices	Address	Hex	Bus			
HM65	Master		SMBUS Master			
CPU Thermal Sensor SODIMMO SODIMMO SODIMMO Thermal Sensor on SODIMMO Thermal Sensor on SODIMMO Thermal Sensor on board Power thermal management TS	0111 101x 1010 000x 1010 010x 0011 000x 0011 010x 1101 100x 1101 101x	7Ah A0h A4h 30h 34h 98h 96h	Thermal Sensor			

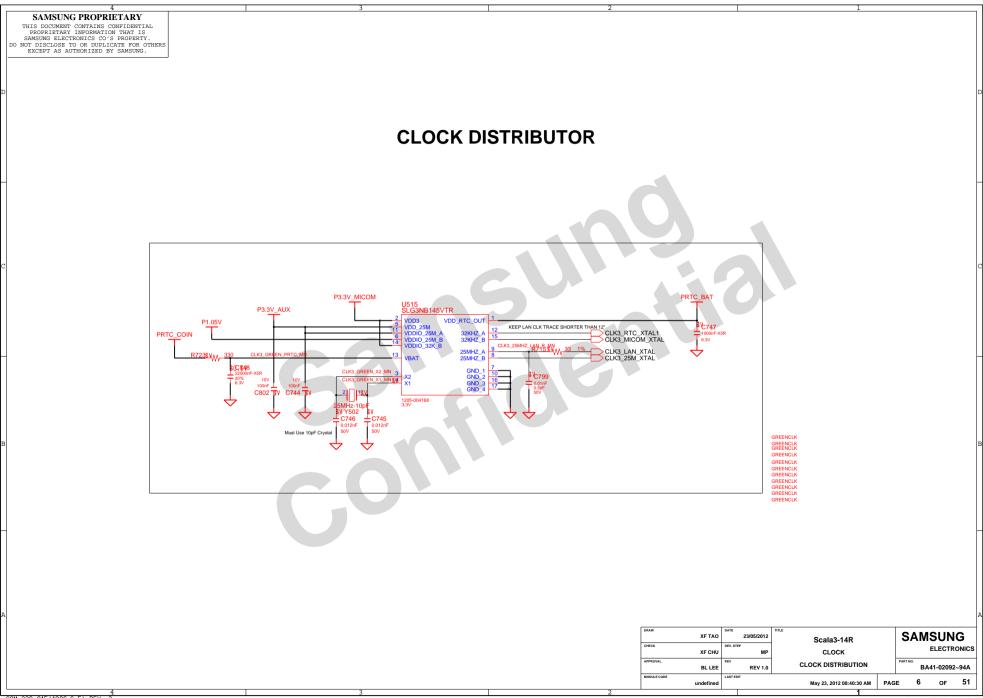
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CHECK	XF CHU	DEV. STEP MP	MAIN				ELECTR	ONICS
APPROVAL	BL LEE	REV 1.0	BOARD INFO		PART NO.	ВА	41-02092	2~94A
MODULE CODE	undefined	LAST EDIT	May 23, 2012 08:40:30 AM	PAG	E	3	OF	51

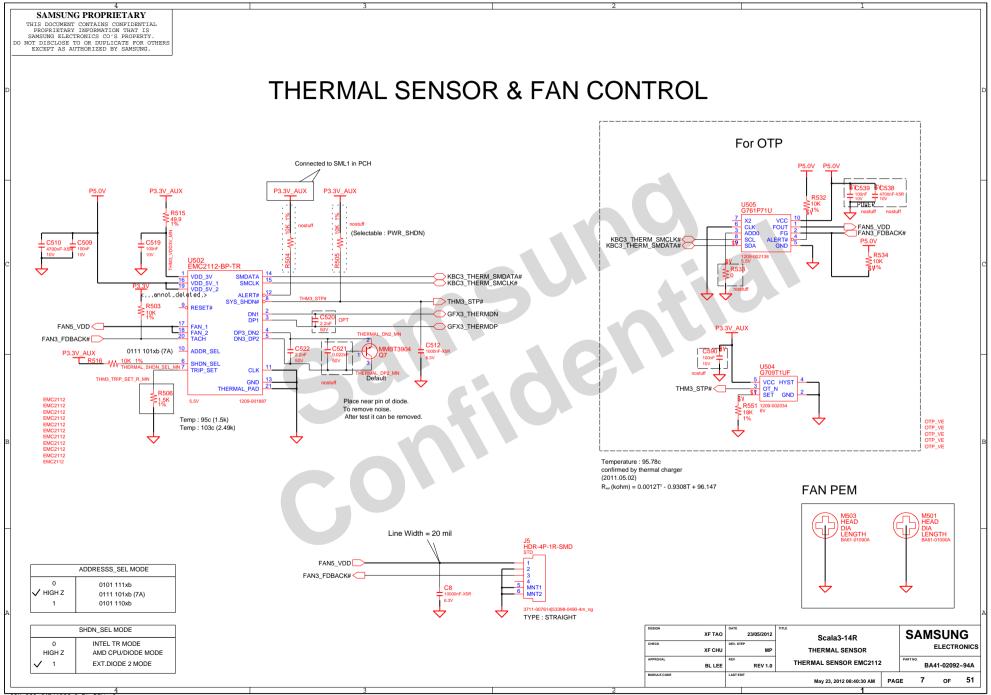
C0M-22C-015(1996.6.5) REV. 3

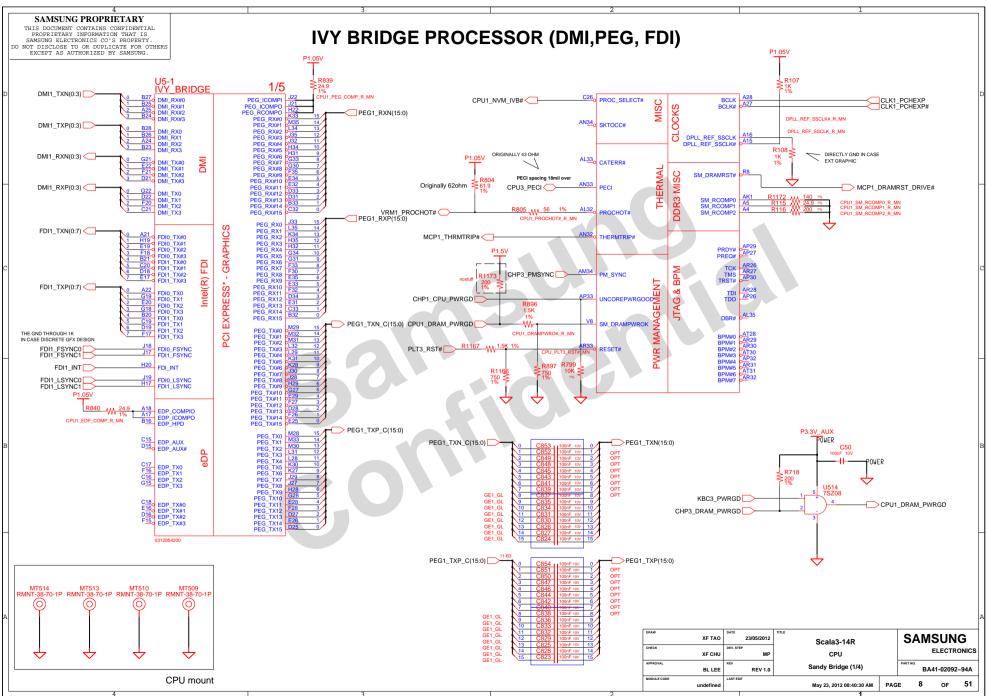
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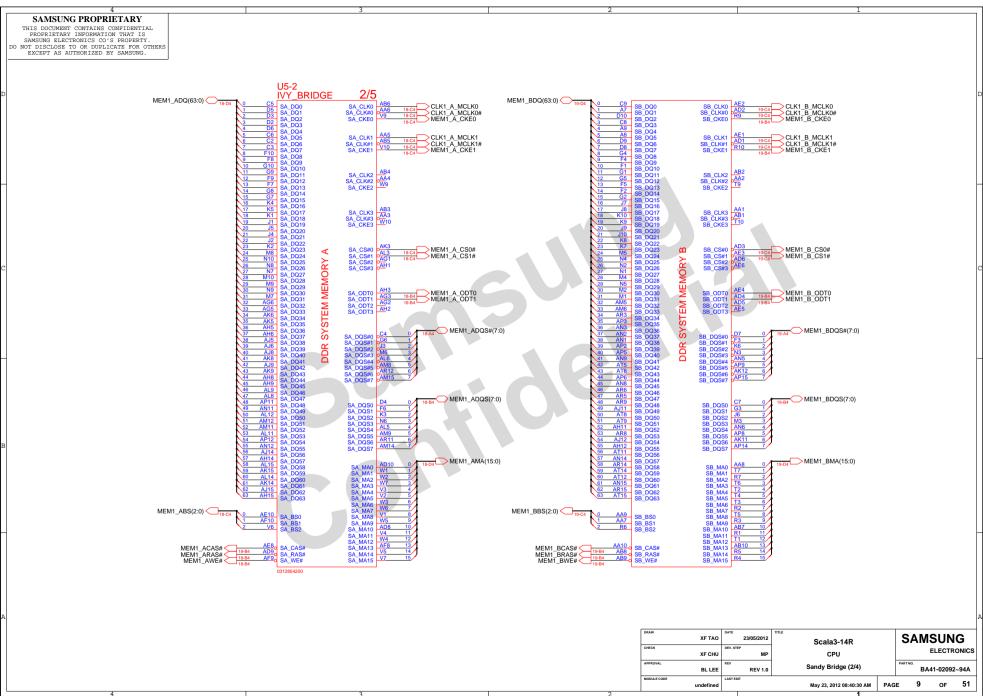


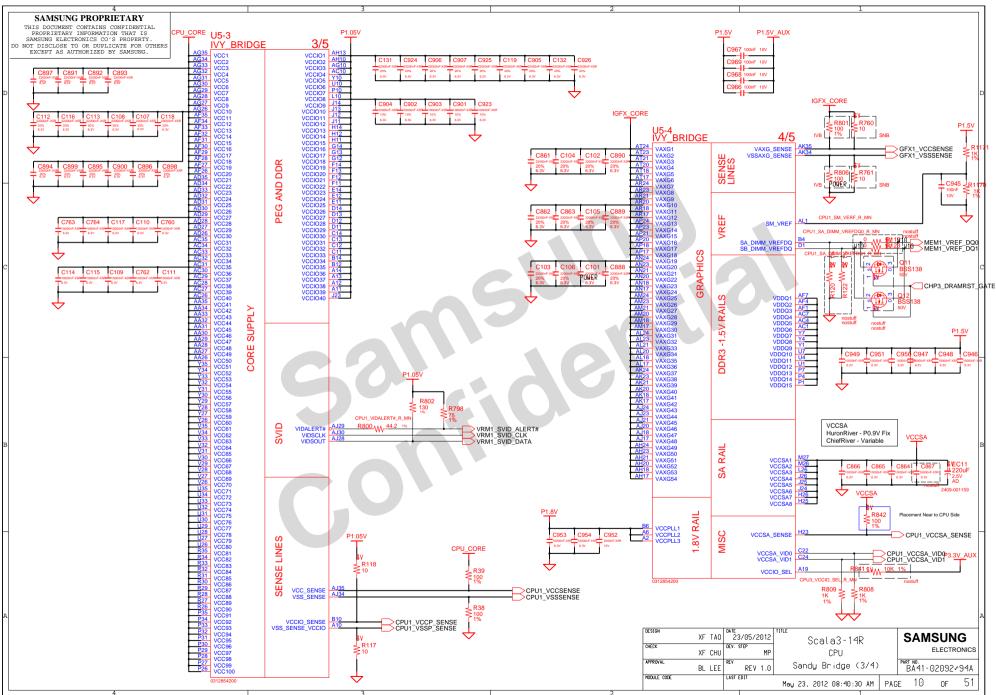


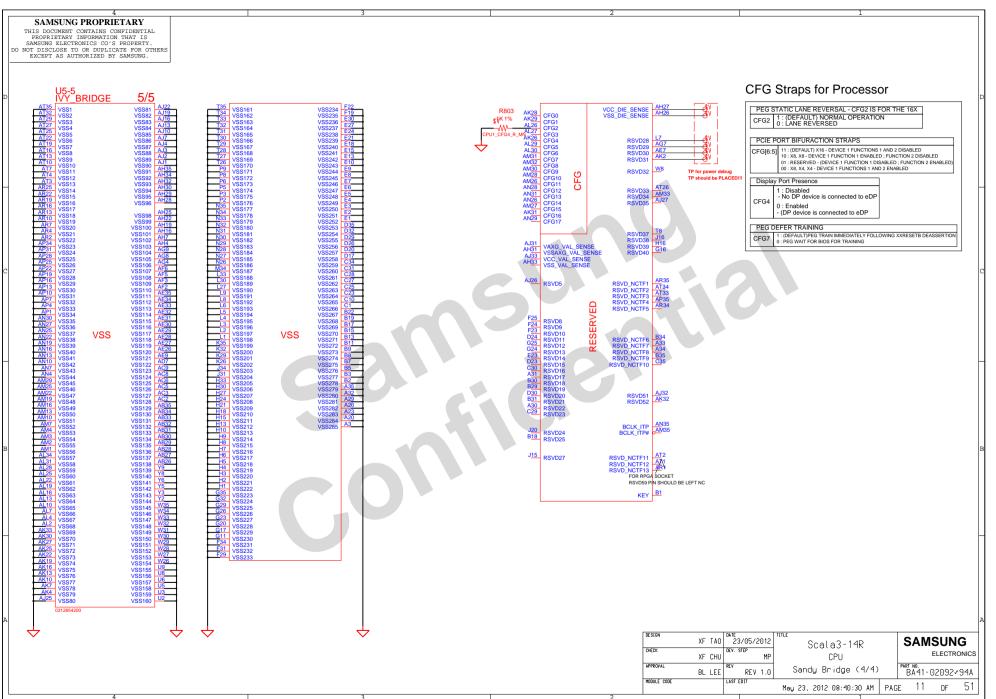


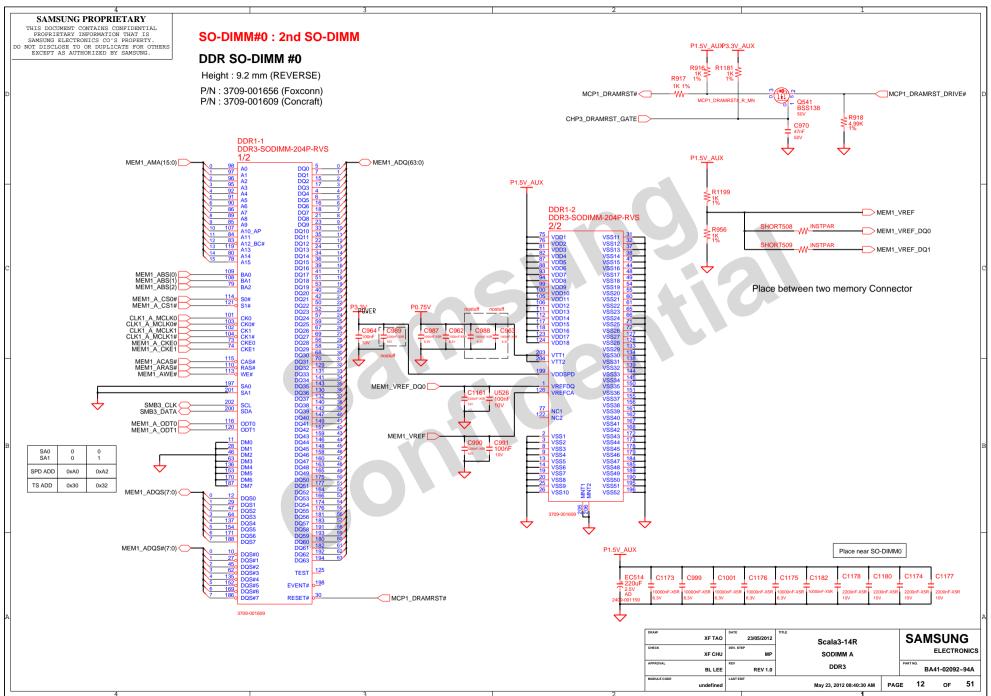


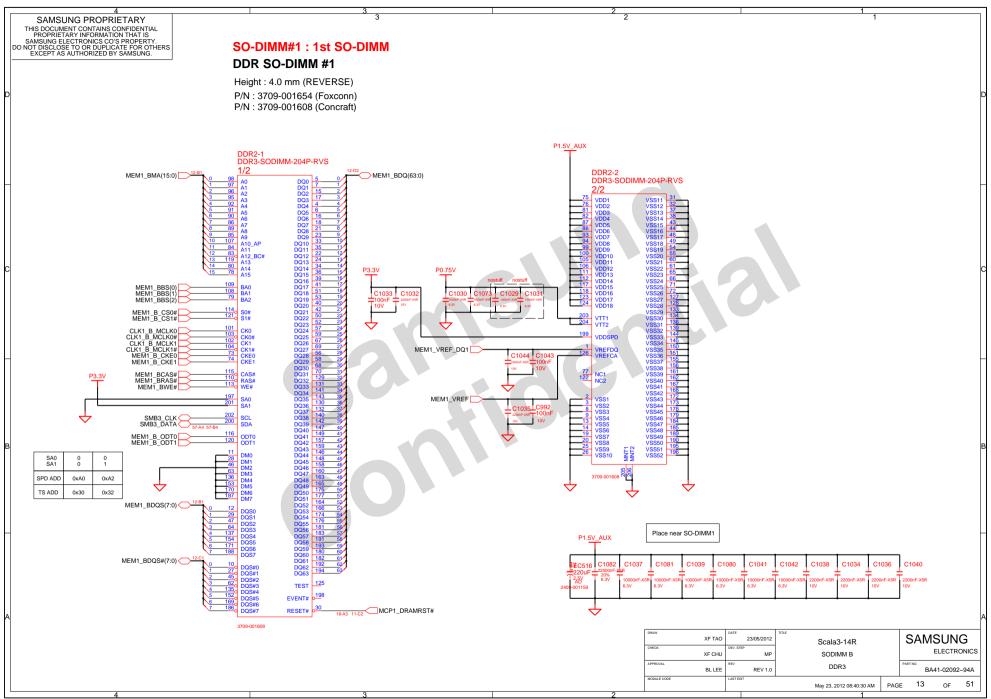


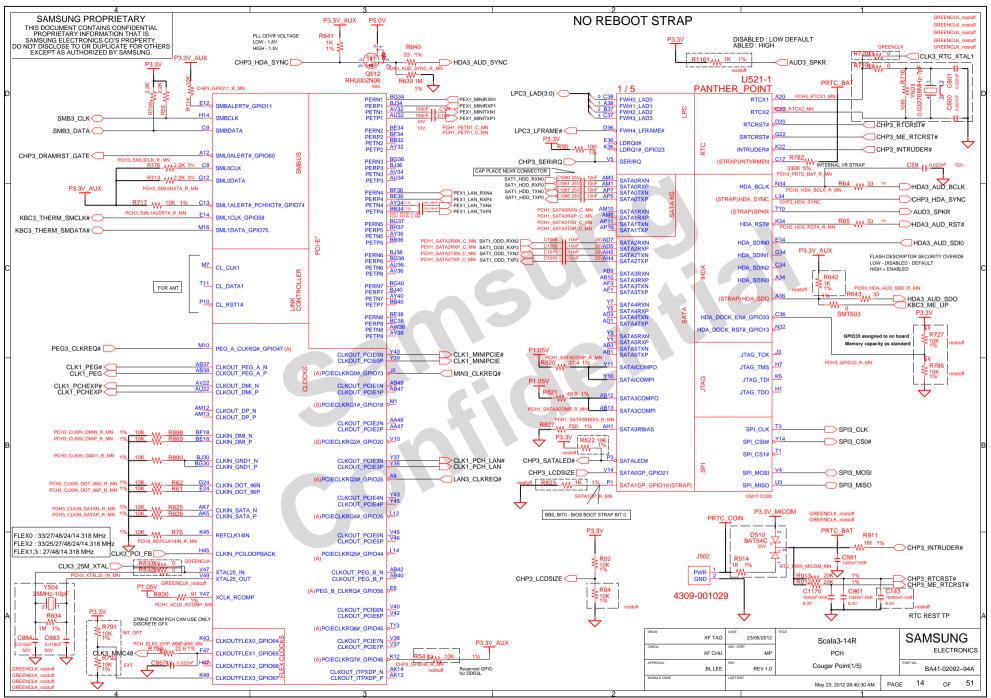


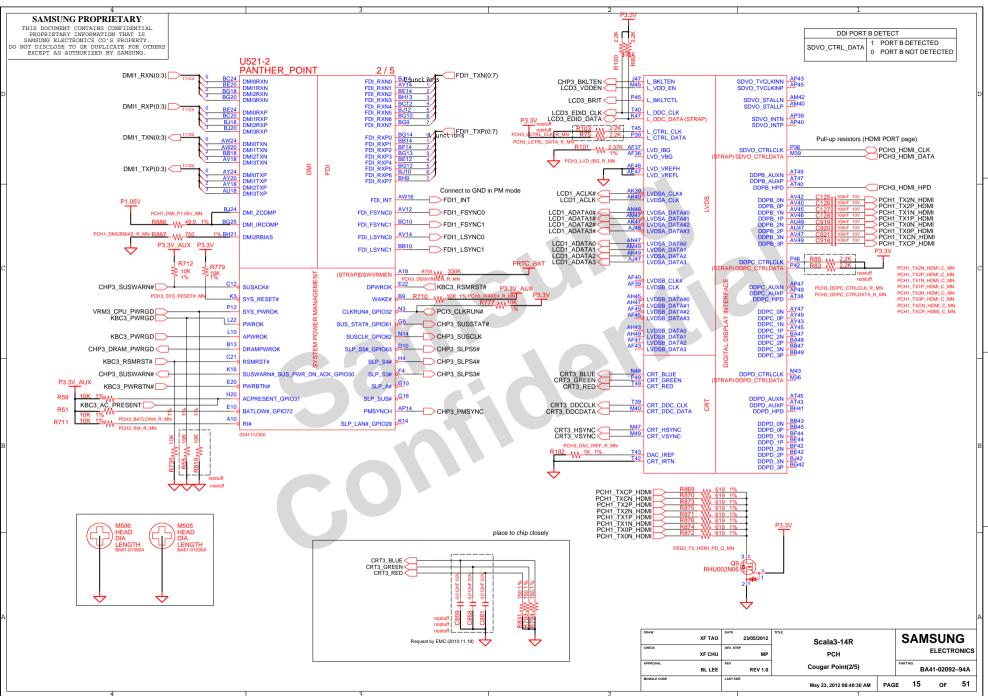


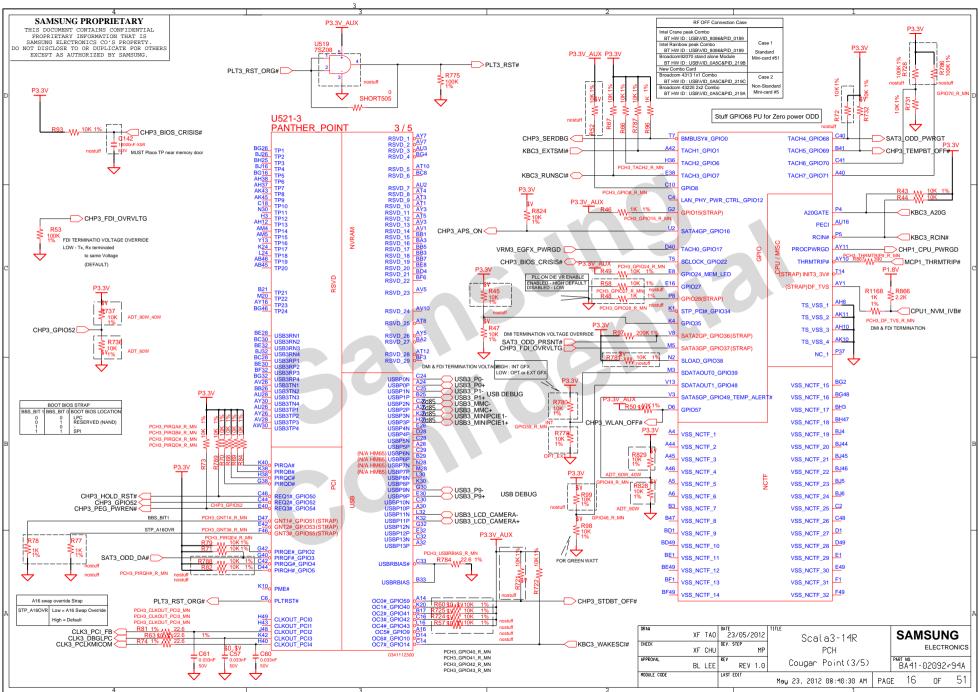


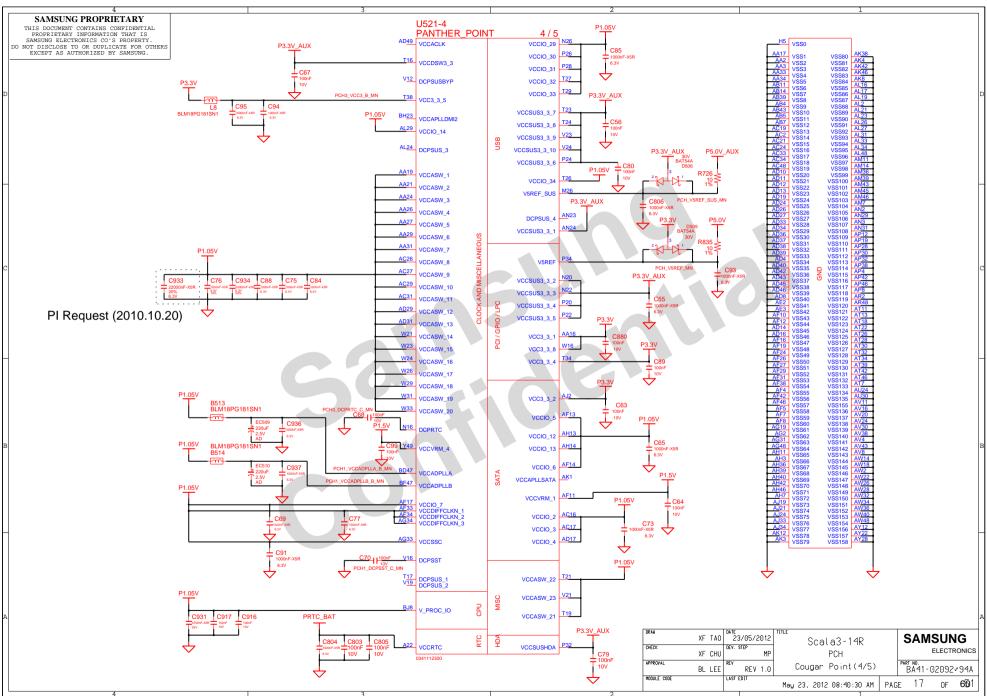


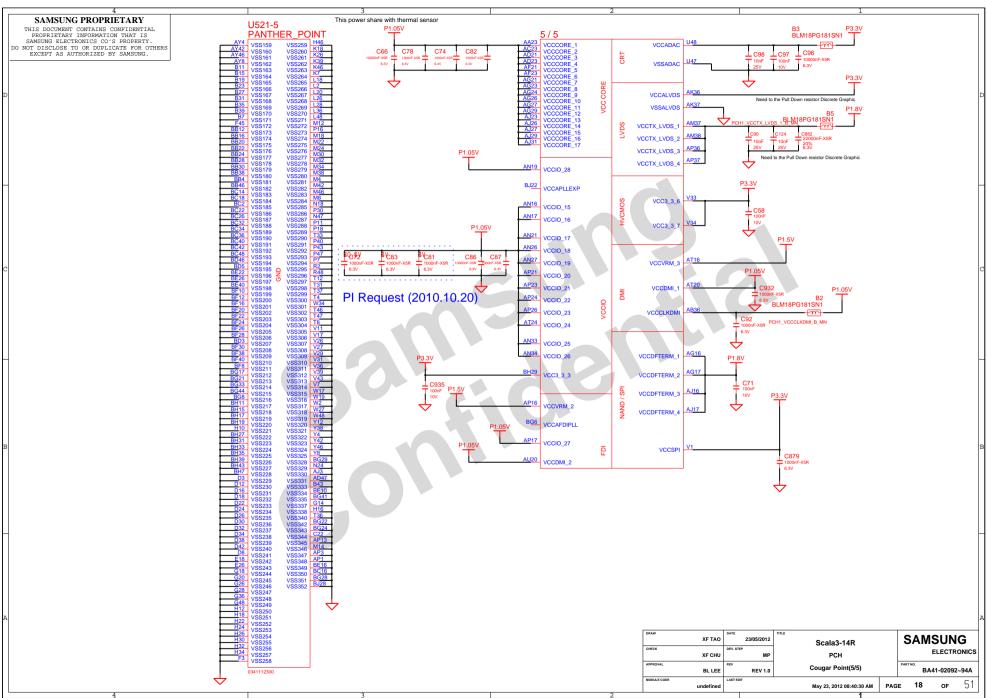


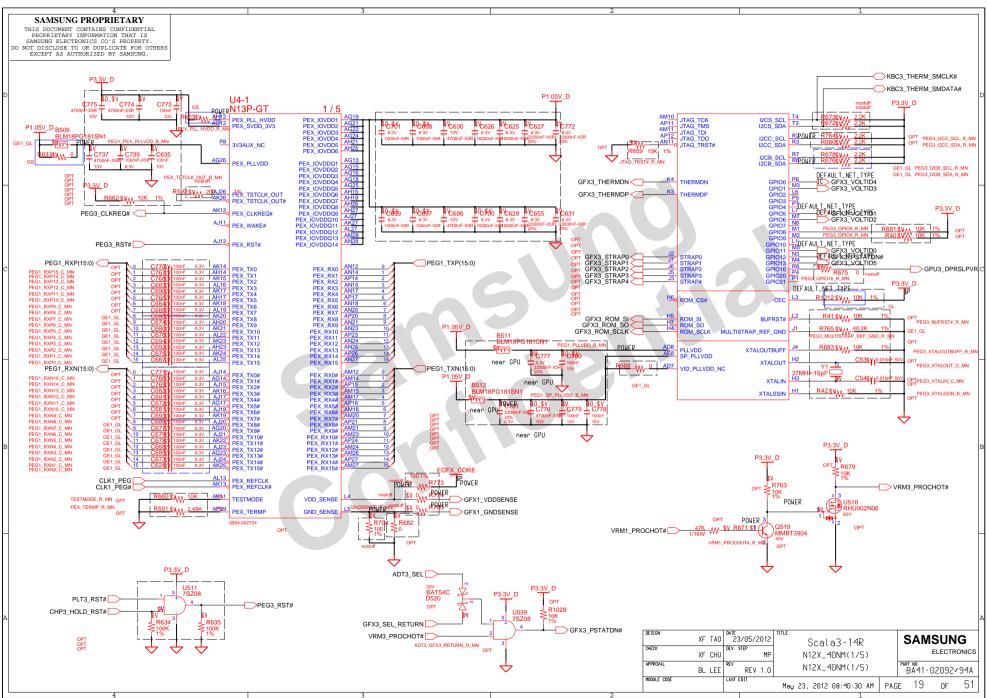


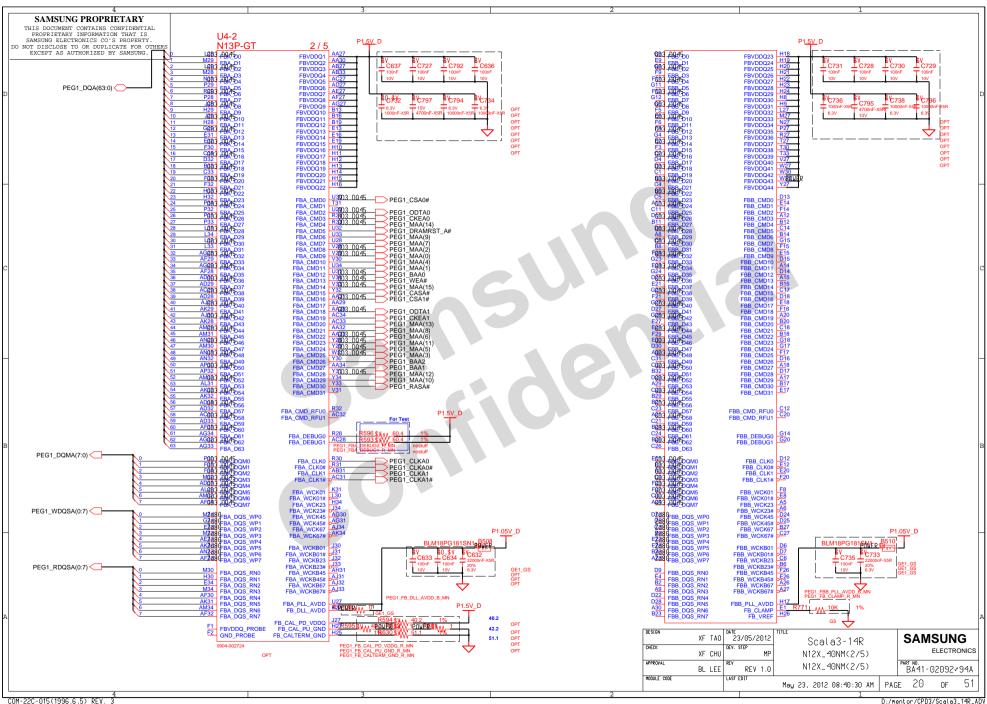


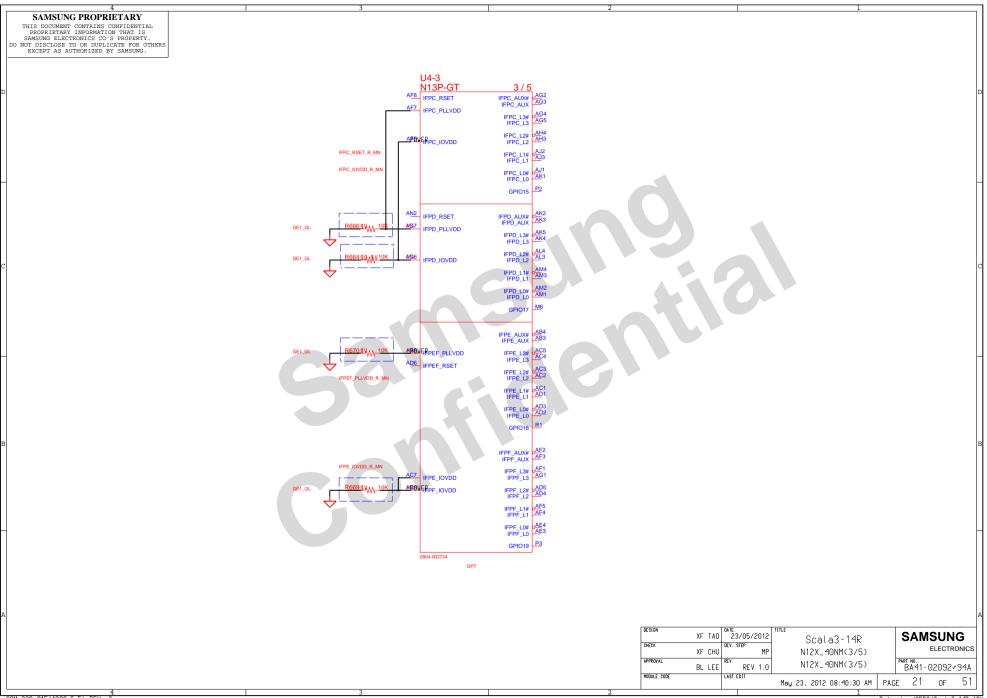


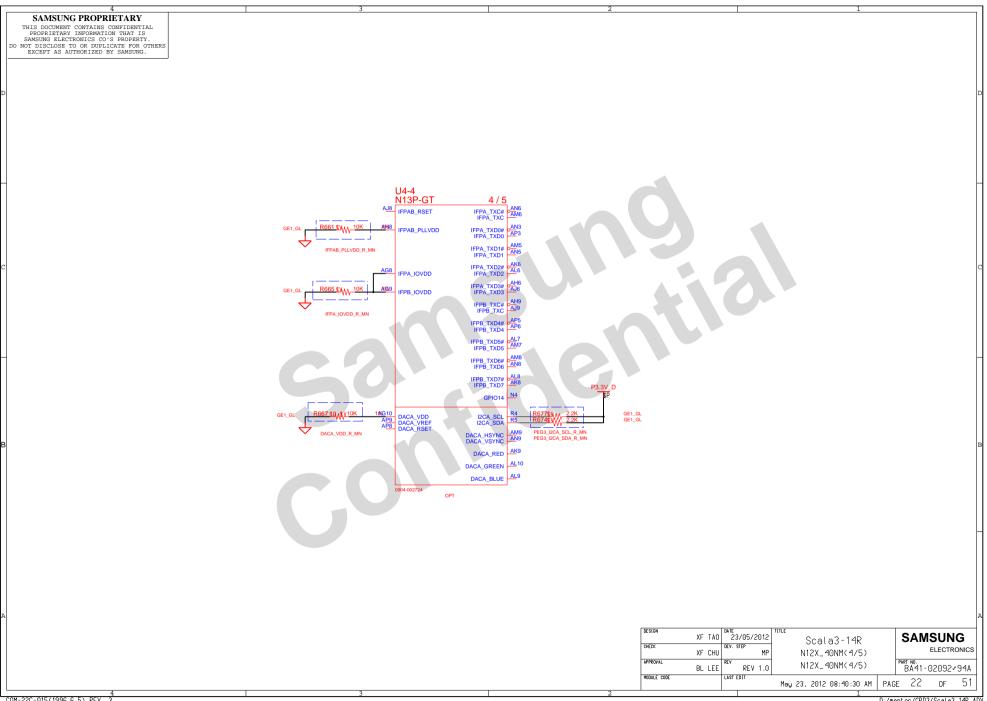


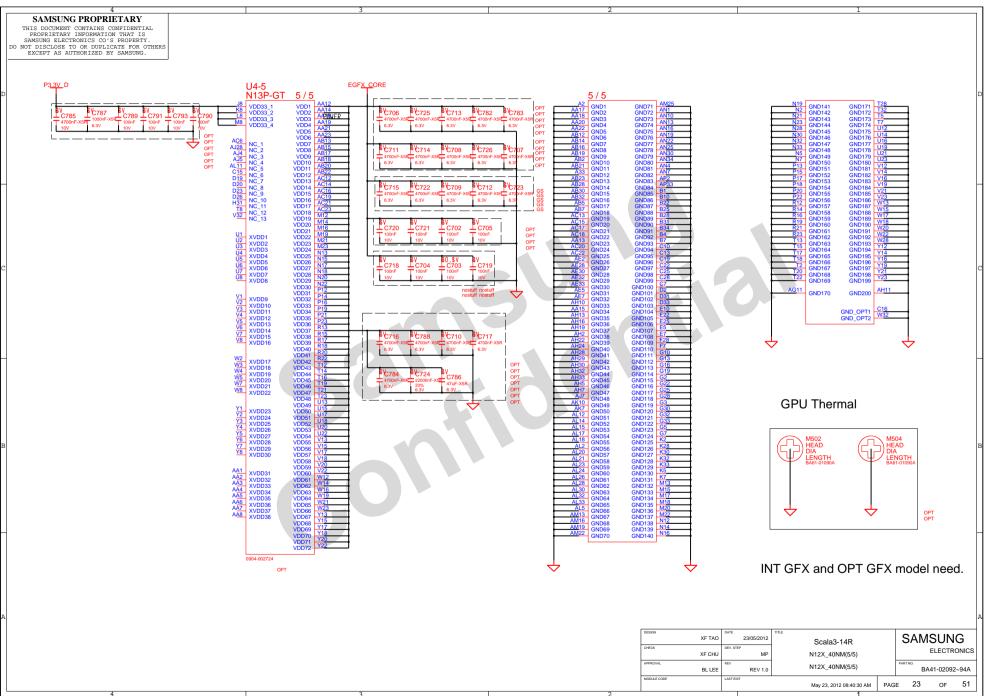


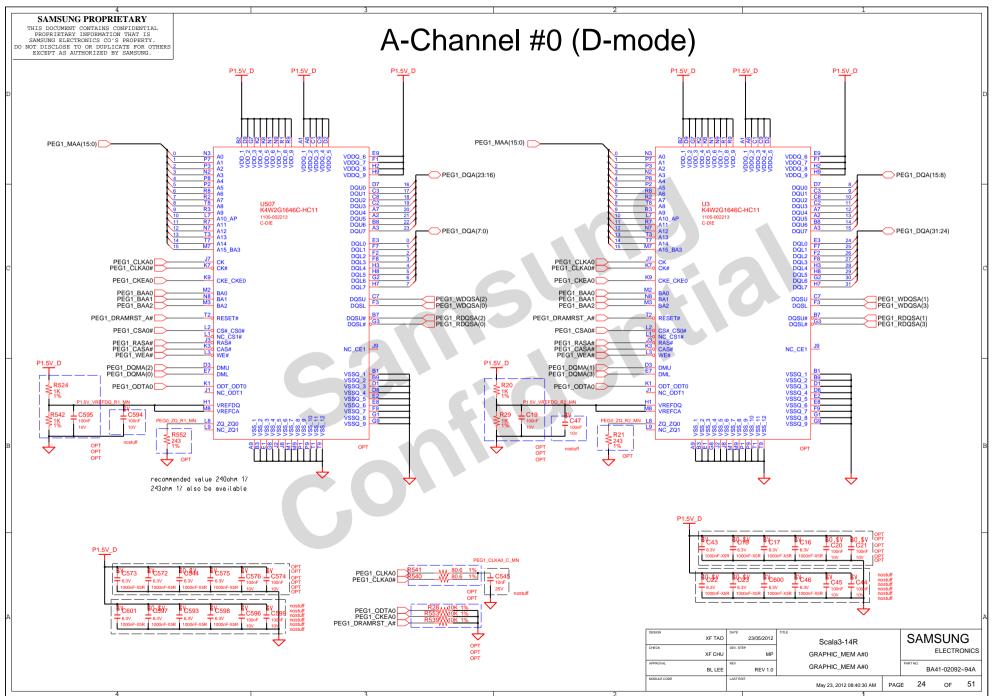


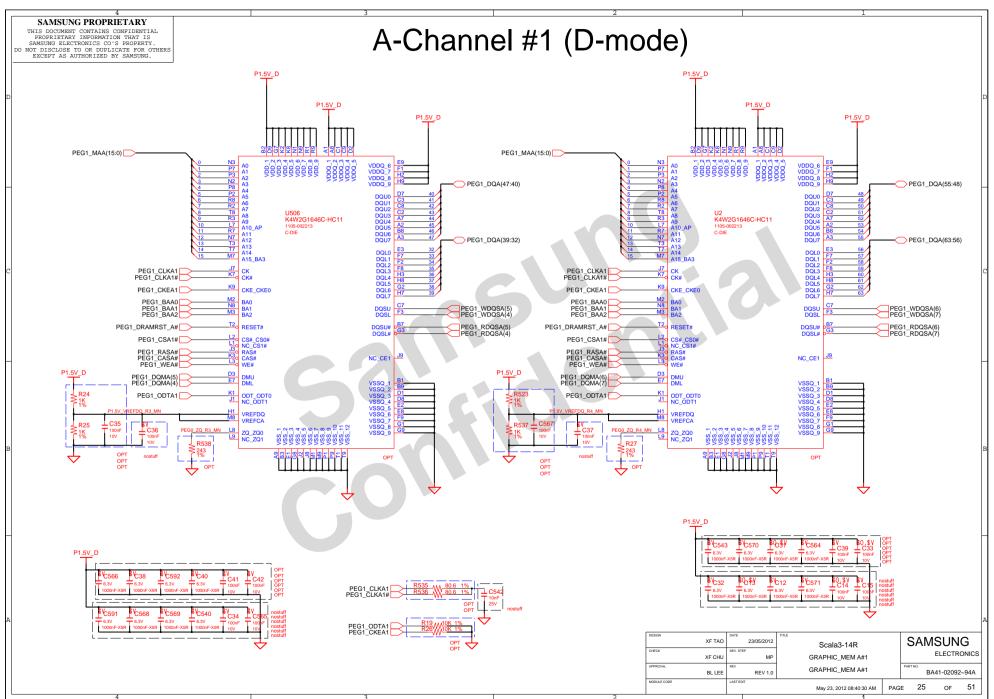










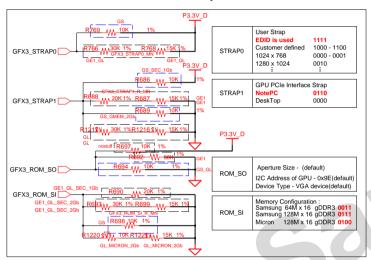


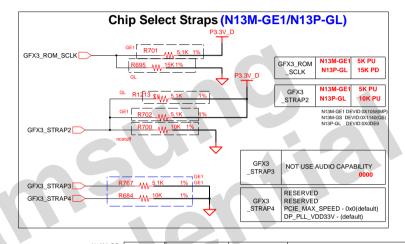
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N13x STRAP

Function Straps





Chip Select Straps (N13M-GS)	
GFX3_ROM_SCLK	GFX3_ROM
GFX3_STRAP3 R685 N, 10K 1% GS GFX3_STRAP4 R685 N, 10K 1%	GFX3 _STRAP3 N13M-GS 10K PD
<u></u>	GFX3 _STRAP4 N13M-GS 10K PD

GS	STRAP PIN	STRAP MAPPING	Resistance	Polarity
	STRAP0	RAMCFG[0]	10K ohm	Pull-up to 3V3 for binary '1'
	STRAP1	RAMCFG[1]	10K ohm	Pull-down to GND for binary '0' Samsung 64M x 16 gDDR3 0011
	STRAP2	RAMCFG[2]	10K ohm	Samsung 128M x 16 gDDR3 0101
	STRAP3	RAMCFG[3]	10K ohm	Micron 128M x 16 gDDR3 0001
	ROM_SO	VGA_DEVICE	10K ohm	Pull-down to GND(no display)
	ROM_SI	SUB_VENDOR	10K ohm	Pull-up to 3V3 if VBIOS ROM exsits Pull-down to GND if no VBIOROM
	ROM_SCLK	SMB_ALT_ADDR	10K ohm	Pull-down to GND
	STRAP4	PCI_MAX_SPEED	10K ohm	Pull-down to GND

NAON OF ANAOR OL					
N13M-GE1/N13P-GL	STRAP PIN		BIT 2	BIT 1	ВІТ0
	STRAP0 STRAP1	USER[3] 3GIO_PAD_CFG_ ADR[3]	USER[2] 3GIO_PAD_CFG_ ADR[2]	USER[1] 3GIO_PAD_CFG_ ADR[1]	USER[0] 3GIO_PAD_CFG_ ADR[0]
	ROM_SO ROM SI	FB[1] (GE1) RAMCFG[3]	FB[0] (GE1) RAMCFG[2]	SMB_ALT_ADDR RAMCFG[1]	VGA_DEVICE RAMCFG[0]
	STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
	ROM_SCLK	PCI_DEVID[4]	SUB_VENDOR	(GL)	
	STRAP3 STRAP4	SOR3_EXPOSED RESERVED	SOR2_EXPOSED RESERVED	SOR1_EXPOSED PCIE_MAX_SPEED	SOR0_EXPOSED DP_PLL_VDD33V

DESIGN XF TAO	23/05/2012	Scala3-14R		SAM	ISUN	IG
CHECK XF CHU	DEV. STEP MP	GRAPHIC_MEM C#0		ELECTRONICS		
APPROVAL BL LEE	REV REV 1.0	GRAPHIC_MEM C#0		PARTNO.	A41-0209	2~94A
MODULE CODE	LASTEDIT	May 23, 2012 08:40:30 AM	PAG	E 26	OF	51

RESISTOR

15K

20K

25K

30K

35K

PULL UP PULL DOWN

1010

1011

1100

1101

1110

0001

0010

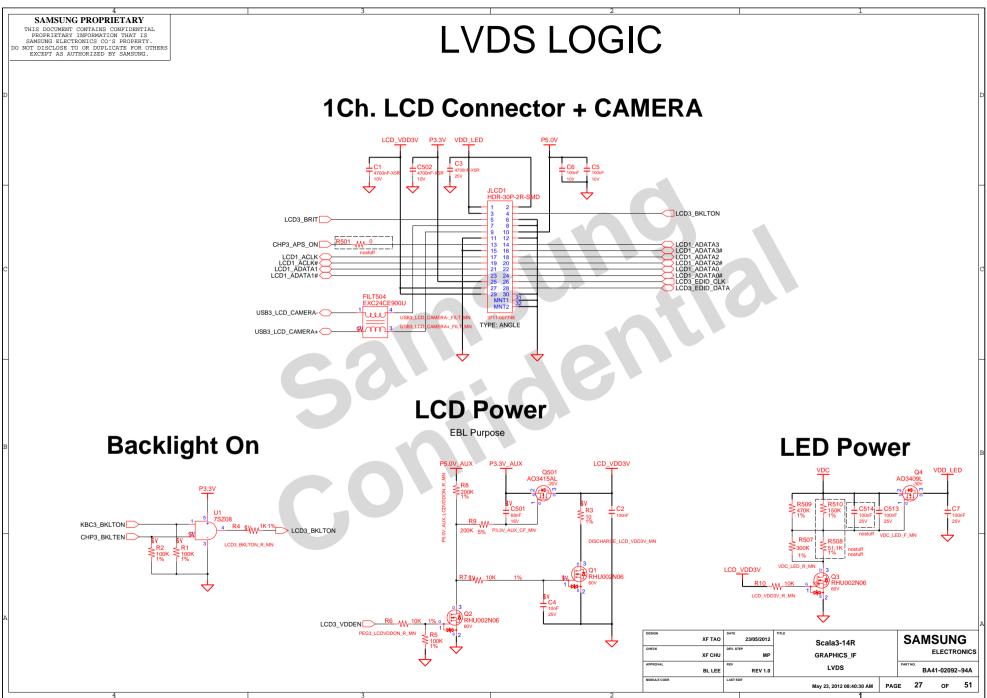
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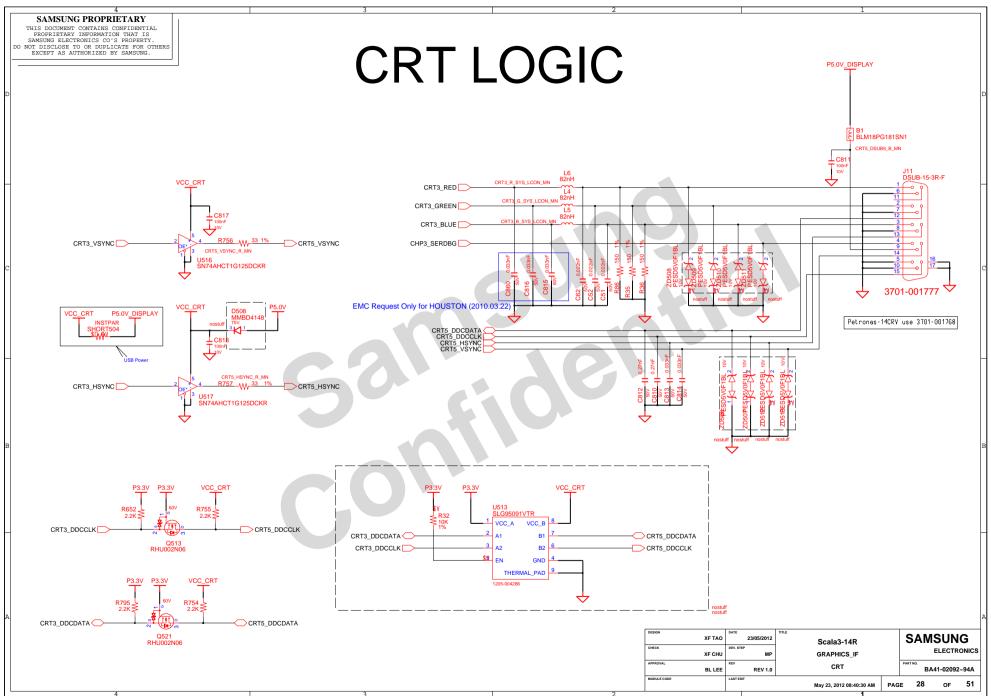
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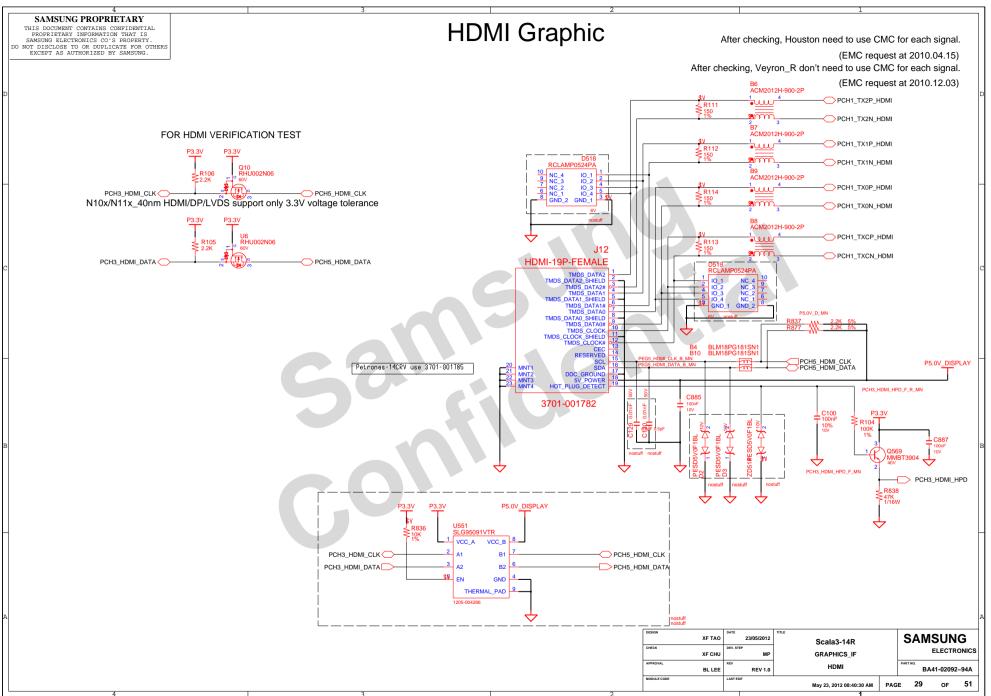
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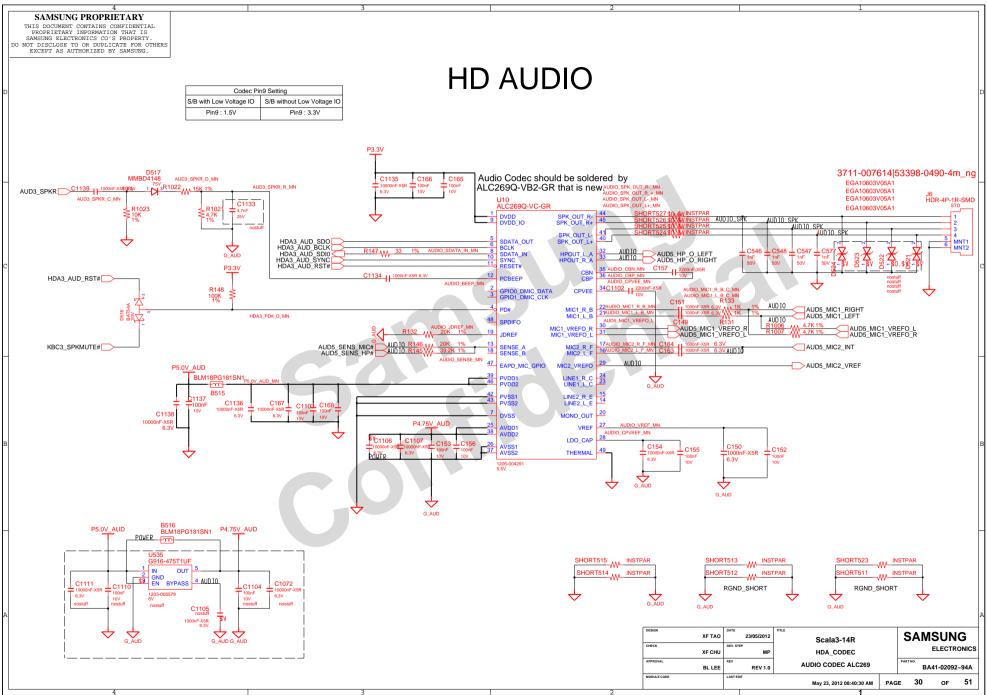
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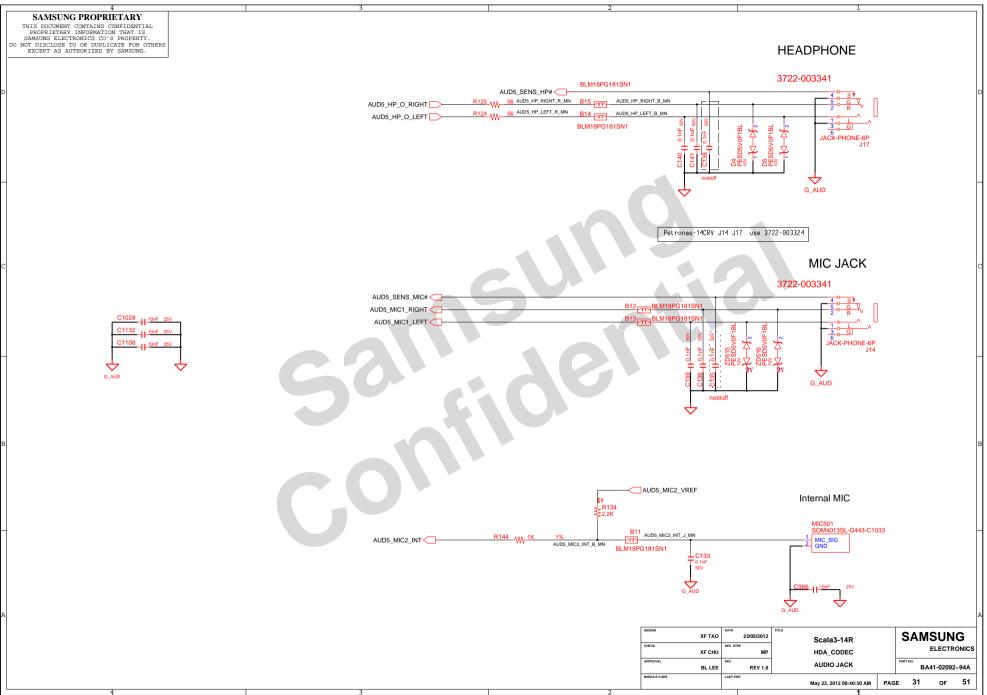
COM-22C-015(1996.6.5) REV. 3

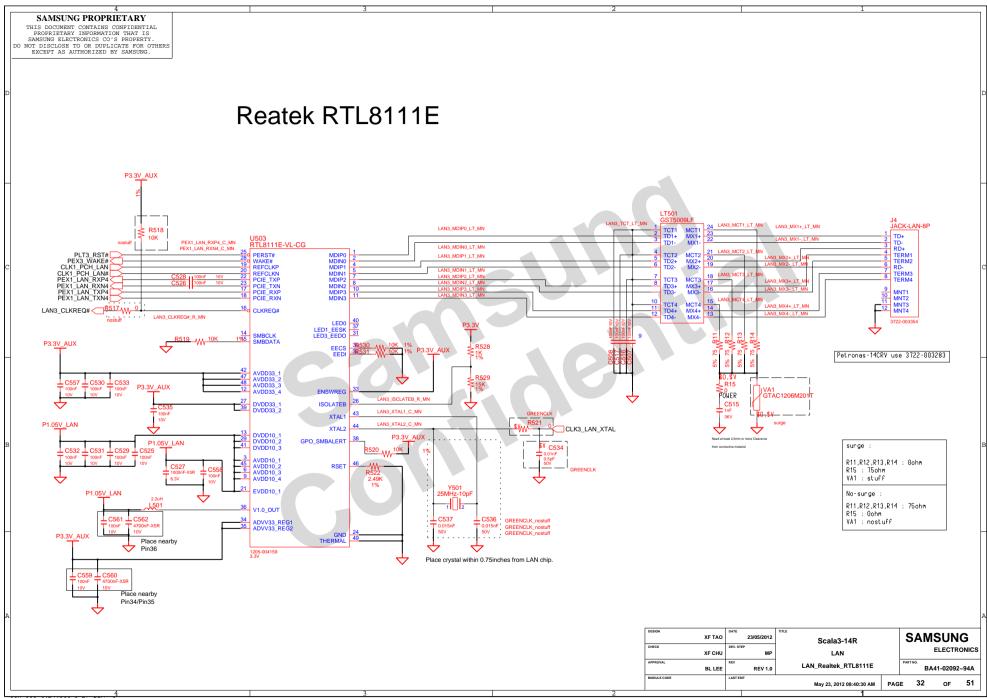


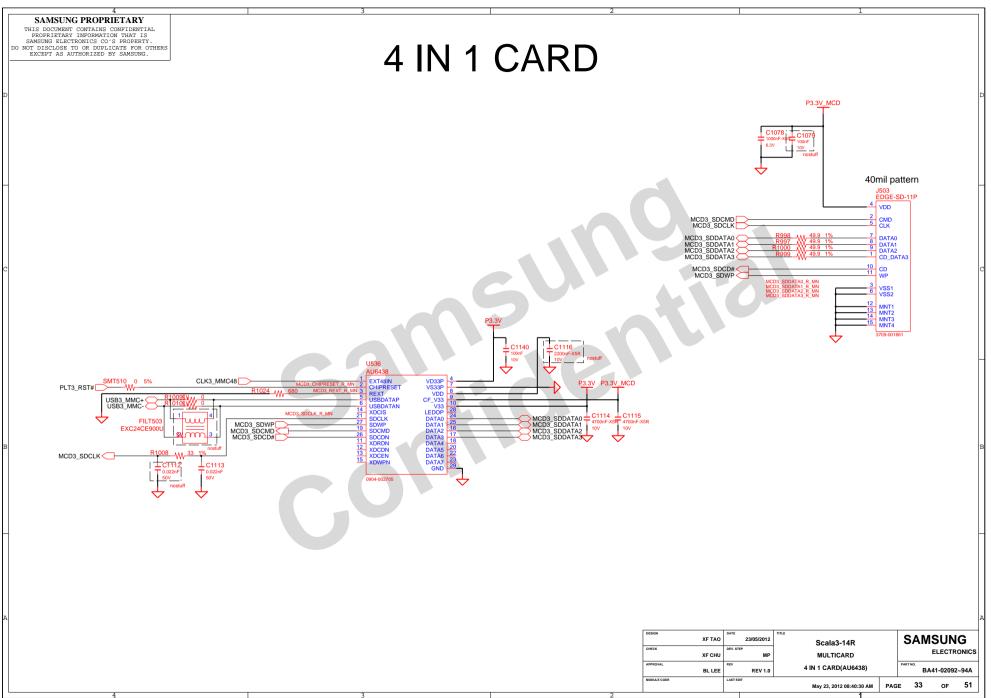


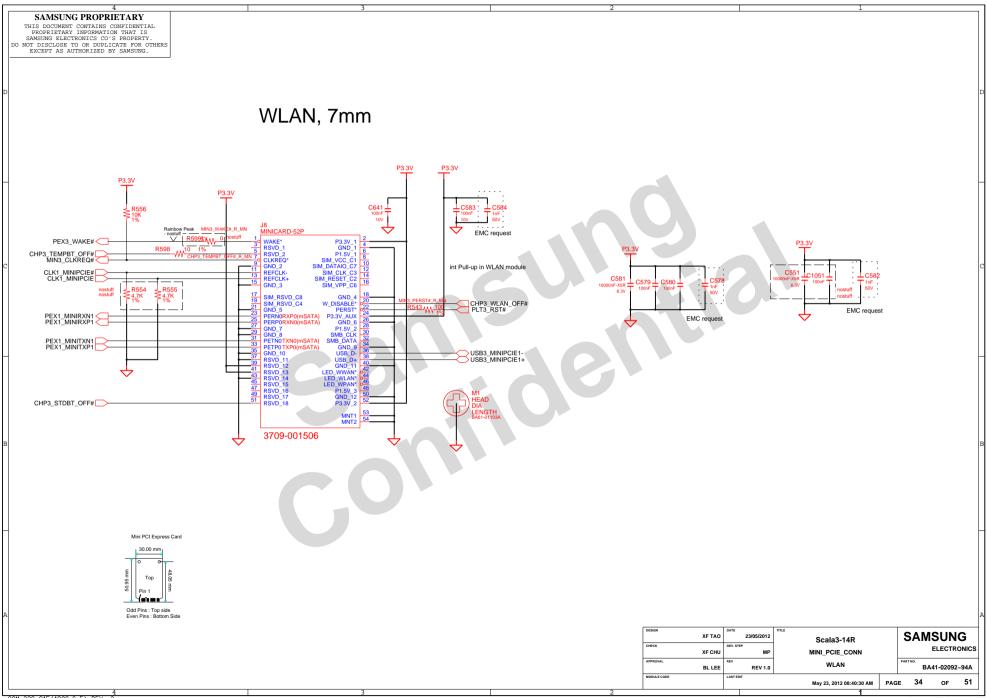


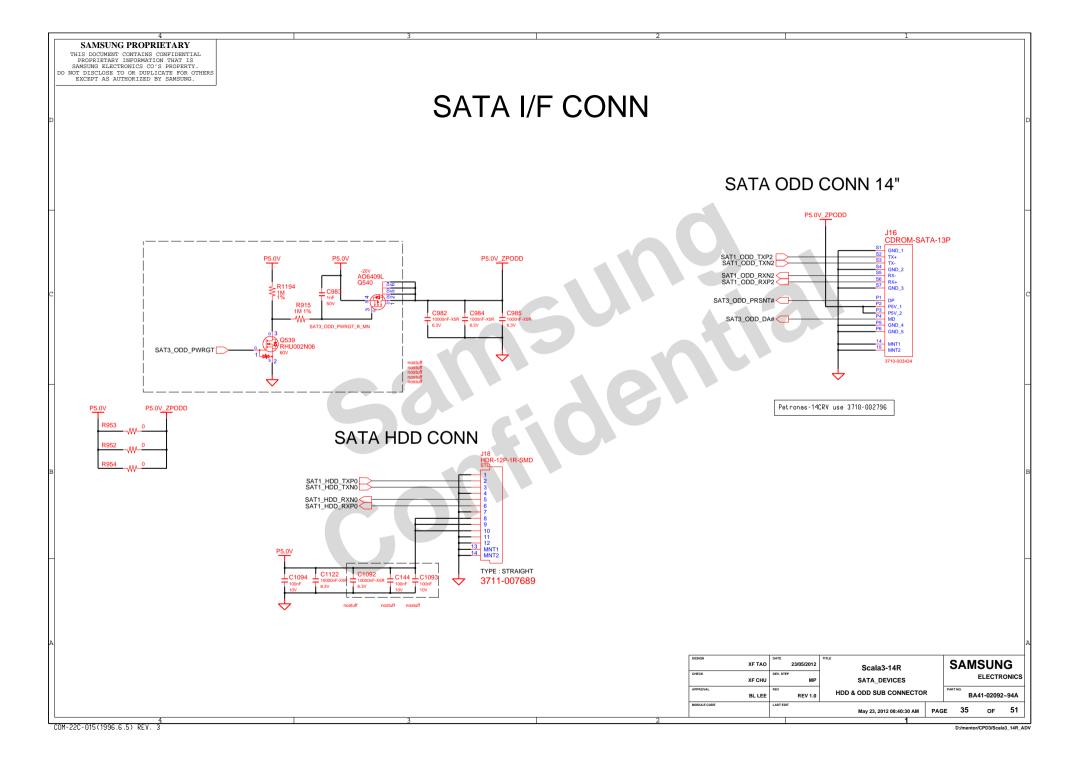


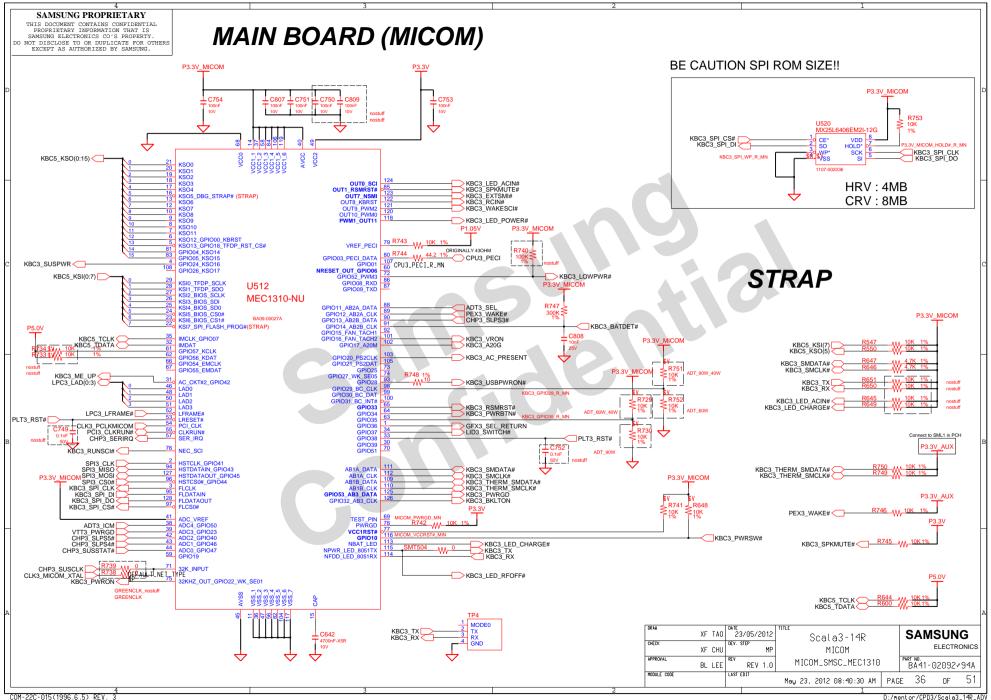


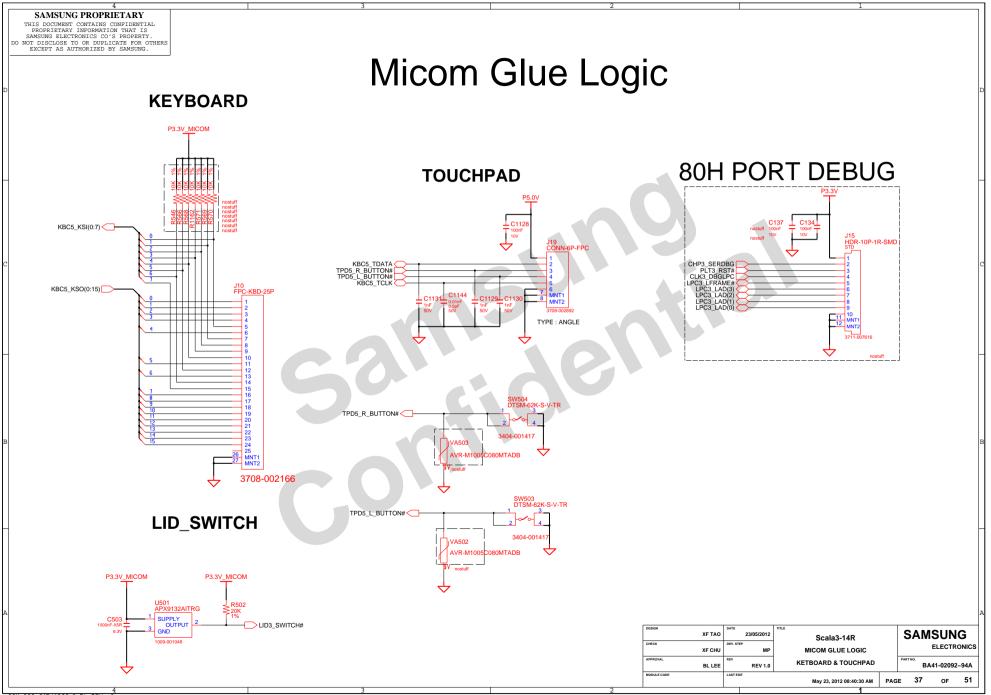


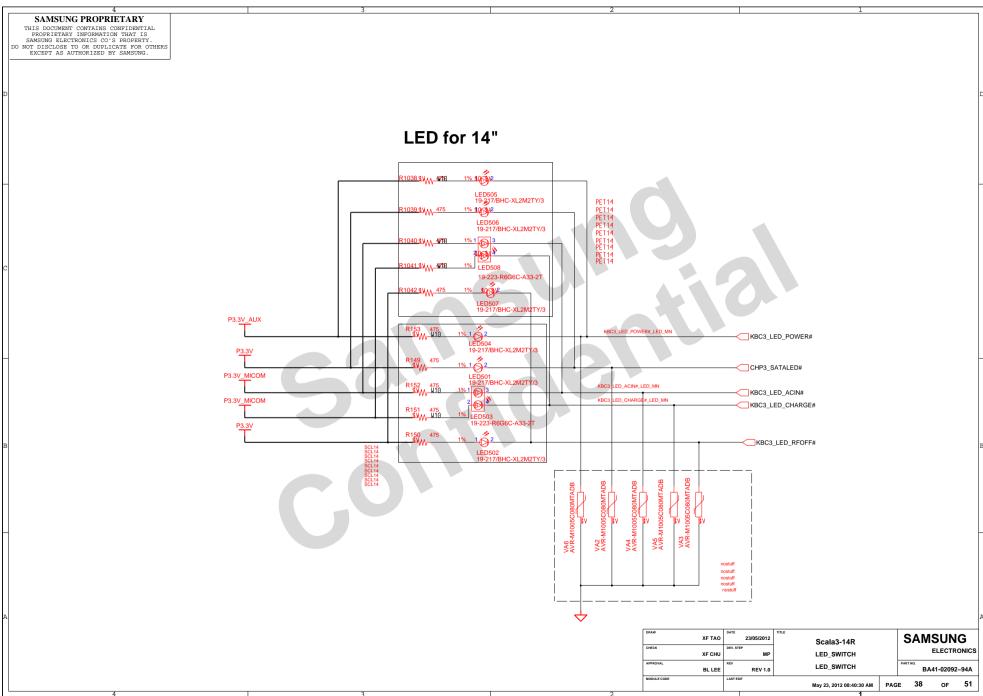


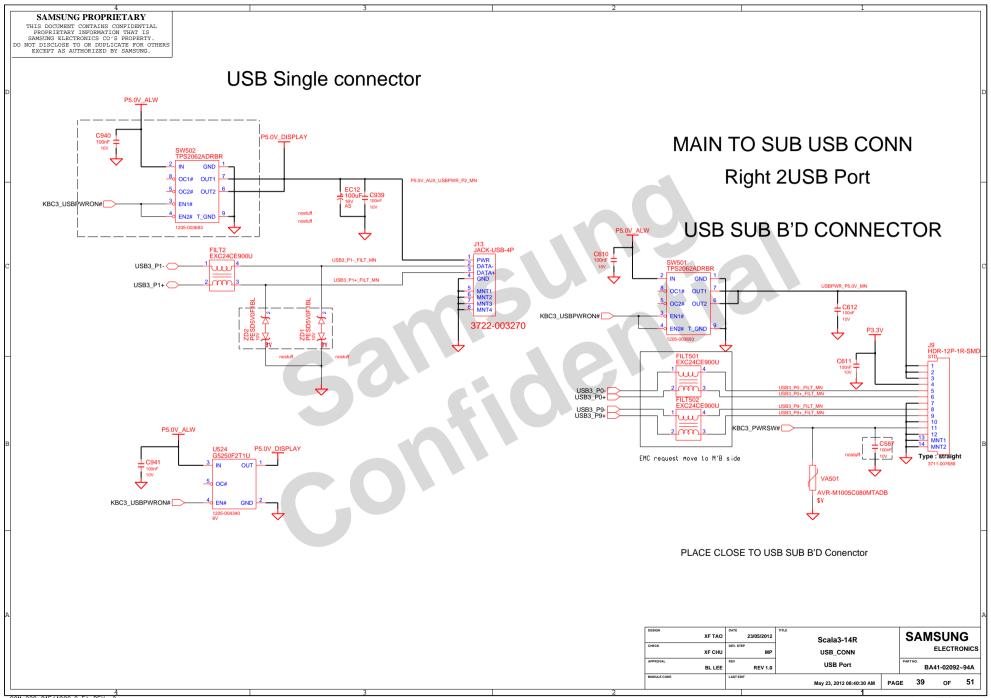


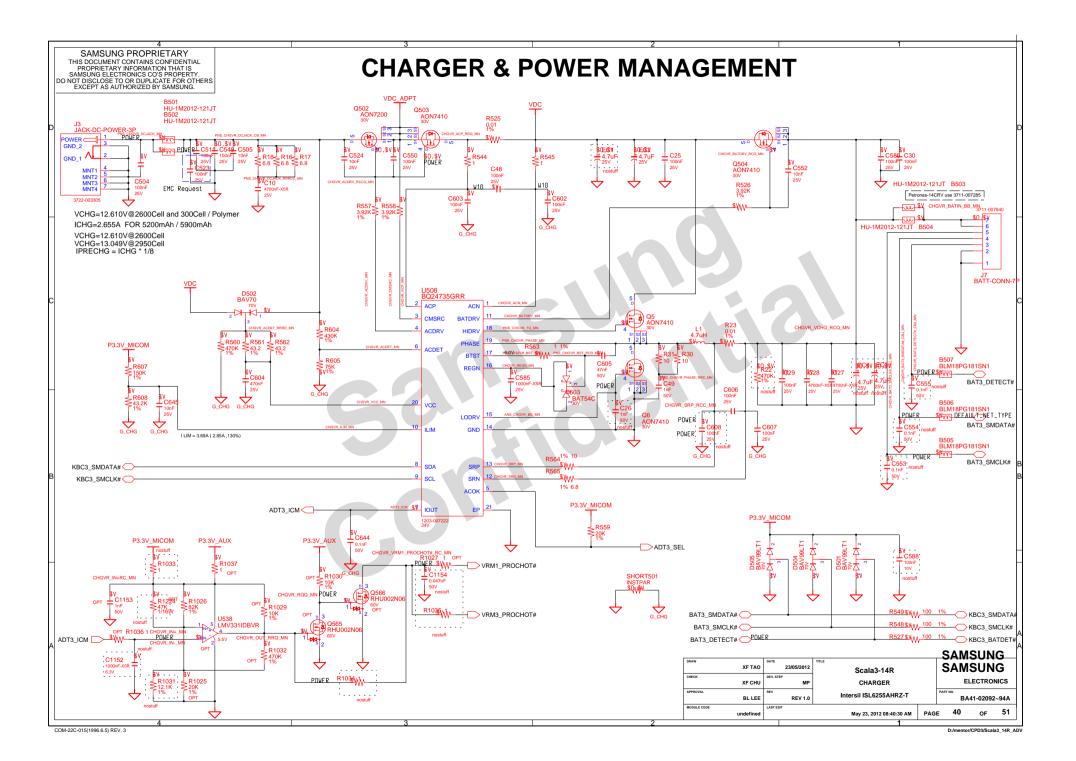


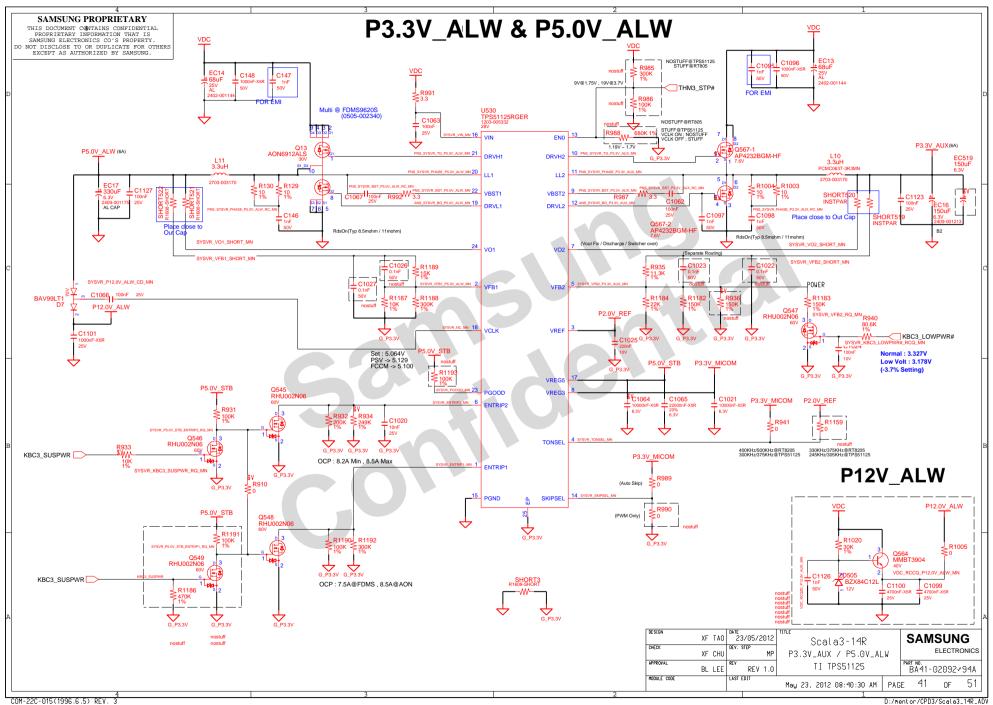


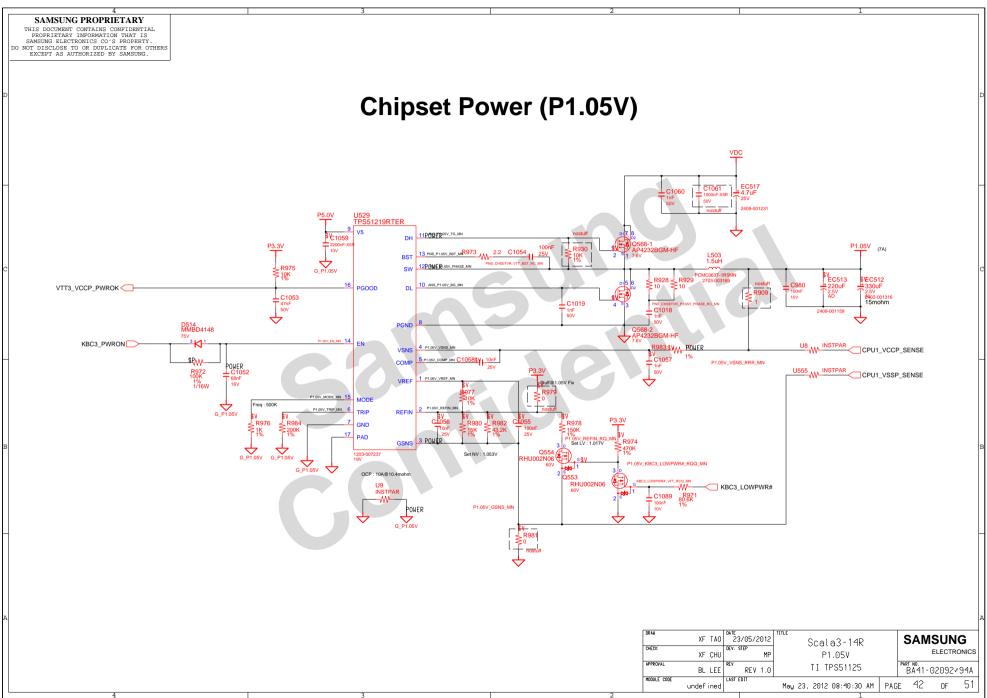


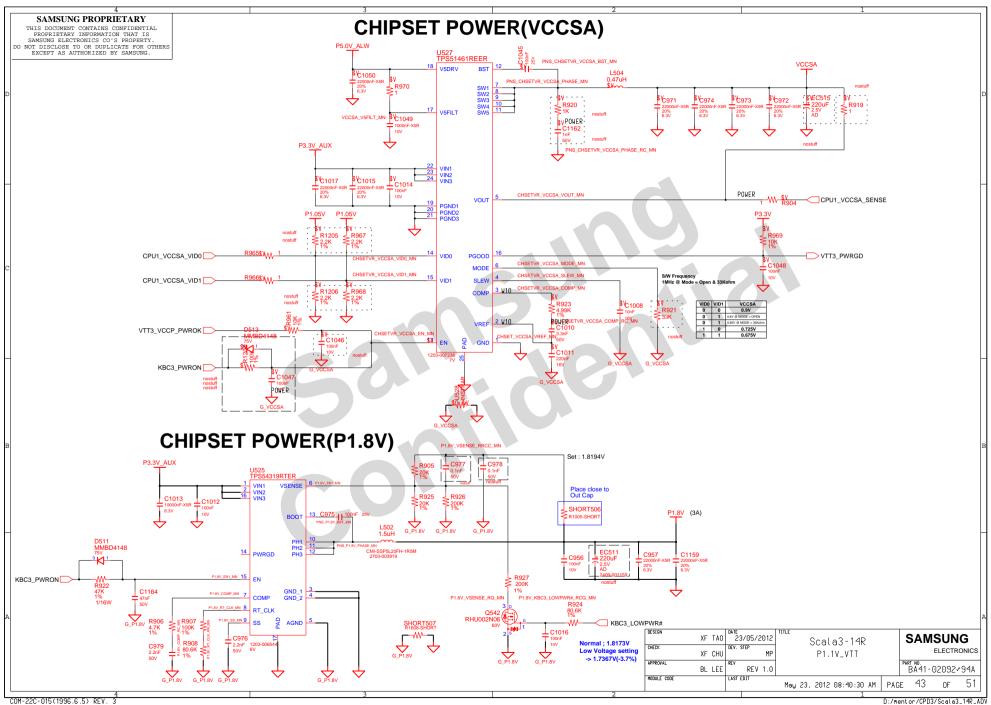


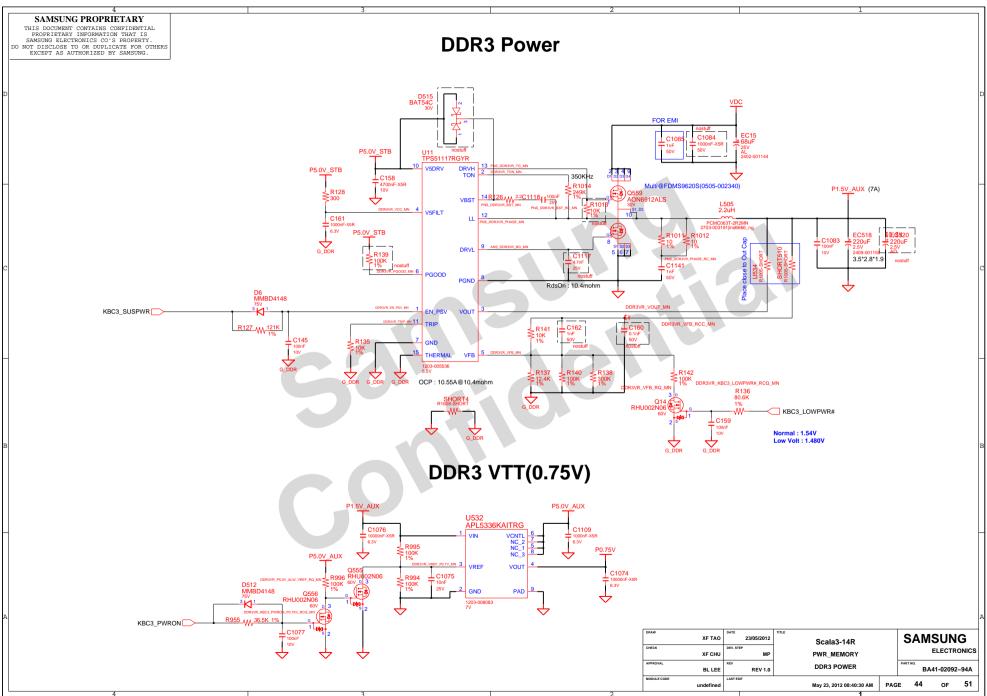


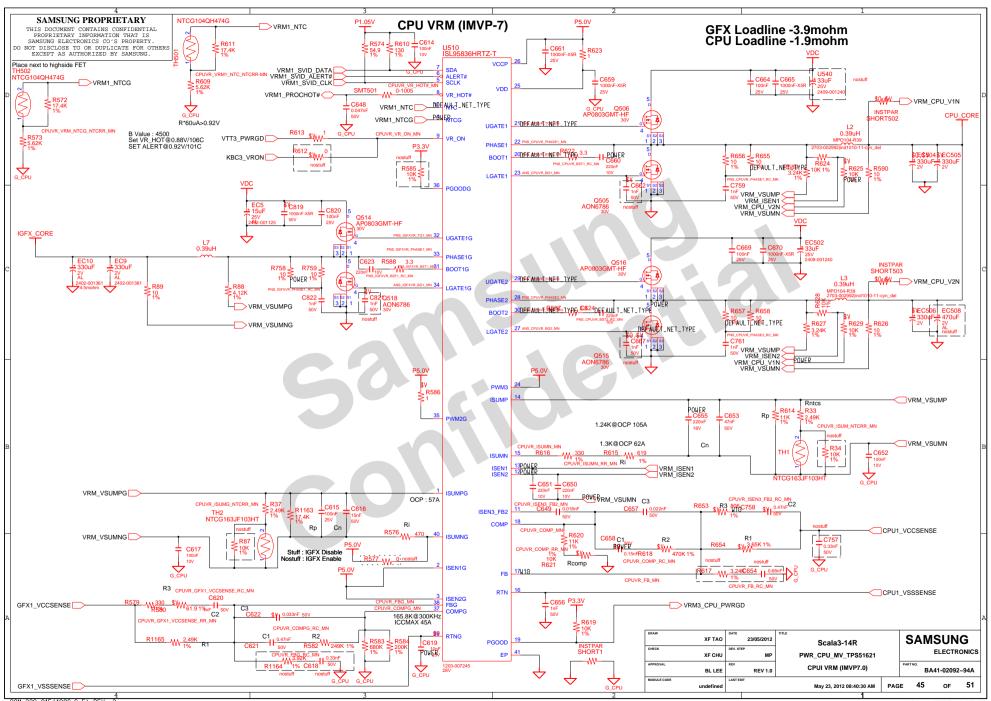


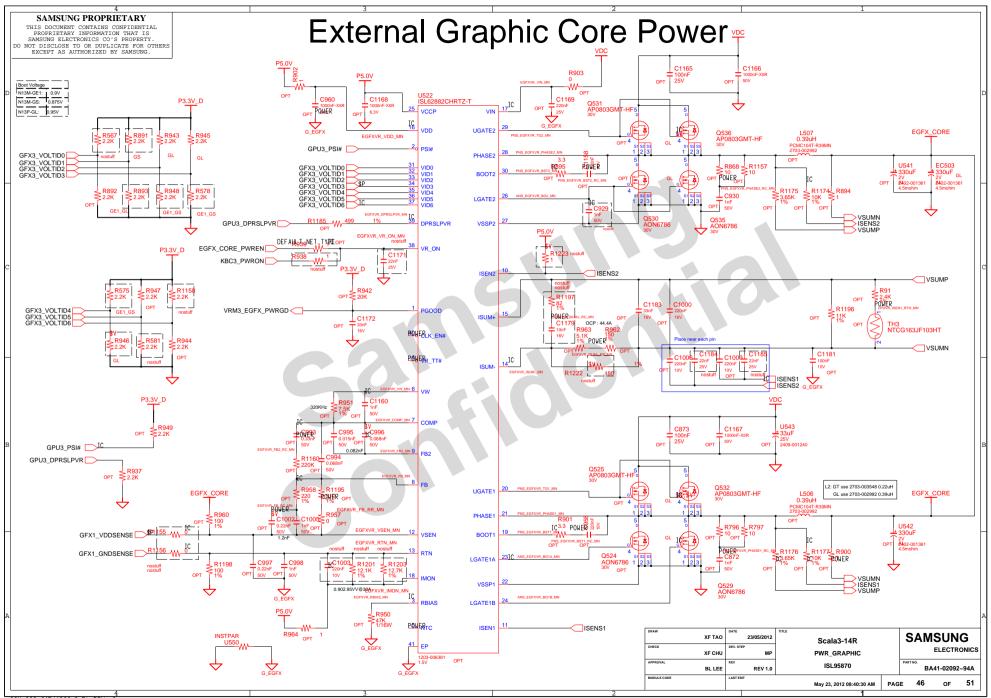












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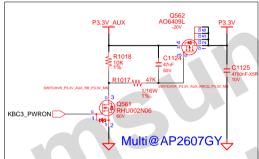
Switched Power 1

(P5.0V -> P3.3V -> EGFX CORE -> P1.8V -> P1.5V -> P0.75V -> P1.05V -> P0.85V_SA -> CPU_CORE)

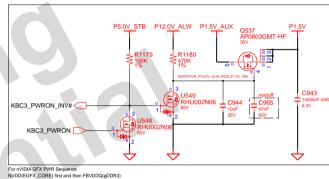
P5.0V_AUX



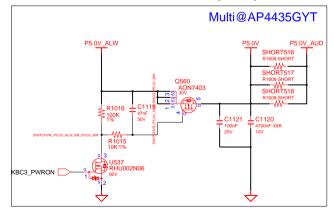
Switched Power On (P3.3V)



Switched Power On (P1.5V)



Switched Power On (P5.0V)



	DRAW XF TAO CHECK XF CHU	DEV. STEP		Scala3-14R Switched PWR 2	_	SAMSUNG ELECTRONICS				
	BL LEE	REV	1.0	Switched Power			BA41-02	092~94	A	
	MODULE CODE undefined	LASTEDIT		May 23, 2012 08:40:30 AM	PAGI	47	, oi	_F 5′	1	
				1						

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