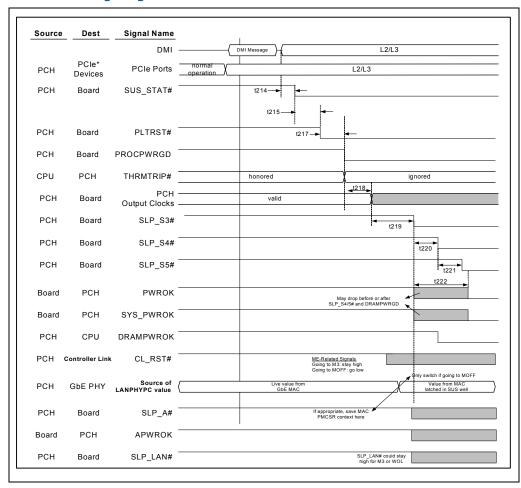


Figure 8-6. S0 to S5 Timing Diagram



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