Compal Confidential

Model Name: ZIUS6/S7
File Name: LA-A321PR01

BOM P/N:4319OP38L5x -- **ZIUS7**

4319OP38L0x -- ZIUS6



Compal Confidential

ZIUS6/S7 M/B Schematics Document

Intel SharyBay ULT Processor + AMD SUN Pro

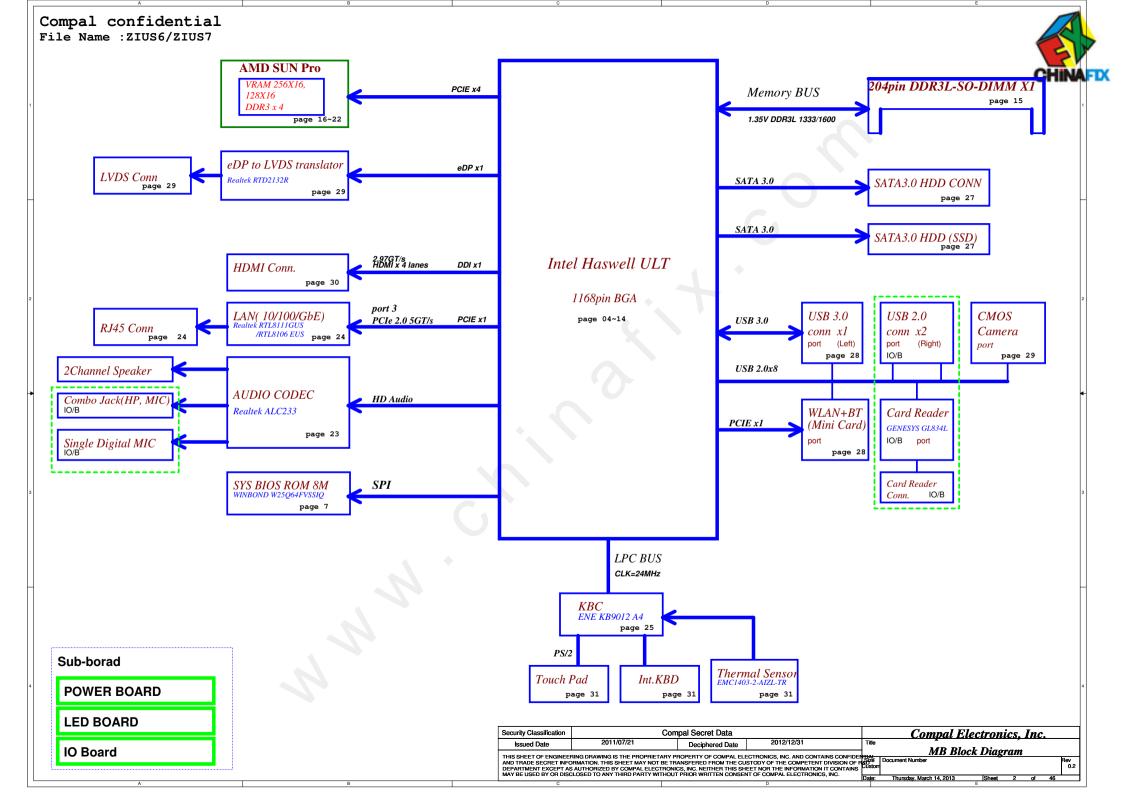
2013-03-14

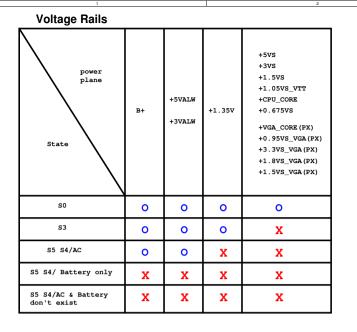
REV: 0.2

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Use of the control of





SIGNAL STATE SLP_S0# SLP_S3# SLP_S4# SLP_S5# +VALW +V +VS Clock Full ON HIGH HIGH S3 (Suspend to RAM) LOW LOW HIGH HIGH ON ON OFF OFF S4 (Suspend to Disk) HIGH ON OFF T.OW T.OW T.OW OFF OFF S5 (Soft OFF) LOW LOW LOW ON OFF

BOARD ID Table

Board ID	PCB Revision
0	
1	
2	0.2
3	0.1
4	
5	
6	
7	

BOM Structure Tal	ole //
BTO Item	BOM Structure
Connector	, ME@
45 LEVEL	450
Unpop	
CPU OPTION	CPU1@
INTEL UMA	UMA@
AMD SUN Pro	PX@
VRAM Option	S1G@ M1G@ H1G@
	S2G@ M2G@
LAN RTL8111GUS	8111@ SWR@
LAN RTL8106EUS	8106@ S W R@
USB3.0	USB3@
EMI PART	EMI@
ESD PART	ESD@
	*

EC SM Bus1 address

EC SM Bus2 address

Smart Battery Charger LC SWI DUSZ audiess

Thermal Sensor EMC1403-2-AIZL-TR 1001_101xb Panel - eDP to LVDS translator SharkBay ULT AMD SUN Pro

CPU SM Bus address

CPU SML0 Bus address

DCVICC
DDR DIMM
WALN
Touch Pad

Address 0XA0

Address

Device

Address

USB 2.0 Port Table

	USB 2.0	Port	3 External USB Port
		0	USB 2.0 Port (I/O Board)
		1	USB 3.0/2.0 Port (MB)
		2	USB 2.0 Port (I/O Board)
		3	Card Reader
		4	Touch Screen (reserve)
		5	Camera
ĺ		6	Mini Card (WLAN/BT)
		7	

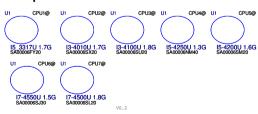
USB 3.0 Port Table

	Port			PCII	Table	
	2	USB 3.0 Port (MB)		Port	Lane	
1	3		Γ	1		
	4		L	2	/_	LAN
				3		WLAN
				4		
	SATA Port Table			5	0 1	GPU
	Port				3	
	3		T -		0	

SMBUS Control Table

	HOST	VGA	BATT	KB9012	SODIMM	WLAN	PANEL sensor ADM1032	Thermal sensor EMC1403	VGA sensor ADM1032	CPU Sensor	Touch Pad	Security ROM	NFC
EC_SMB_CK1 EC_SMB_DA1	KB9012 +3VLP	Х	+3VLP	Х	Х	Х	X	Χ	Х	Х	Х	Х	Х
EC_SMB_CK2 EC_SMB_DA2	KB9012 +3VS	+3VS	Х	Х	Х	Х	+3VS	+3VS	+3VS	+3VALW	Х	Х	Х
SMBCLK SMBDATA	CPU +3VALW	Х	X	Х	+3VS	+3VS	Χ	Х	Х	Х	+3VS	+3VS	+3VS
SML0CLK SML0DATA	CPU +3VALW	Х	Х	Х	X	X	Х	Χ	Х	Х	Х	Х	Х
SML1CLK SML1DATA	KB9012 +3VS	Χ	Х	+3VS	Х	Χ	Х	Χ	Х	+3VALW	Х	Х	Χ

CPU part



PCB part

Security Classification

PCB OR LA-A321P REV0 M/B DA600104000

VRAM



mSATA SSD HDD



K4W2G1646E-BC1A		MT41J128M16JT-093G:KHynix 128x16 Vram H5TC2G63FFR-11C
		Compal Electronics, Inc.
	Title	•
IAL	Size Do	otes List

Issued Date

2011/07/12

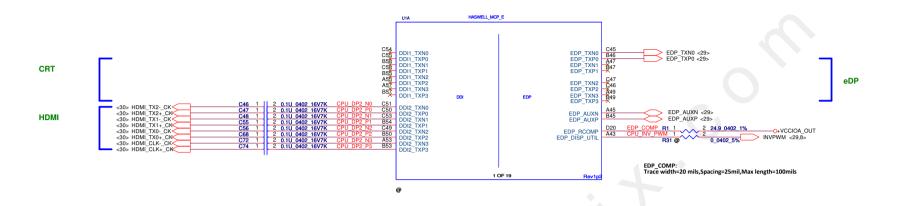
Deciphered Date

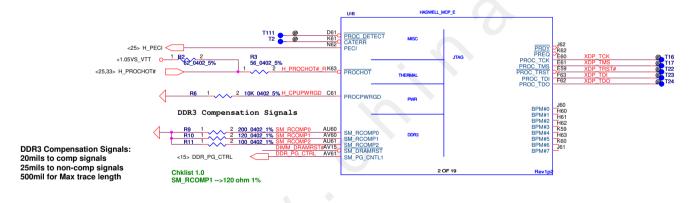
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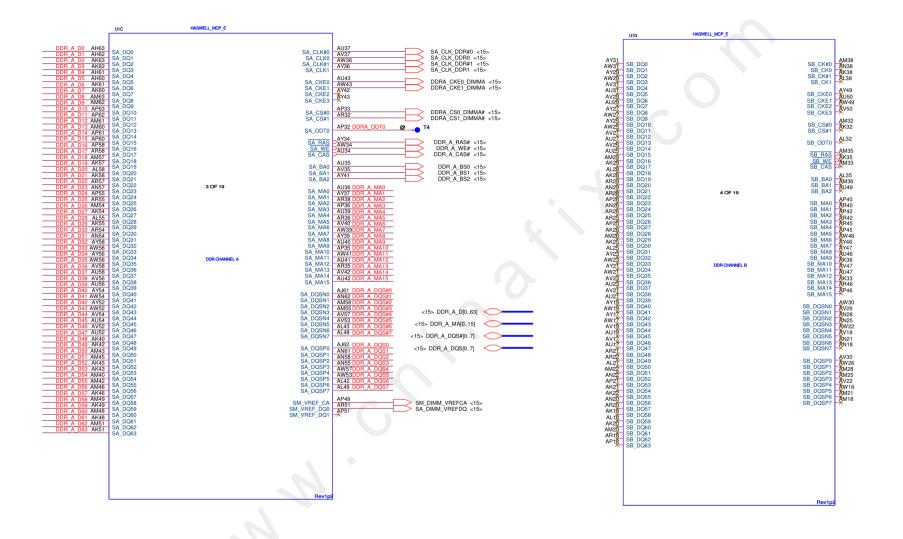


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Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title	HSW MCP(1/11) DDI,MSIC,XD	\overline{P}	
00404040				SIZE Custorr	Document Number LA-A321PR02	Rev 0.2	

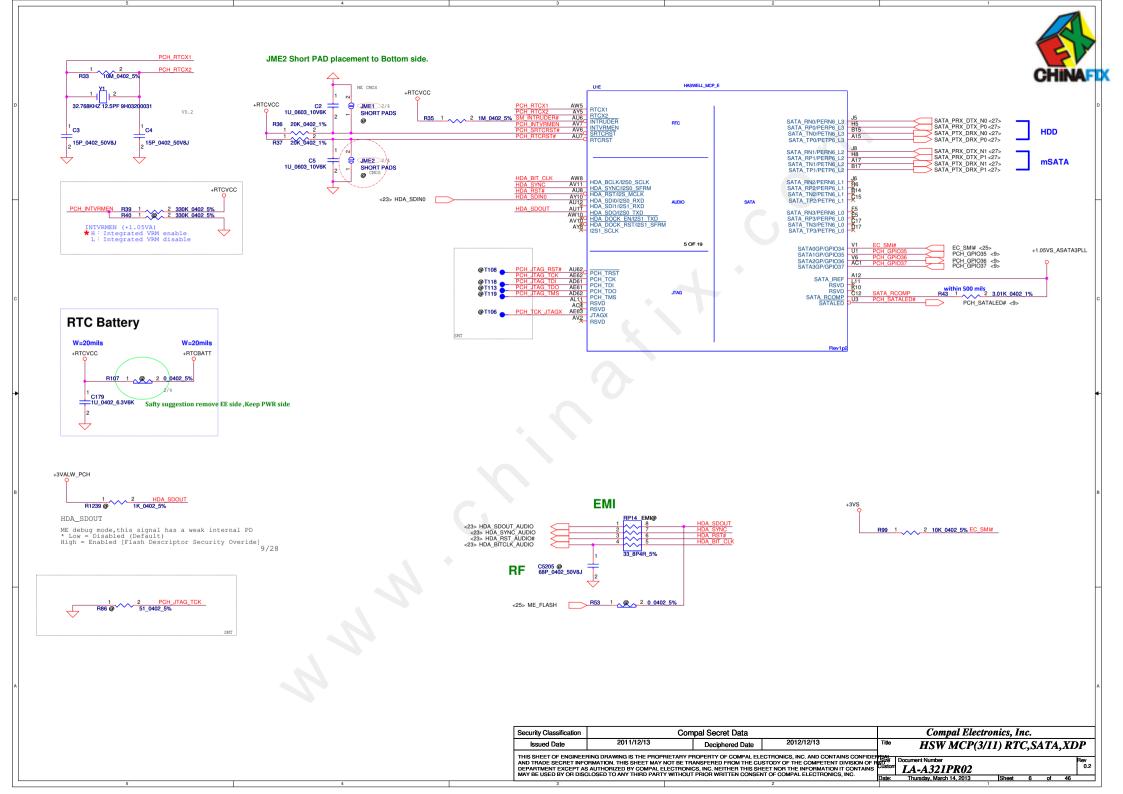
Date: Thursday, March 14, 2013

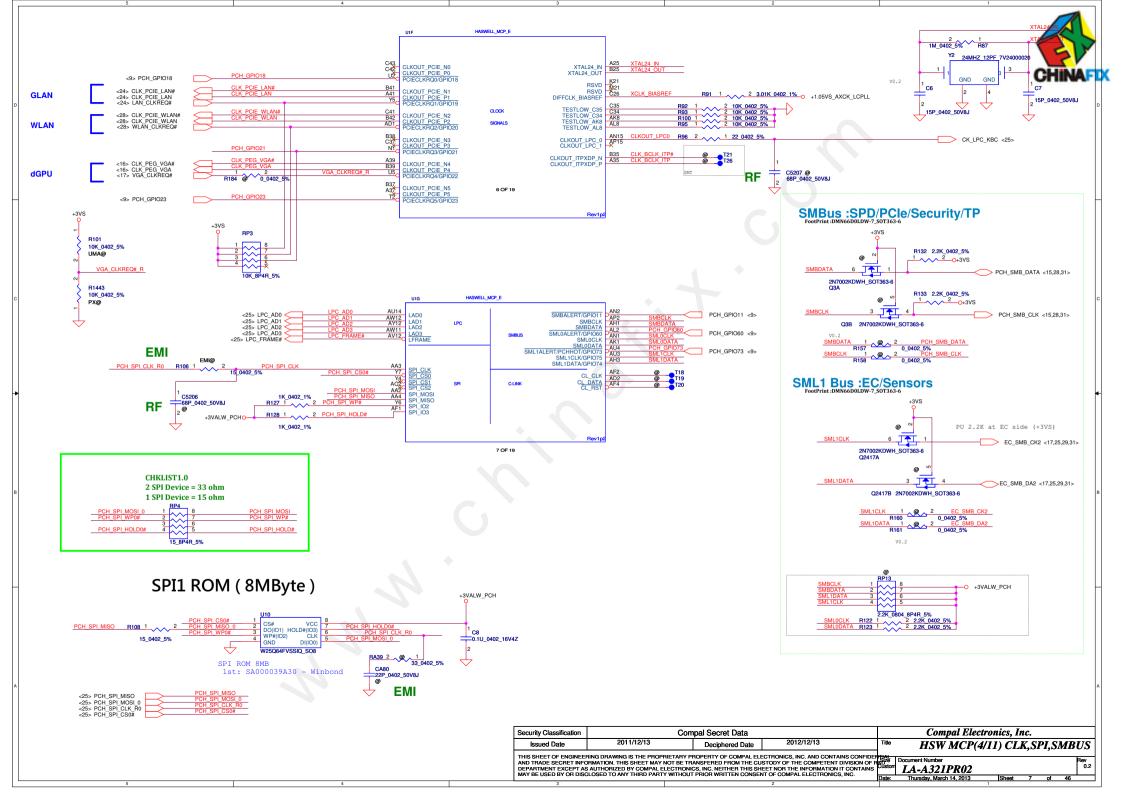
Sheet 4 of 46

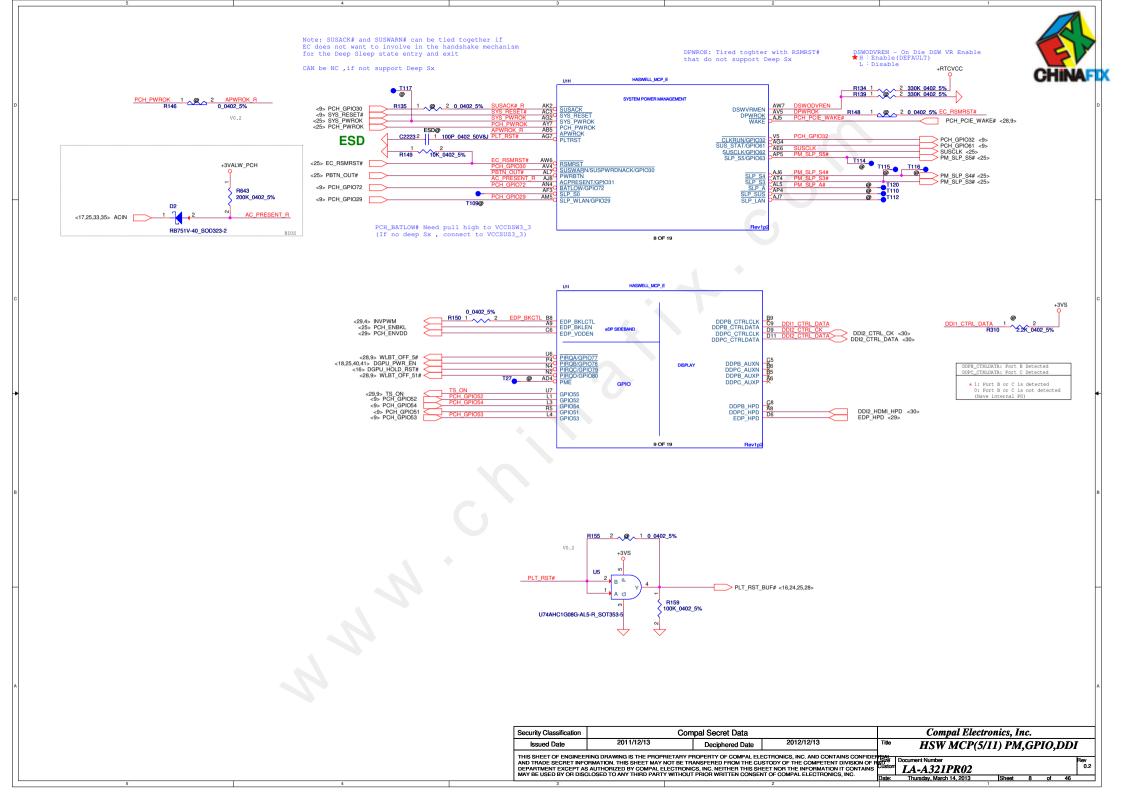


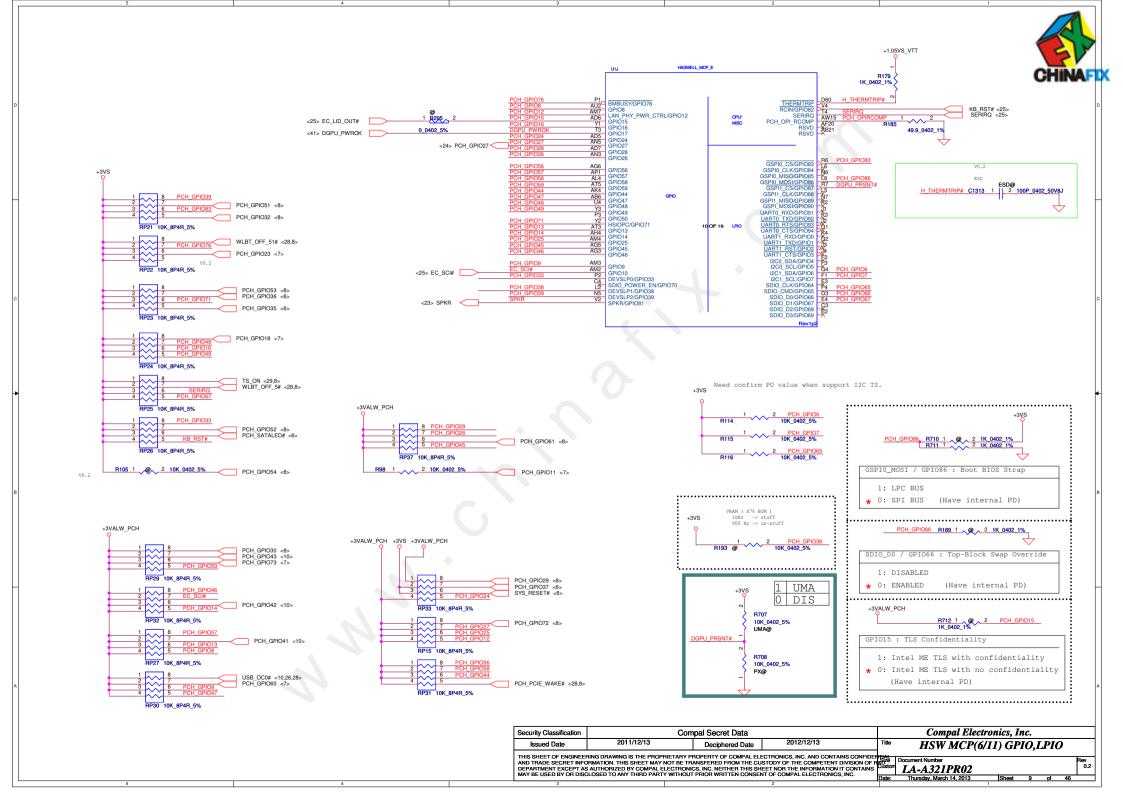


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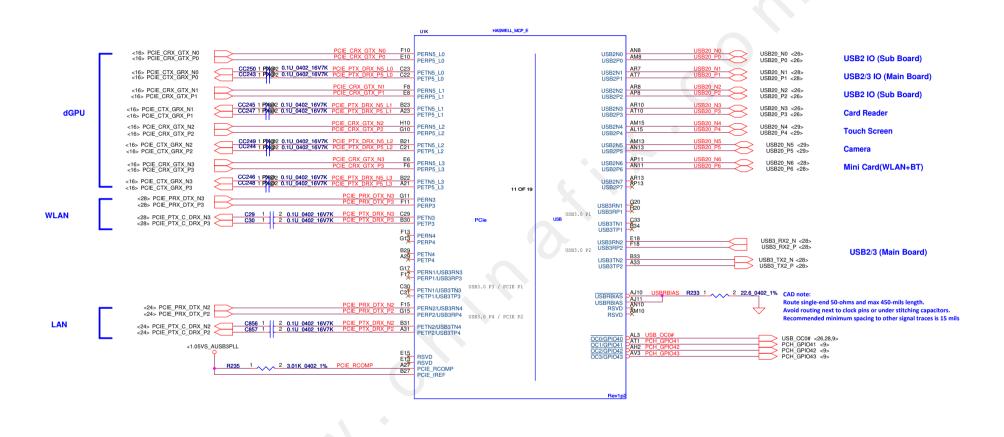




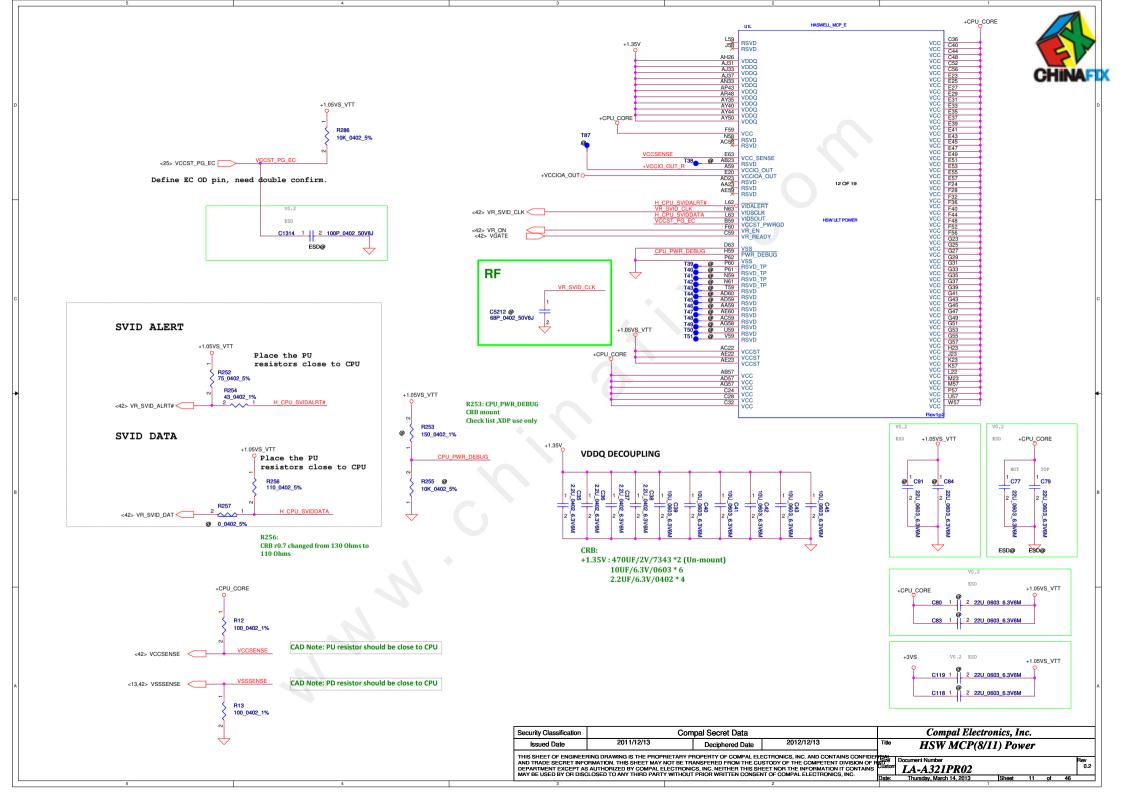




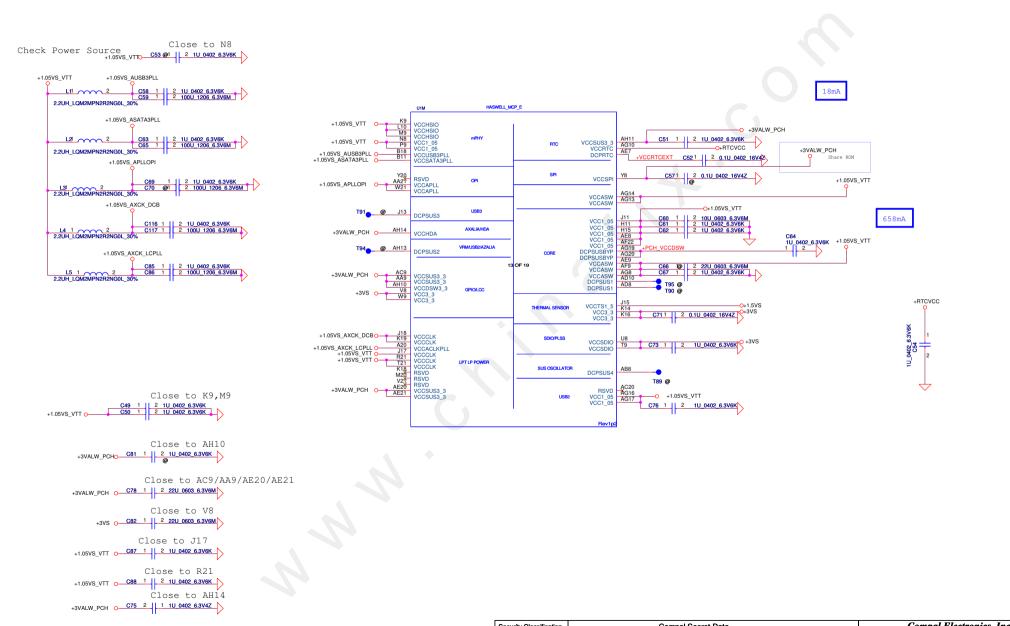




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Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title	HSW MCP(7/	(1) PC	IE,U	SB			
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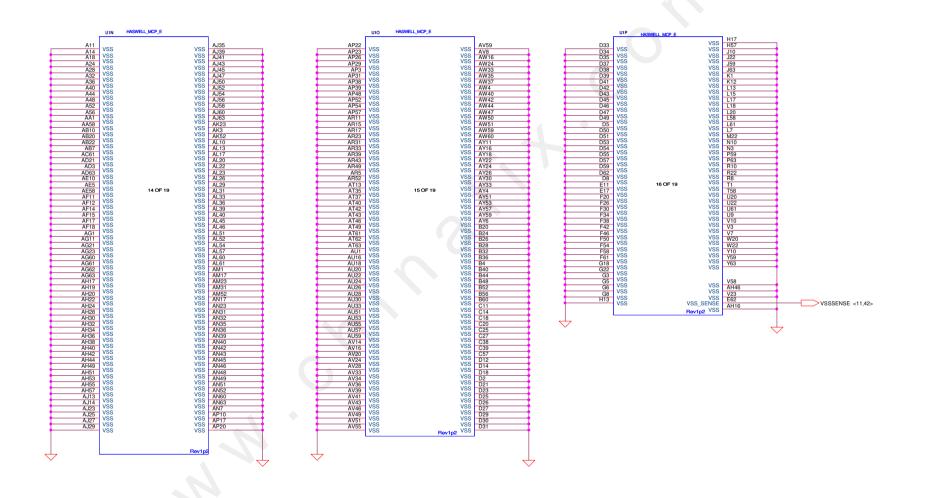






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Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title HSW MCP(9/11) Power					
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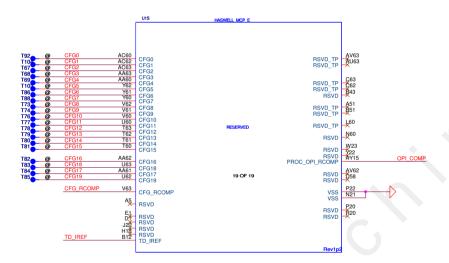




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Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title	HSW MCP(10/11) GND		
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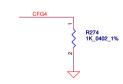




CFG Straps for Processor



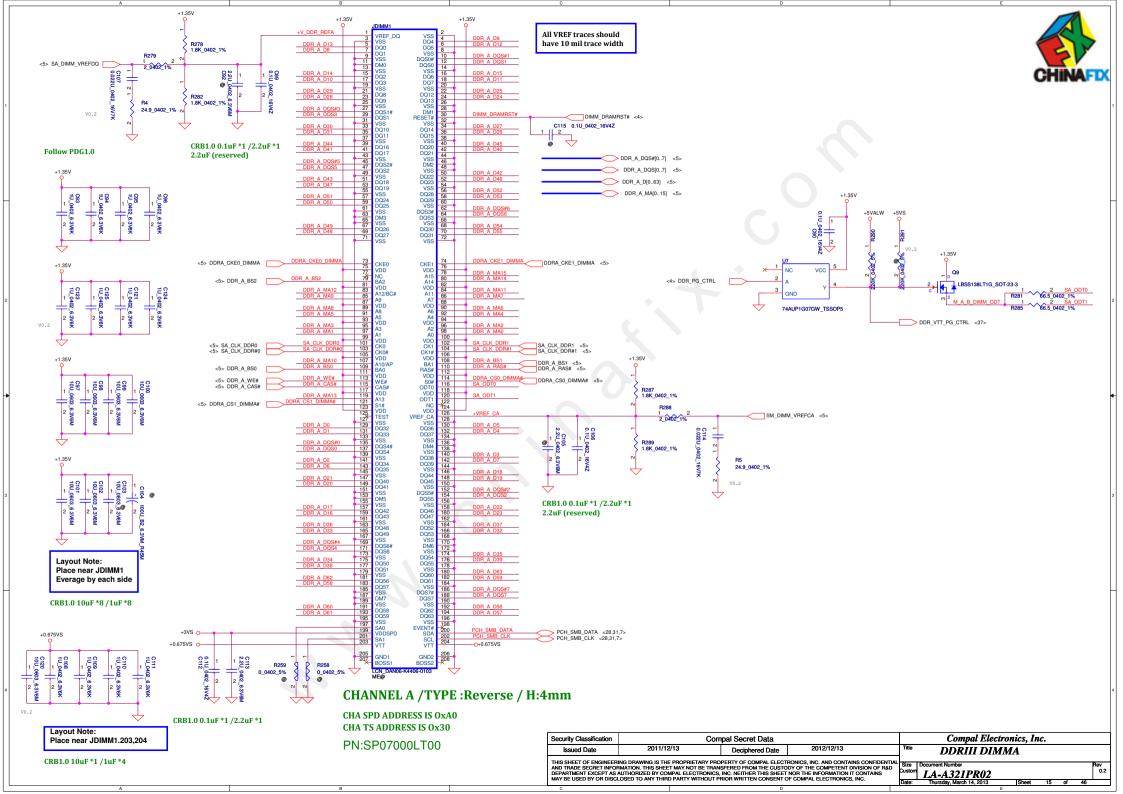
Physical	Debug Enable (DFX Privacy)
Physical CFG3	1: DISABLED
	0: ENABLED; SET DFX ENABLED BIT IN DEBUG INTERFACE MSR



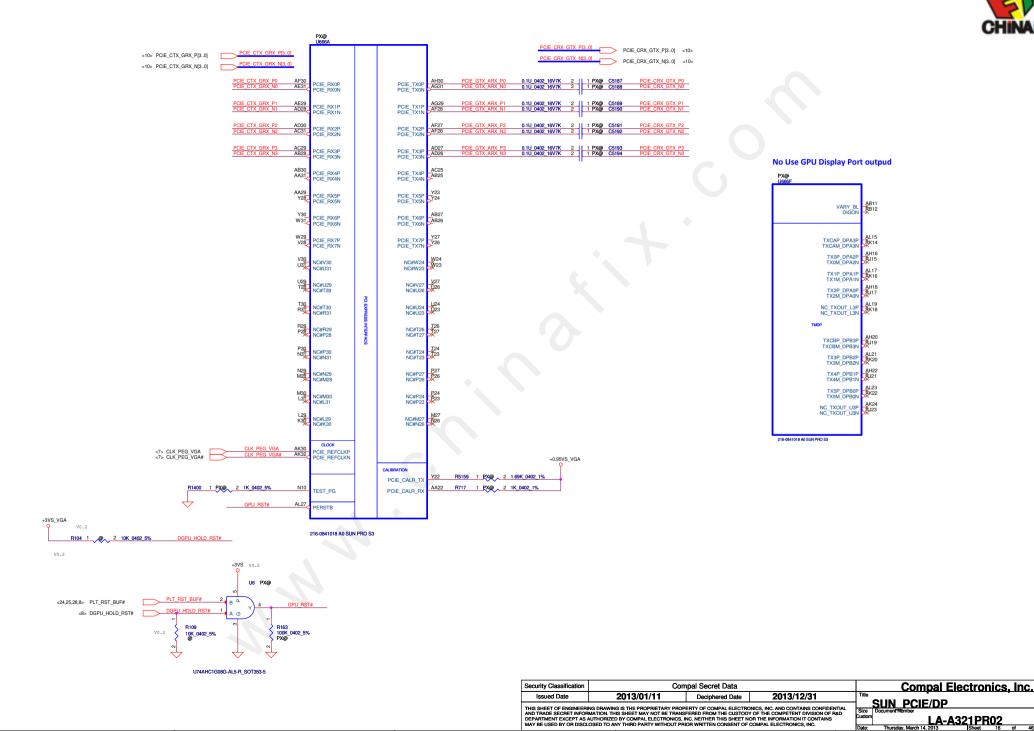
Display Port Presence Strap						
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port					
	0 : Enabled; An external Display Port device is connected to the Embedded Display Port					

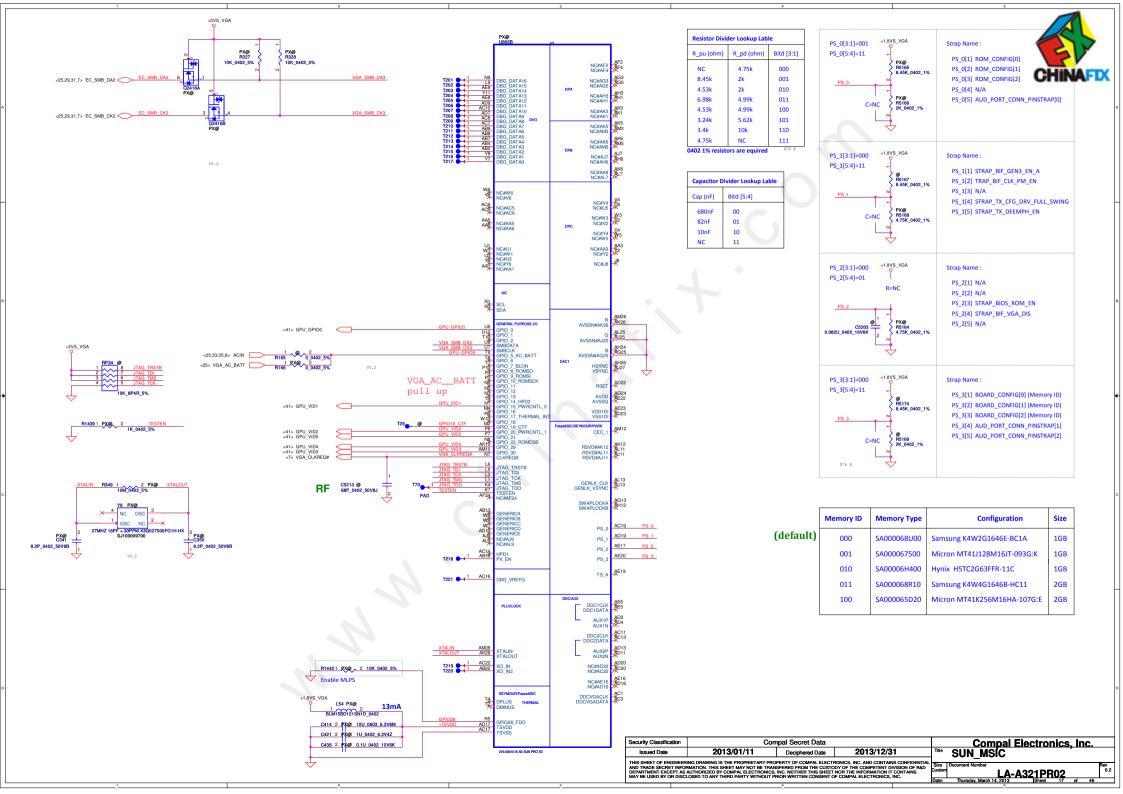
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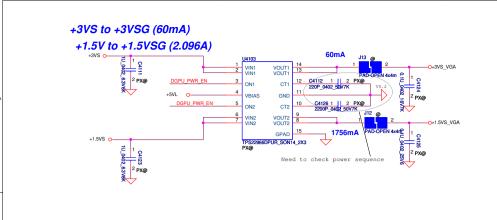
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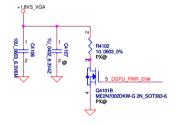




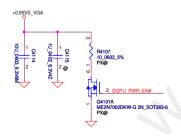


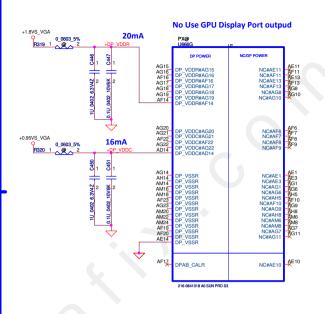


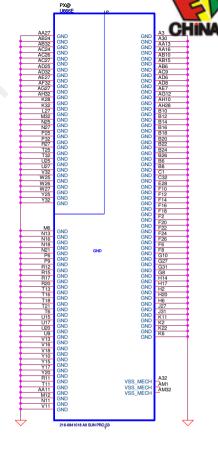
+1.8VALW to +1.8VSG (818mA)

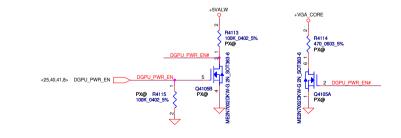


+0.95VALW to +0.95VSG (4.016A)



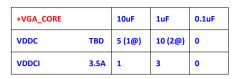






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+0.95VS_VGA		10uF	1uF	0.1uF
PCIE_VDDC	2.5A	2 (1@)	5 (1@)	0
BIF_VDDC	1.4A	0	0	0
SPLL_VDDC	100mA	1	1	1

+1.5VS_VGA		10uF	1uF	0.1uF
VDDR1	1.5A	3	5	5

+1.8VS_VGA		10uF	1uF	0.1uF
PCIE_PVDD	100mA	1	1	1
MPLL_PVDD	130mA	1	1	1
SPLL_PVDD	75mA	1	1	1
VDDR4	(300mA)	0	0	0
VDD_CT	13mA	1	1	1
+TSVDD	13mA	1	1	1
+DP_VDDR		0	0	0
+DP_VDDC		0	0	0

10uF

25mA 0

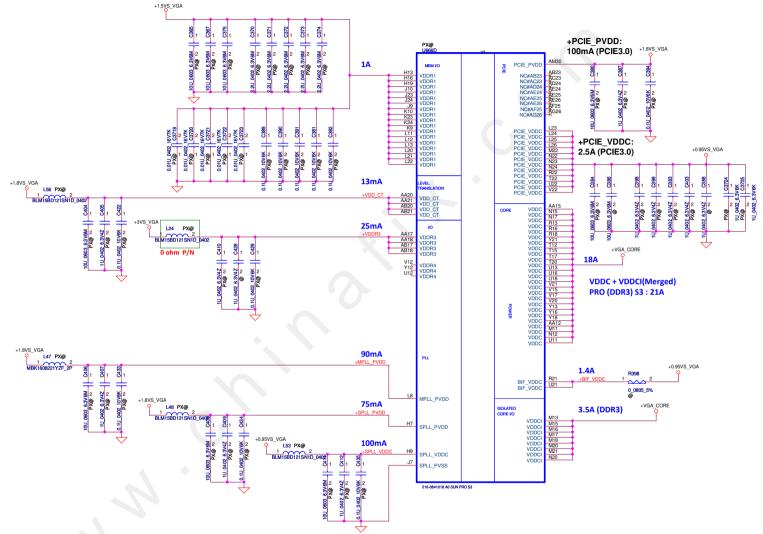
1uF

2 (1@)

0.1uF

+3VS_VGA

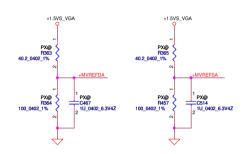
VDDR3



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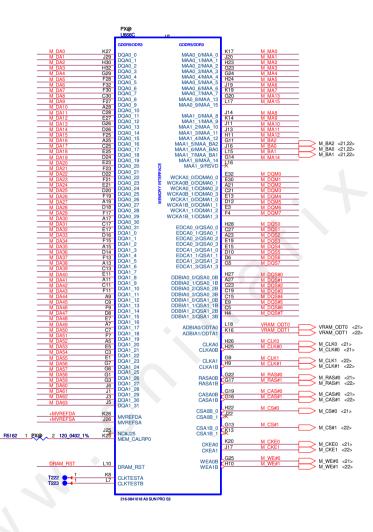








Place close to GPU (within 25mm) and place componment close to each other



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Tries SUN_MEM

Tries SUN_MEM

SUN_MEM

SUN_MEM

SUN_MEM

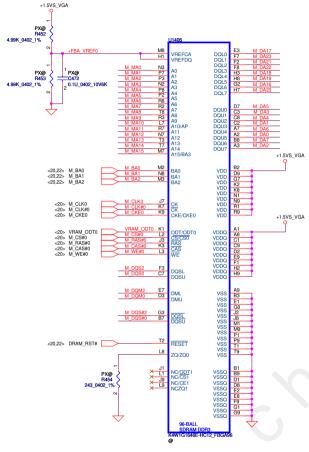
SUN_MEM

Sun_Decompart Namibor Obulent Namibo

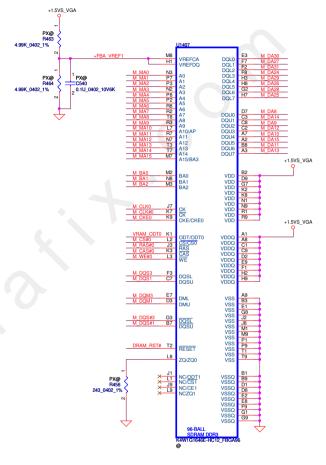
Memory Partition A - Lower 32 bits

<20.22> M_DA[63.0] M_DA[63.0]
<20.22> M_MA[15.0] M_MA[15.0]
<20.22> M_DOM[7.0] M_DOM[7.0]
<20.22> M_DOS[7.0] M_DOS[7.0]
M_DOS[7.0] M_DOS[7.0]







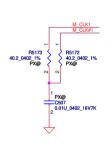


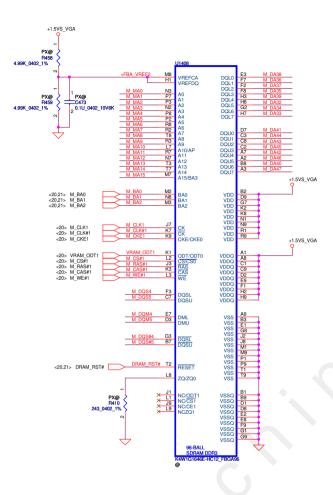


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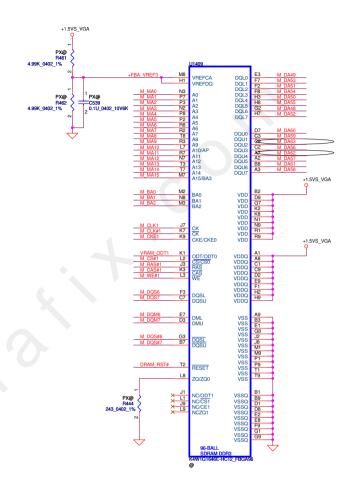
Memory Partition A - Upper 32 bits





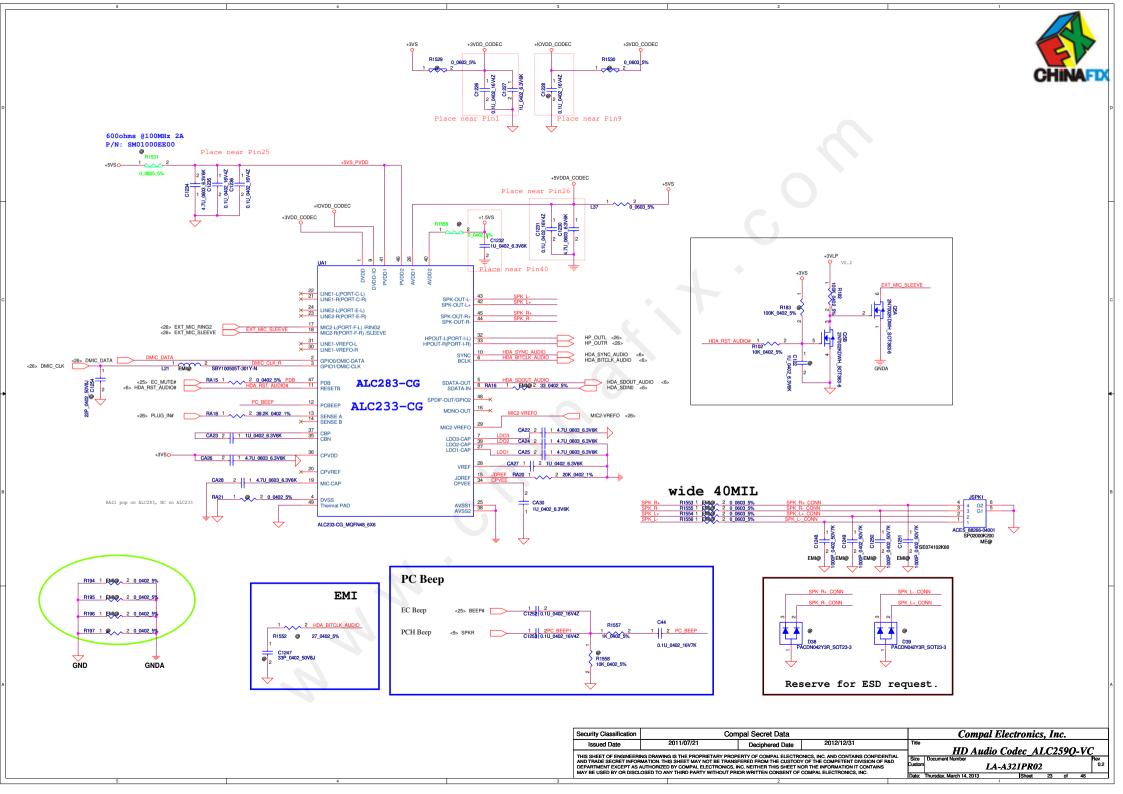


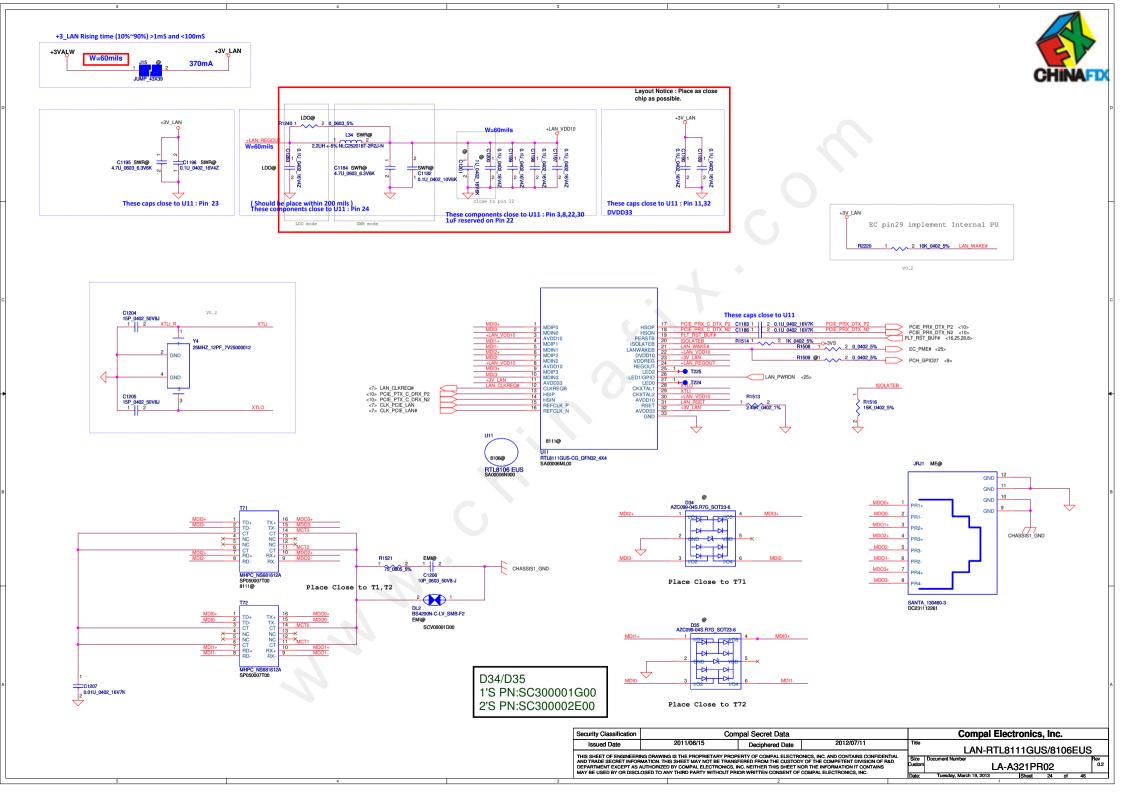


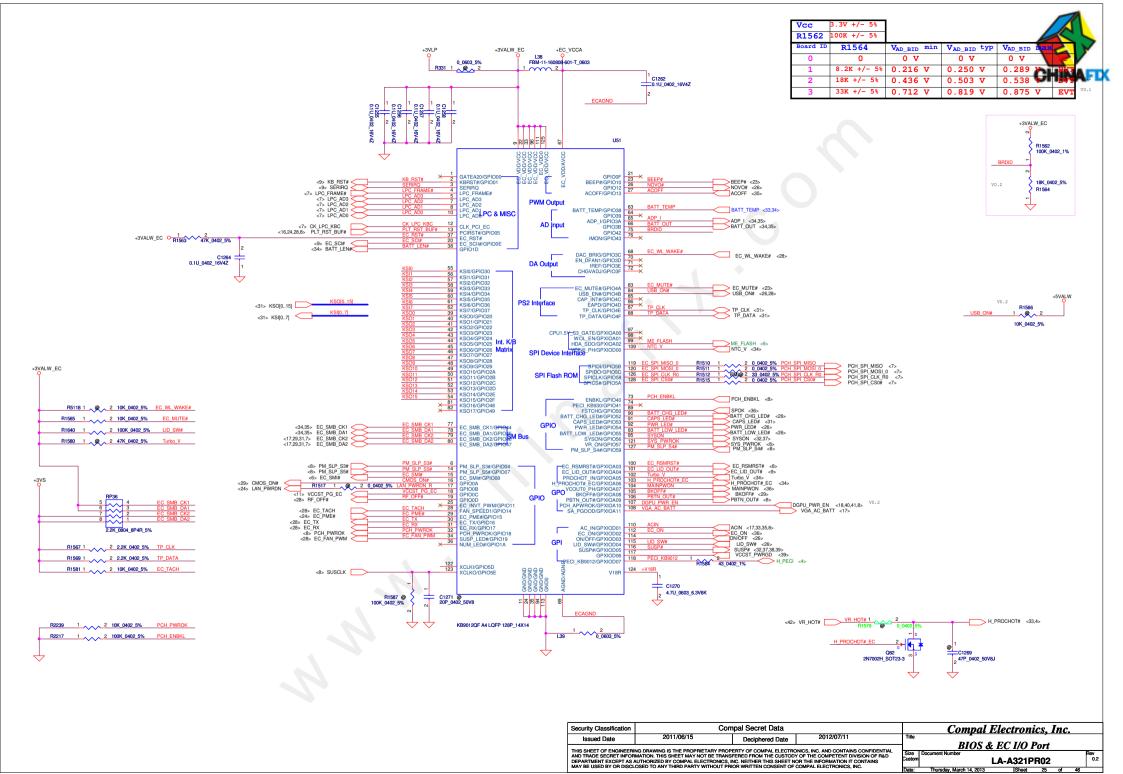




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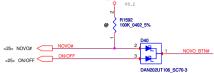






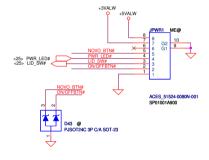
I/O Board CONN.

Connector: 0.3A / pin

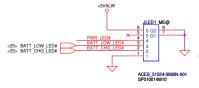


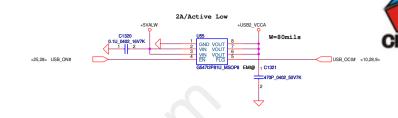


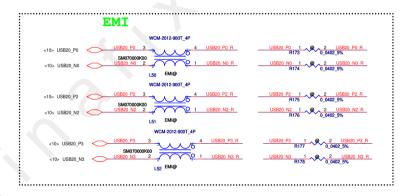
Power Board

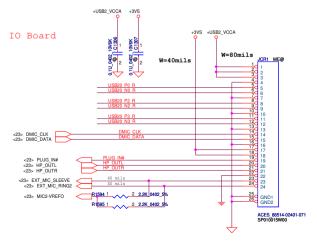


LED Board

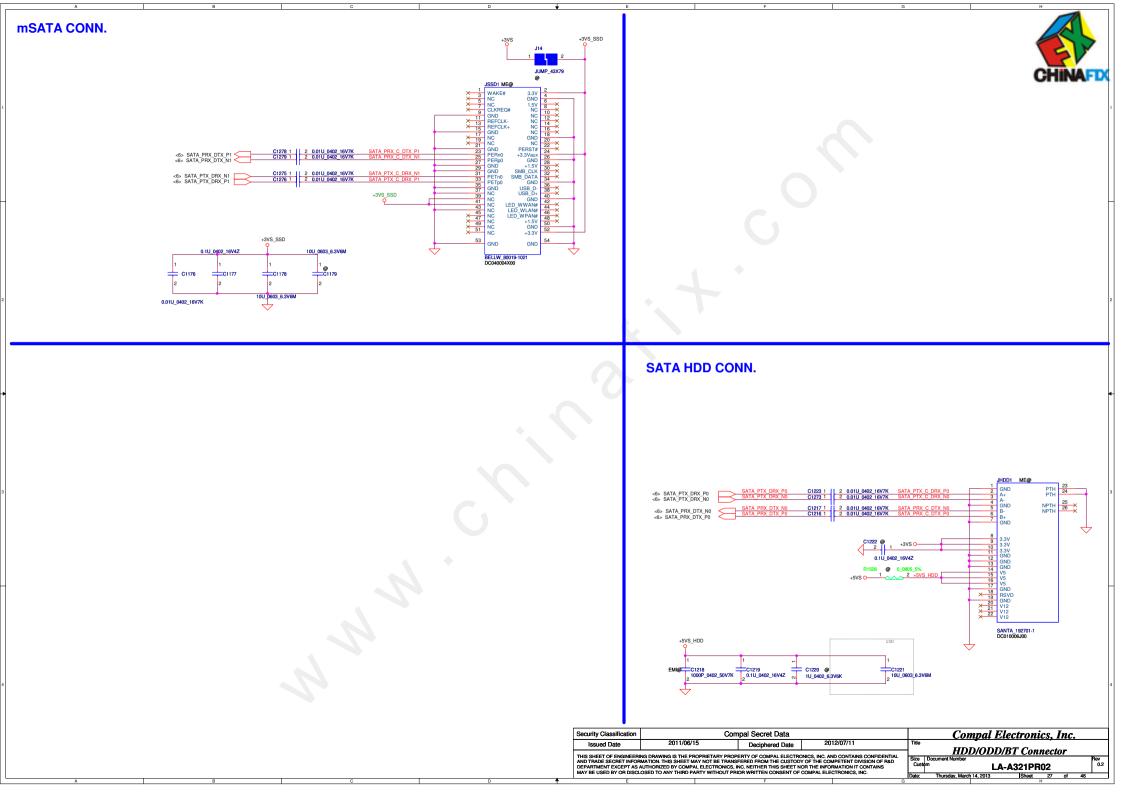


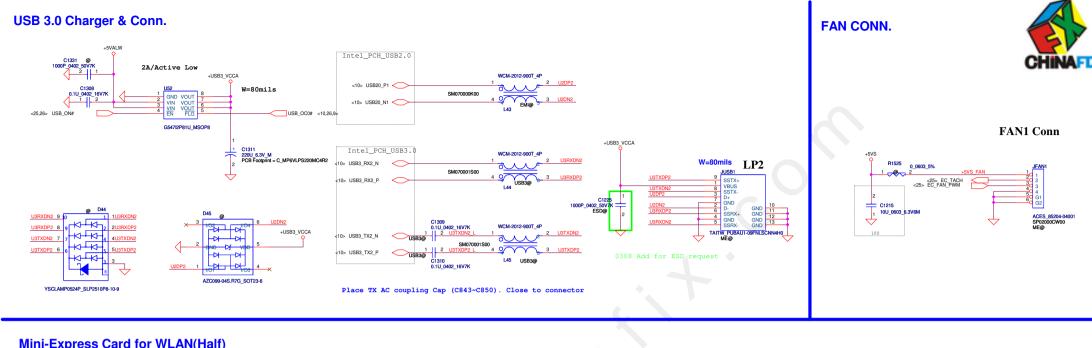




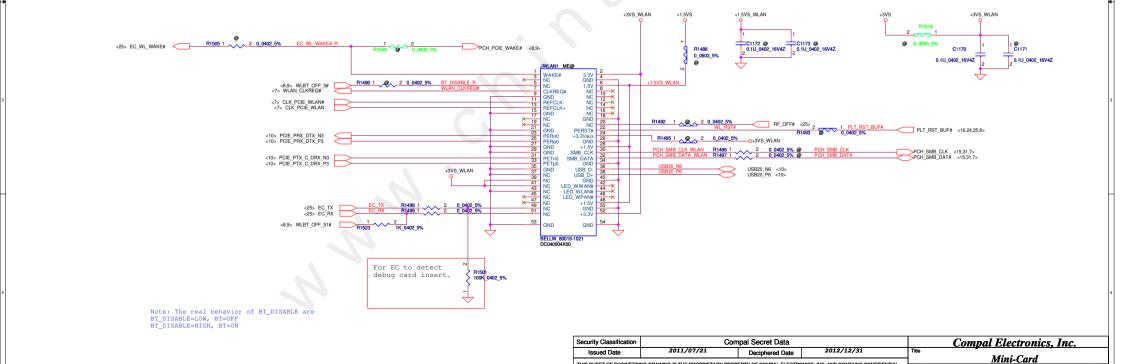


Security Classification	Compal Secret Data 2011/06/15 Parish and Parish 2012/07/11				Compal Electronics, In	c.	
Issued Date	Issued Date 2011/06/15 Deciphered Date 2012/07/11				ROM/KBD/PWR/CR/LED/TP Co	nn.	
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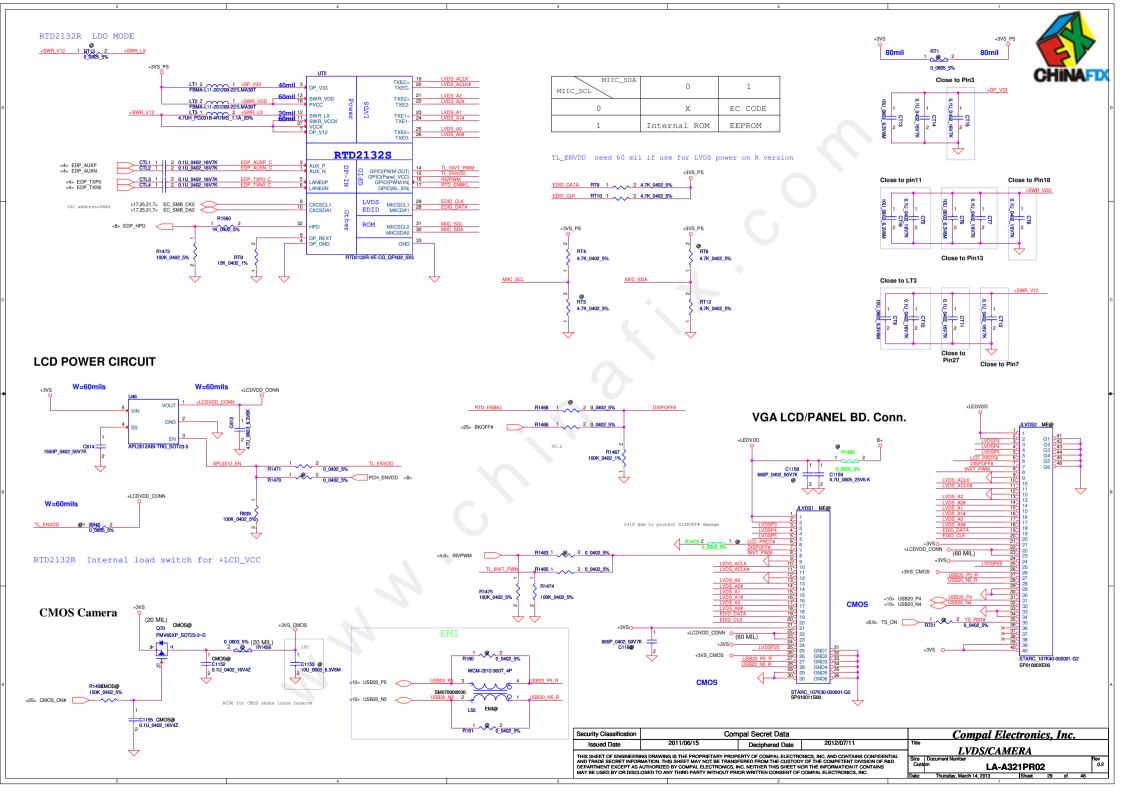


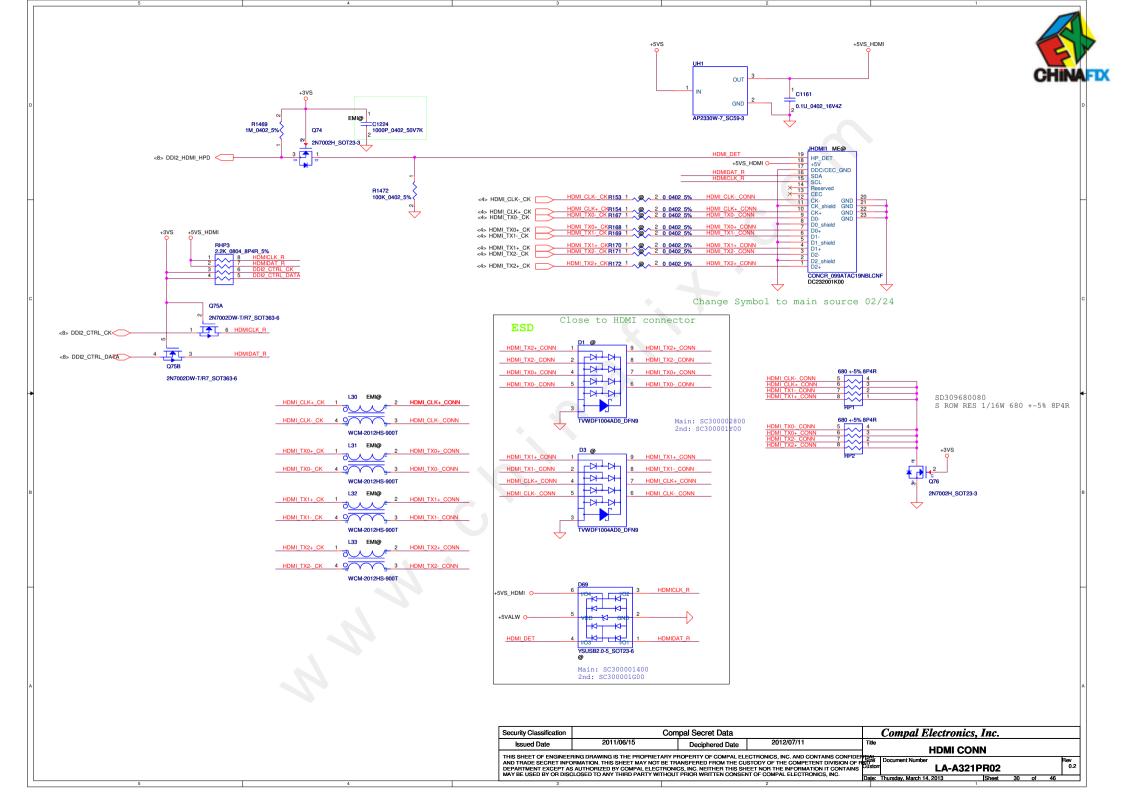
Mini-Express Card for WLAN(Half)

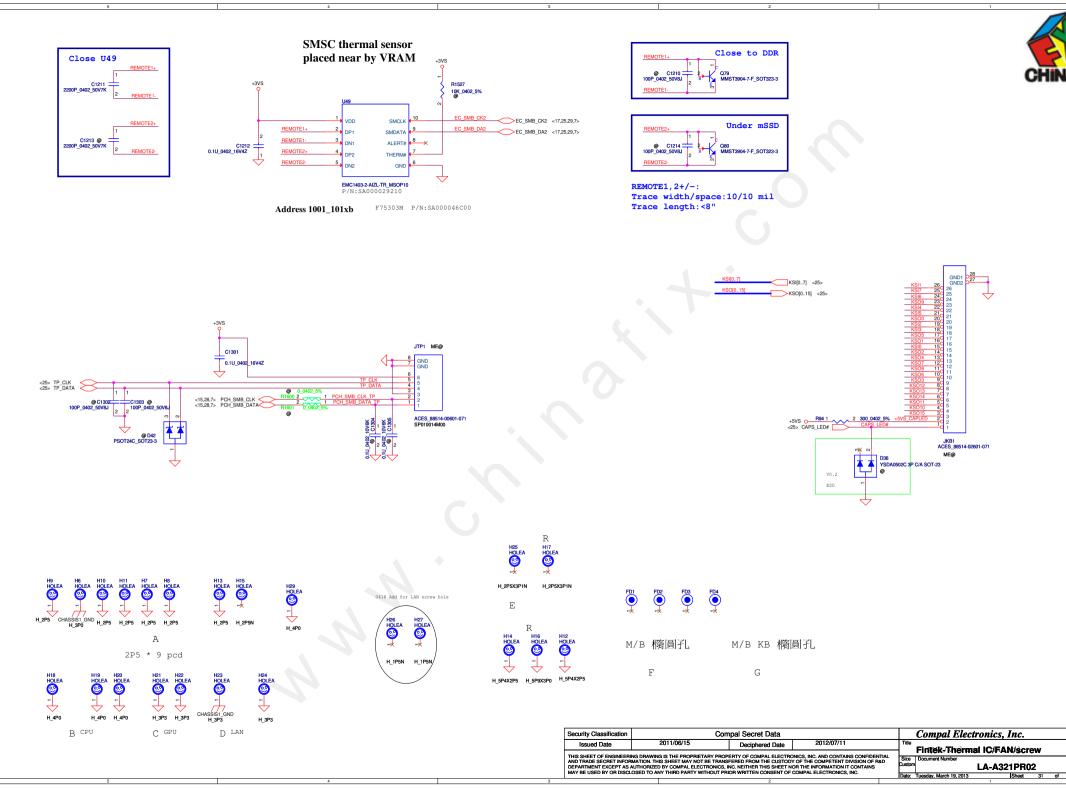


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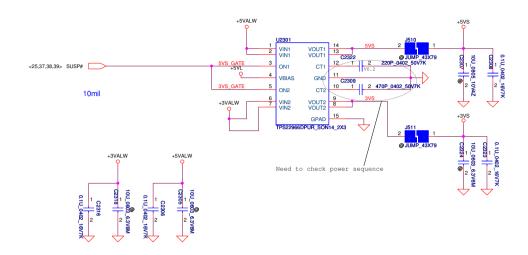


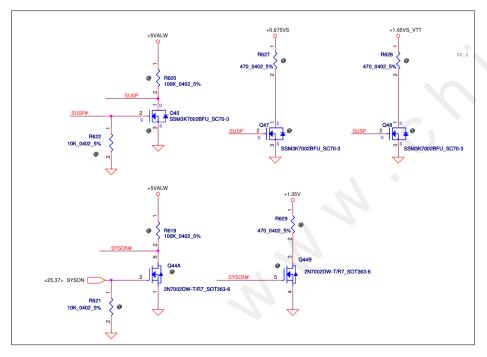


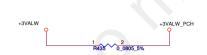
+5VALW TO +5VS +3VALW TO +3VS

+3VALW TO +3VALW(PCH AUX Power)
+5VALW TO +5VALW(PCH AUX Power)

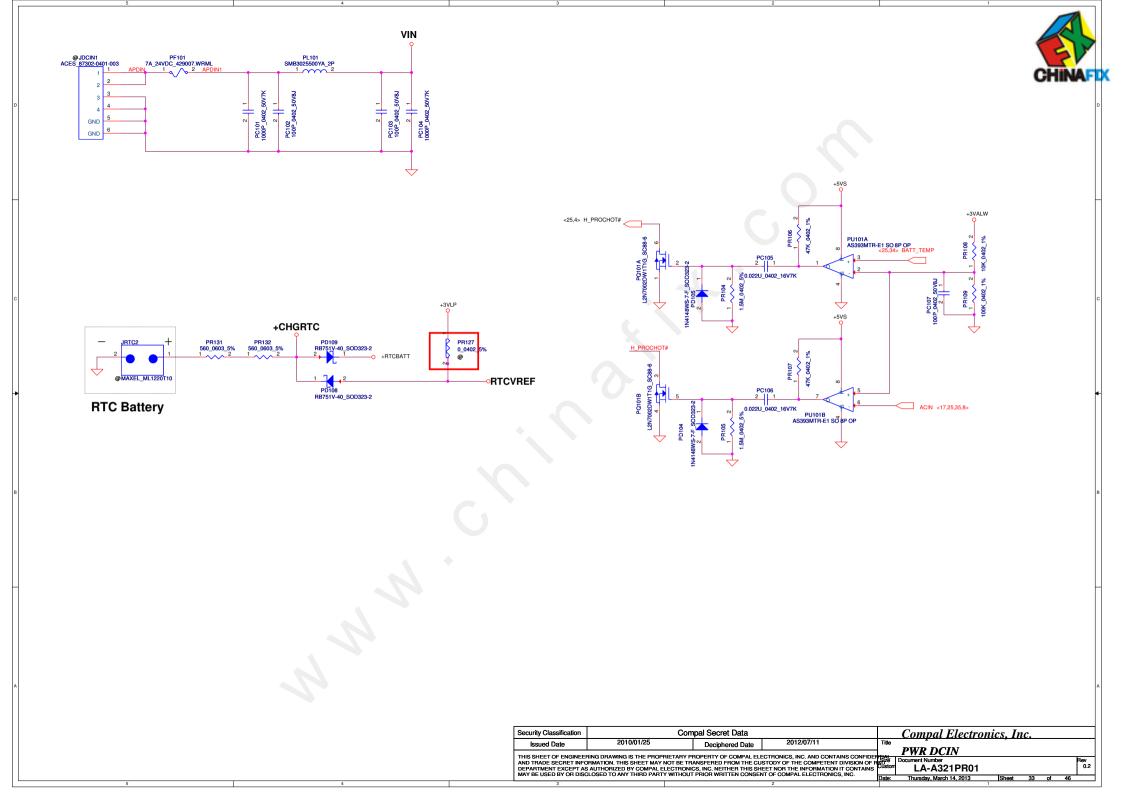


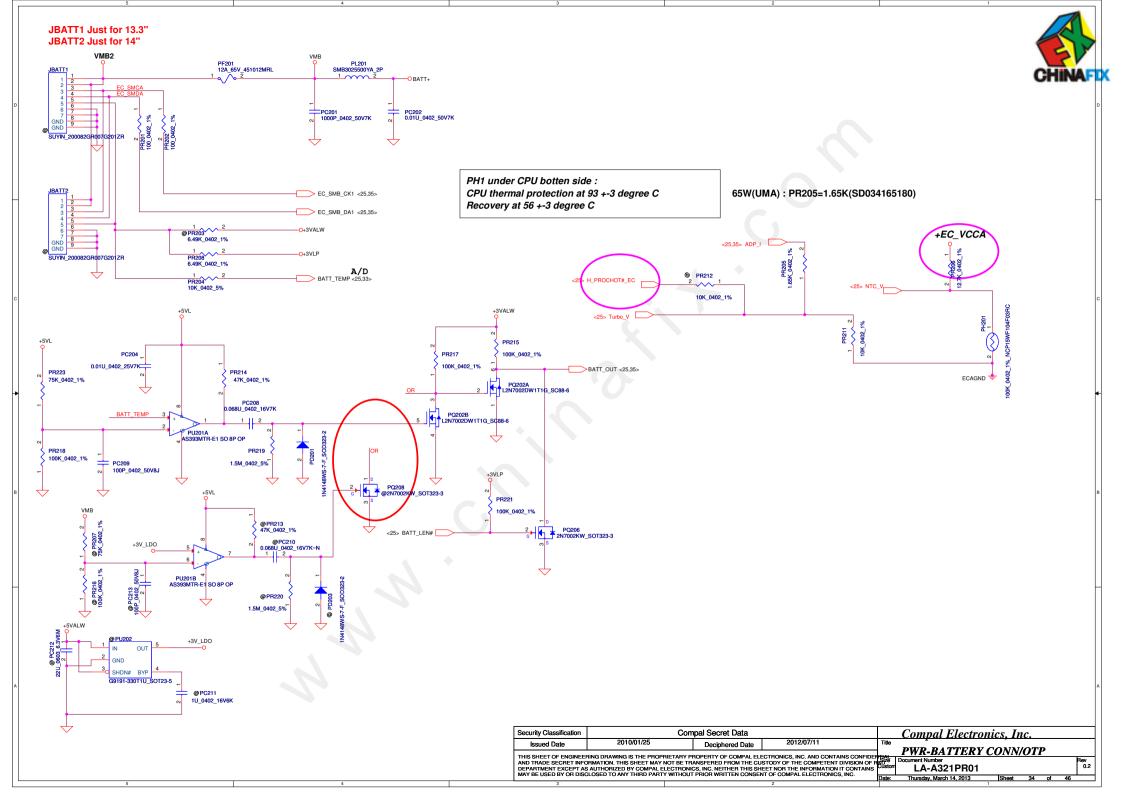


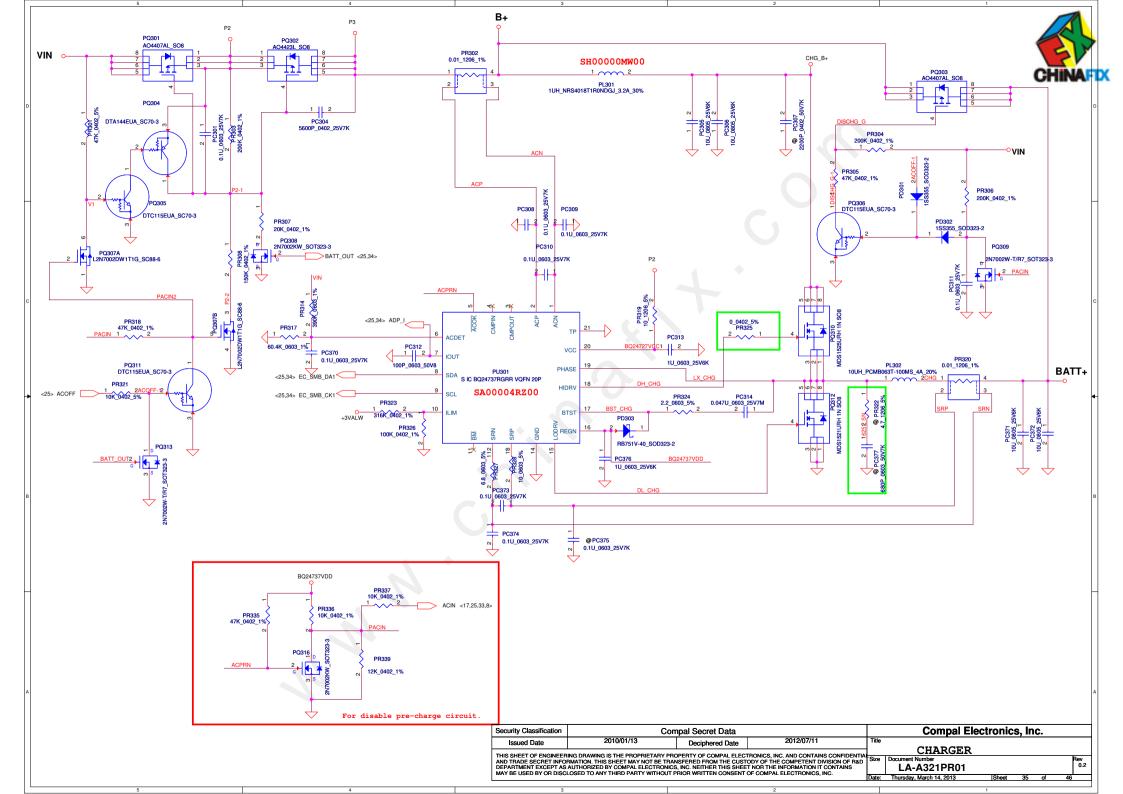


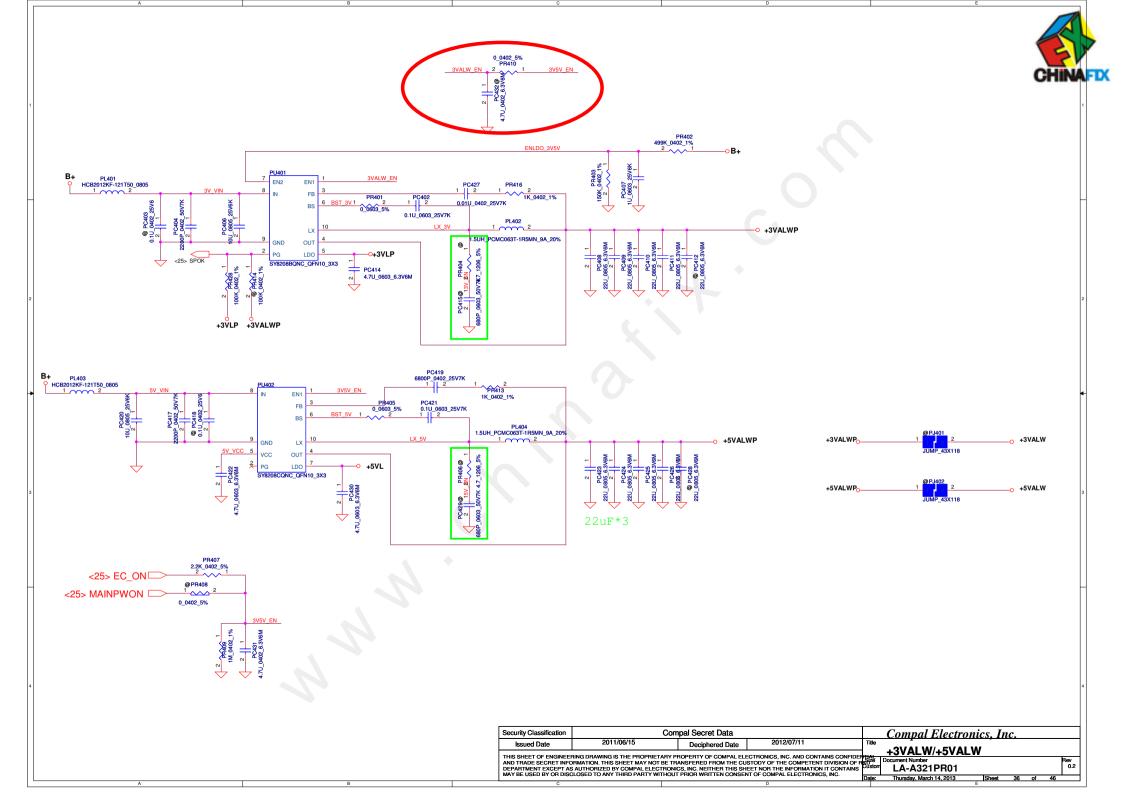


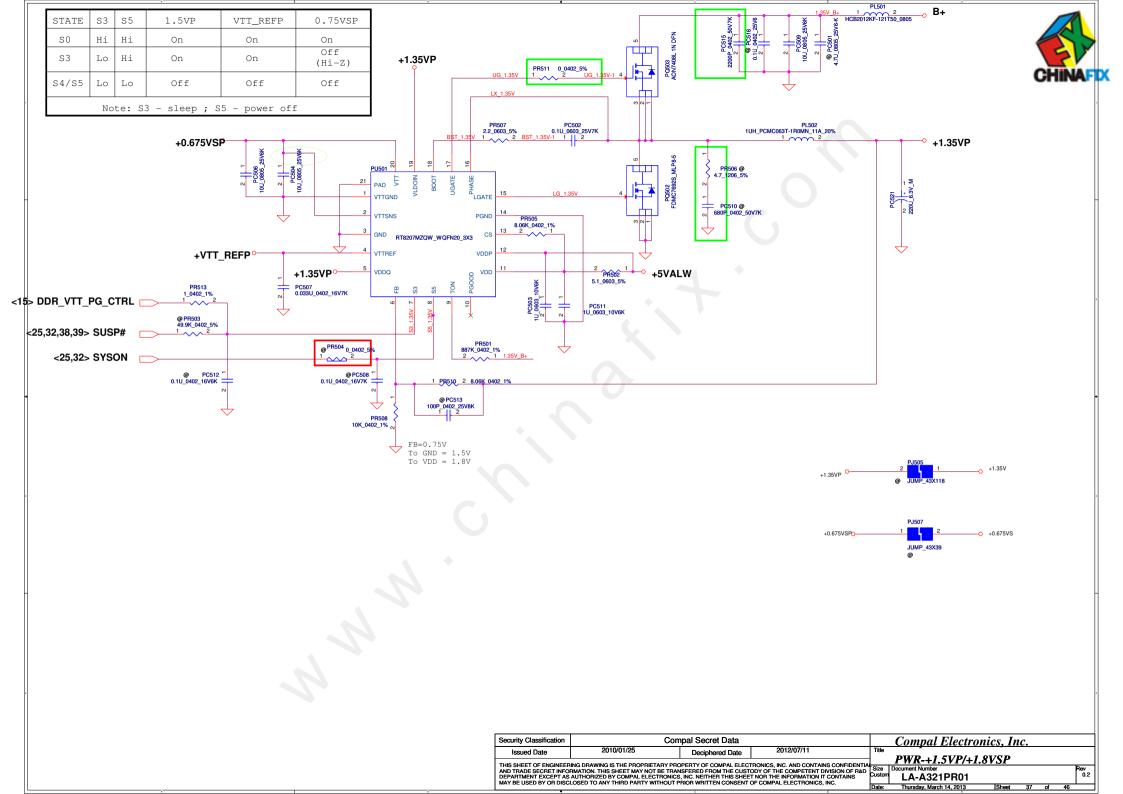
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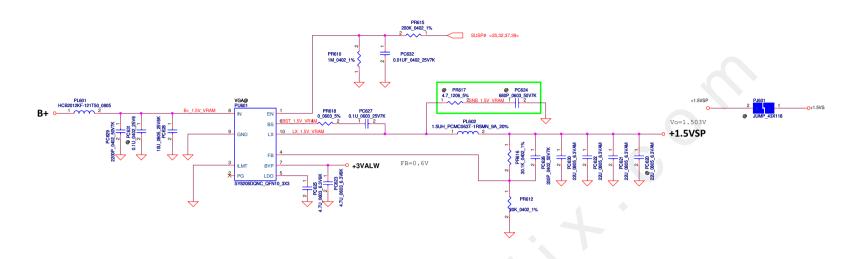


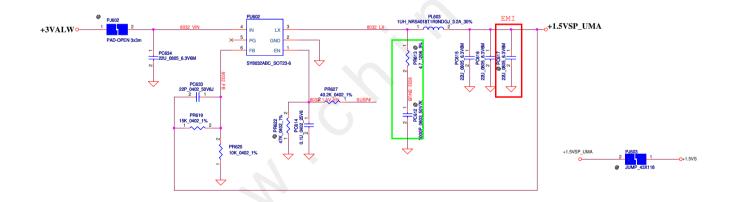








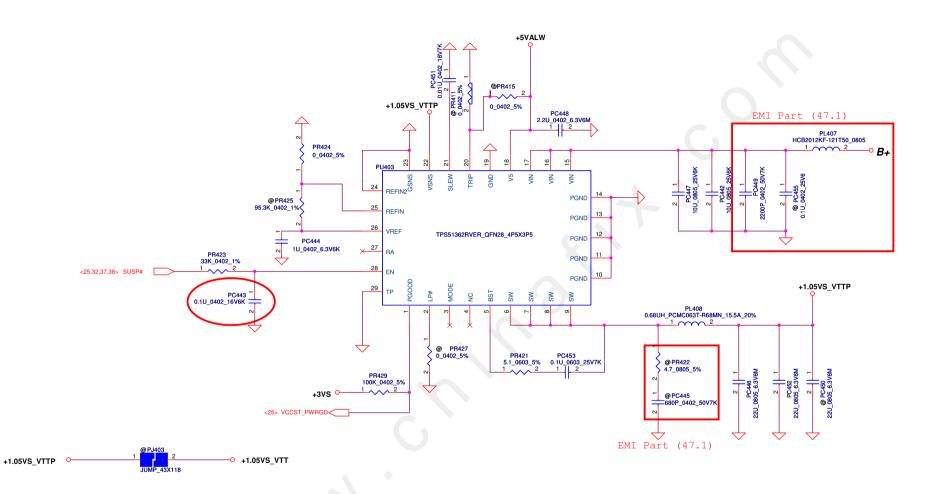




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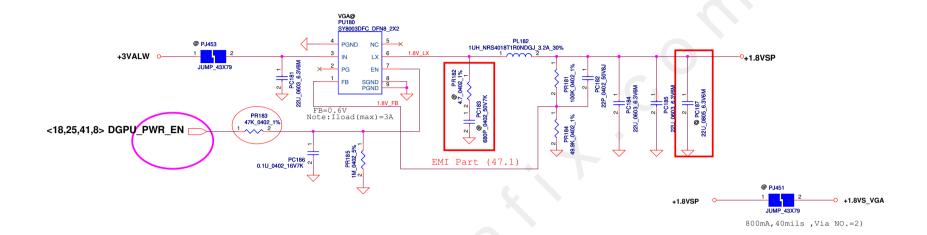


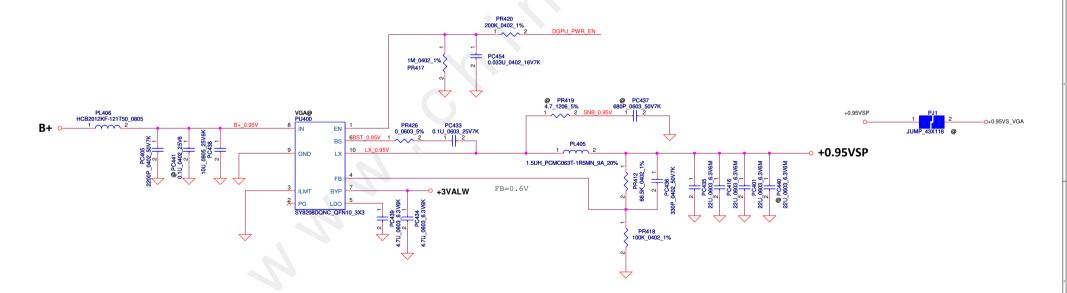


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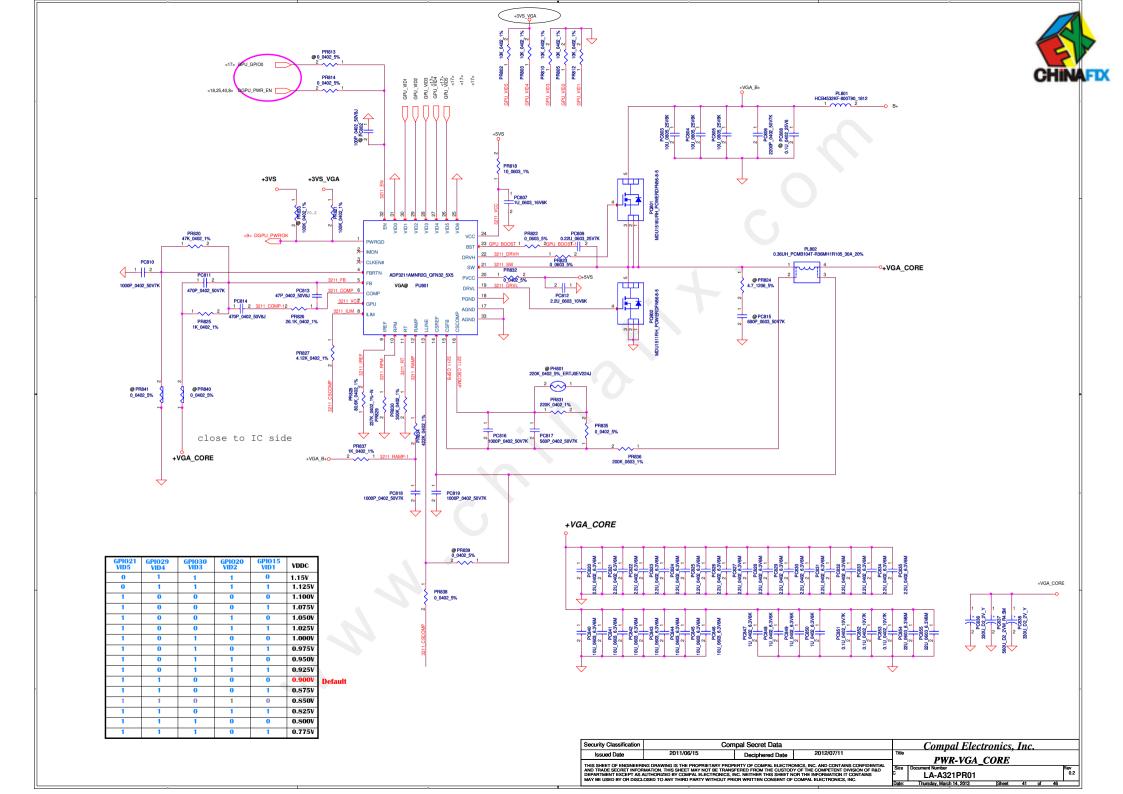


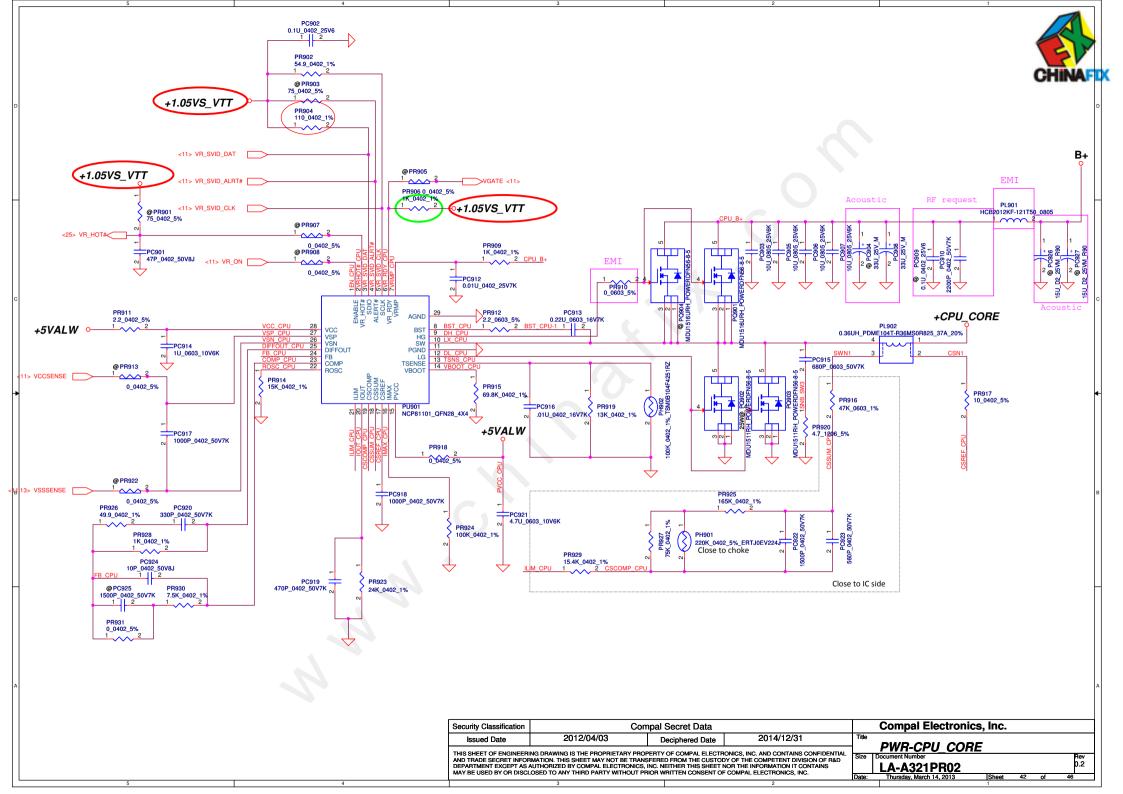
1.8V controller (35.15), Support component (35.16





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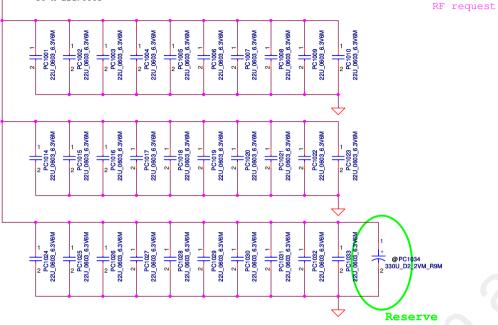






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Version change list (P.I.R. List)

Page 1 of 1 for PWR



Item	Modify List	PG#	Reason for change	Date	Phase
1	For HW sequence	44	ADD PR821 100K ohm pull high +VS_VGA	3/11	SDV
2	For HW sequence	37	Remove PC512 0.1U_0402	3/11	SDV
3	For HW sequence	39	Change PC443 to 0.1U_0402	3/11	SDV
4	For HW sequence	40	Change PC186 to 0.1uf	2/7	SDV
5	For HW sequence	40	Change PC454 to 0.033uf	2/7	SDV
6	For HW sequence	40	Change PR183 to 47K ohm	2/7	SDV
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C38-AMD KABINI Schematic

Version change list (P.I.R. List)

Page 1 of 1 for HW





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