

Berry IO Board Schematics Document

Arrandale

Intel PCH

2010-02-04

REV : A00

DY : Nopop Component

WWAN : Pop for WWAN

TP : Pop for Touch Panel

<Core Design>



Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Cover Page

Size
A3

Document Number

Berry IO Board

Rev

A00

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REVISION :-1



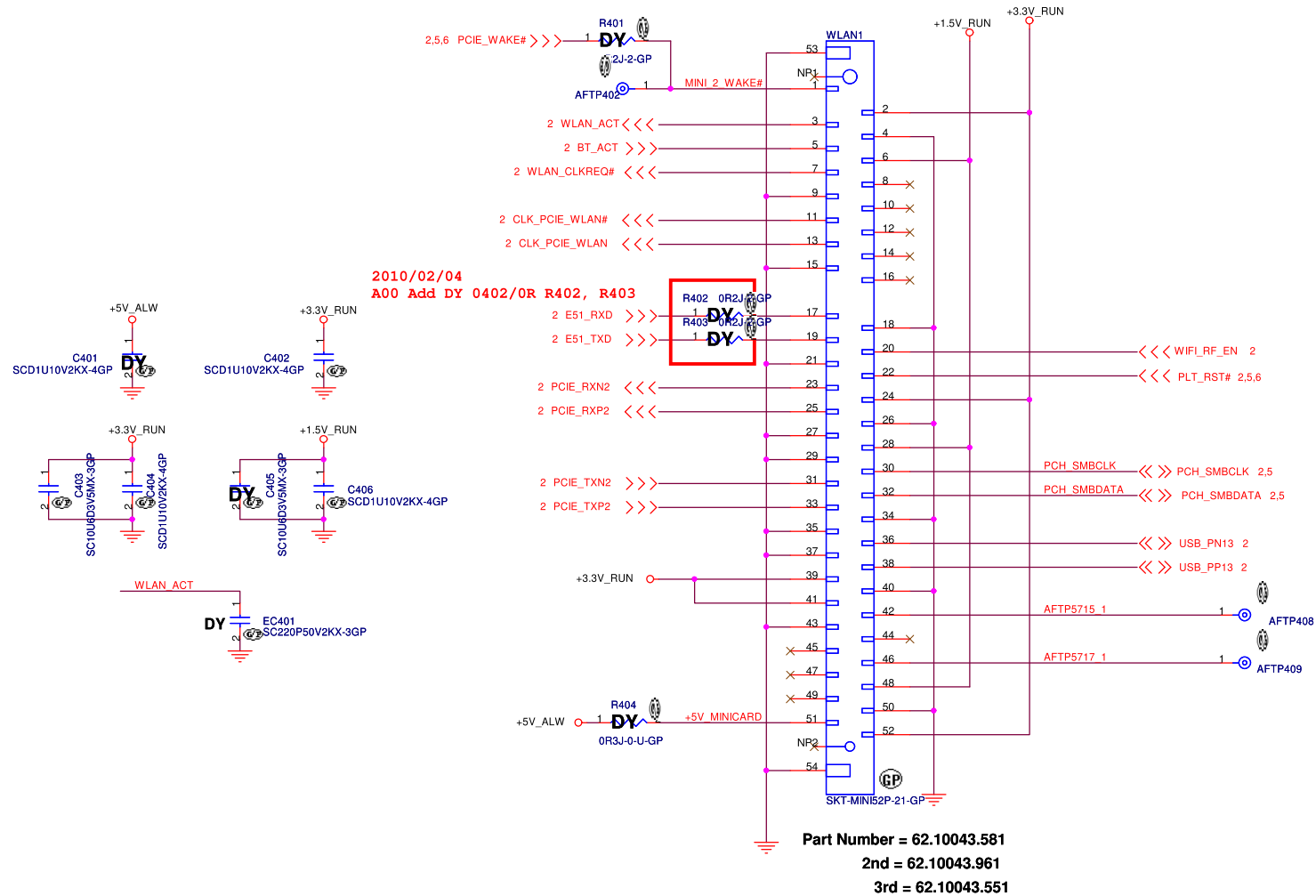
DCin CONN



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SSID = Wireless

Mini Card Connector(802.11a/b/g)



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BATT CONN			A00
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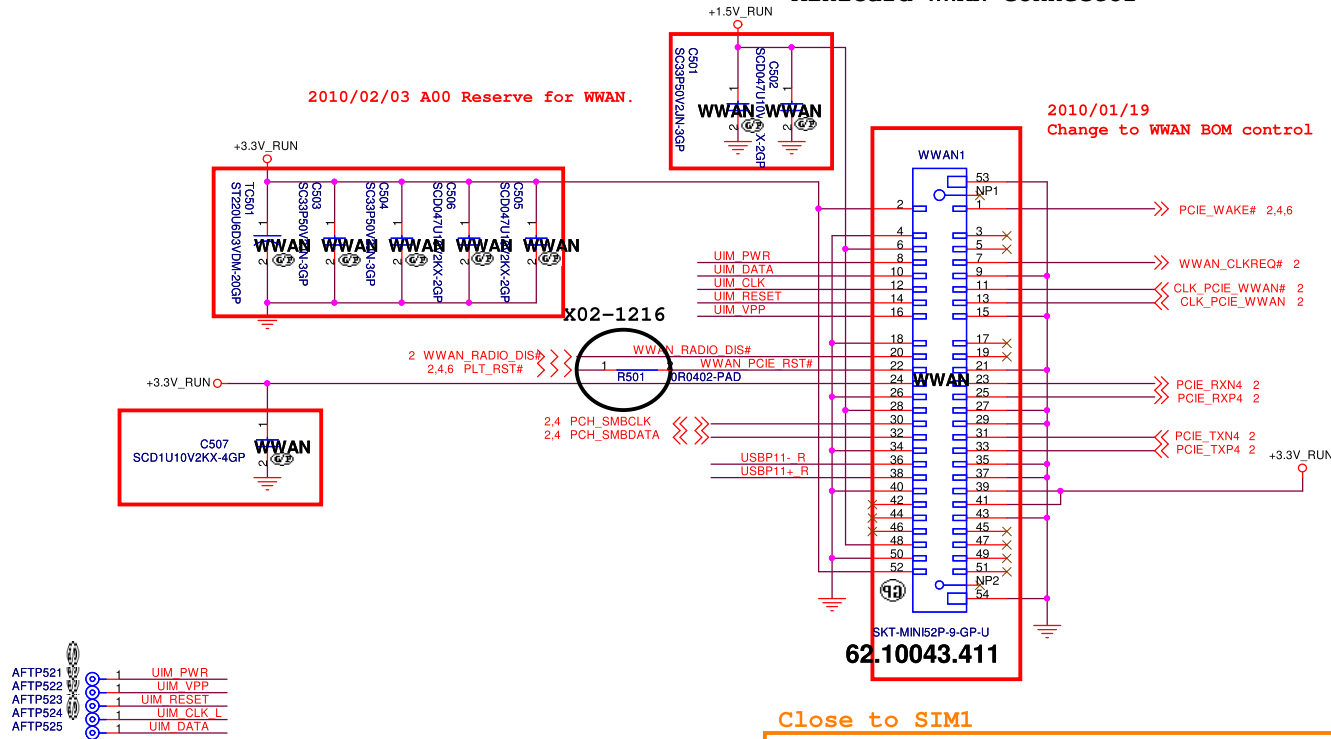
Layout note: Place caps C401~C407 close WWAN1 connector.

C901 Please near WWAN1

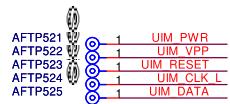
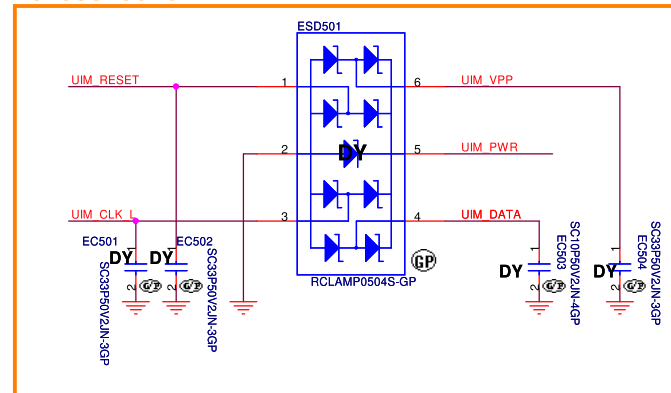
MiniCard WWAN connector

2010/02/03 A00 Reserve for WWAN.

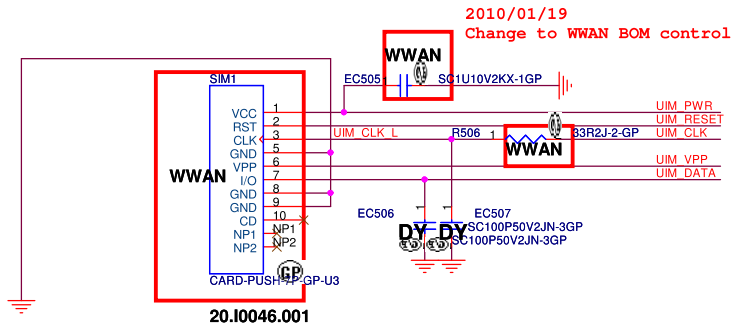
2010/01/19
Change to WWAN BOM control



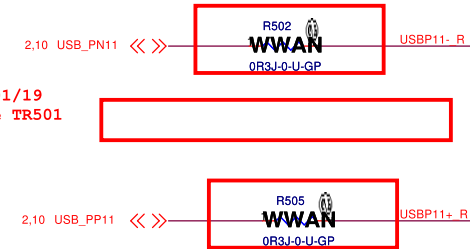
Close to SIM1



2010/01/19
Change to WWAN BOM control



Place caps EC806, EC807 closest to SIM1



2010/02/03 A00 Change R502 and R505 to DY 0603/0 ohm resistor.
Reserve for WWAN.

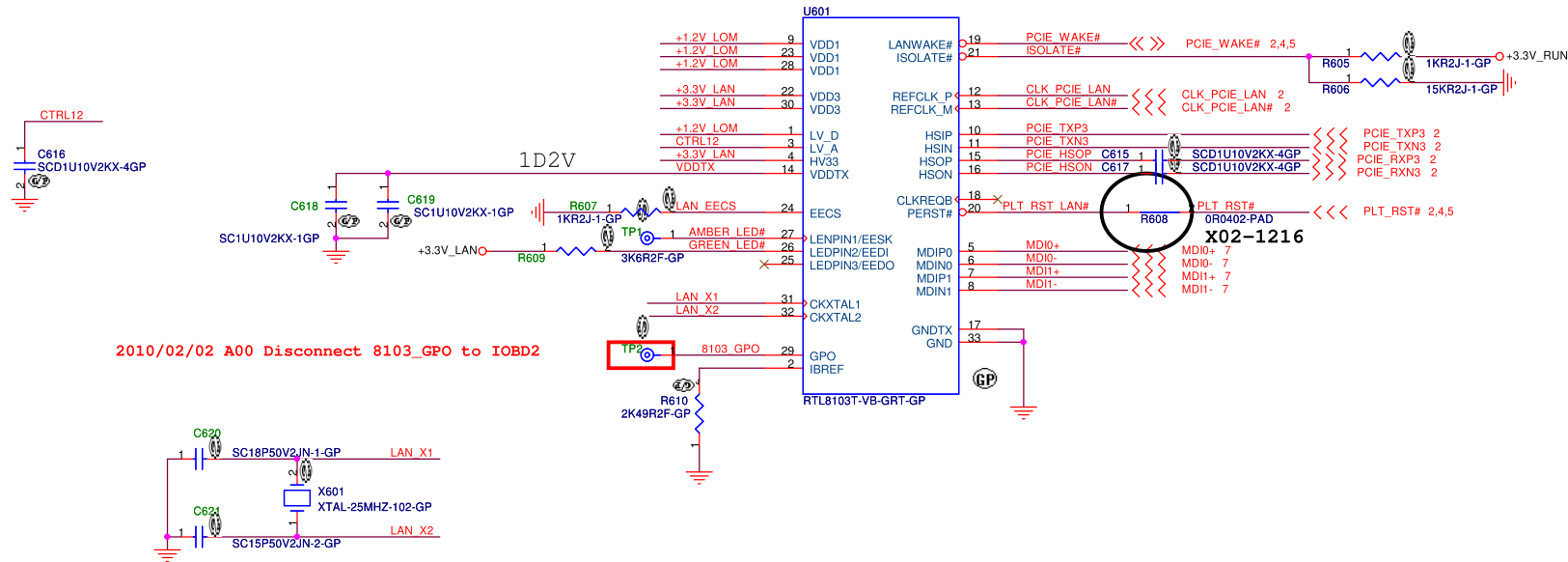
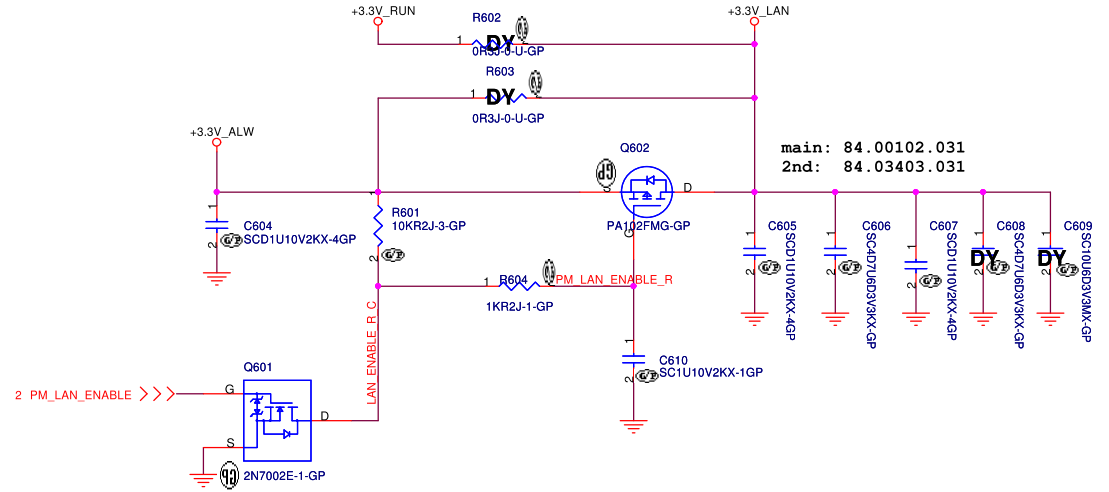
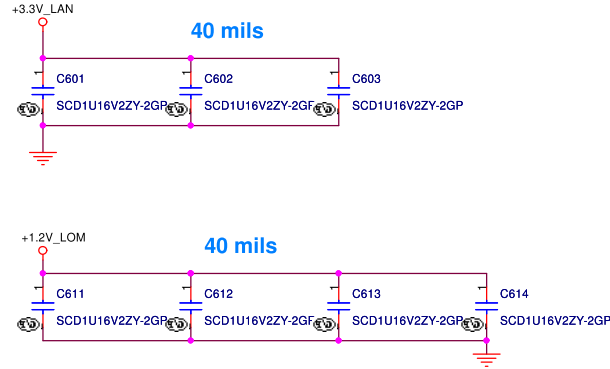
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Title **WWAN/SIM Card**

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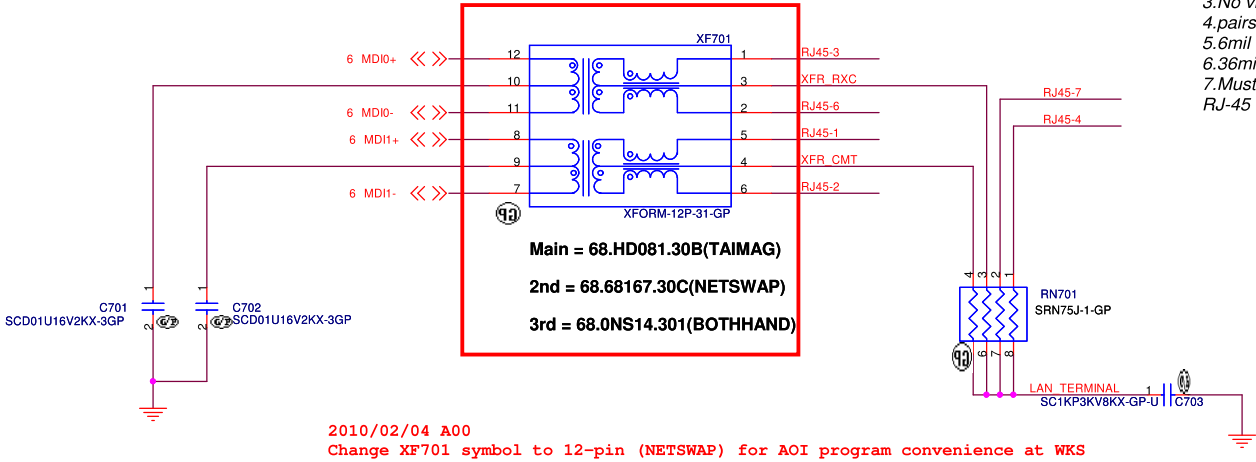
Main: 82.30020.851
2nd : 82.30020.791

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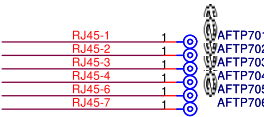
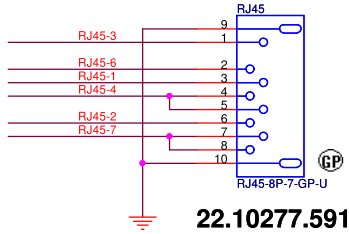
Title			LAN RTL8103T
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10/100M Lan Transformer

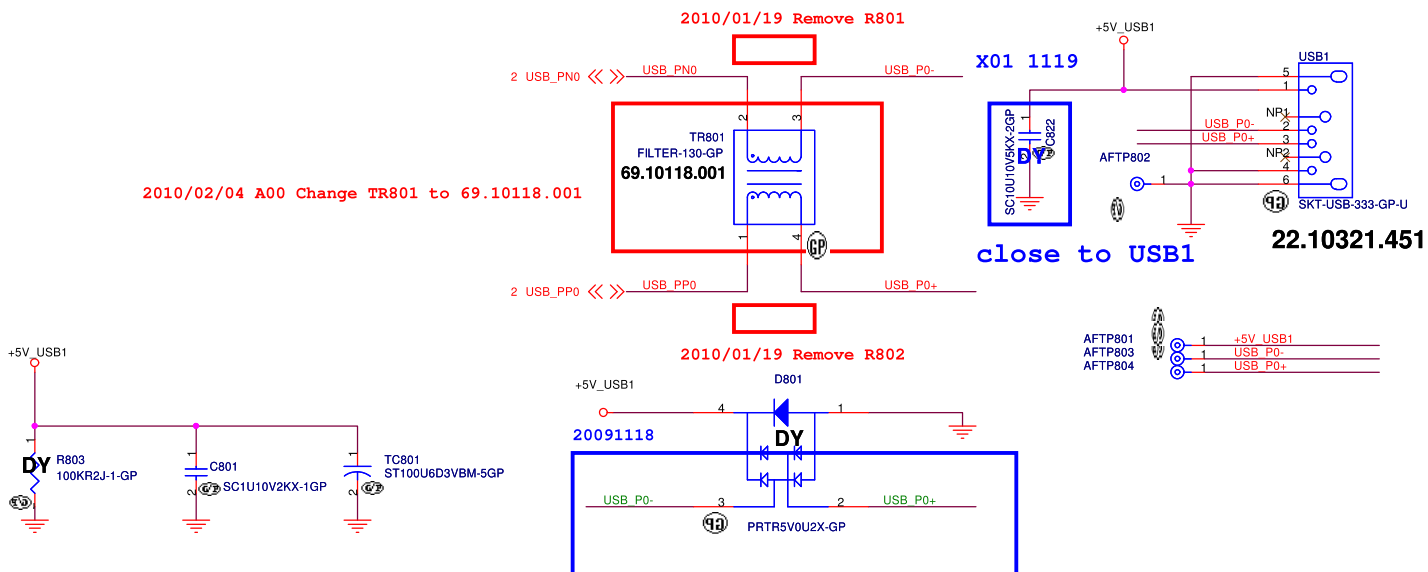


- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat, except RJ-45 moat.

RJ45 Connector



SSID = USB



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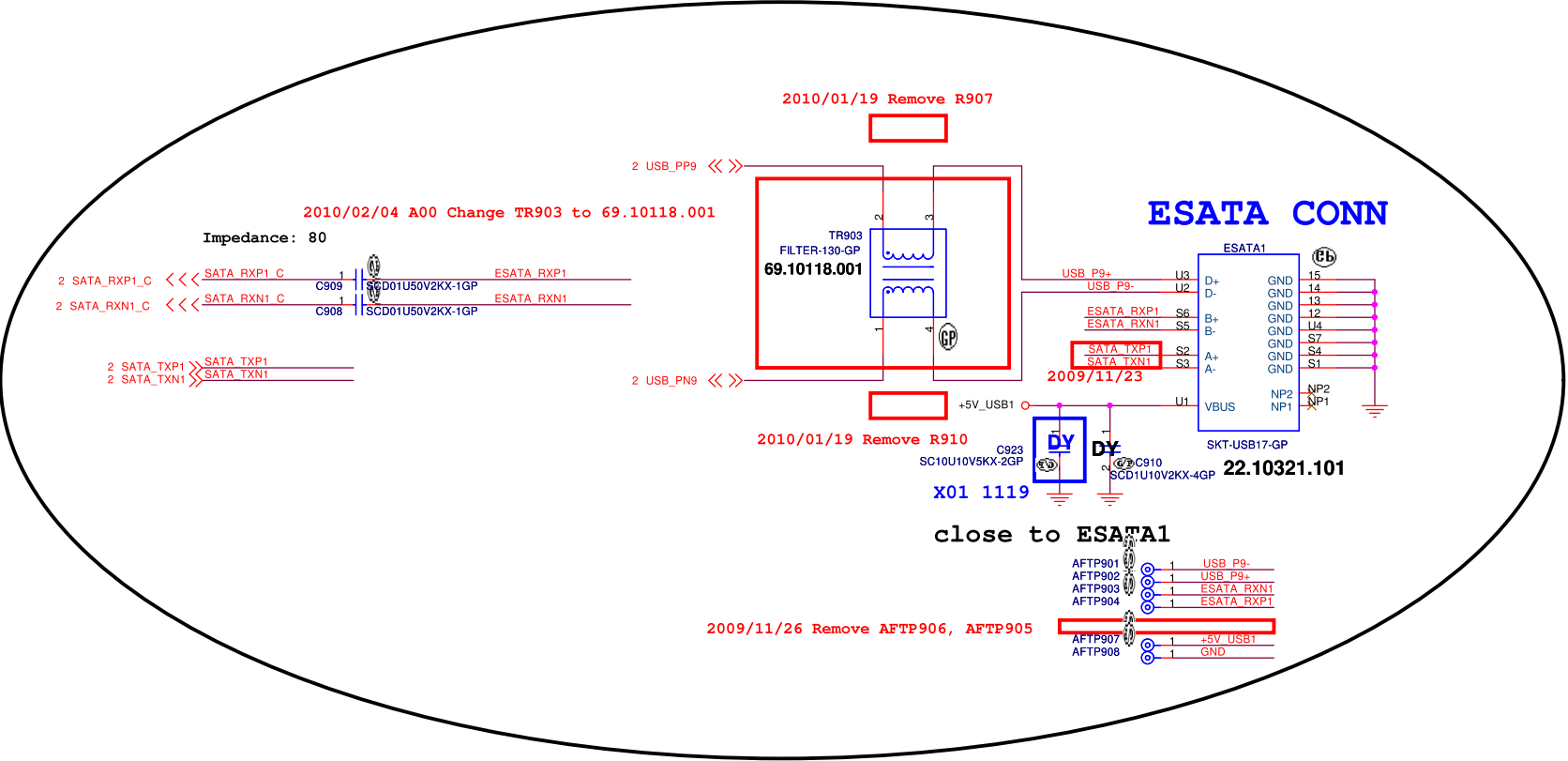
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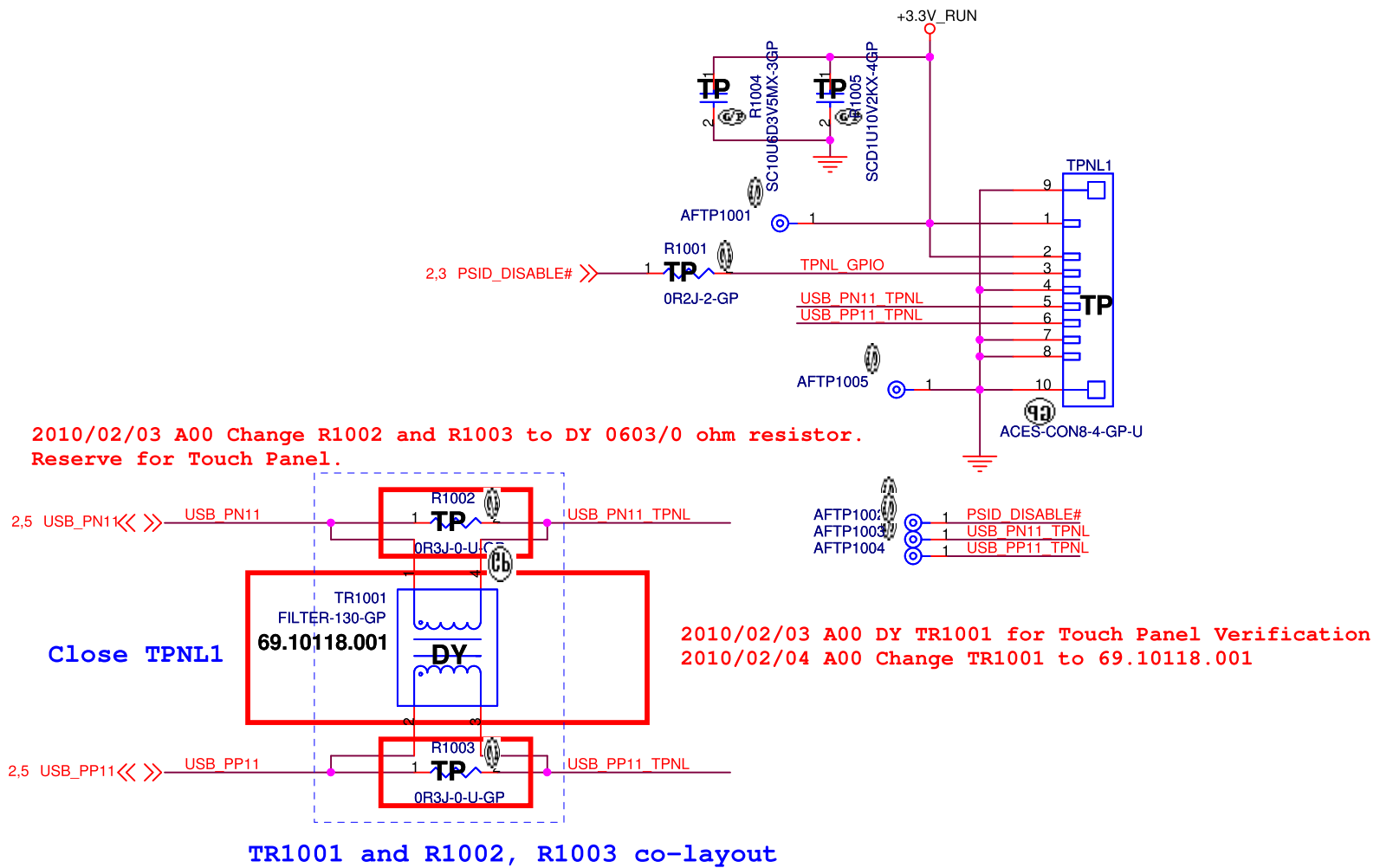
Title: **USB Port**

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SSID = ESATA

X02-1230





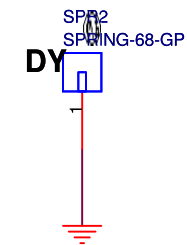
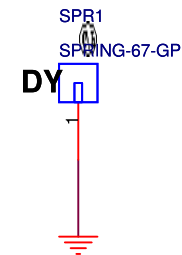
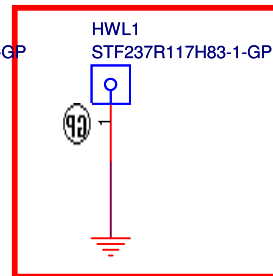
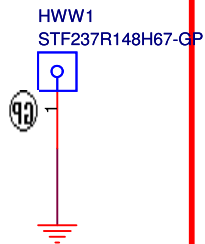
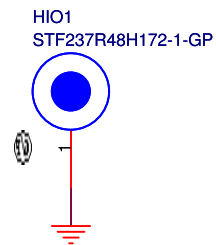
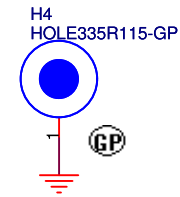
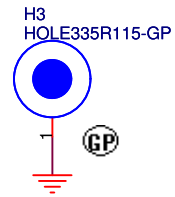
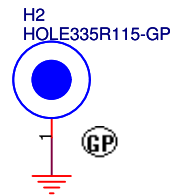
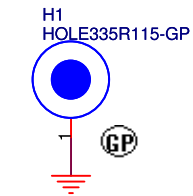
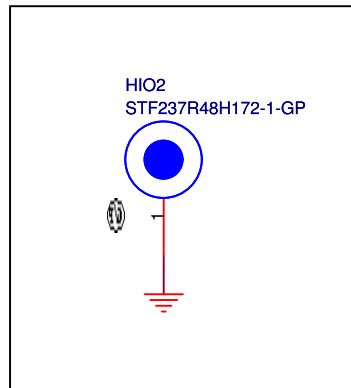
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Title		
Touch Panel Conn		
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20091116



20100202 A00 Change the HWL1 standoff from 34.4W005.001 to 34.4CK01.001

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Title

RESERVED

Size
A4

Document Number


Berry IO Board

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A00

Date: Tuesday, February 02, 2010

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Title			
<i>Change List</i>			
Size	Document Number		Rev
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