

COMPAL CONFIDENTIAL

MODEL NAME : NIM00  
PCB NO : LA5461P(DA60000DD10) DAZ09L00100  
BOM P/N : 46174031L01

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Argos M/B  
Schematics Document

2009-06-24

REV:1.0

@X7618431L01 Parade HDMI LS  
@X7618431L02 ST HDMI LS

PCB1  
JBL00  
LS-3807P REV0  
Sniffer/B  
PCB 042 LA-5461P REV0.2 B

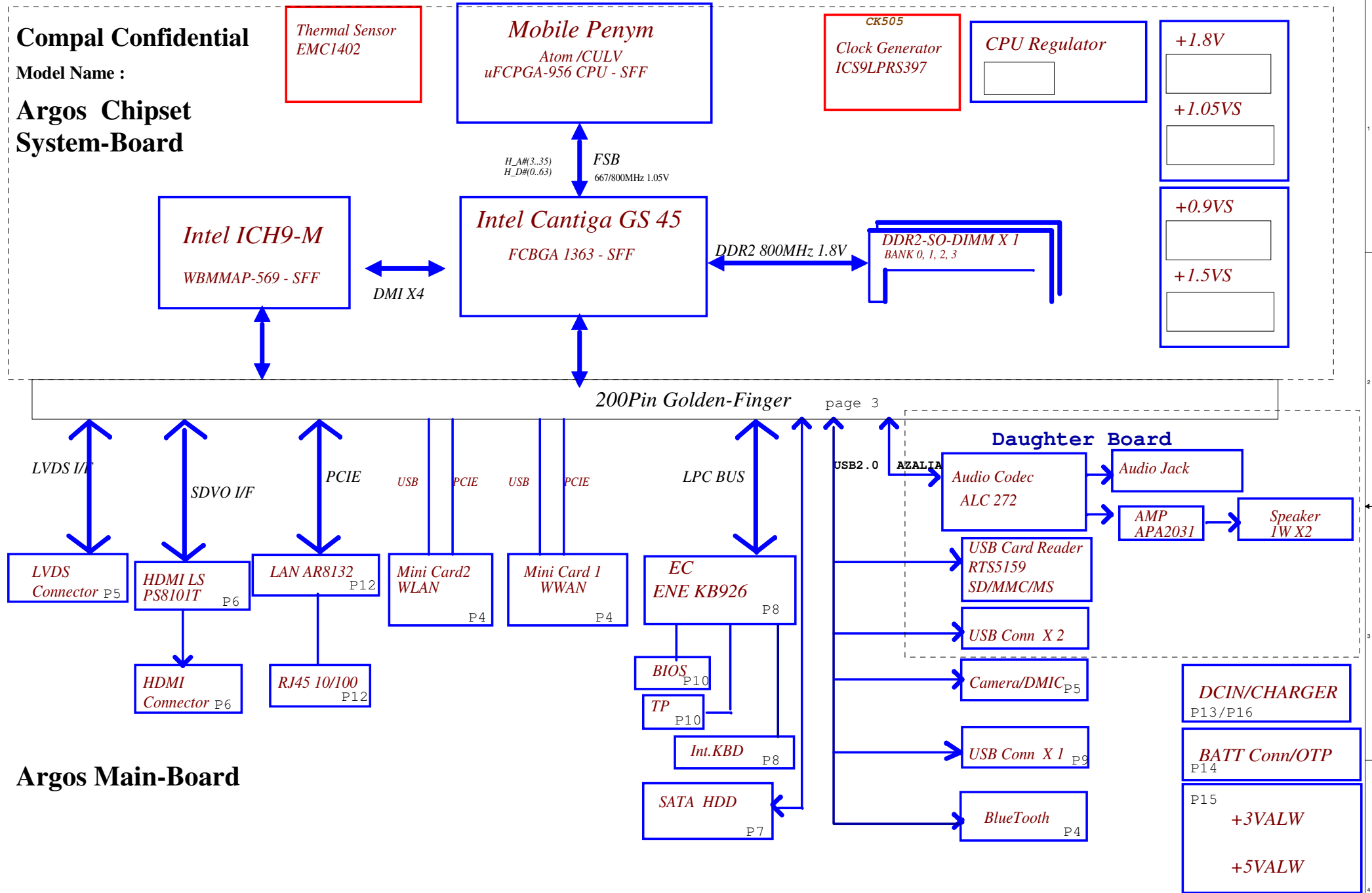
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Model Name :

## Argos Chipset System-Board



## Argos Main-Board

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Block Diagram		
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Voltage Rails    O MEANS ON    X MEANS OFF

power plane State	+B	+5VALW +3VALW	+5VS +3VS +1.8VS +1.5VS
S0	O	O	O
S1	O	O	O
S3	O	O	X
S5 S4/AC	O	O	X
S5 S4/ Battery only	O	X	X
S5 S4/AC & Battery don't exist	X	X	X

USB Port	Device
0	USB Right 1
1	USB Right 2
2	USB Left
3	NC
4	WLAN
5	WWAN
6	Bluetooth
7	Camera
8	Cardreader
9	NC
10	NC
11	NC

SATA Port	Device
0	HDD

PCIE Port	Device
1	LAN
2	JWLAN1
3	
4	JWWAN1
5	
6	

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	10100000
CLOCK GENERATOR (EXT.)	D2	11010010

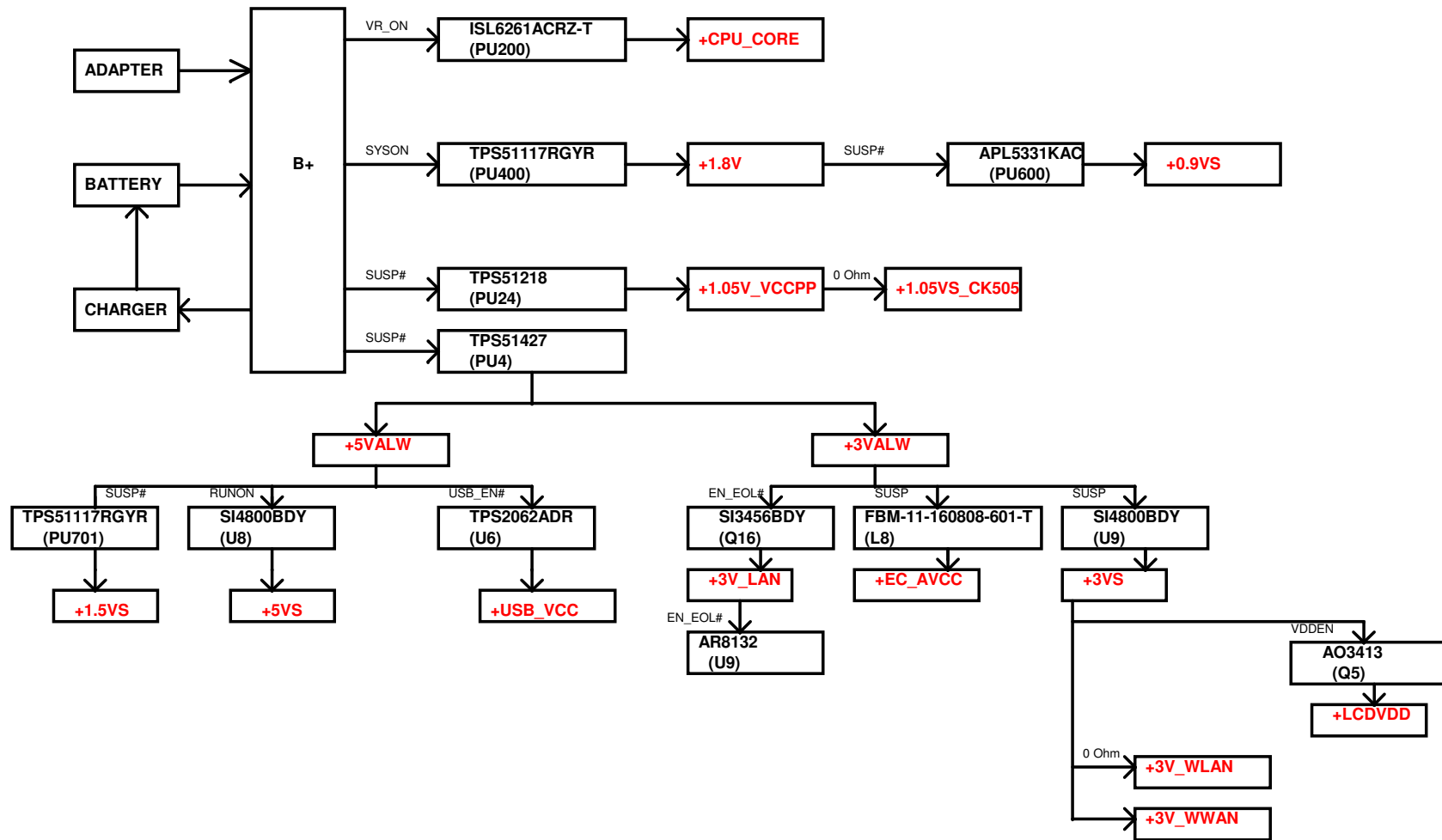
SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM	CLK CHIP	MINI CARD	LCD
SMB_EC_CK1 SMB_EC_DA1	KB926	X	V	X	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X
SMB_CK_CLK1 SMB_CK_DAT1	ICH9	X	X	X	X	V	V	V	X
LCD_CLK LCD_DAT	Cantiga	X	X	X	X	X	X	X	V

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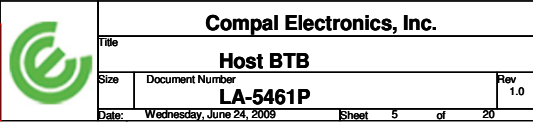


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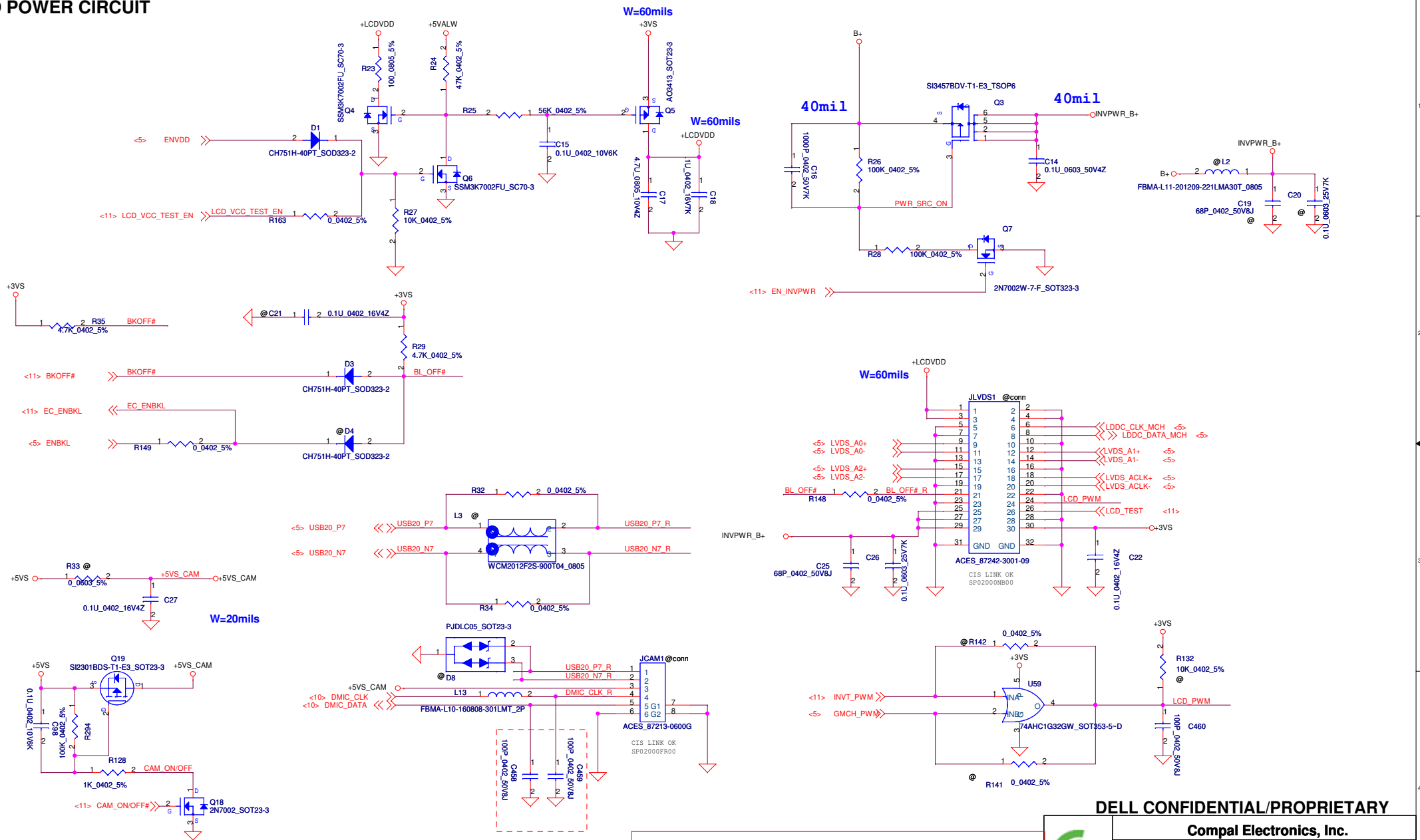
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LCD POWER CIRCUIT



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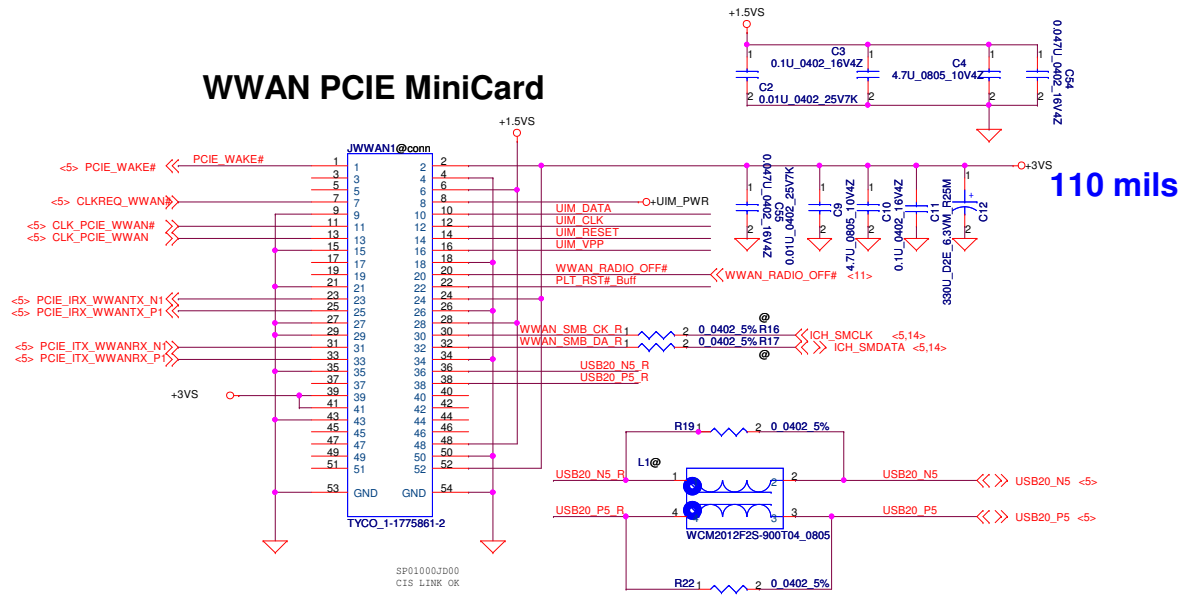
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**LVDS**

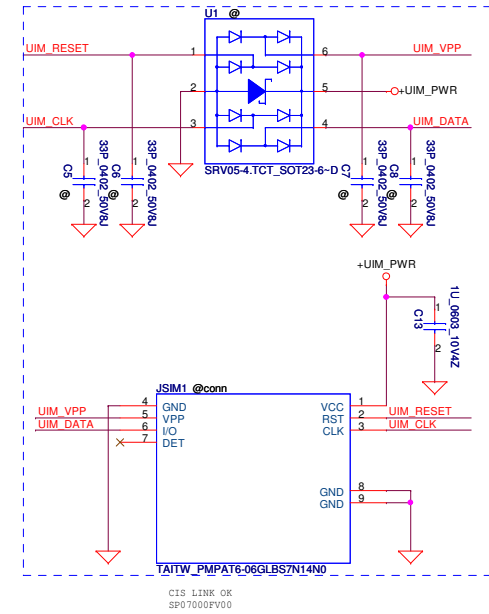
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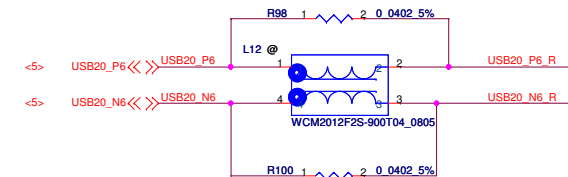
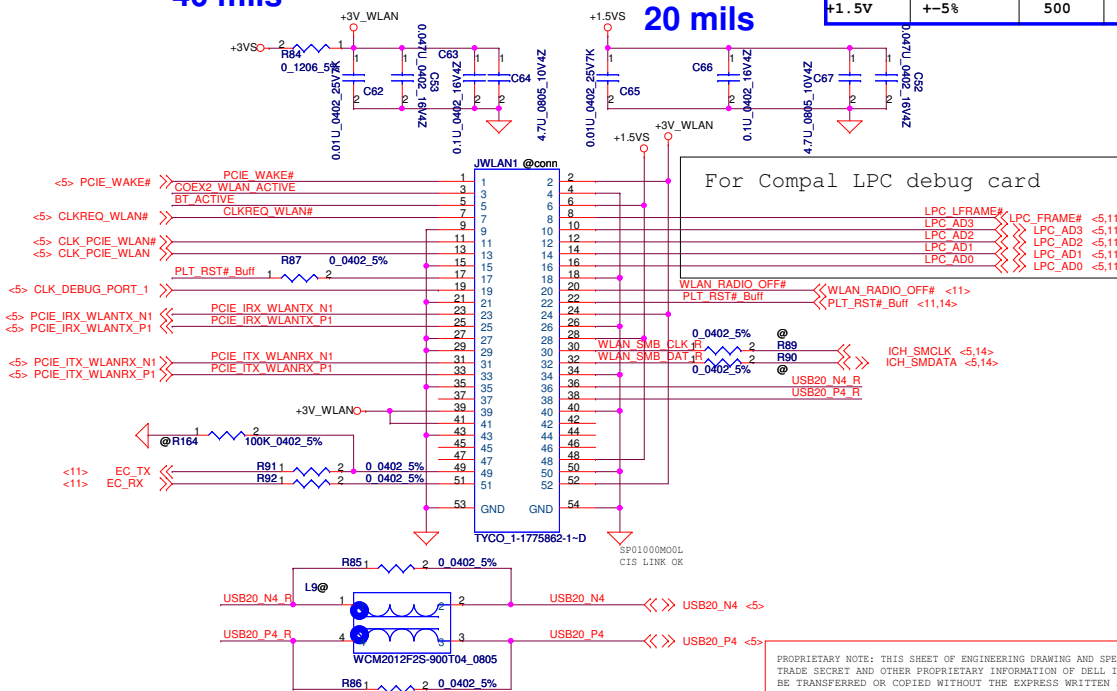
## WWAN PCIE MiniCard



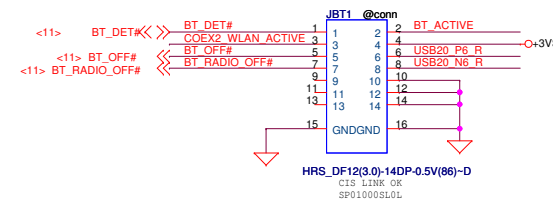
## SIM Card



## 40 mils WLAN/WIMAX PCIE Mini Card



## Bluetooth



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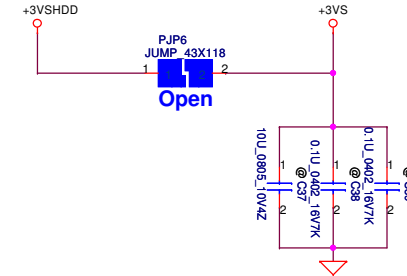
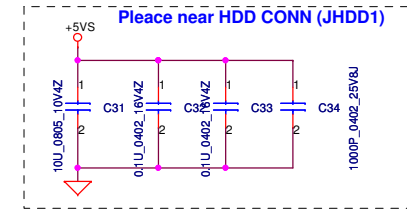
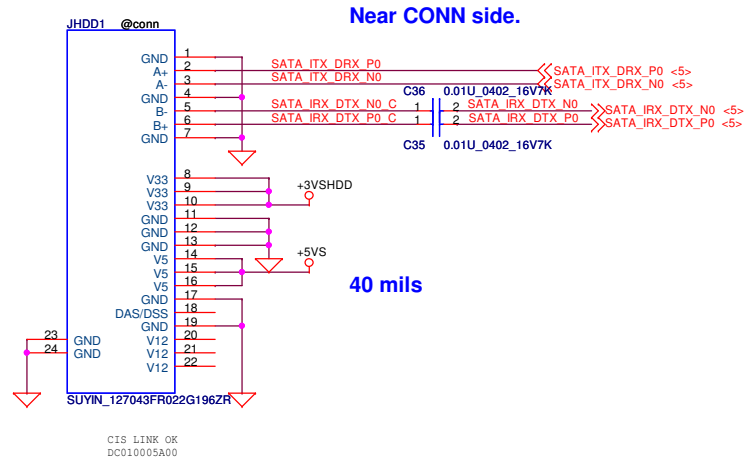
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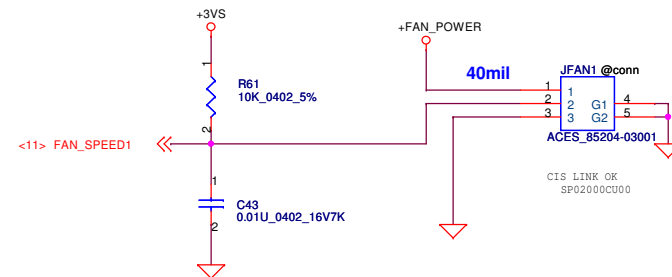
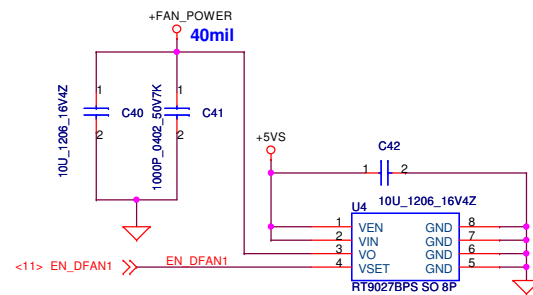




## HDD Connector



## FAN Control circuit



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FAN/HDD

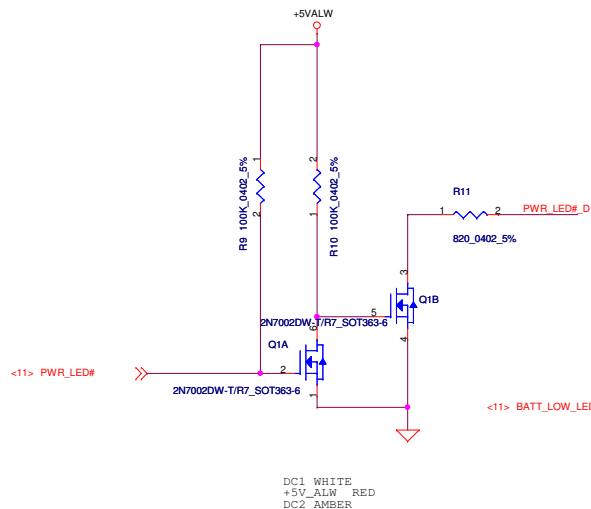
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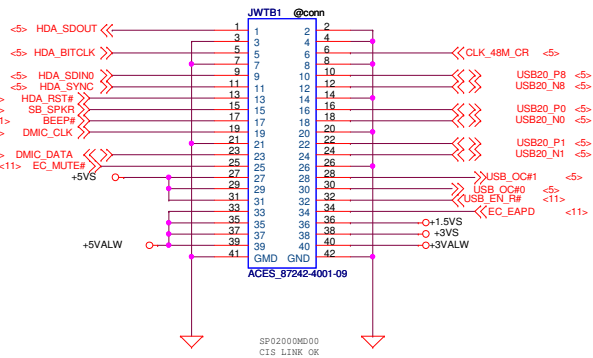
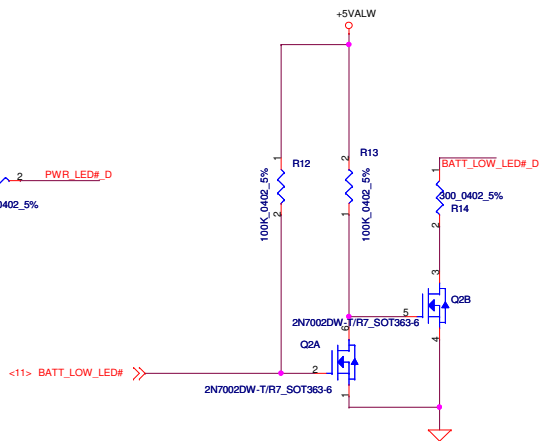
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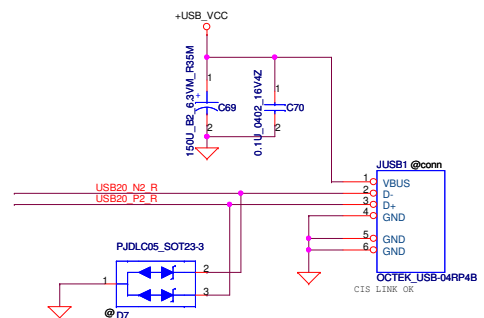
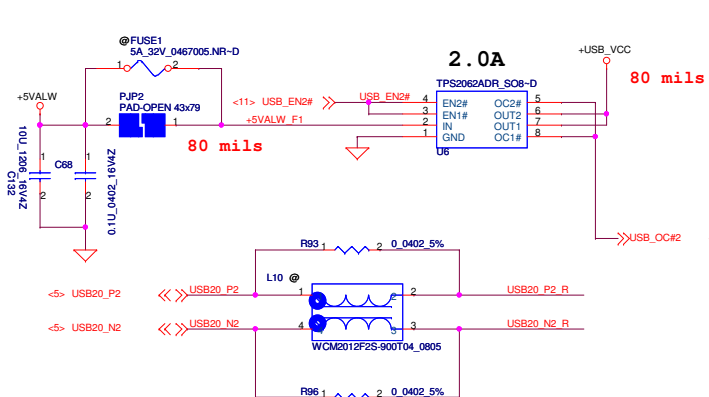
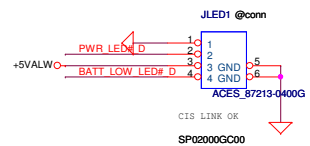
## Power LED



## BATT CHARGE



## LED CONN



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USB/WTB/LED			
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### RSMRST circuit

The RSMRST circuit schematic shows the following components and connections:

- Inputs:**
  - POK:** Connected to R101 (0.0402\_5%) and R126 (0.0402\_5%).
  - RSMRST#:** Connected to R126 (0.0402\_5%) and R1059 (100K\_0402\_5%).
  - CLK\_PCI\_EC:** Connected to R68 (10\_0402\_5%) and C50 (22P\_0402\_50V&J).
  - PLT\_RST#:** Connected to the input of the NAND gate (U58).
- Logic:**
  - U58 (MC74VHC1G08DF2G SC70 5P):** A NAND gate with inputs A and B, and output Y.
  - U58 Pin Connections:**
    - Pin 1 (A): Connected to POK.
    - Pin 2 (B): Connected to RSMRST#.
    - Pin 3 (VCC): Connected to +3VALW.
    - Pin 4 (GND): Connected to GND.
    - Pin 5 (Y): Connected to PLT\_RST#.
- Power and Grounding:**
  - +3VALW:** Connected to R68, R69 (47K\_0402\_5%), C51 (0.1U\_0402\_16V4Z), and R1059.
  - +3VLS:** Connected to R78, R79, R143, R80, R82, R139, and R160.
  - GND:** Connected to the GND pins of U58, C50, C51, and C60.
- Other Components:**
  - R101, R126, R1059, R137, R74, R75, R78, R79, R143, R80, R82, R139, R160:** Resistors with values 0.0402\_5%, 10\_0402\_5%, 100K\_0402\_5%, 10K\_0402\_5%, 47K\_0402\_5%, 2.2K\_0402\_5%, 4.7K\_0402\_5%, 10K\_0402\_5%, and 4.7K\_0402\_5%.
  - C50, C51, C60:** Capacitors with values 22P\_0402\_50V&J, 0.1U\_0402\_16V4Z, and 27P\_0402\_50V&J.
  - X1:** A 32.768KHZ crystal.




ID	BOARD ID	Ra	Rb	Vab
0	0.1(X00)	NC	0	0V
1	0.2(X01)	100K	9.09K	0.25V
2	1.0(A00)	100K	20K	0.55V
3	2.0(A01)	100K	NC	3.3V

		JKB1	@com
KS17	1		
KS16	2		
KS15	3		
KS14	4		
KS13	5		
KS12	6		
KS11	7		
KS10	8		
KS09	9		
KS08	10		
KS07	11		
KS06	12		
KS05	13		
KS04	14		
KS03	15		
KS02	16		
KS01	17		
KS00	18		
KS016	19		
KS015	20		
KS013	21		
KS014	22		
KS09	23		
KS011	24		
KS010	25		
		GND	27
		GND	26

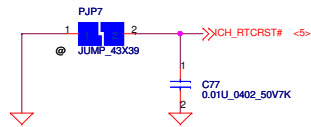
ACES\_88502-25F1

<b>KS08</b>	@C249	100P_0402_25V8K	100P_0402_25V8K	<b>C235</b>	<b>KS17</b>
<b>KS13</b>	@C239	100P_0402_25V8K	100P_0402_25V8K	<b>C236</b>	<b>KS16</b>
<b>KS09</b>	@C249	100P_0402_25V8K	100P_0402_25V8K	<b>C237</b>	<b>KS15</b>
<b>KS12</b>	@C240	100P_0402_25V8K	100P_0402_25V8K	<b>C441</b>	<b>KS00</b>
<b>KS11</b>	@C241	100P_0402_25V8K	100P_0402_25V8K	<b>C442</b>	<b>KS01</b>
<b>KS010</b>	@C248	100P_0402_25V8K	100P_0402_25V8K	<b>C443</b>	<b>KS02</b>
<b>KS011</b>	@C247	100P_0402_25V8K	100P_0402_25V8K	<b>C238</b>	<b>KS14</b>
<b>KS10</b>	@C242	100P_0402_25V8K	100P_0402_25V8K	<b>C444</b>	<b>KS03</b>
<b>KS012</b>	@C246	100P_0402_25V8K	100P_0402_25V8K	<b>C445</b>	<b>KS04</b>
<b>KS013</b>	@C245	100P_0402_25V8K	100P_0402_25V8K	<b>C446</b>	<b>KS05</b>
<b>KS014</b>	@C244	100P_0402_25V8K	100P_0402_25V8K	<b>C447</b>	<b>KS06</b>
<b>KS015</b>	@C243	100P_0402_25V8K	100P_0402_25V8K	<b>C448</b>	<b>KS07</b>
<b>KS016</b>	@C251	100P_0402_25V8K			

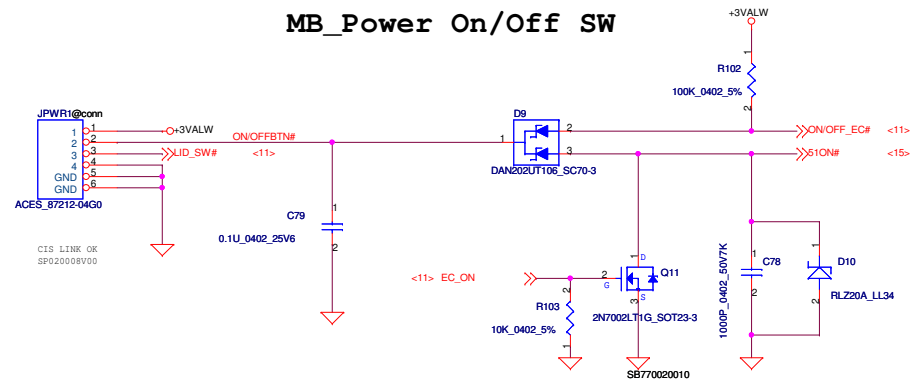
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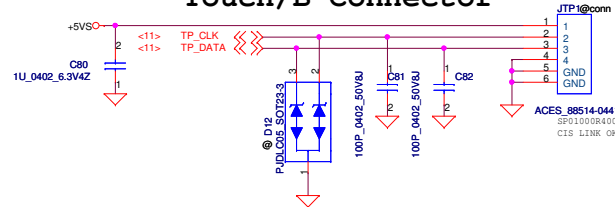
## RTC Reset SW



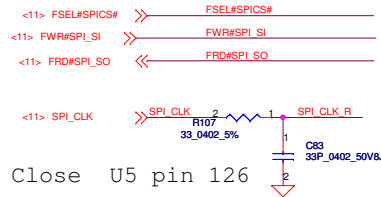
## MB\_Power On/Off SW



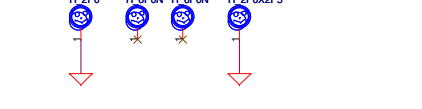
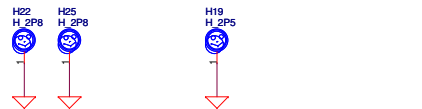
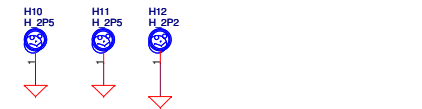
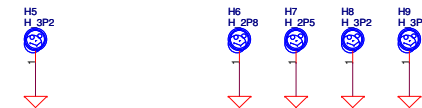
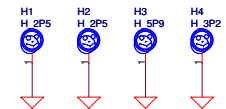
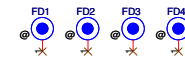
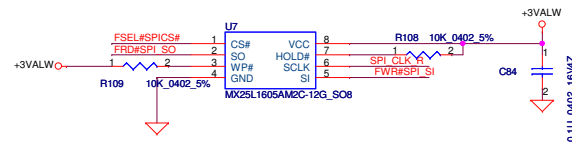
## Touch/B Connector



## System SPI Flash ROM (16Mb)



Close U5 pin 126



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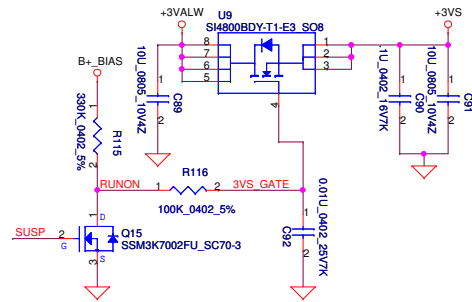
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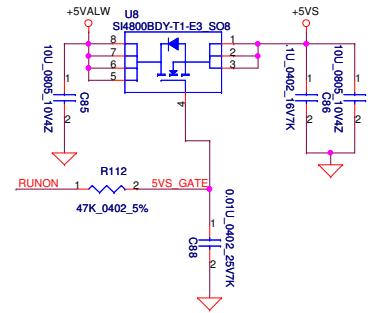
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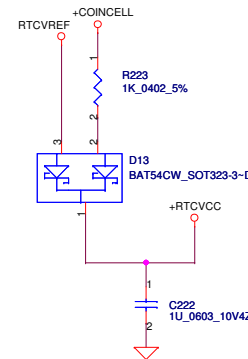
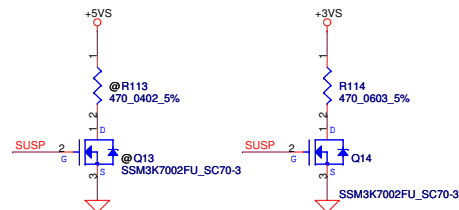
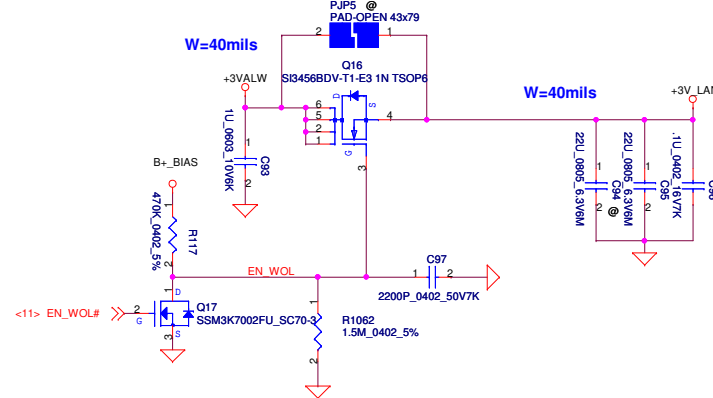
## +3VALW to +3VS Transfer



## +5VALW to +5VS Transfer



## +3VALW to +3LAN Transfer



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Power Control

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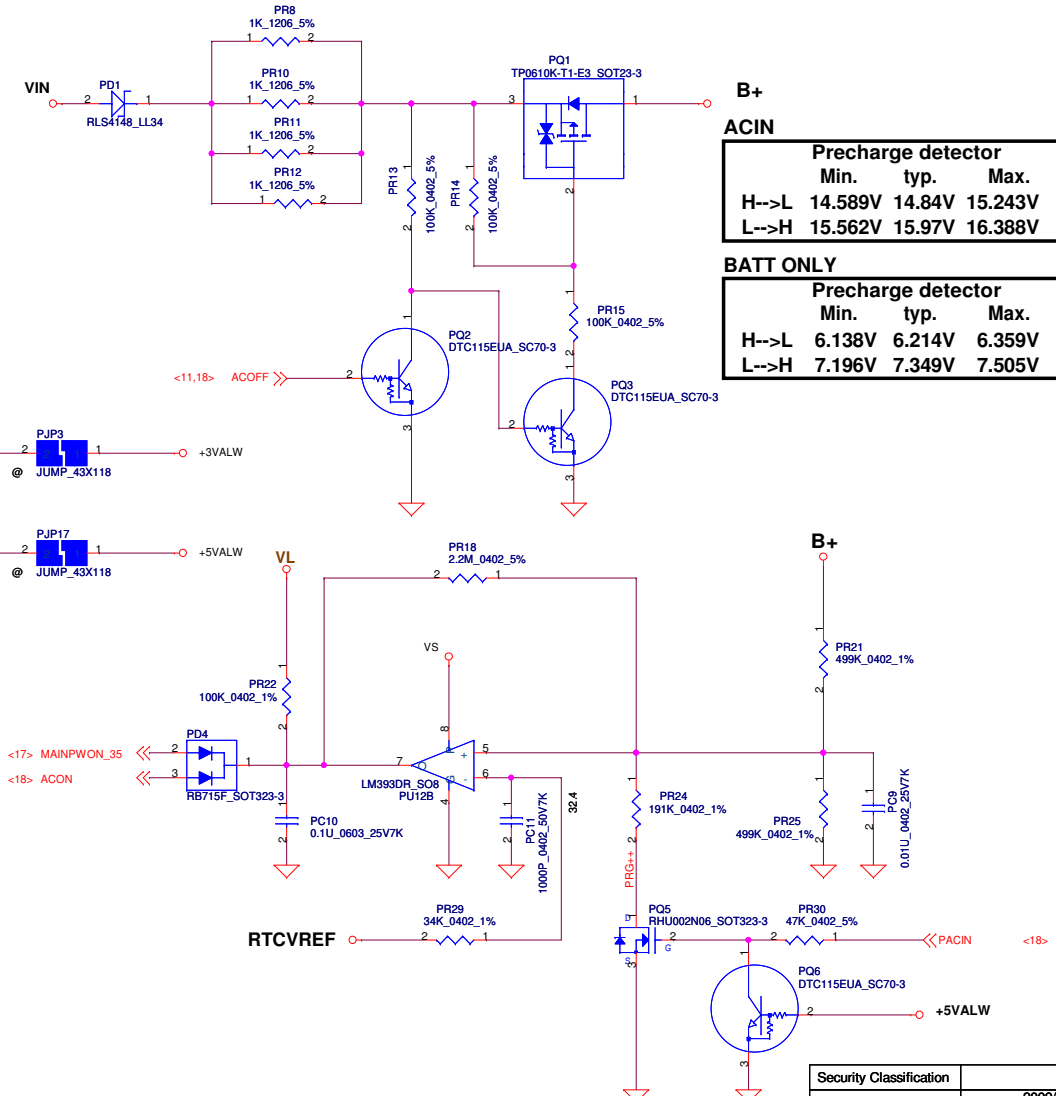
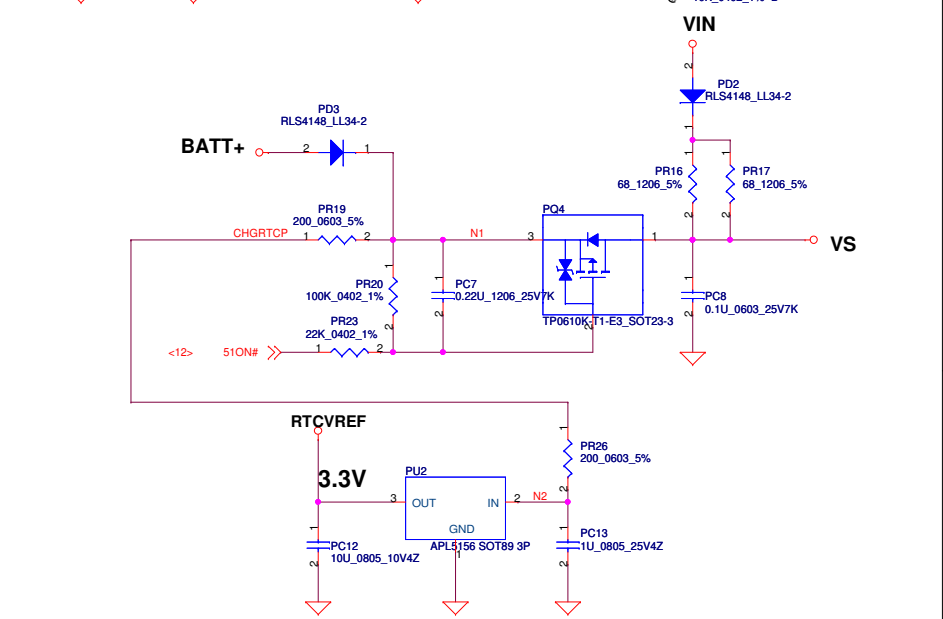
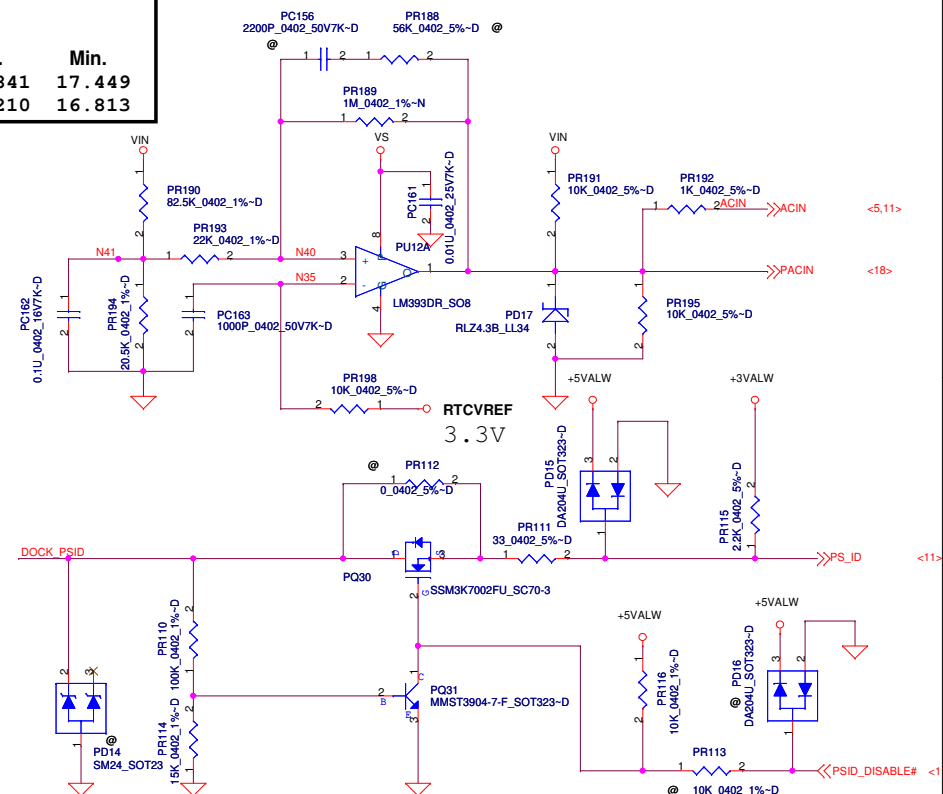
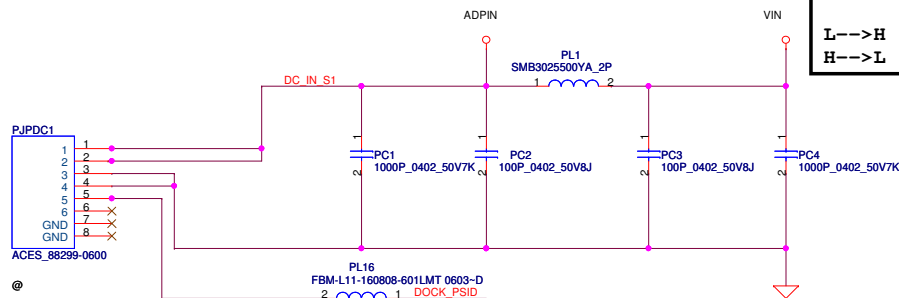
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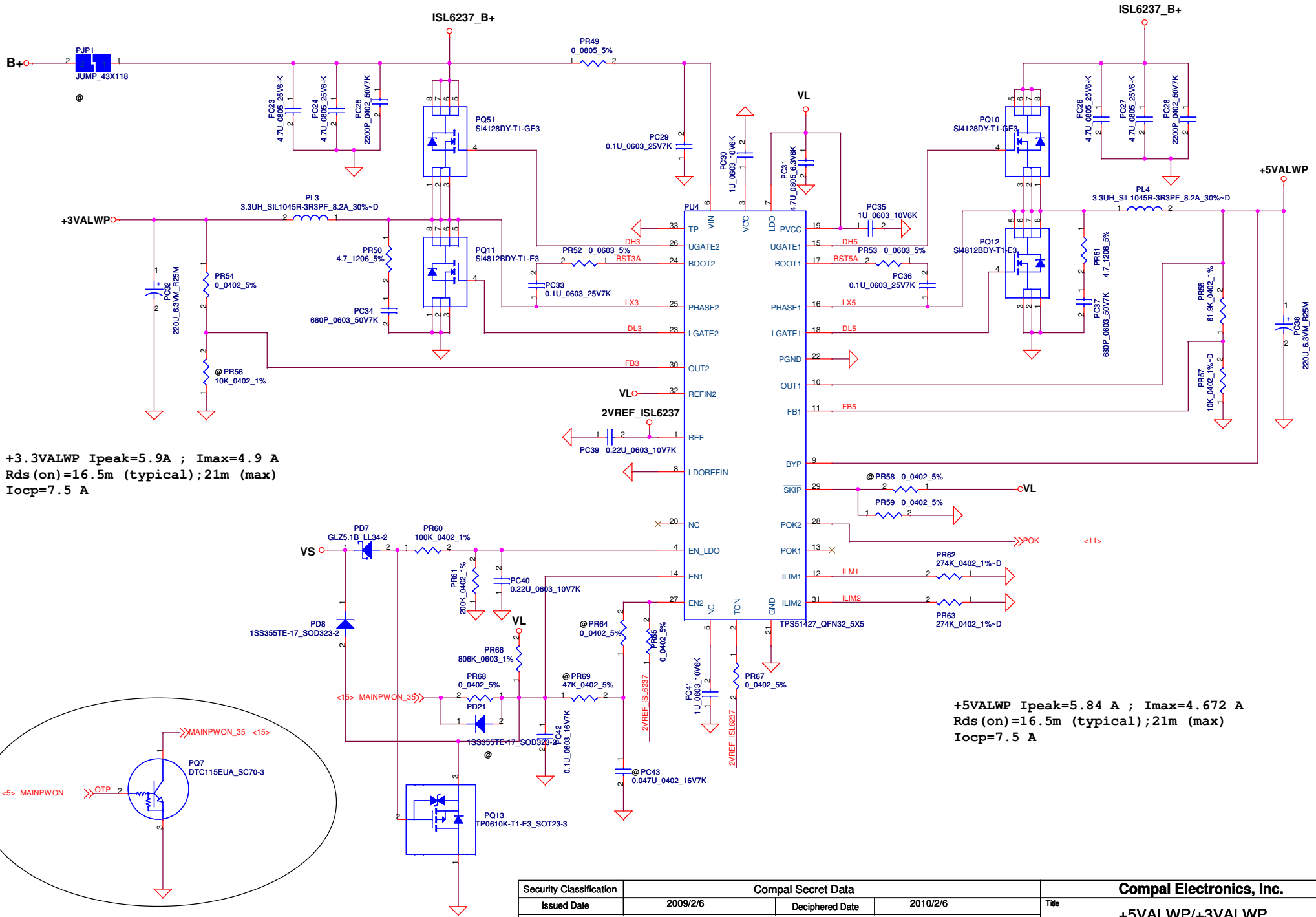
# Vin Detector

	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813









+3.3VALWP Ipeak=5.9A ; Imax=4.9 A  
Rds(on)=16.5m (typical);21m (max)  
Iocp=7.5 A

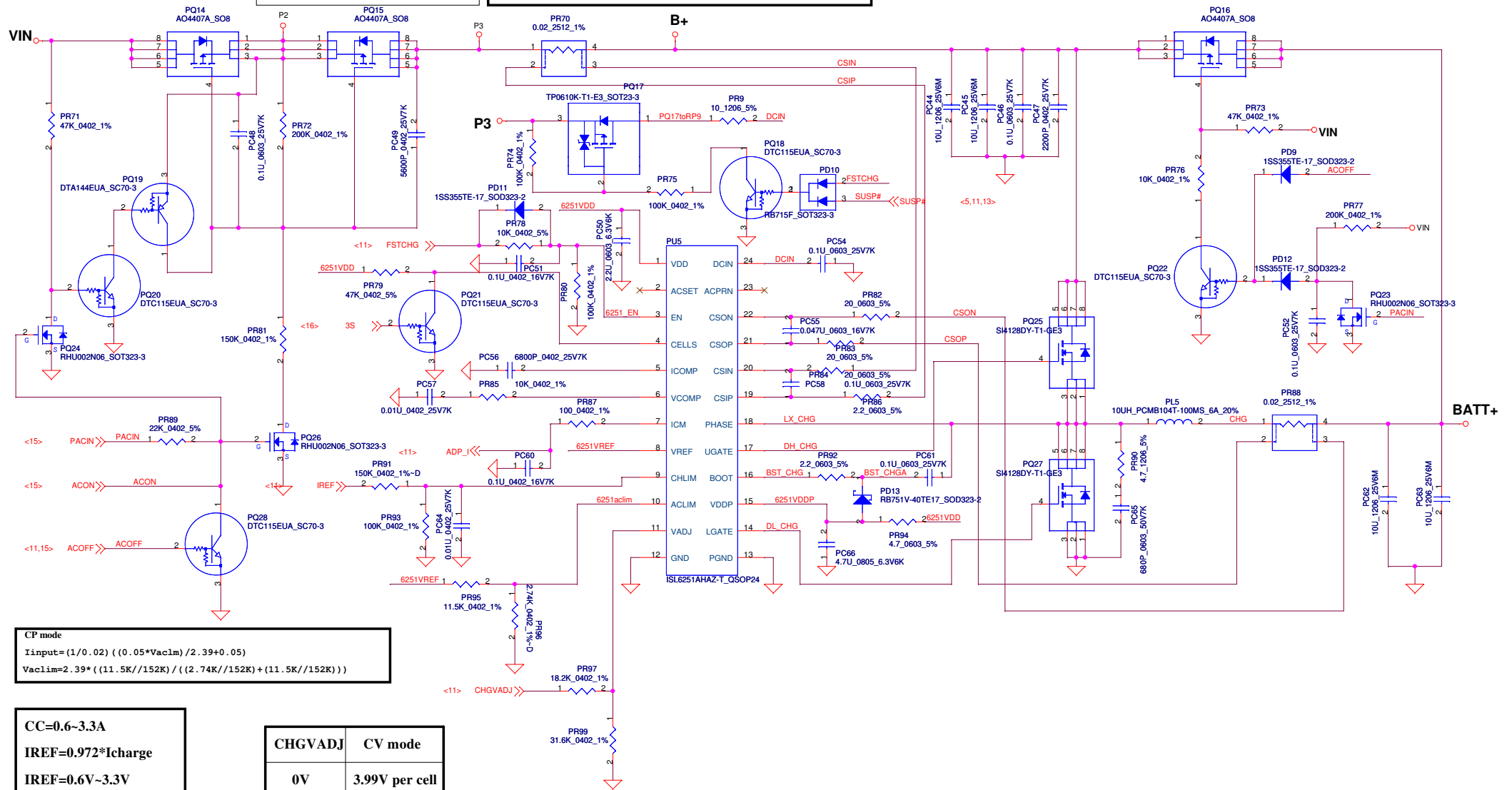
+5VALWP Ipeak=5.84 A ; Imax=4.672 A  
Rds(on)=16.5m (typical);21m (max)  
Iocp=7.5 A

Security Classification		Compal Secret Data				Compal Electronics, Inc.						
Issued Date		2009/2/6		Deciphered Date		2010/2/6		Title				
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I<sub>ada</sub>=0~3.34A (65W)

$$ADP\_I = 19.9 * I_{adapter} * R_{sense}$$

$$CP = 90\% * I_{adapter} \text{ (rating)}; CP = 3.003A$$




CP mode  
 $I_{input} = (1/0.02) * ((0.05 * V_{ac1m}) / (2.39 + 0.05))$   
 $V_{ac1m} = 2.39 * ((11.5K / 152K) / ((2.74K / 152K) + (11.5K / 152K)))$

CC=0.6~3.3A  
 IREF=0.972\*Icharge  
 IREF=0.6V~3.3V

CHGVADJ	CV mode
0V	3.99V per cell
1.91V	4.2V per cell
3.3V	4.35V per cell

Version change list (P.I.R. List)		power section		Page 1 of 1																																					
Item	Reason for change	PG#	Modify List	Date	Phase																																				
01	Reduce adapter plug in inrush current	P16	Add PR9 10 ohm	05/18/09	PT/ST																																				
02	adjust adapter plug in power sequence	P17	change PR68 from 0 to 100k, PC42 from 0.047u to 1u	05/18/09	PT/ST																																				
03	For OTP schematic	P17	Add PQ7	05/18/09	PT/ST																																				
04																																									
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Version Change List (P. I. R. List)

Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.															
6	LVDS	2009/5/4	Compal	Support EC & MCH PWN BL	reserve OR gate (U59,R132) (Pop OR gate)	X01															
6	LVDS	2009/5/4	EC	Panel power up sequence.	Change D2 to 0 ohm(R163)	X01															
8	HDMI	2009/5/4	ME	DFX issue	Change HDMI connector to DIP type (TAITW_PDVBR0-19FLBS4NN4N1)	X01															
11	EC	2009/5/4	Compal	follow CPU board SCI# to ALW power plane	Change EC_SCI# pull high from +3VS to +3VALW	X01															
12	PWBOTTOM	2009/5/4	Compal	Add LID SW in PWR board duplication reserve ESD	Change Power botton connector to 4 pin,del D11	X01															
5,6	Camrea	2009/5/6	BIOS	For external USB connector power manage feature implementation	Change Camera USB Port 3 to Port 7	X01															
5	OTP	2009/5/6	Compal	OTP no function	Add VL power in JHOST pin 6	X01															
14	Cyrstal	2009/5/7	Compal	For cyrstal EA result	Change Y1 to 25MHZ_12PF, C114 to 15pf,C24 to 18pf	X01															
10	WTB	2009/5/7	ME	For ASSY issue	Redefine JWTB1 pinout	X01															
6	LVDS, DMIC	2009/5/8	Compal	LCD sequence T1 issue,DMIC EMI issue	Change R25 to 56K,C15 to 0.1u,R30 to L13, pop C458,C459	X01															
5	CLOCK	2009/5/8	Compal	duplication reserve R for CLK_EC on CPB and MB	Del R6	X01															
12	PW SW	2009/5/12	Compal	Power derating fail	Add R1062 1.5M,change R117 to 470k	X01															
8	HDMI	2009/5/12	Compal	HDMI TEST to got more eye margin	Change R123 to 442 ohm	X01															
12	Pwr/SW	2009/5/13	Compal	Bear S5 Issue Recovery	Change C79 to 25V de-rating parts	X01															
11	EC	2009/5/15	Compal	Change Board ID to X01	Depop R64,pop R65	X01															
5,12	CLOCK	2009/6/15	Compal	For RF team request for WWW noise	Change C83 to 33pF,reserve C1252,C1253,C1254	A00															
11	EC	2009/6/15	Compal	Change Board ID to A00	Depop R65,pop R66	A00															
11	LID	2009/6/15	Compal	Change LID SW pull hight form PW/B to MB	Add R137	A00															
7	WLAN/WWAN	2009/6/18	Compal	Remove 0 ohm for cost down	Depop R16,R17,R89,R90,Remove R88,R118,R119,R120,R121,R122,R292,R293	A00															
13	3VLAN	2009/6/18	Compal	Remove 0 ohm for cost down	Remove R159	A00															
8	HDMI	2009/6/18	Compal	For Safity test Overload issue	Pop F1, Change R48 to PJP4	A00															
12	SPI	2009/6/18	Compal	Remove 0 ohm for cost down	Remove R104,R105,R106	A00															
13																					
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