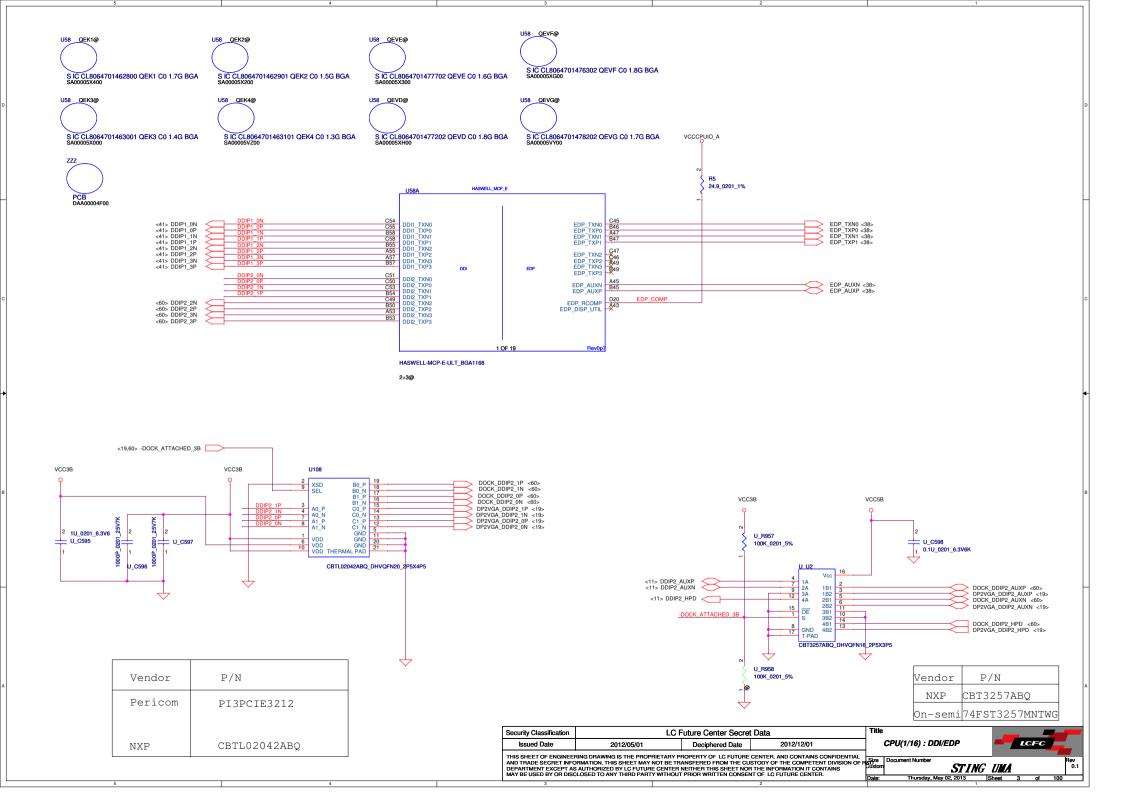
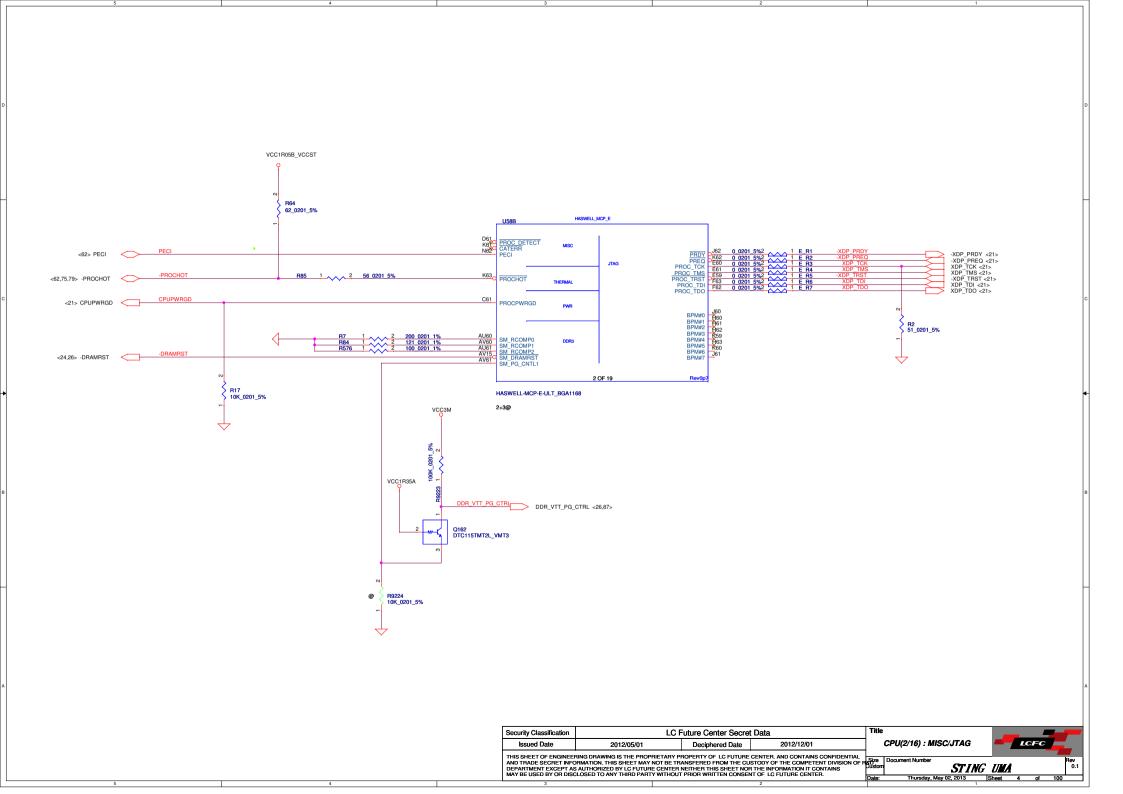
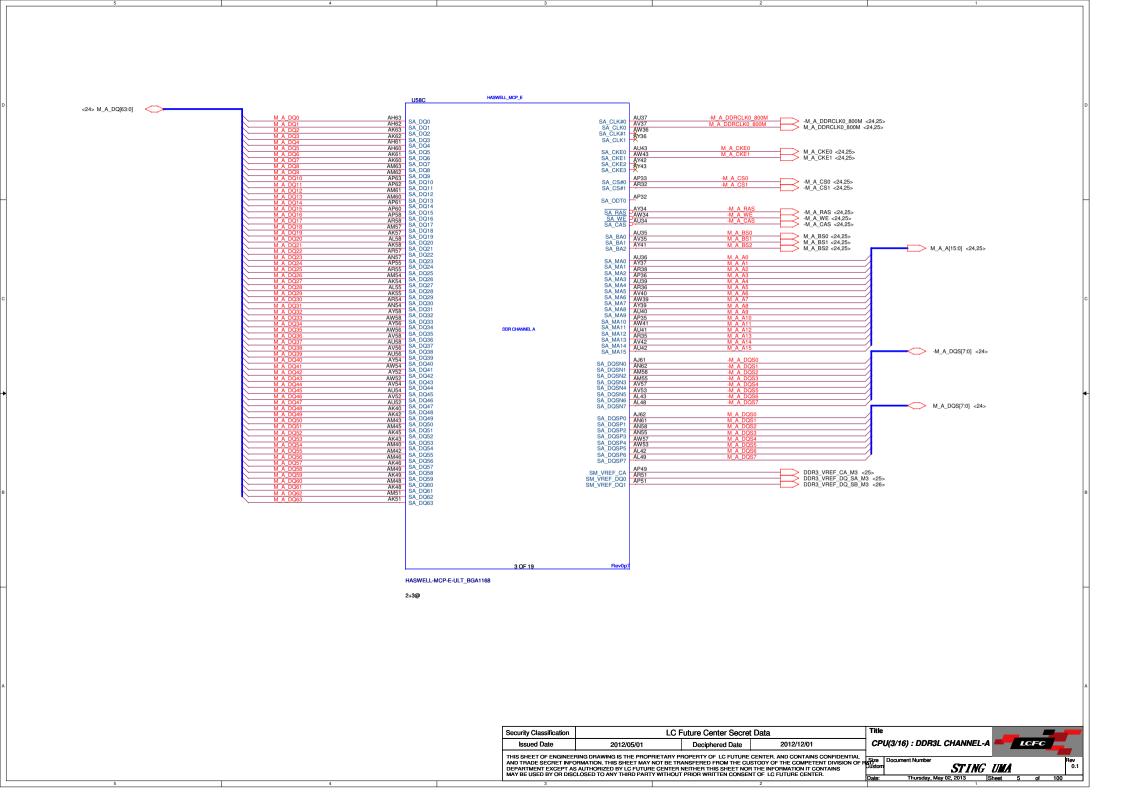
ST-1 UMA LOGIC SCHEMATICS ST-1 **VER0.1** 1.TITLE PAGE 36.BLANK 71.THINK ENGINE(1/2) June/19/2012 2.EC HISTORY 37.BLANK 72.THINK ENGINE(2/2) 3.CPU(1/16): DDI/EDP **38.LCD CONNECTOR 73.DC-IN 4.CPU(2/16)** : MISC/JTAG 39.BLANK 74.BATTERY INPUT **40.EXT CRT INTERFACE** 5.CPU(3/16): DDR3L CHANNEL-A 75.BATTERY CHARGER(BQ24760) 6.CPU(4/16): DDR3L CHANNEL-B **41.DISPLAY PORT CONNECTOR 76.CHARGER SELECTOR** 7.CPU(5/16): RTC/HDA/SATA/JTAG 42.BLANK 77.BATTERY MONITOR 8.CPU(6/16): CLOCK SIGNALS **43.SATA HDD CONN** 78.DC/DC VCC5M/VCC3M(TPS51220A) 9.CPU(7/16): LPC/SPI/SMBUS/C-LINK 44.BLANK 79.DC/DC VCCCPUCORE(VT1320M) **45.USB POWER/CONN** 10.CPU(8/16): SYSTEM PM 80.DC/DC VCCCPUCORE(VT1334S) 11.CPU(9/16): LCD/PCI/DDI CONTROL **46.GBE CLARKVILLE** 81.BLANK 12.CPU(10/16): GPIO/CPU/MISC/LPIO 82.VCCCPUCORE DECOUPLING **47.GBE LAN SWITCH** 13.CPU(11/16): PCIE/USB **48.GBE MAGNETICS** 83.BLANK **49.RJ45 CONNECTOR** 14.CPU(12/16): POWER 84.BLANK 15.CPU(13/16): POWER 85.DC/DC VCC1R05AMT(VT356) **50.PCIE MINI CARD SLOT** 16.CPU(14/16): GND 51.MEDIA CARD CONTROLLER 86.DC/DC VCC1R35A(VT357) 17.CPU(15/16): NCTF 87.DC/DC VCC0R675B(TPS51200) **52.MEDIA CARD ITERFACE** 18.CPU(16/16): CFG/RESERVED 53.SMART CARD I/F 88.BLANK 19.DDI-VGA CONVERTER 54.AUDIO ALC3232-GR 89.BLANK 20.BLANK **55.AUDIO CONNECTOR** 90.BLANK 21.XDP CONNECTOR **56.AUDIO JACK SENSE** 91.BLANK 92.DC/DC VCC1R5B(BD3551HFN) 22.RTC BATTERY **57.AUDIO EXT MIC I/F** 93.LOAD SW PCH SUS 23.SPI FLASH **58.AUDIO SPEAKER** 24.DDR3L BASE MEMORY CH-A (1/2) **59.AUDIO BEEP** 94.LOAD SW LAN 25.DDR3L BASE MEMORY CH-A (2/2) **60.DOCKING CONNECTOR** 95.BLANK 26.DDR3L SO DIMM CHANNEL-B 61.MEC1633L(1/3) 96.LOAD SW B 62.MEC1633L(2/3) 27.BLANK 97.BLANK 28.BLANK 63.MEC1633L(3/3) 98.LOAD SW WWAN & WLAN **29.BLANK** 64.KEYBOARD CONNECTOR 99.PTH FOR SCREW HOLES 30.BLANK 65.TOUCH PAD CONNECTOR 100.3rd USB 31.BLANK 66.BLANK 32.BLANK **67.FAN CONNECTOR** 33.BLANK 68.G-SNESOR 34.BLANK 69.TPM lenovo 35.BLANK 70.EEPROM/SMBUS SW

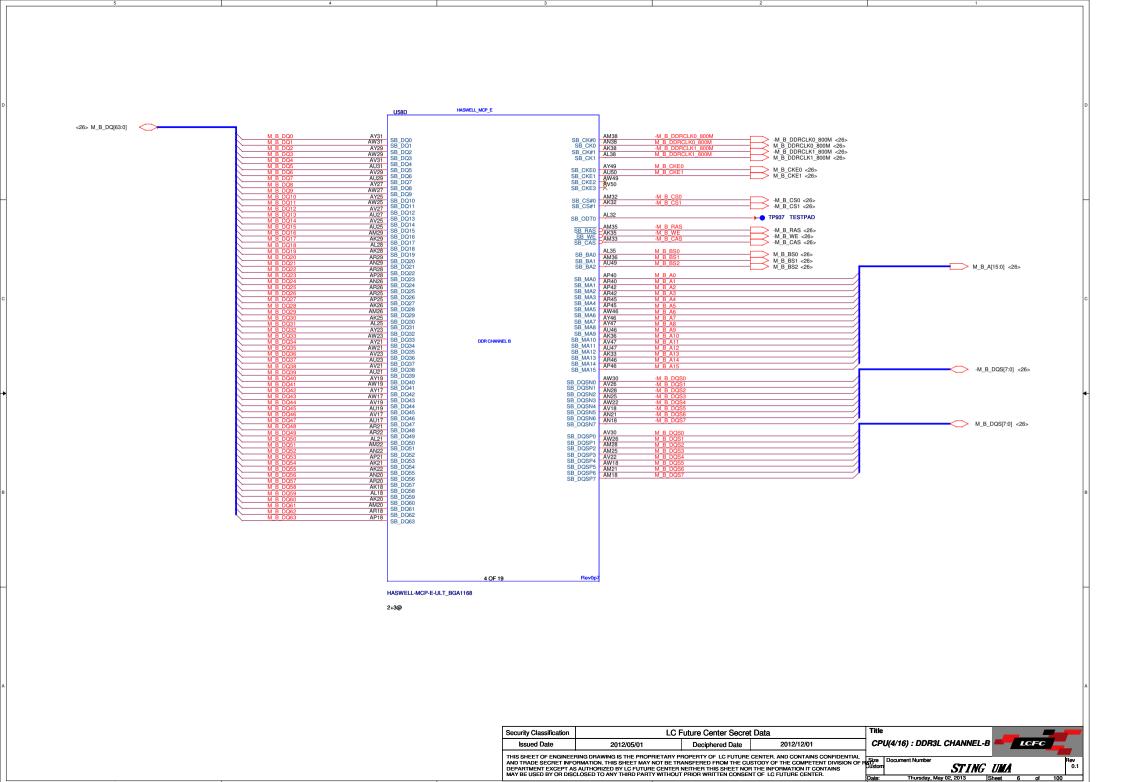
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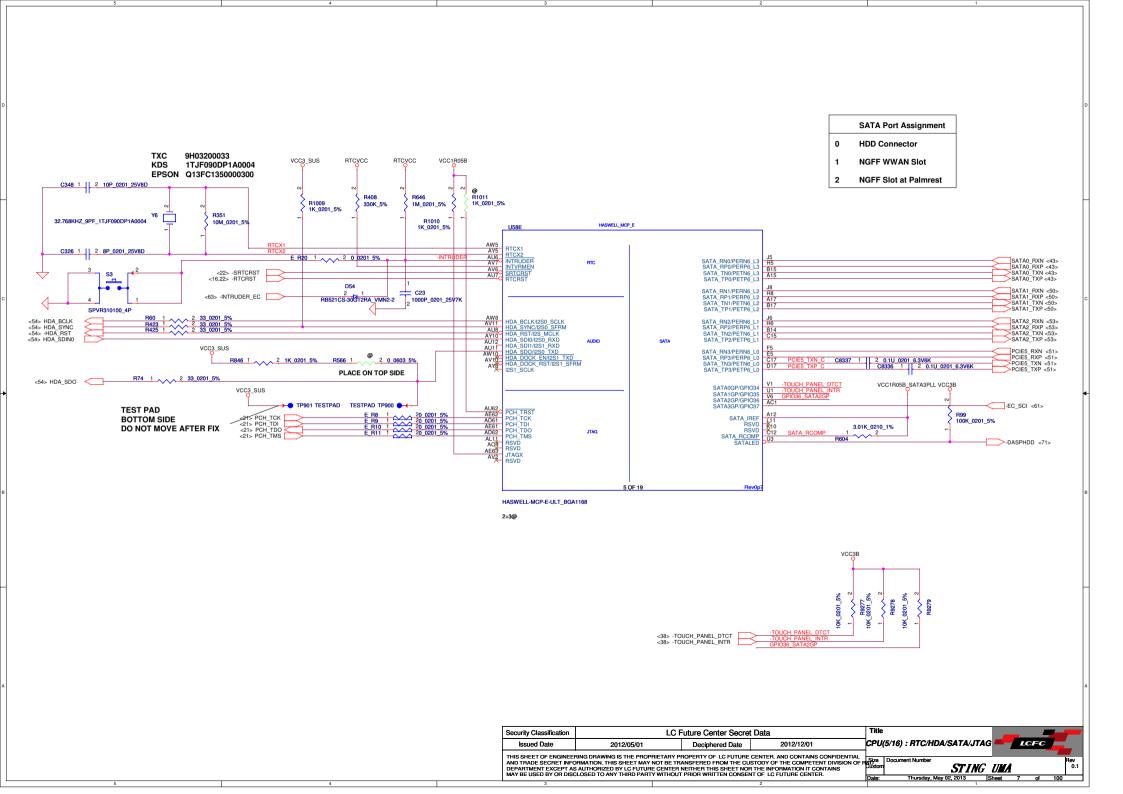
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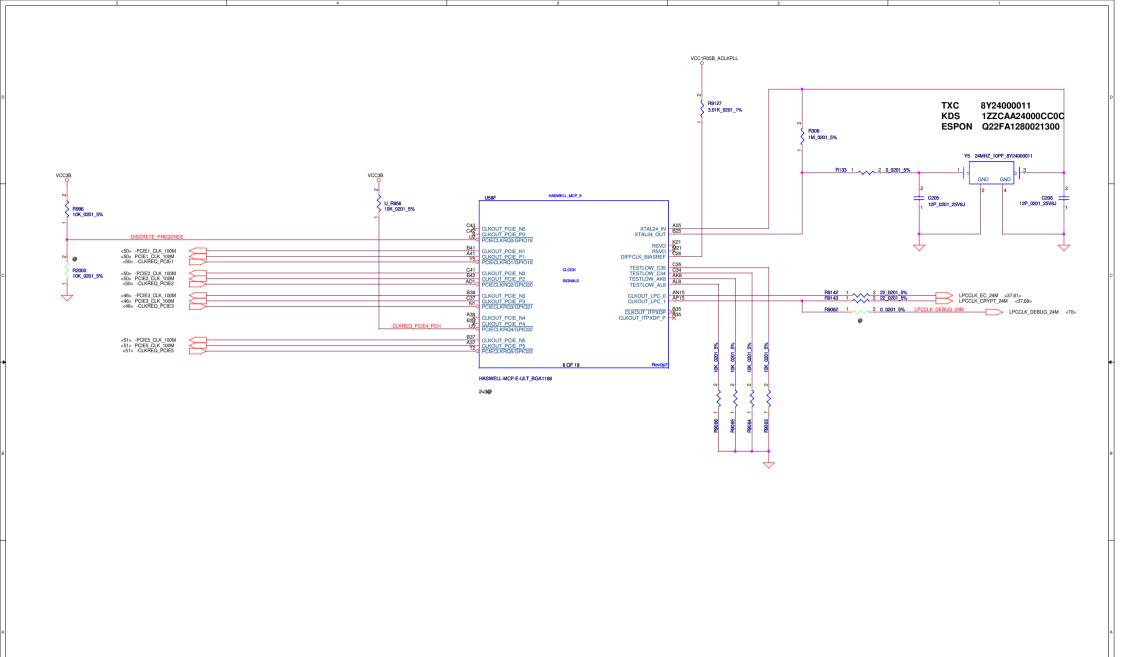




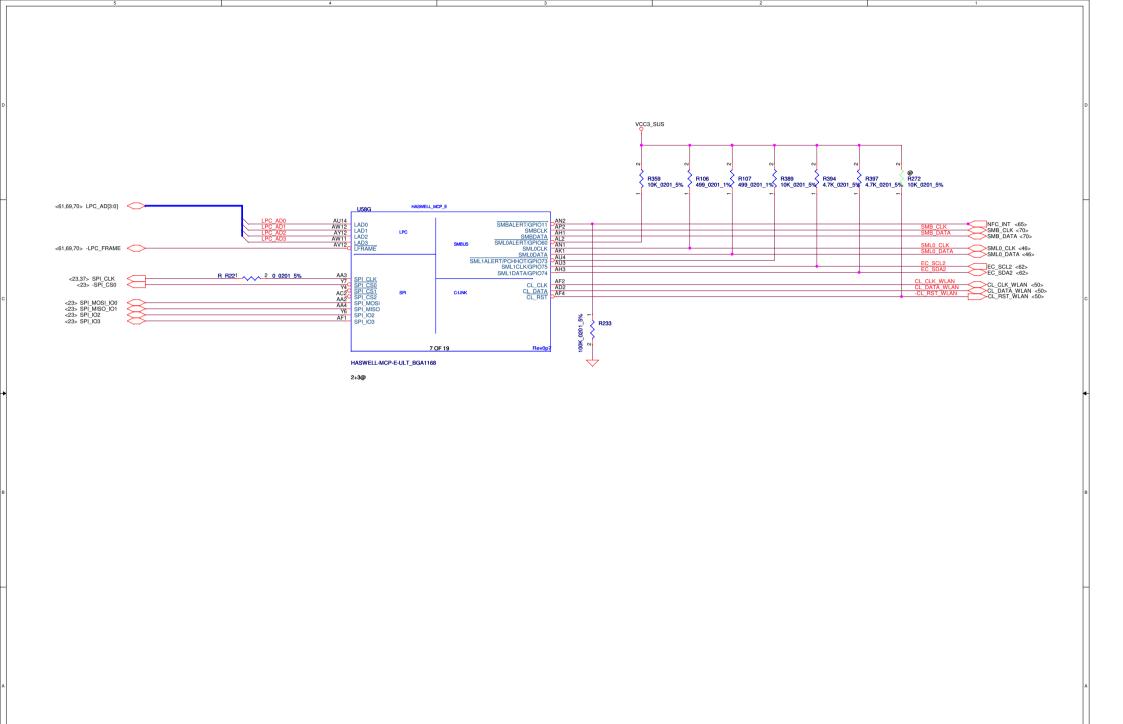








Securi	ity Classification	LC	LC Future Center Secret Data					_ 4	
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AND TR	THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF LC FITTIRE CENTER, AND CONTAINS CONFIDENTIAL AND TRUE SECRET INFORMATION, THIS SHEET MAY NOT BE TRANSPIRED FROM THE QUOTO OF THE COMPETENT DIVISION OF RAD MAY BE LISED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF LC FITTIRE CENTER.					Document Number STING			Rev 0.1
					Date:	Thursday, May 02, 2013	Sheet	8 of	100
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LC Future Center Secret Data

Issued Date

2012/05/01

Deciphered Date

2012/12/01

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CPU(7/16): LPC/SPI/SMBUS/C-LINK

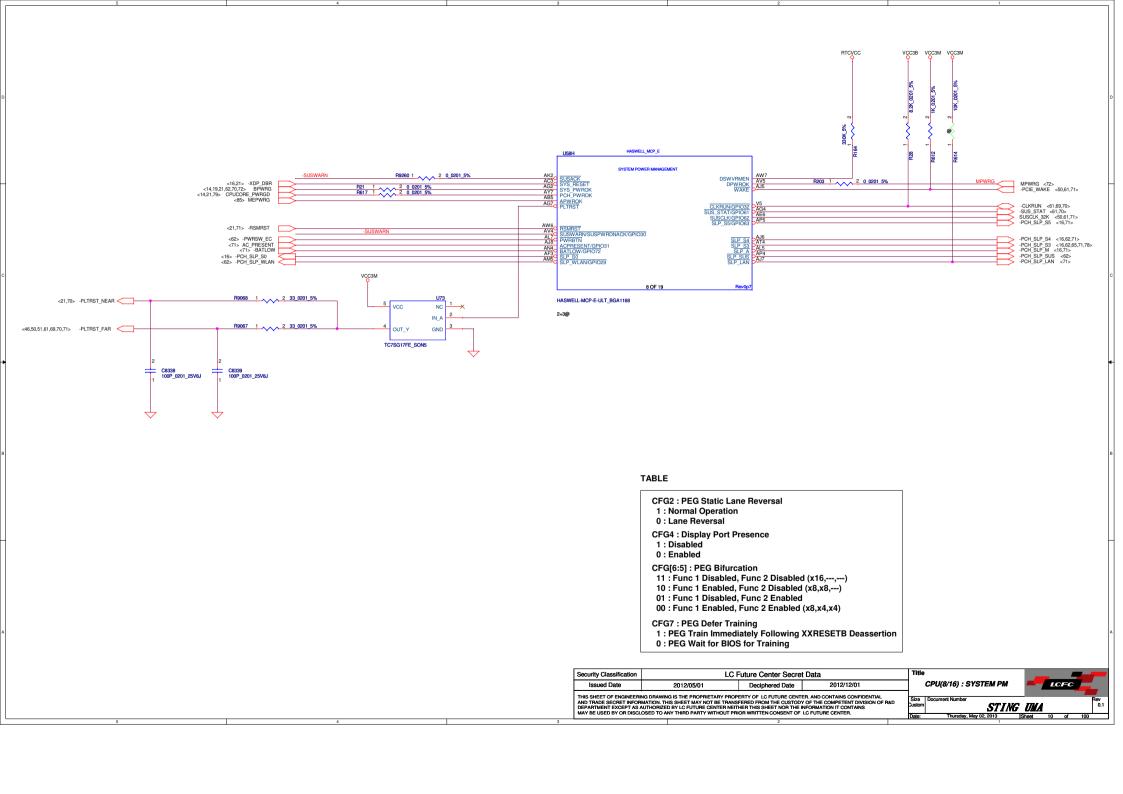
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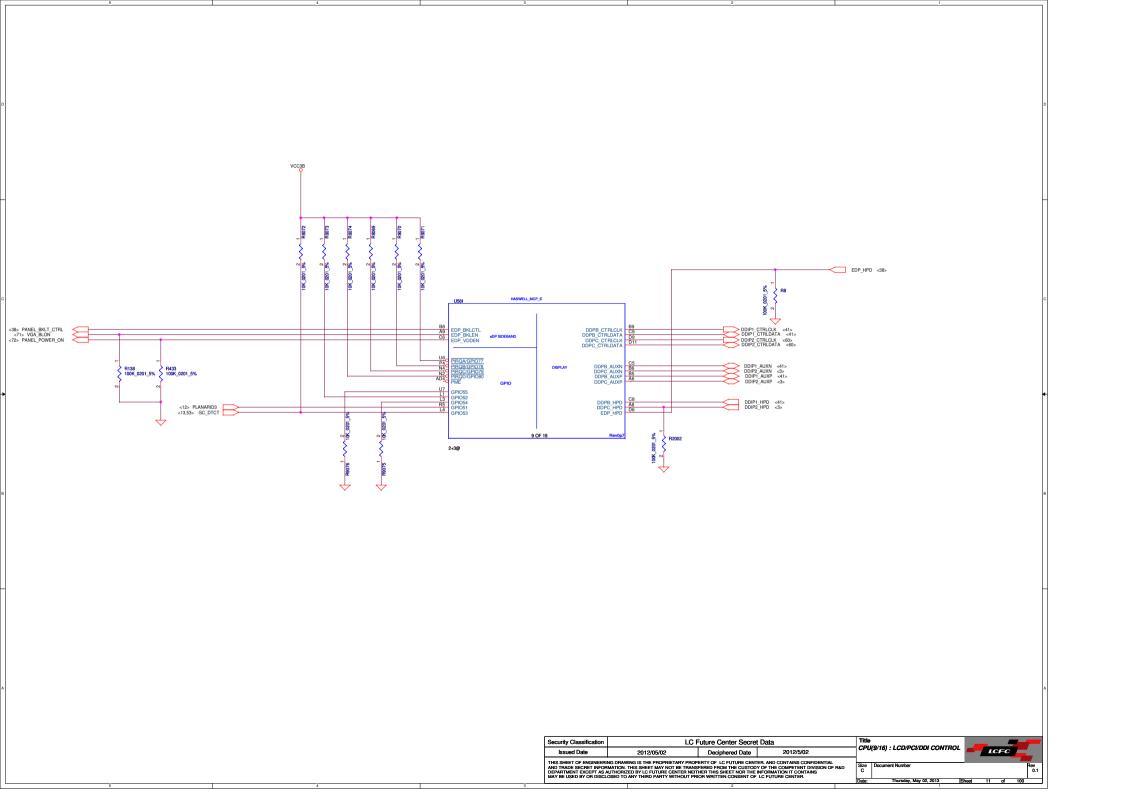
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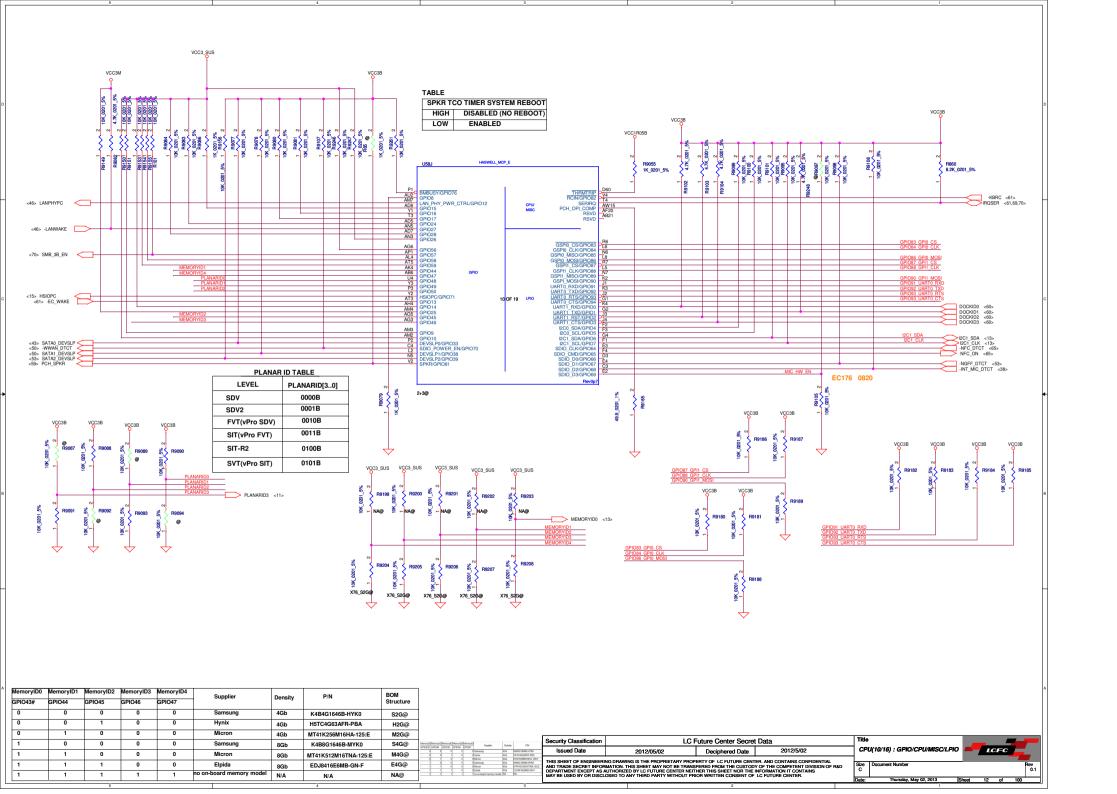
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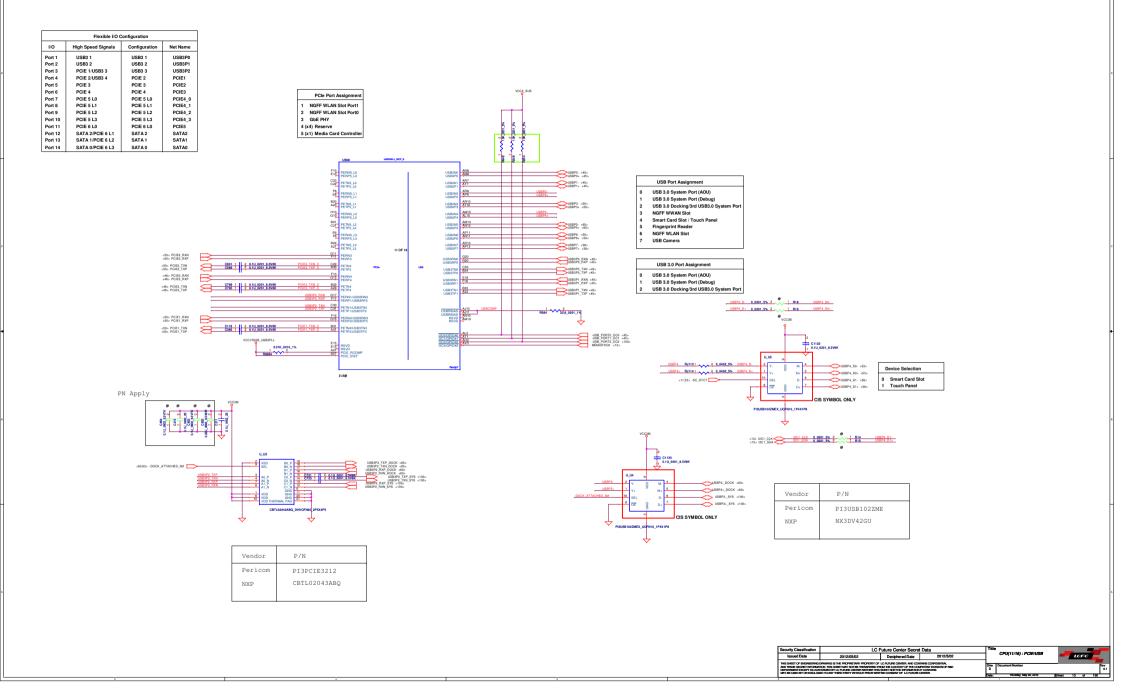
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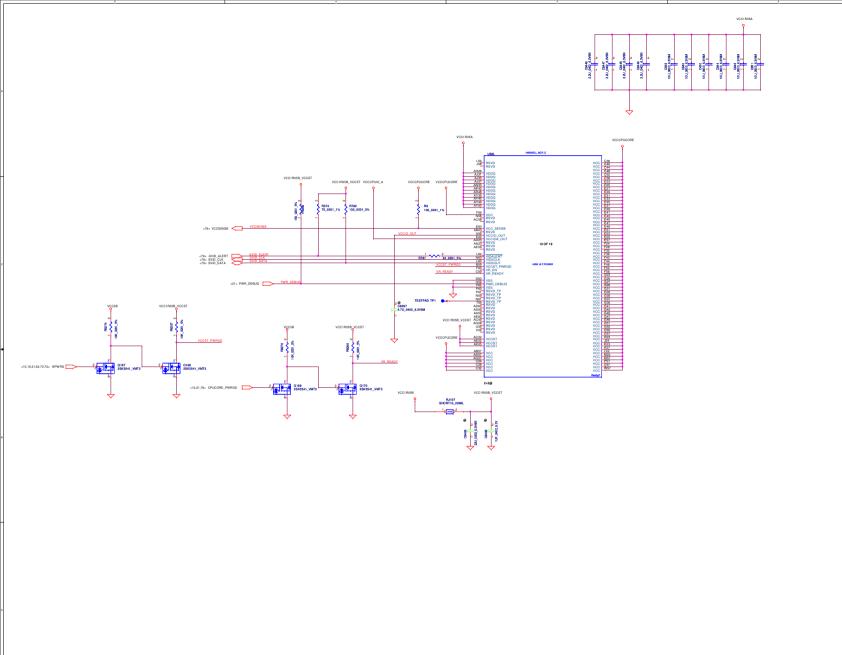
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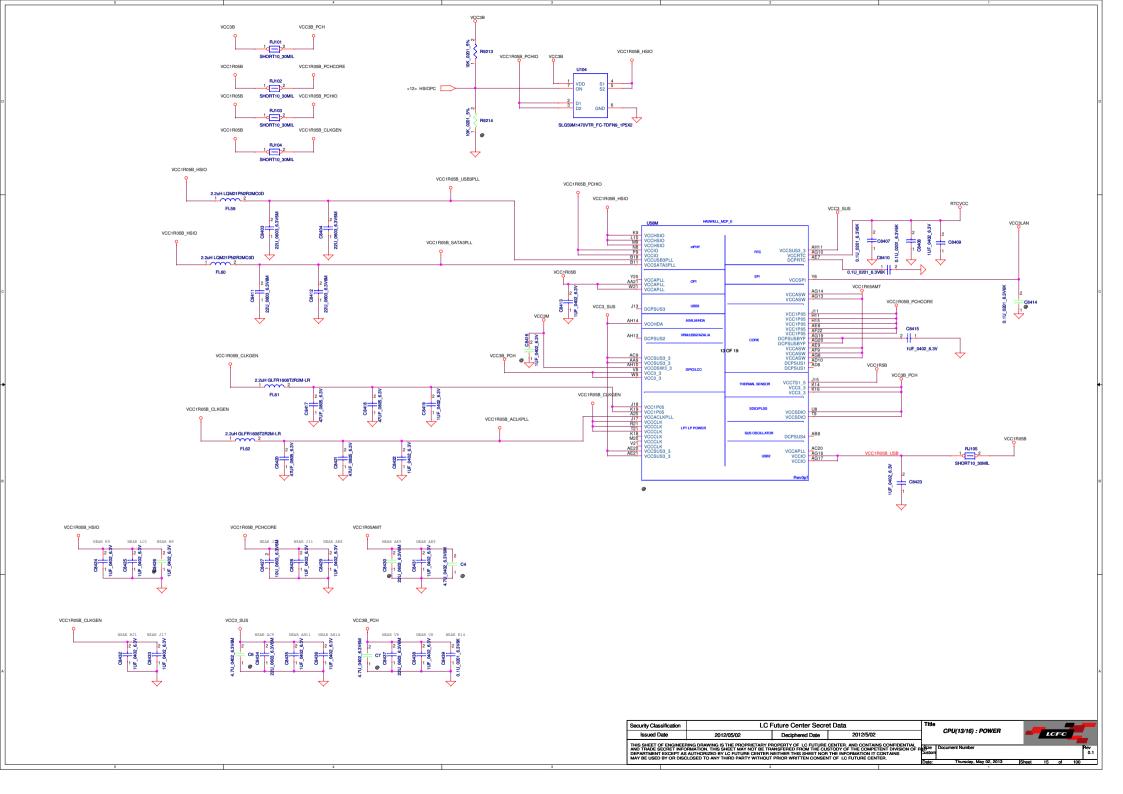




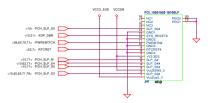


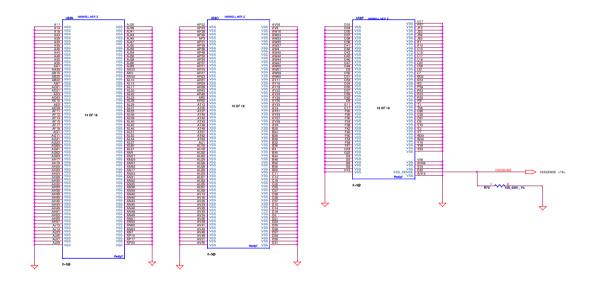


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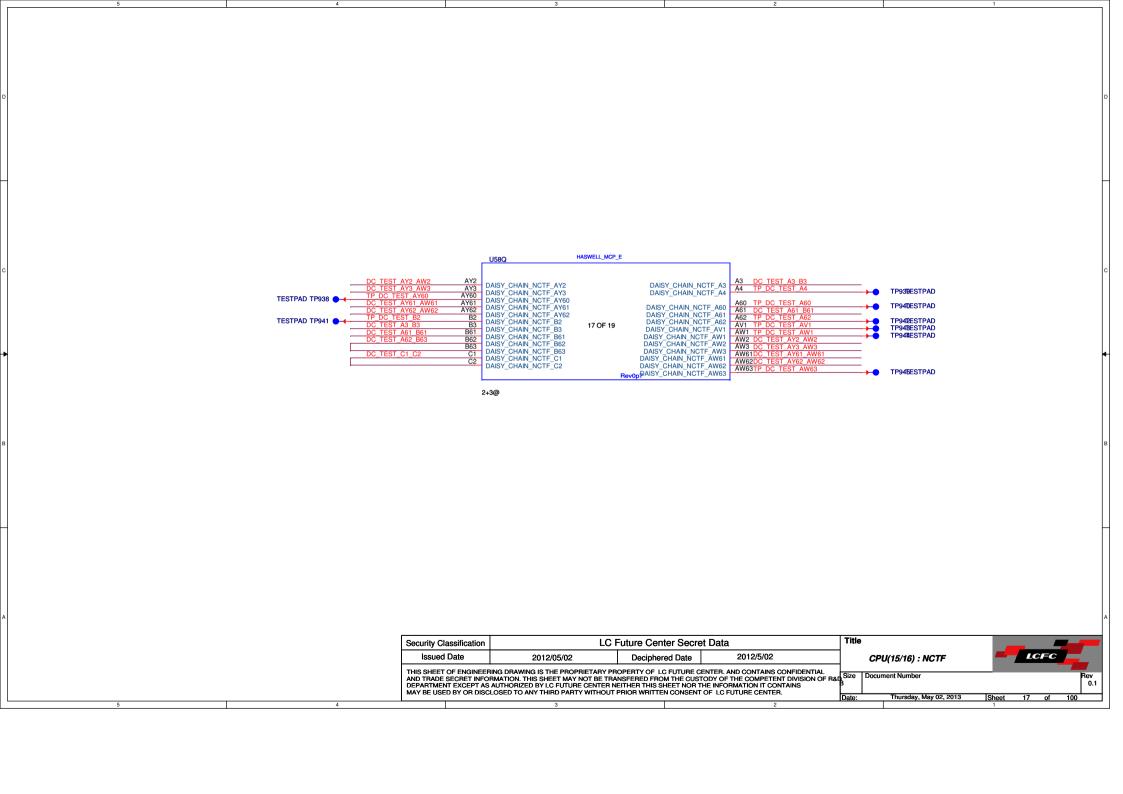


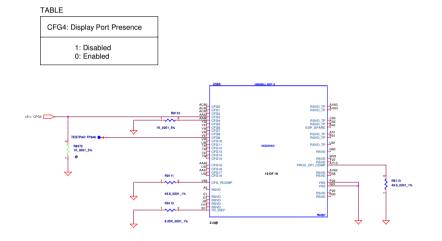
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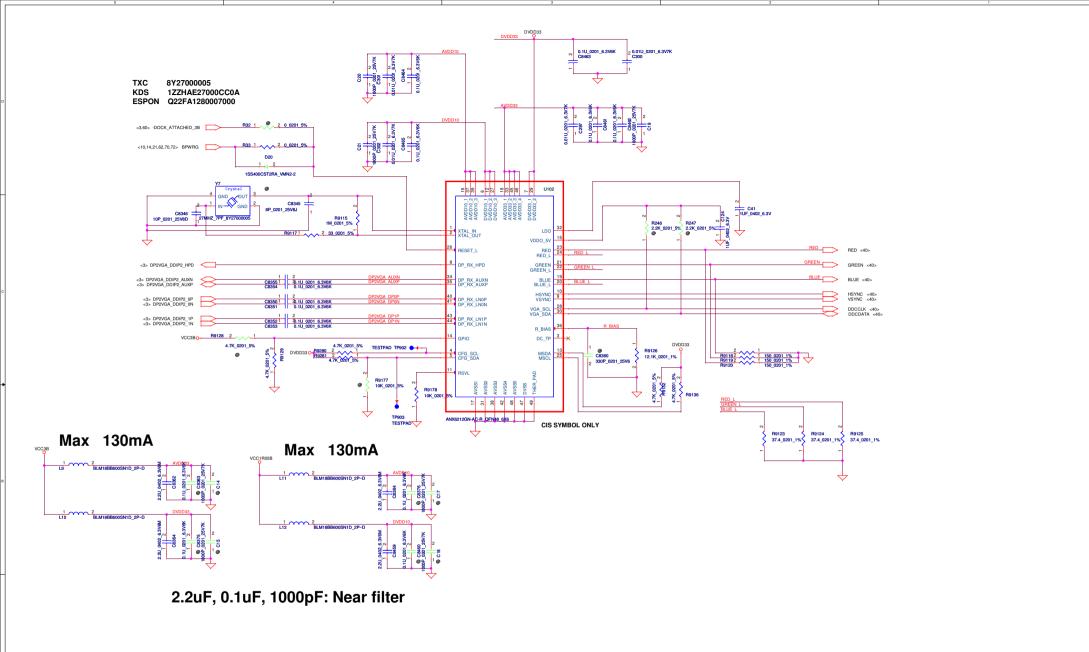
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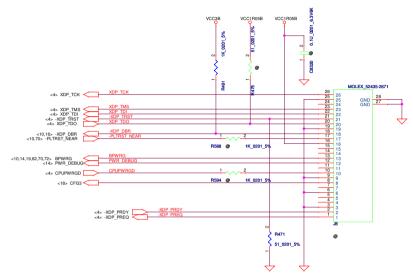
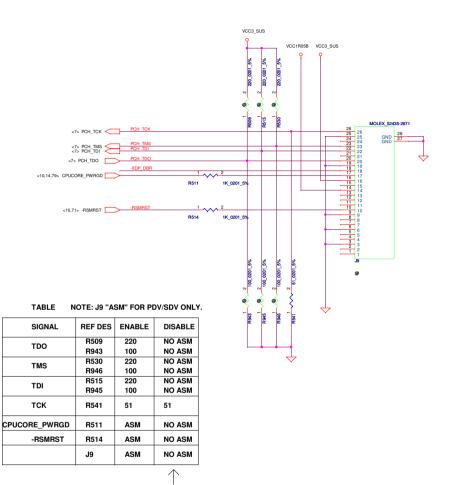


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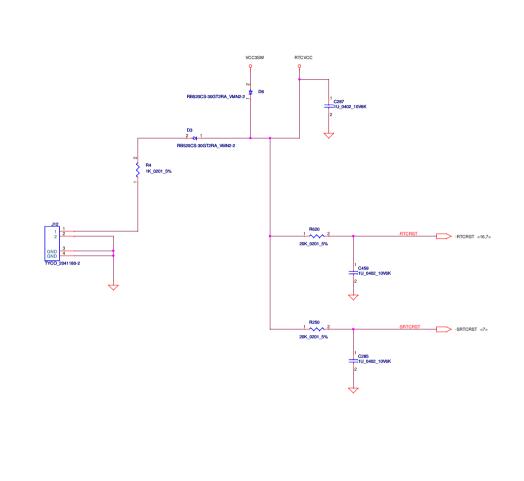
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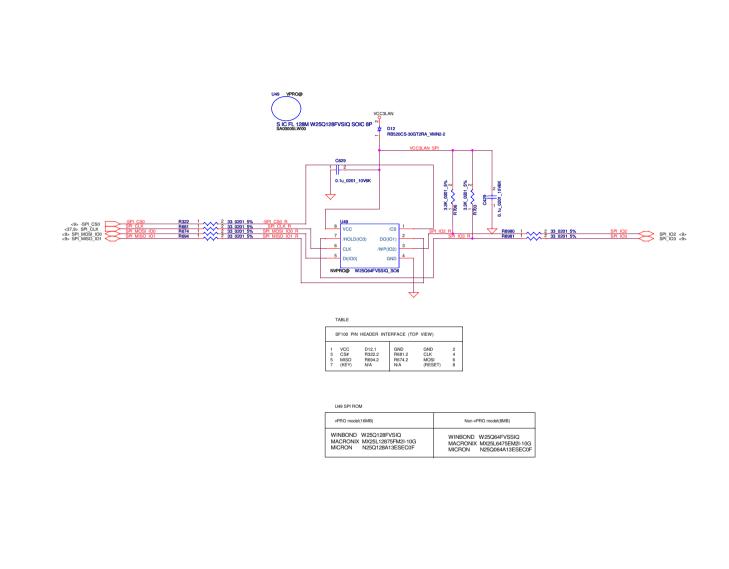


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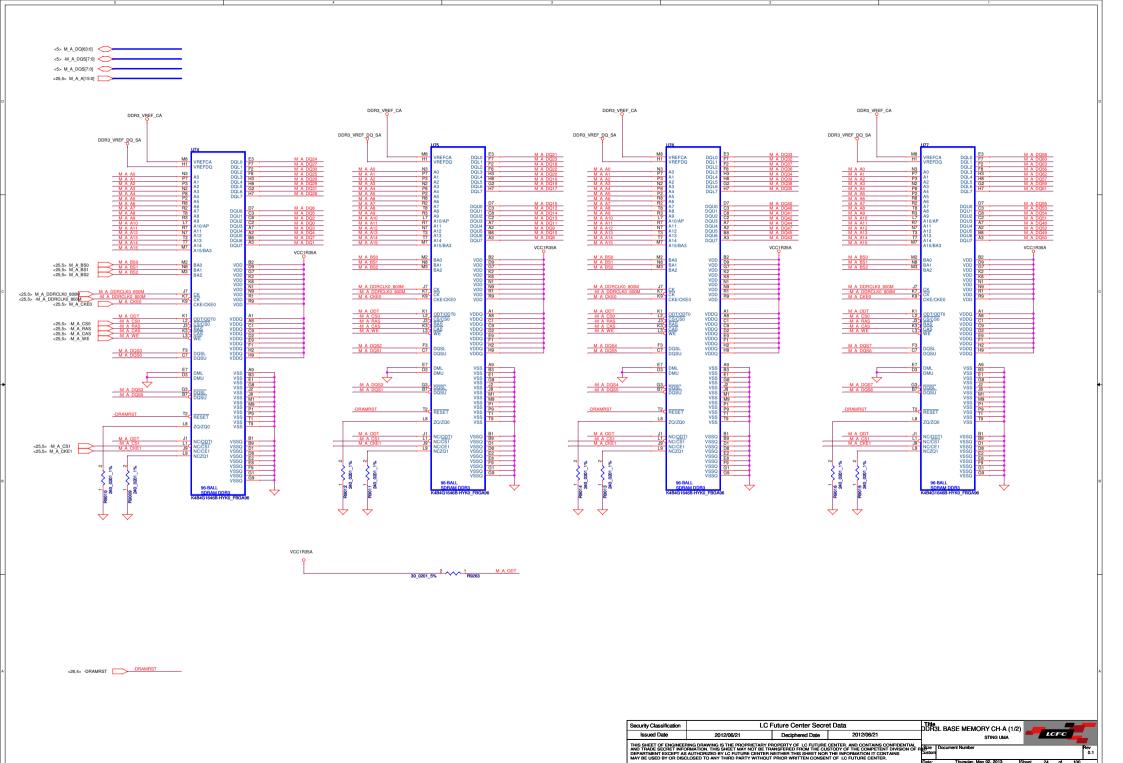
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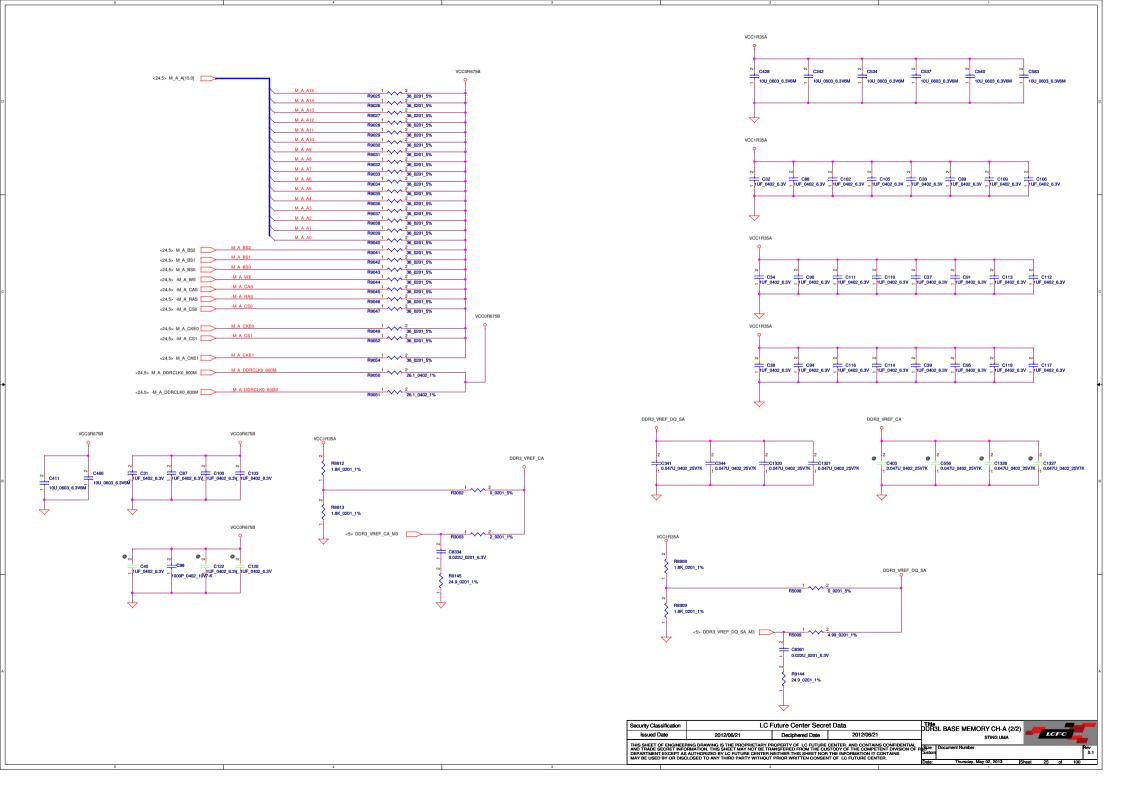


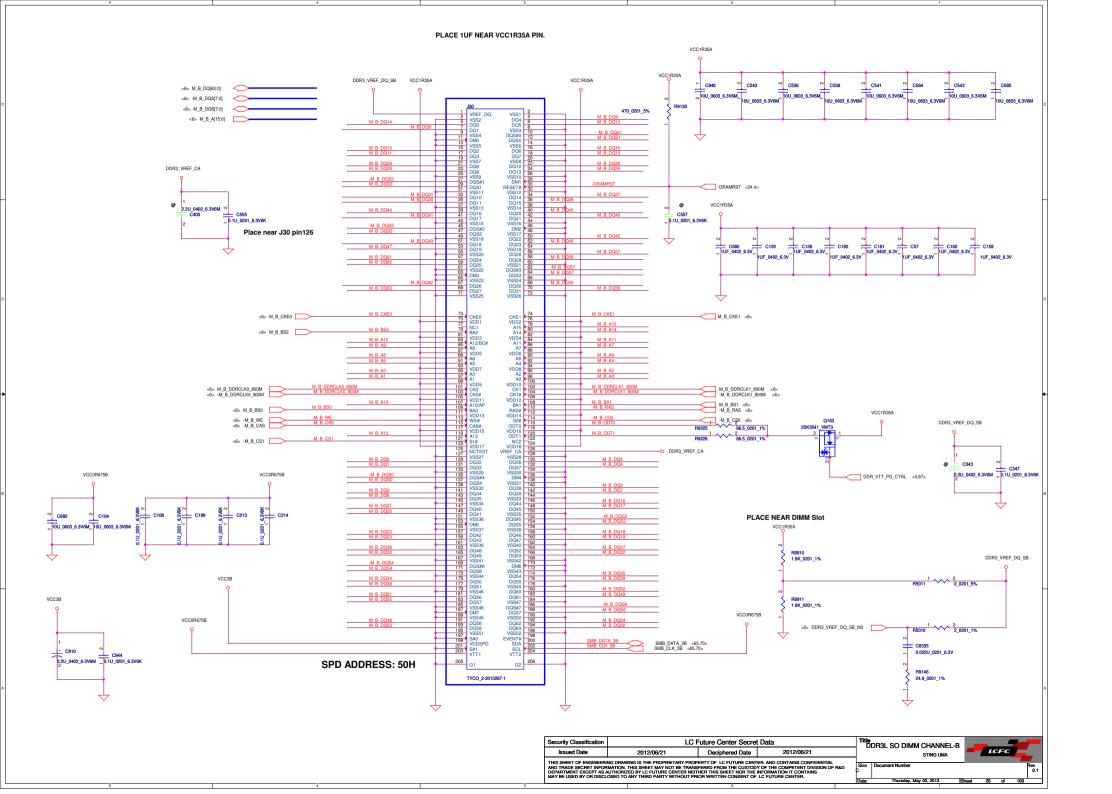
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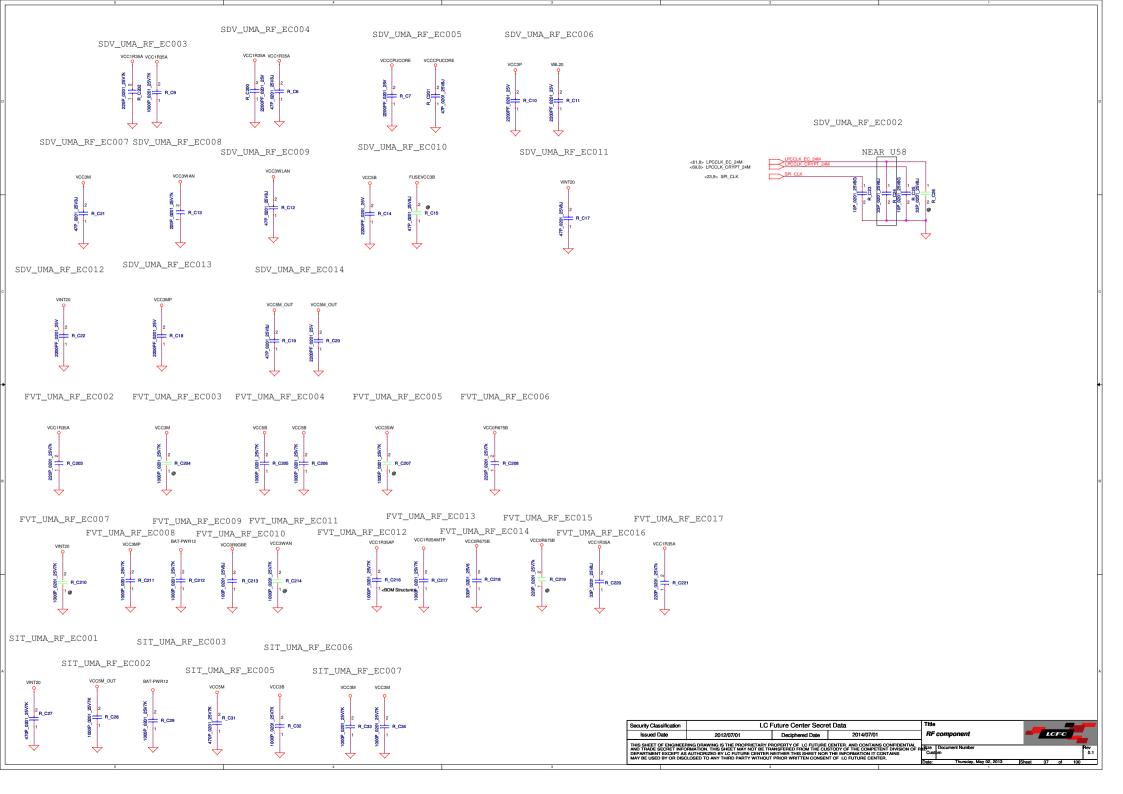
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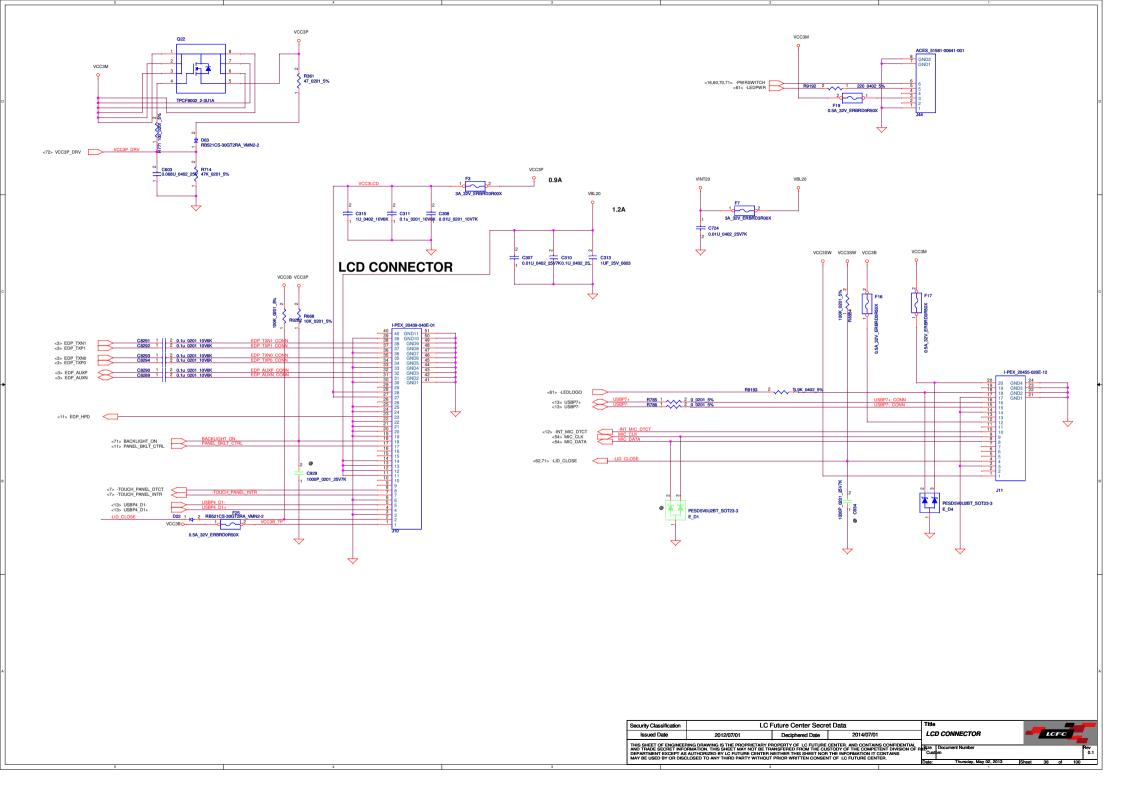
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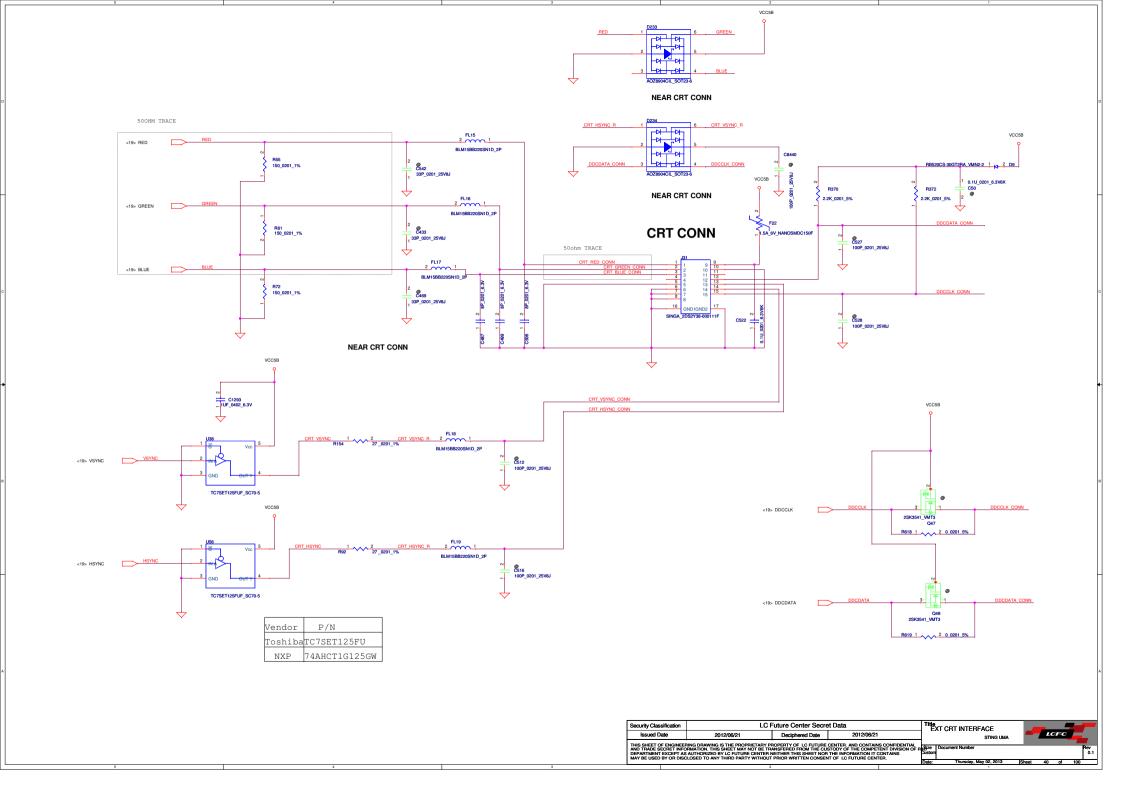
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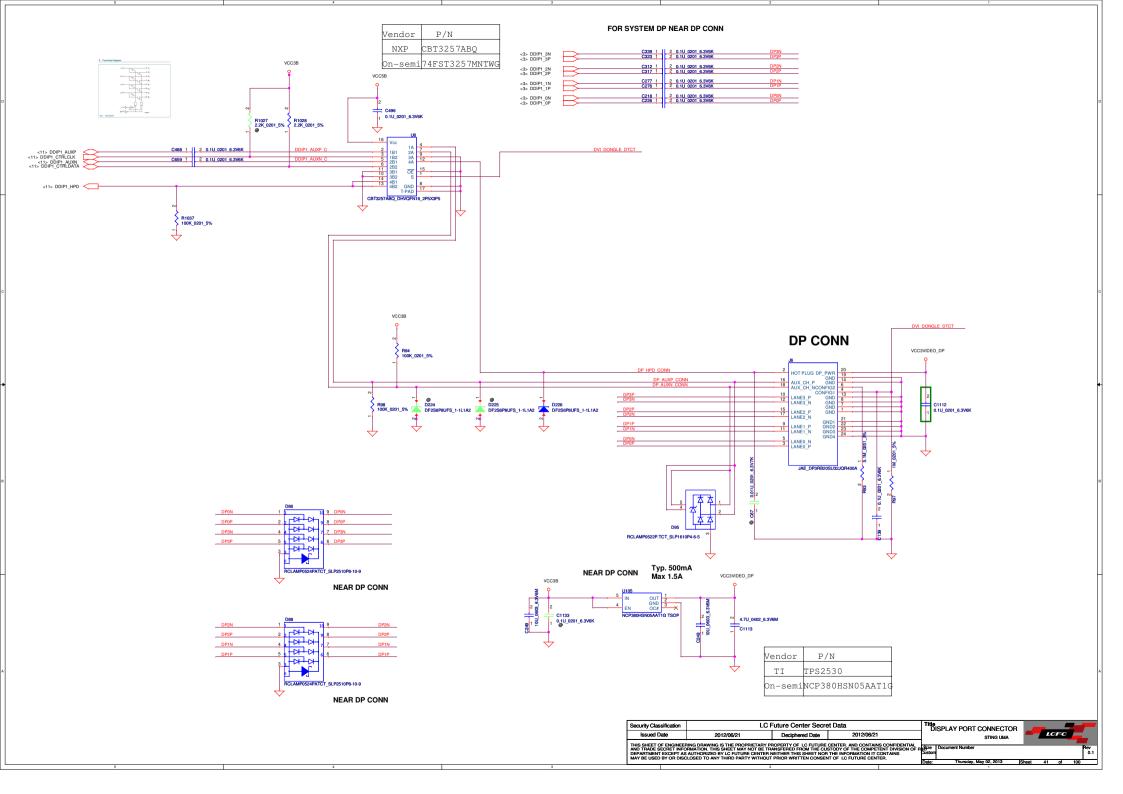
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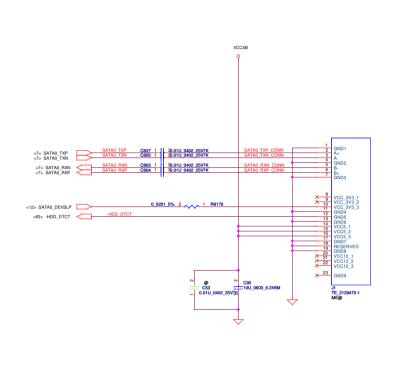




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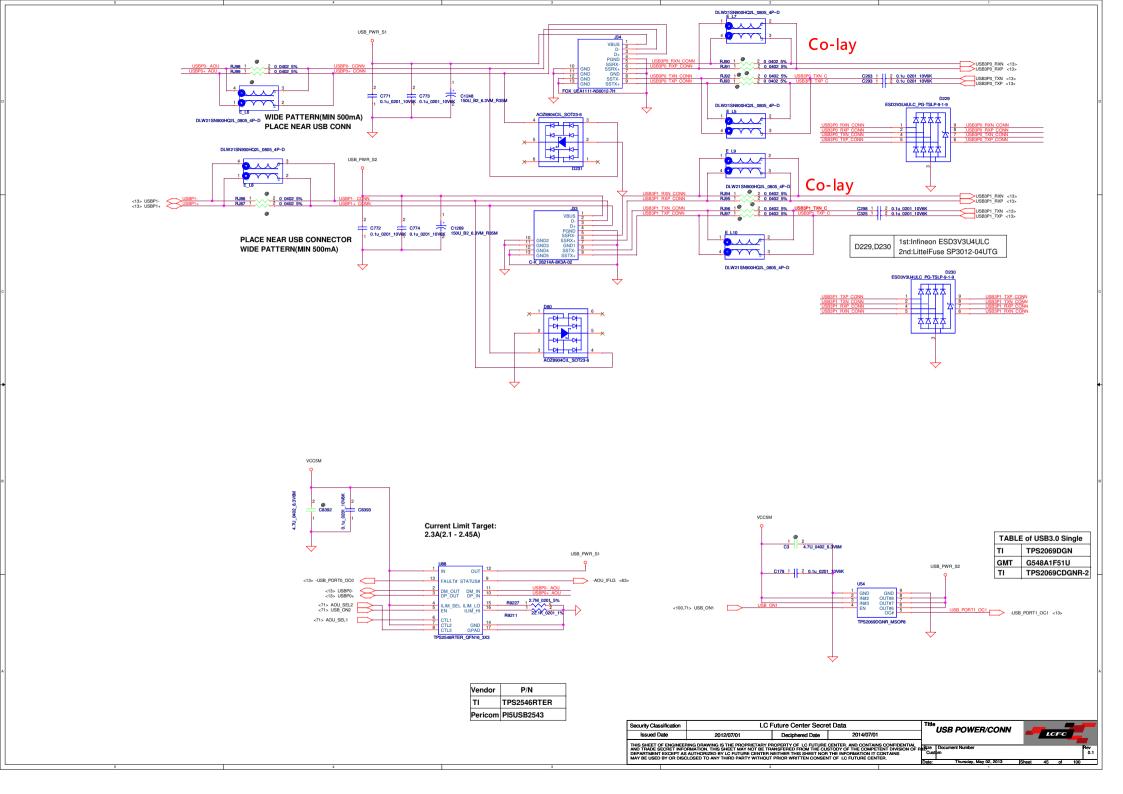
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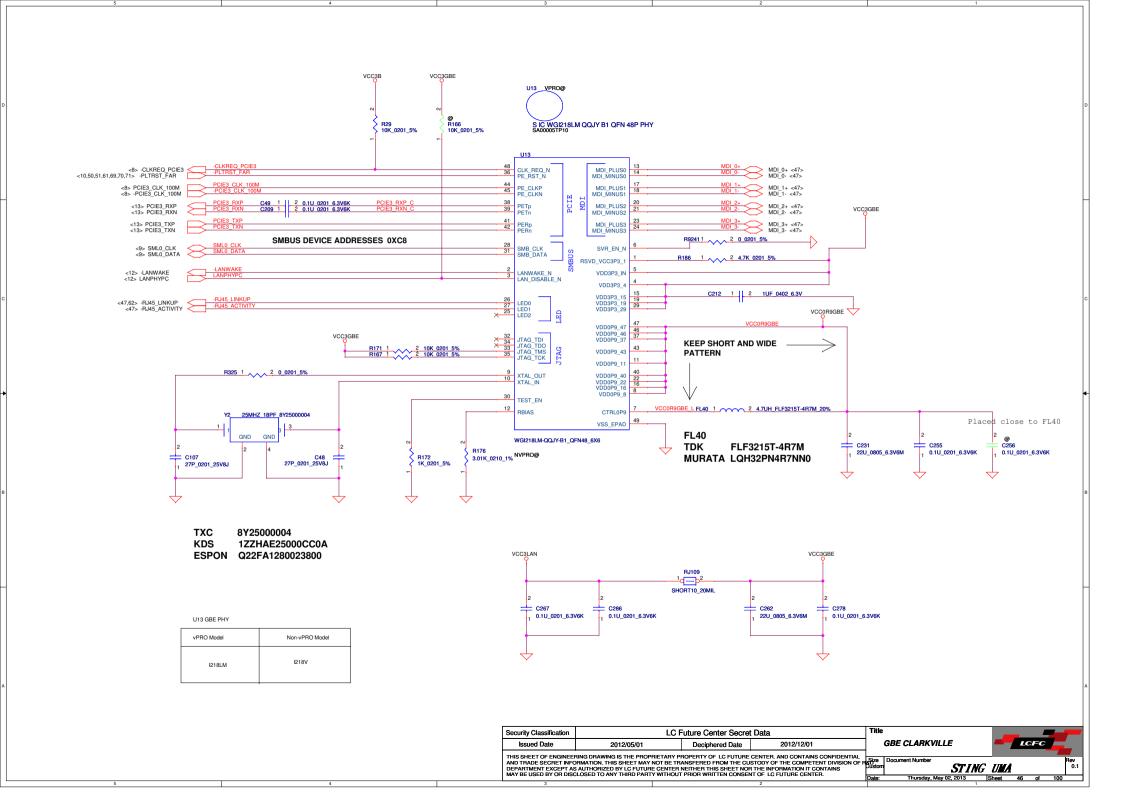
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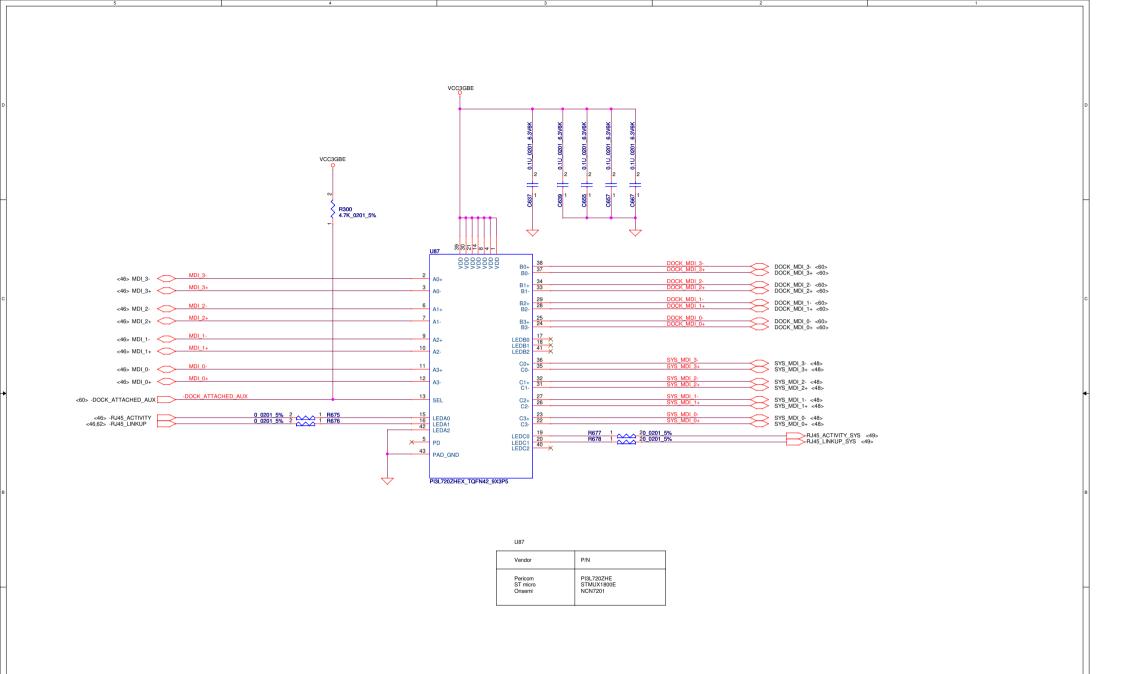
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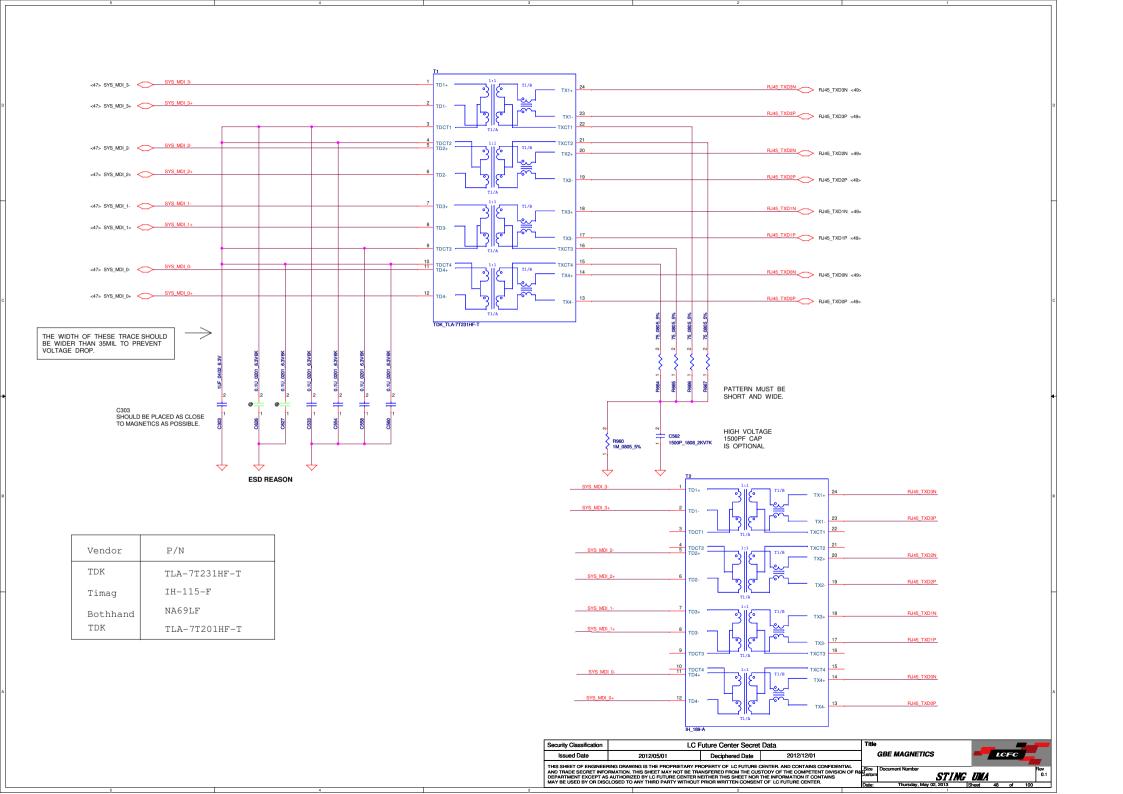
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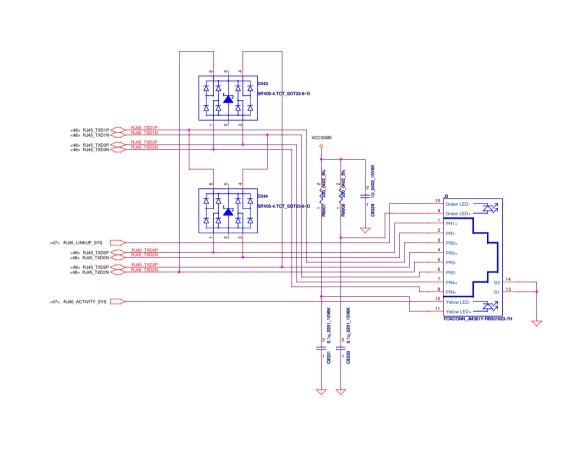






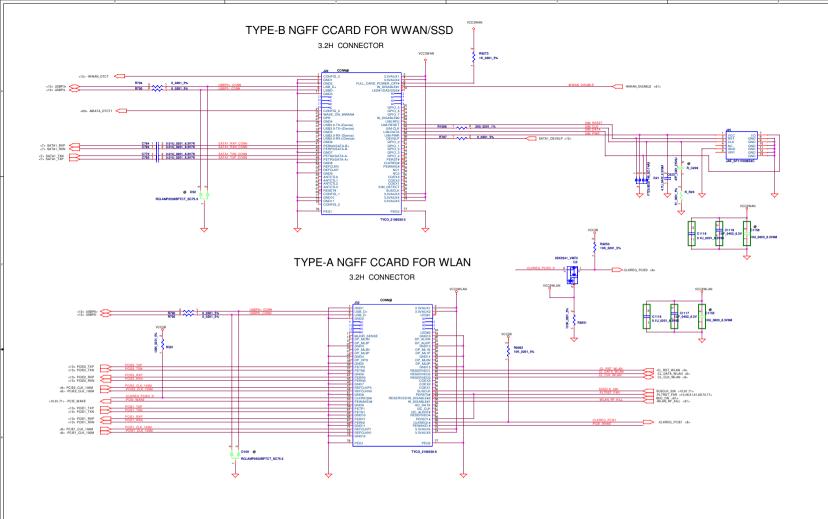
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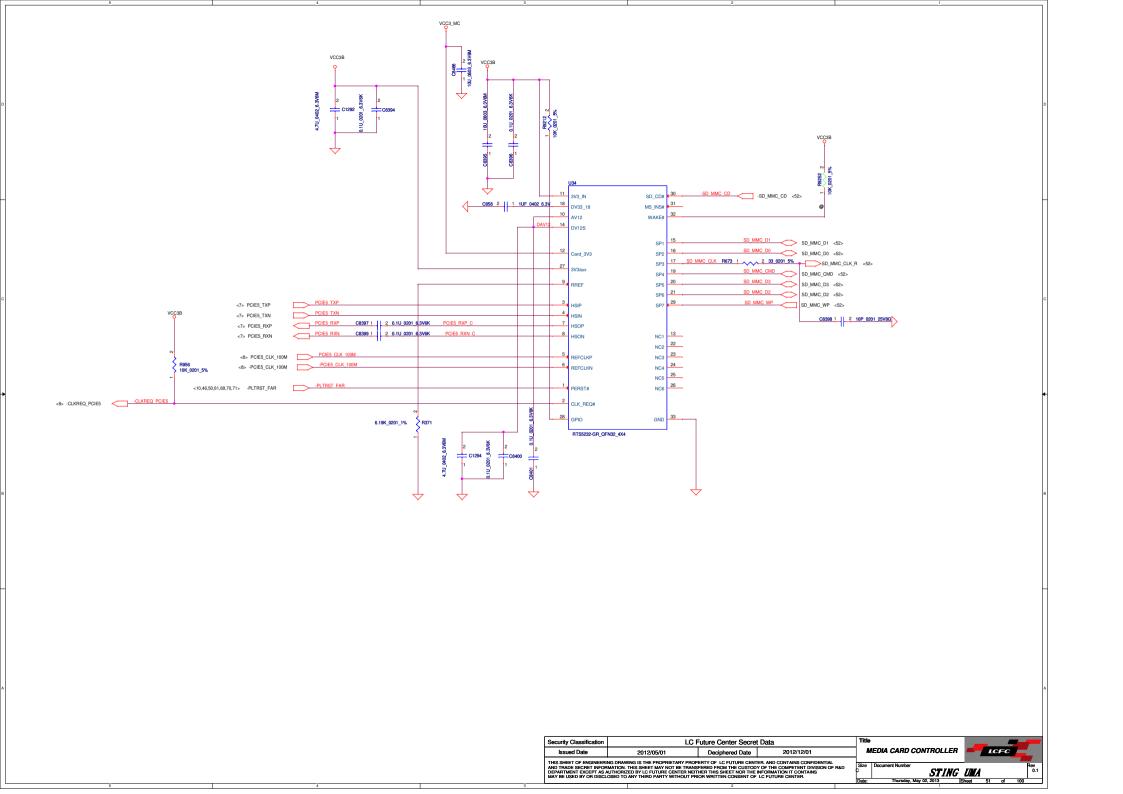


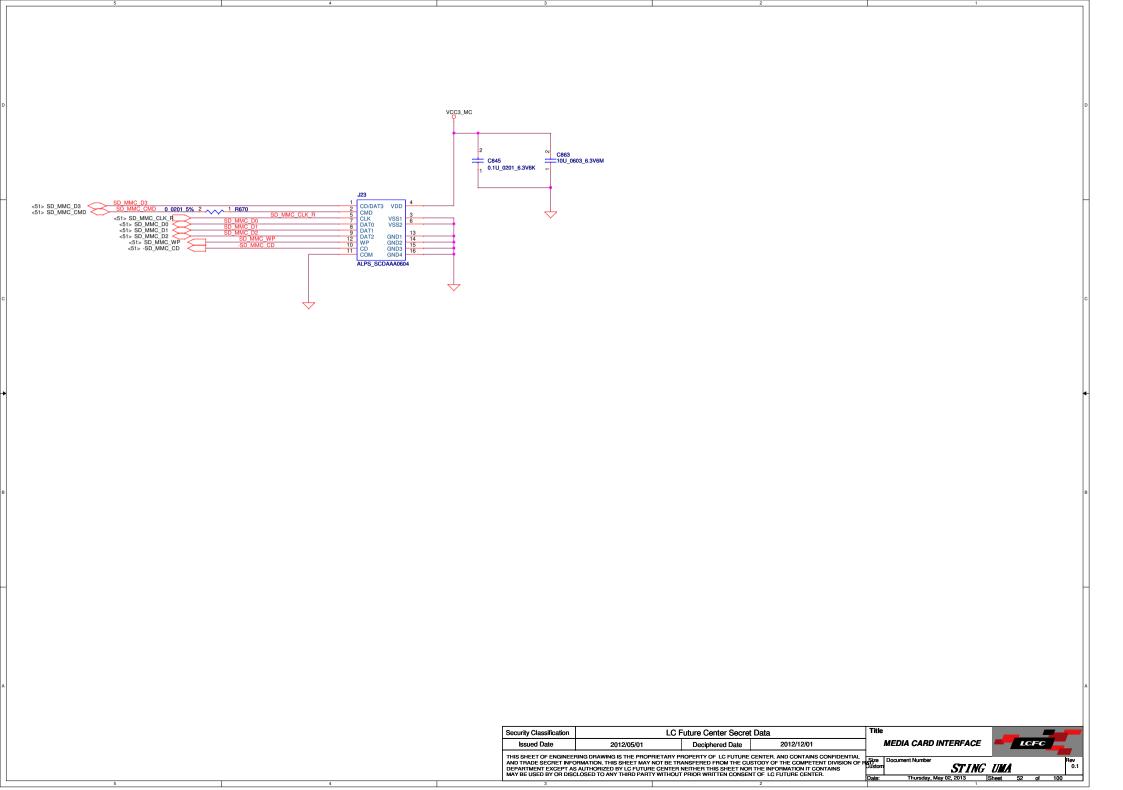
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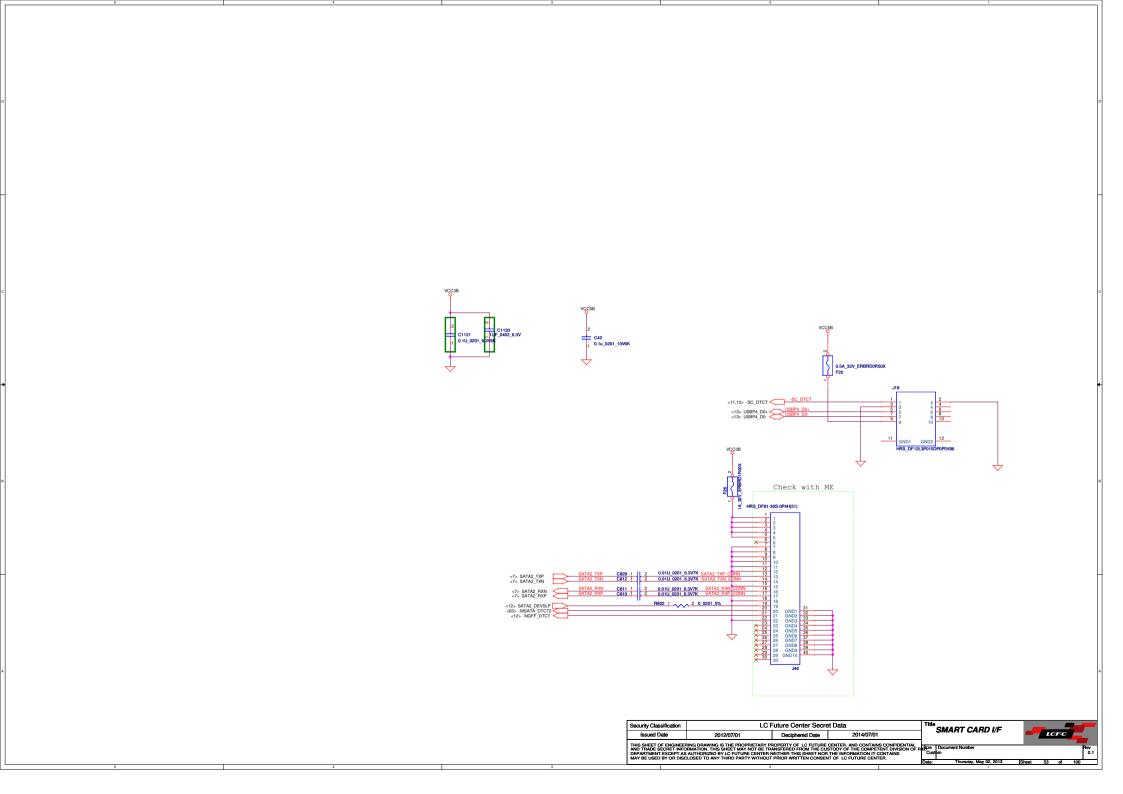


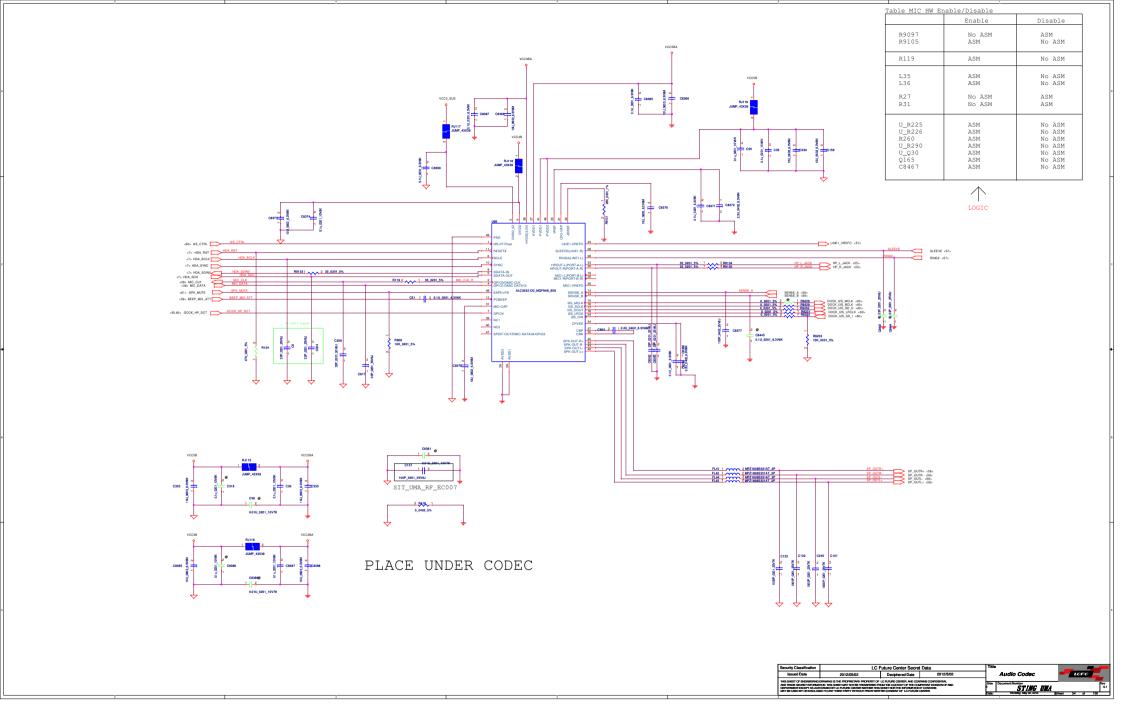


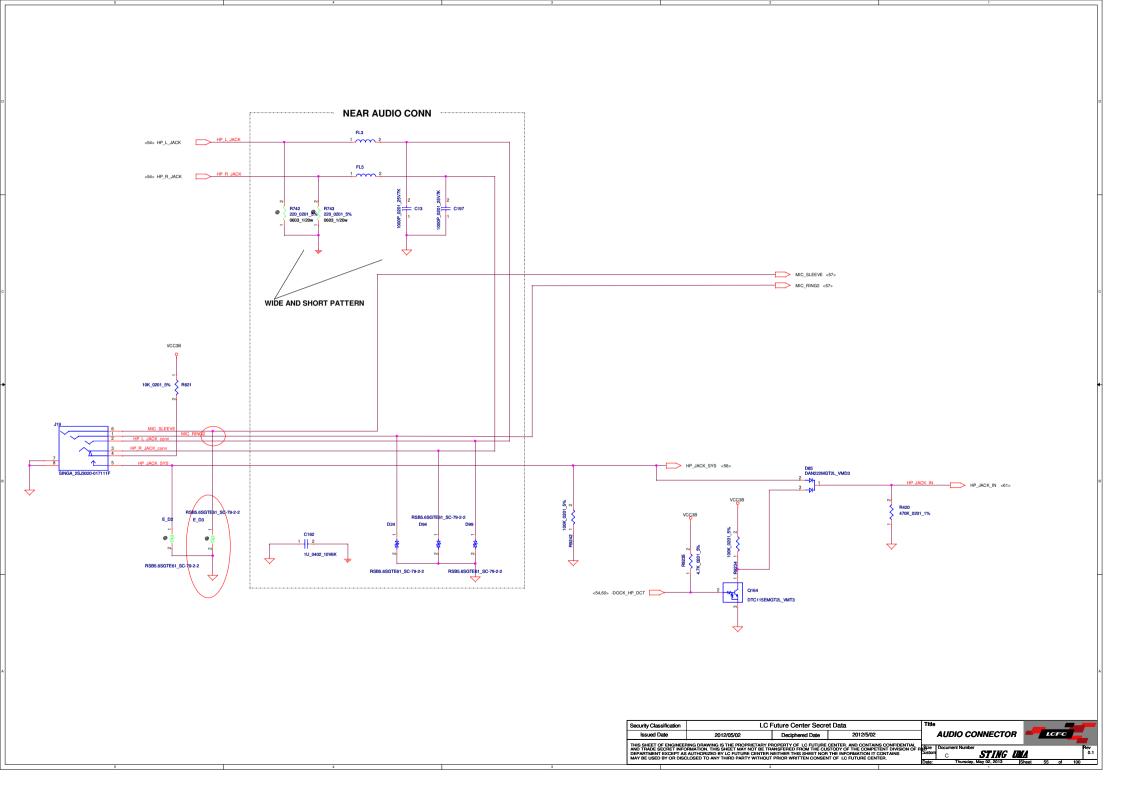
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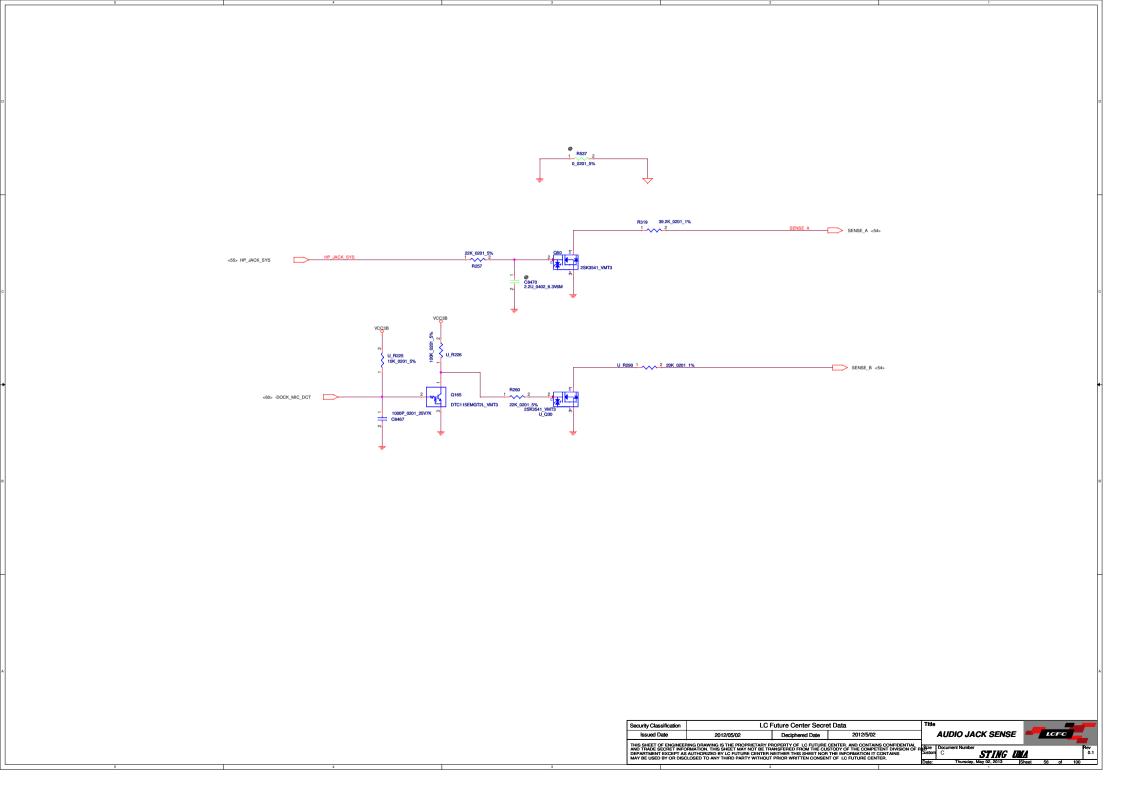


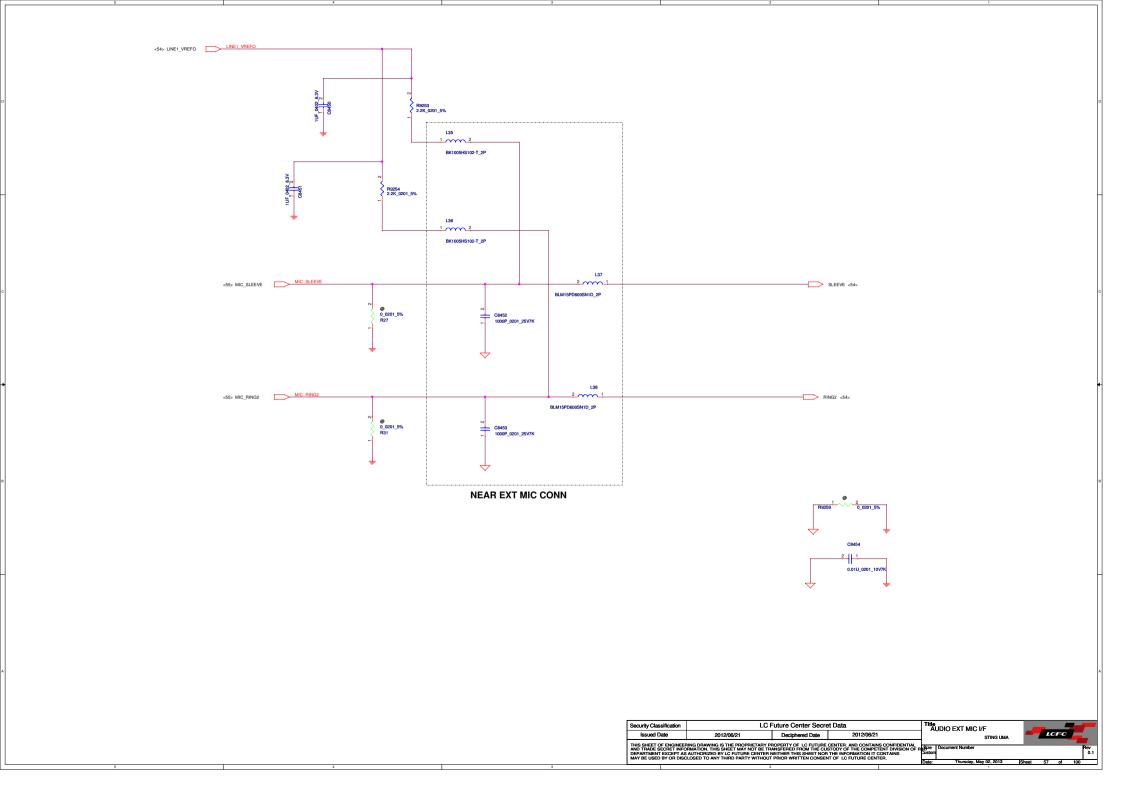


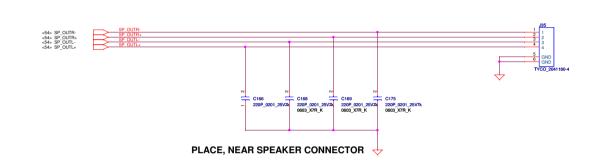




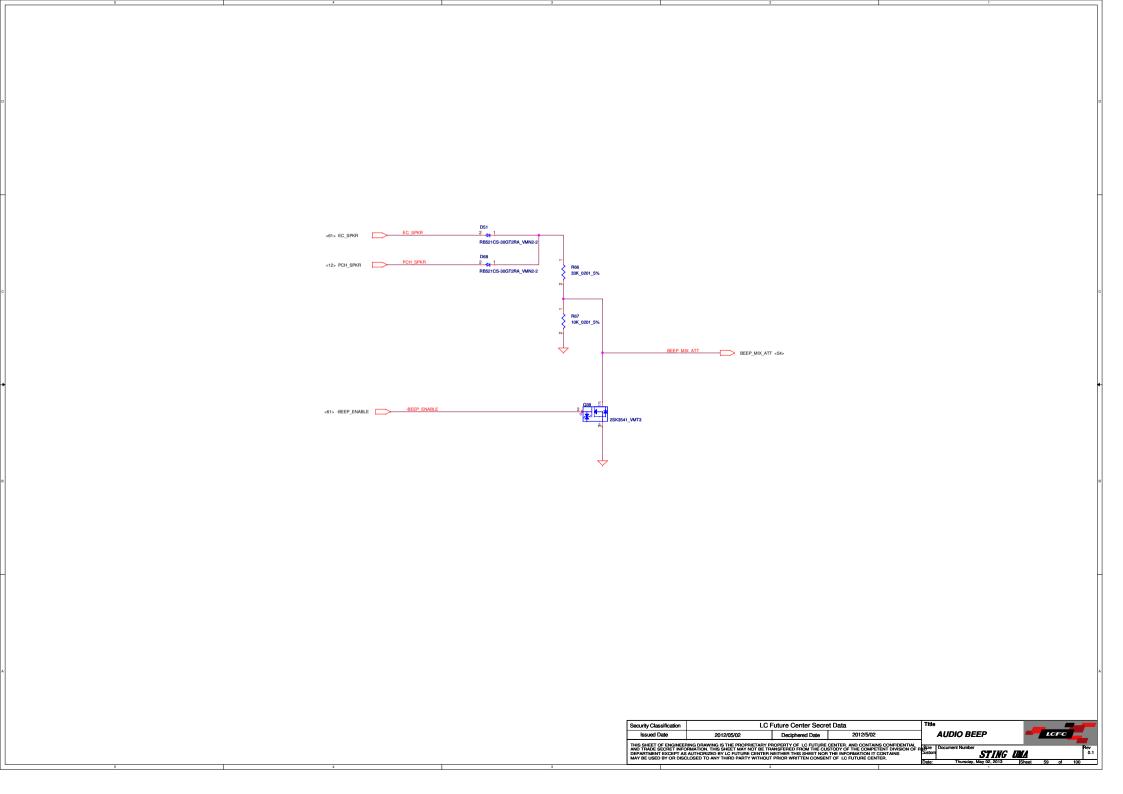


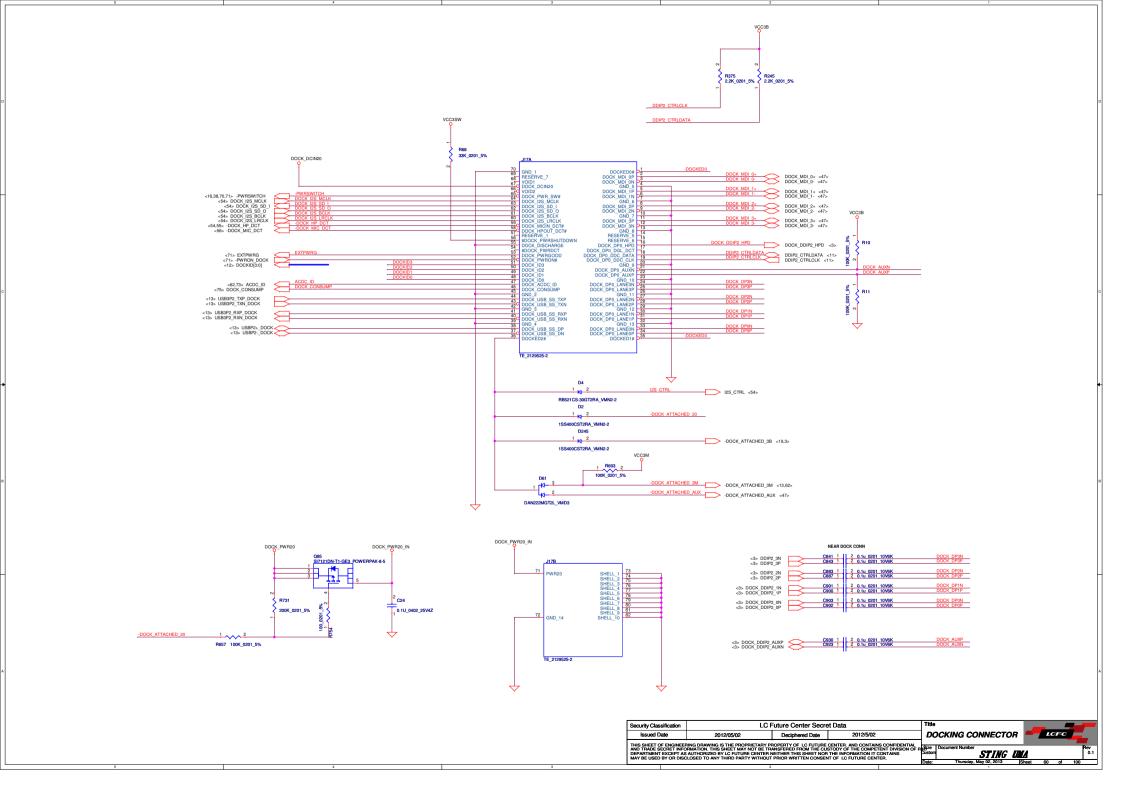


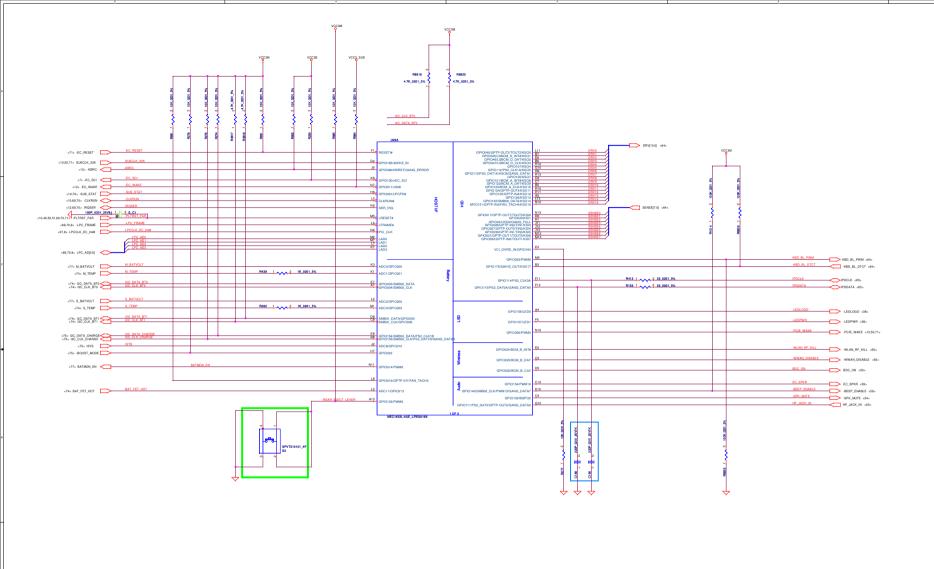




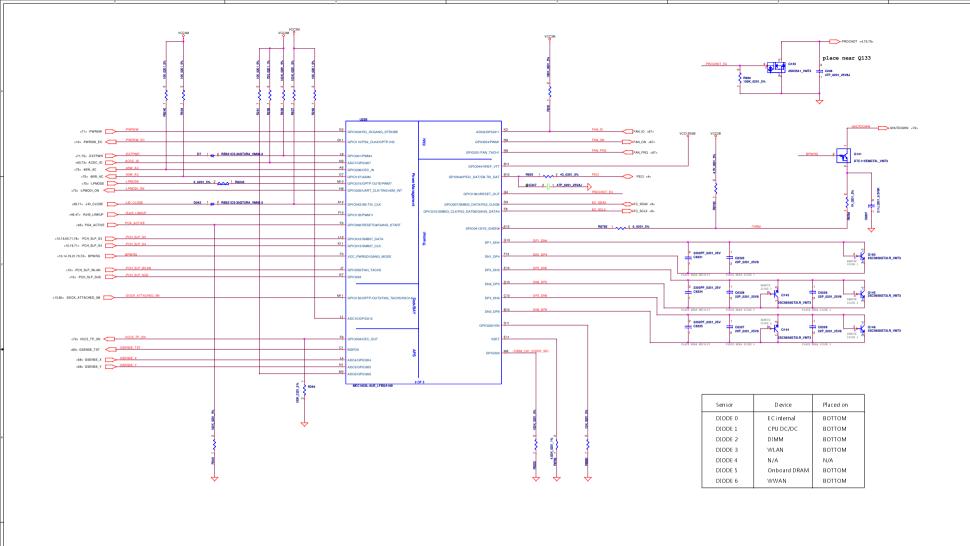
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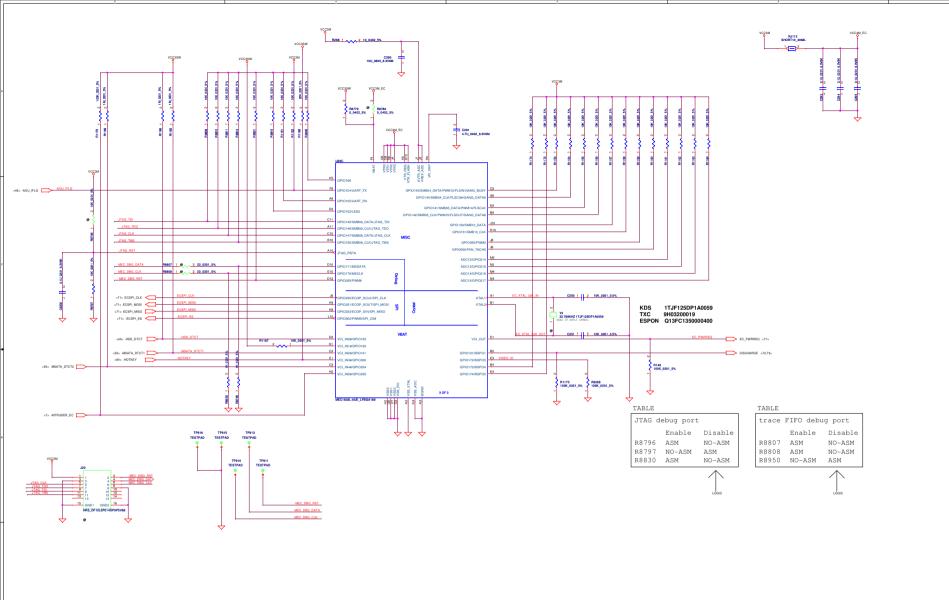




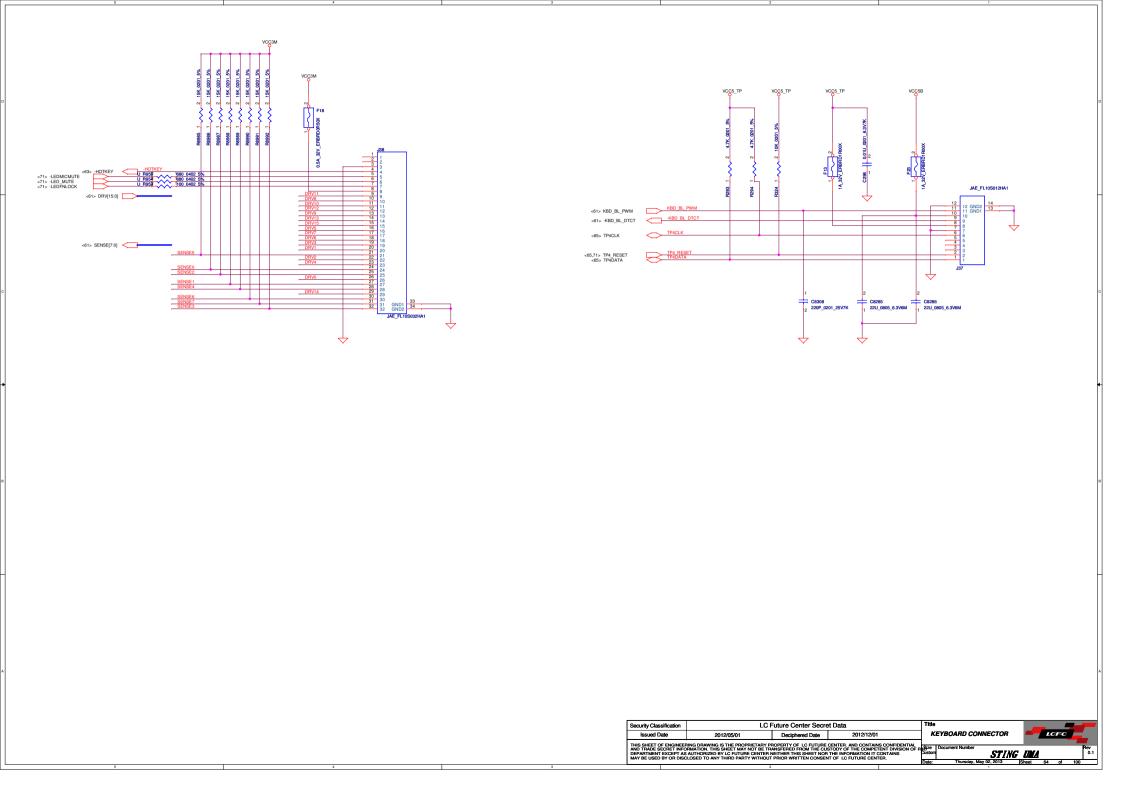
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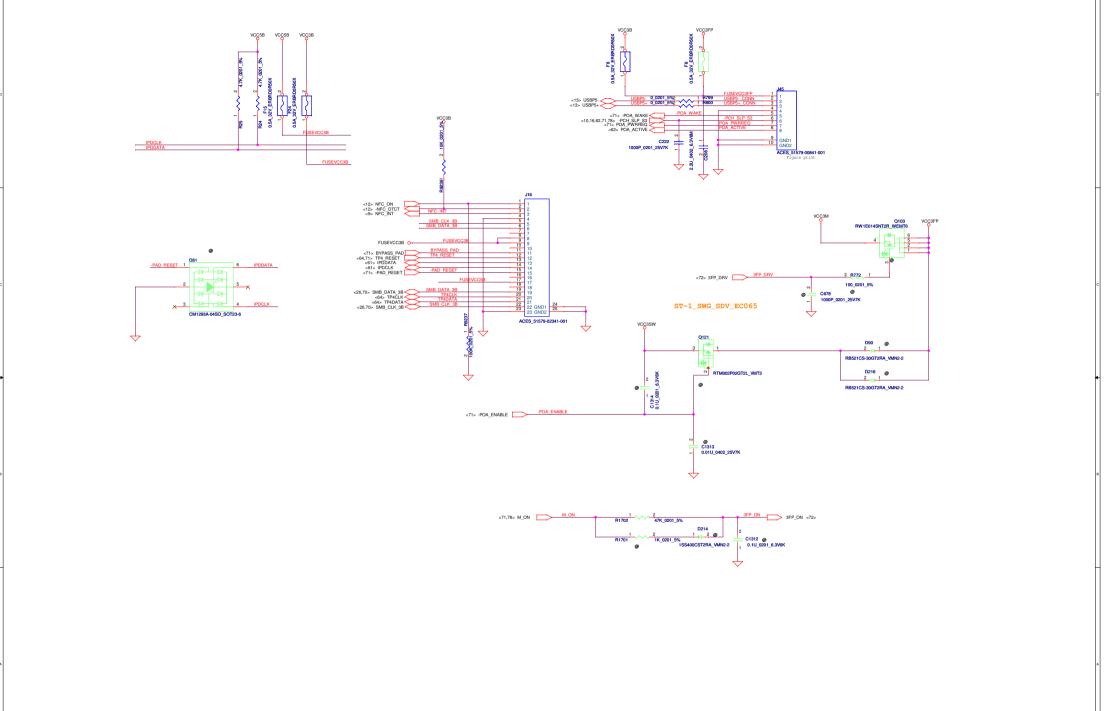


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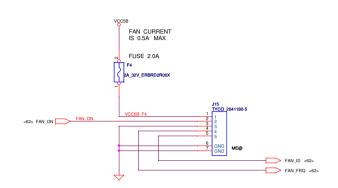
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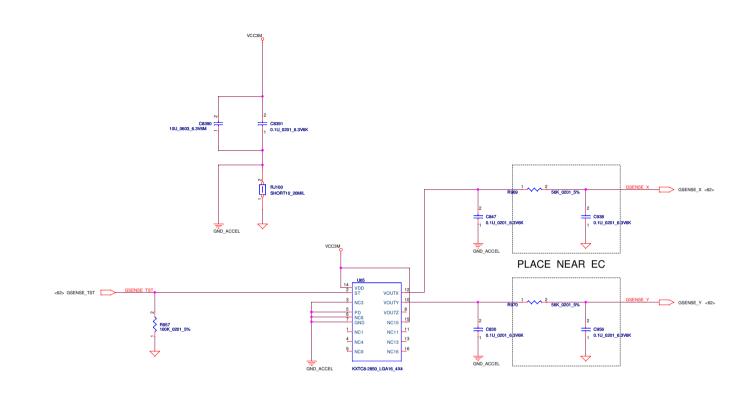


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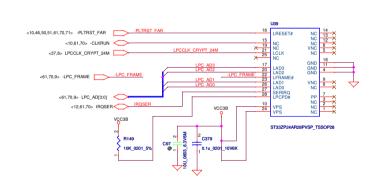
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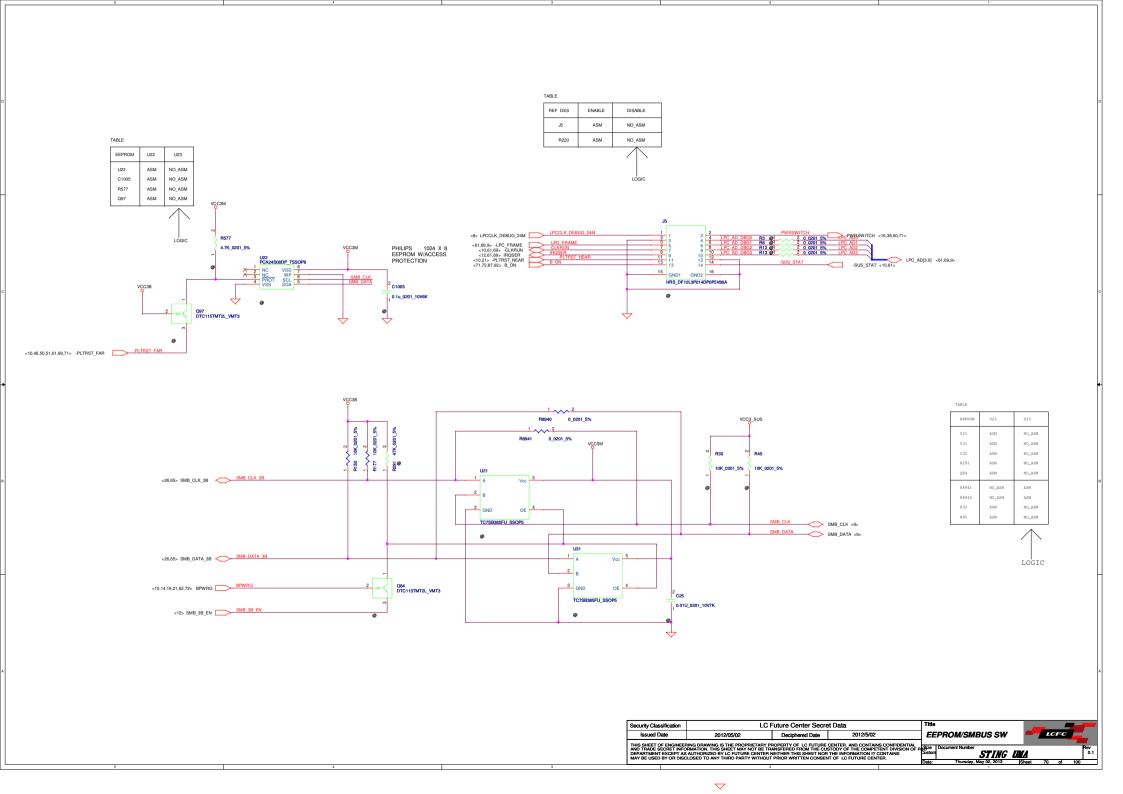


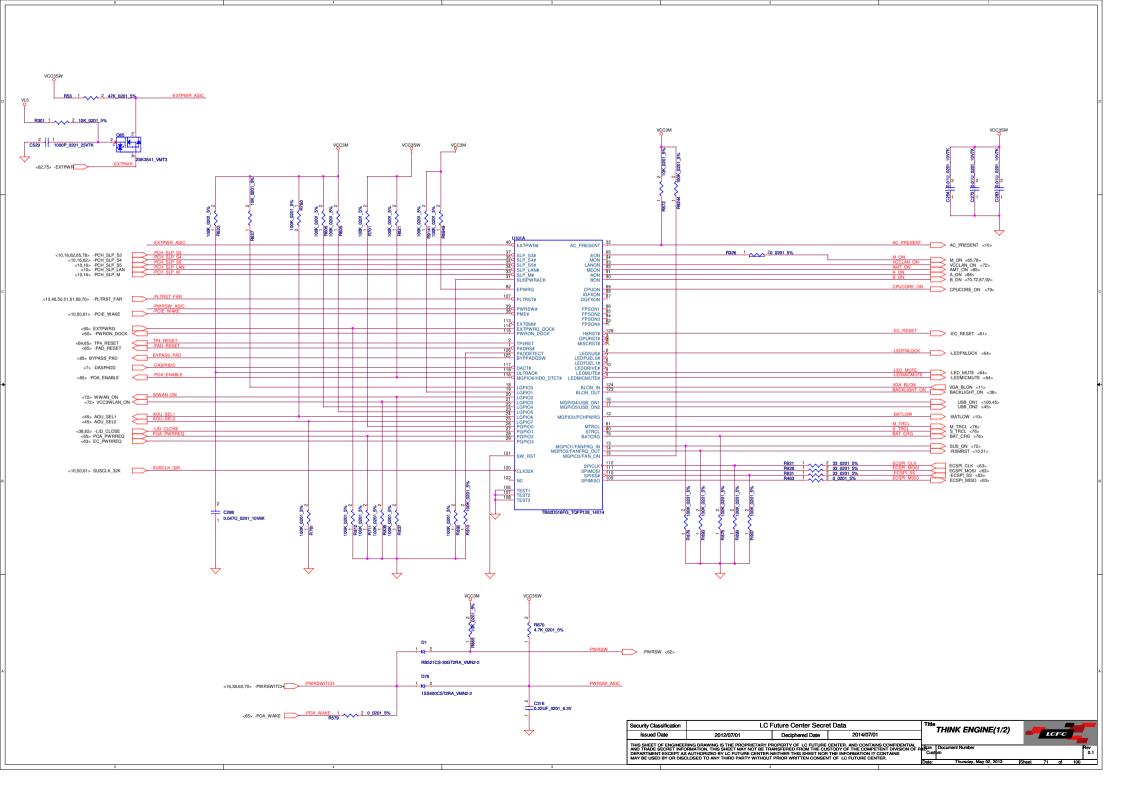
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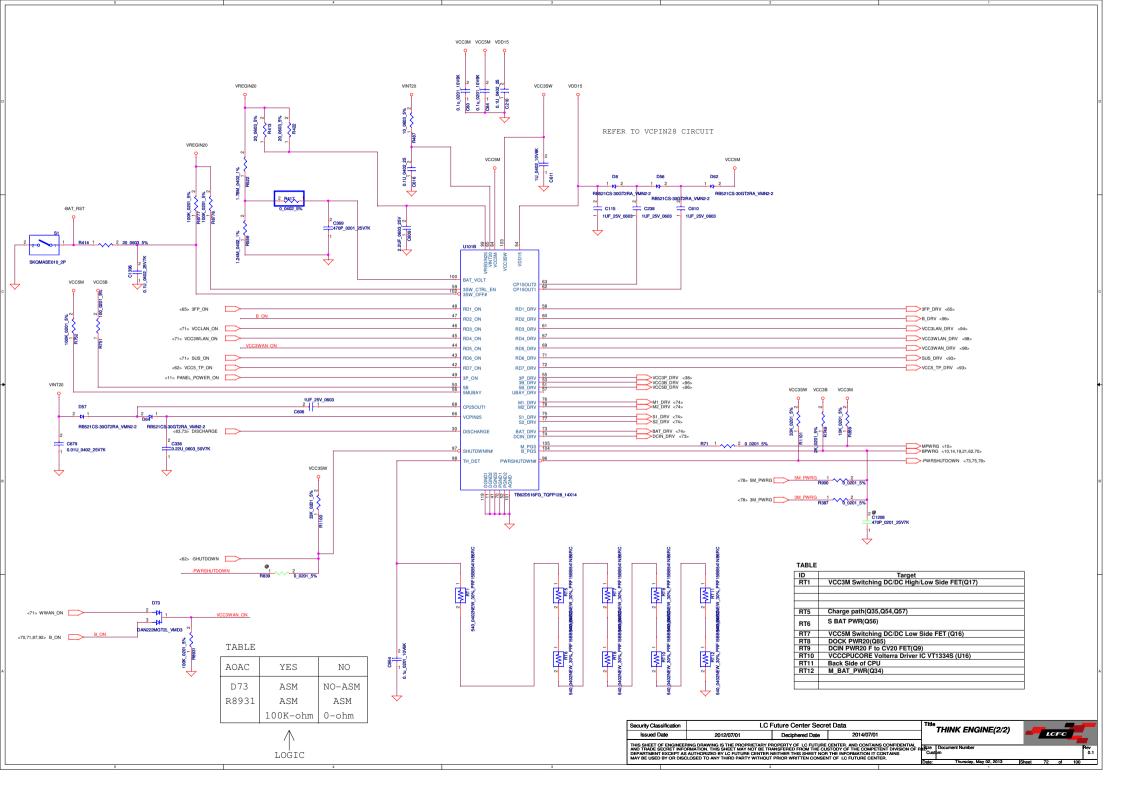
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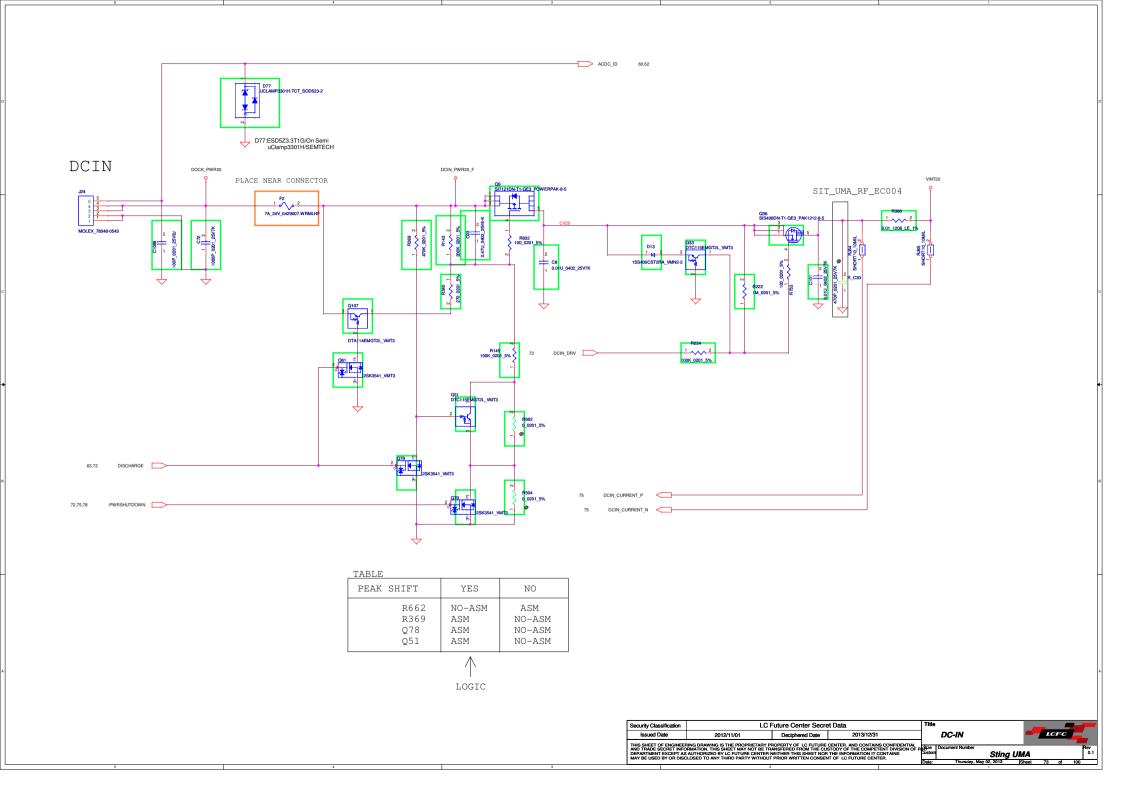
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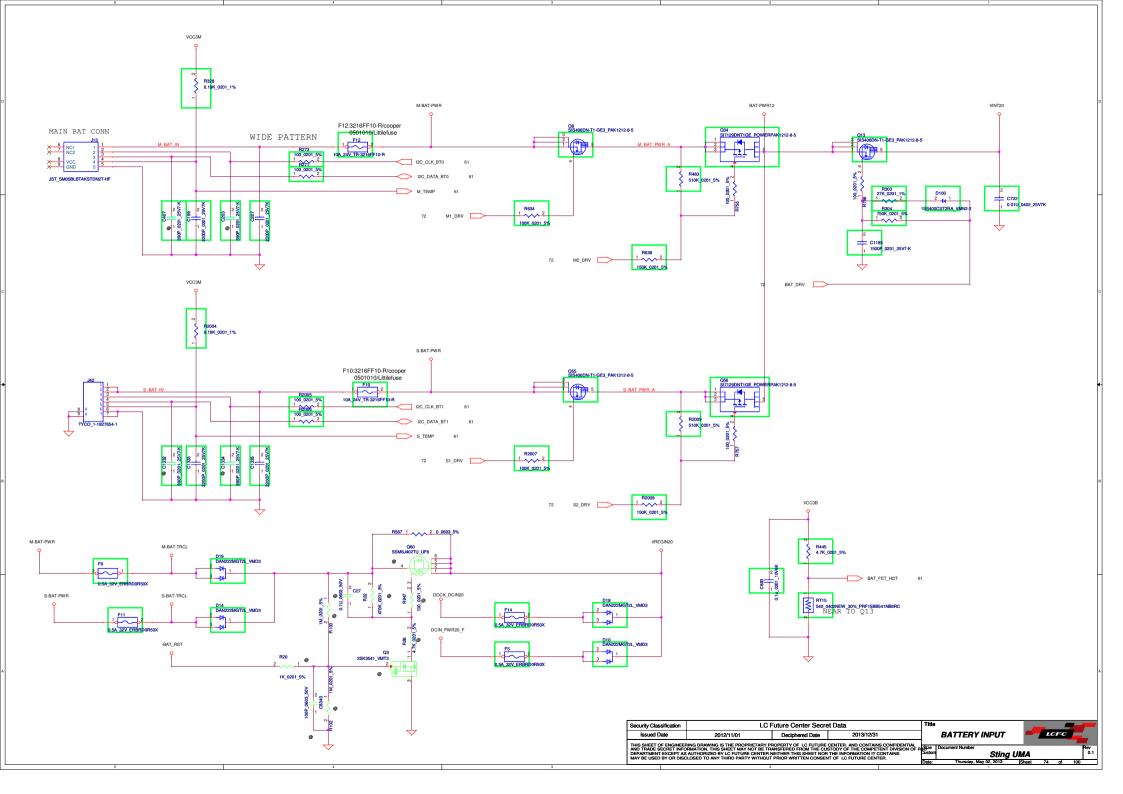
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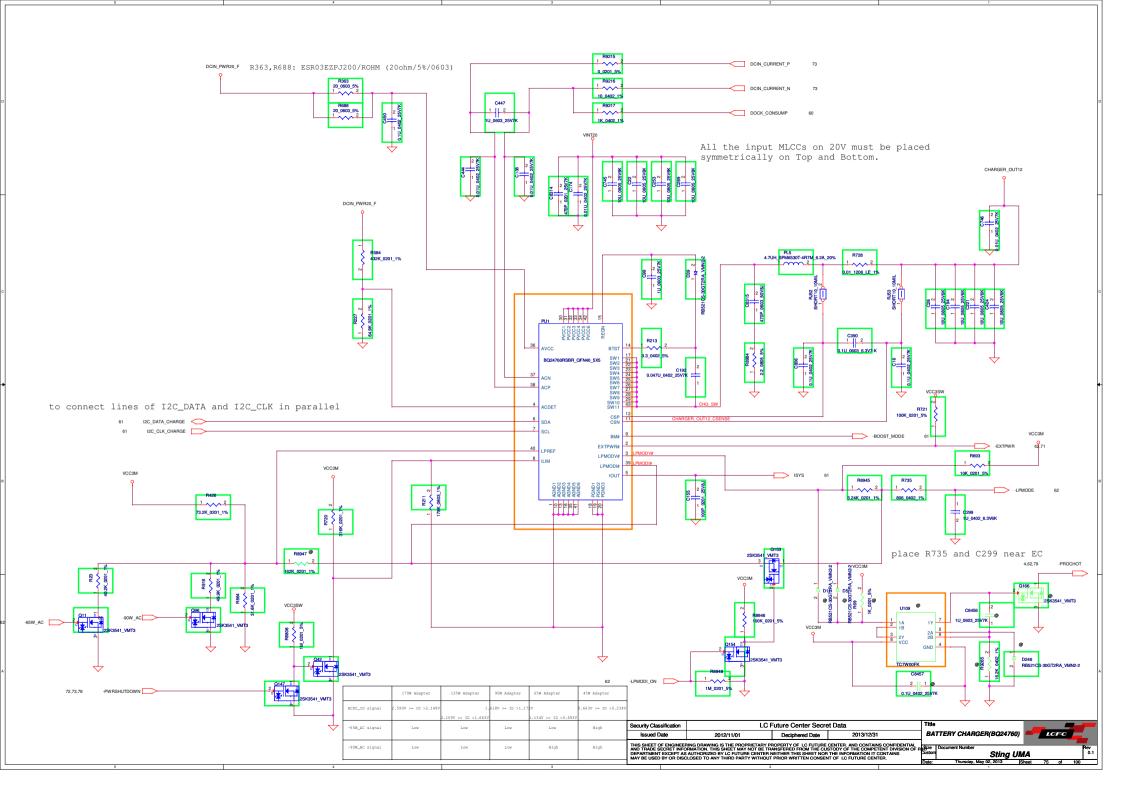


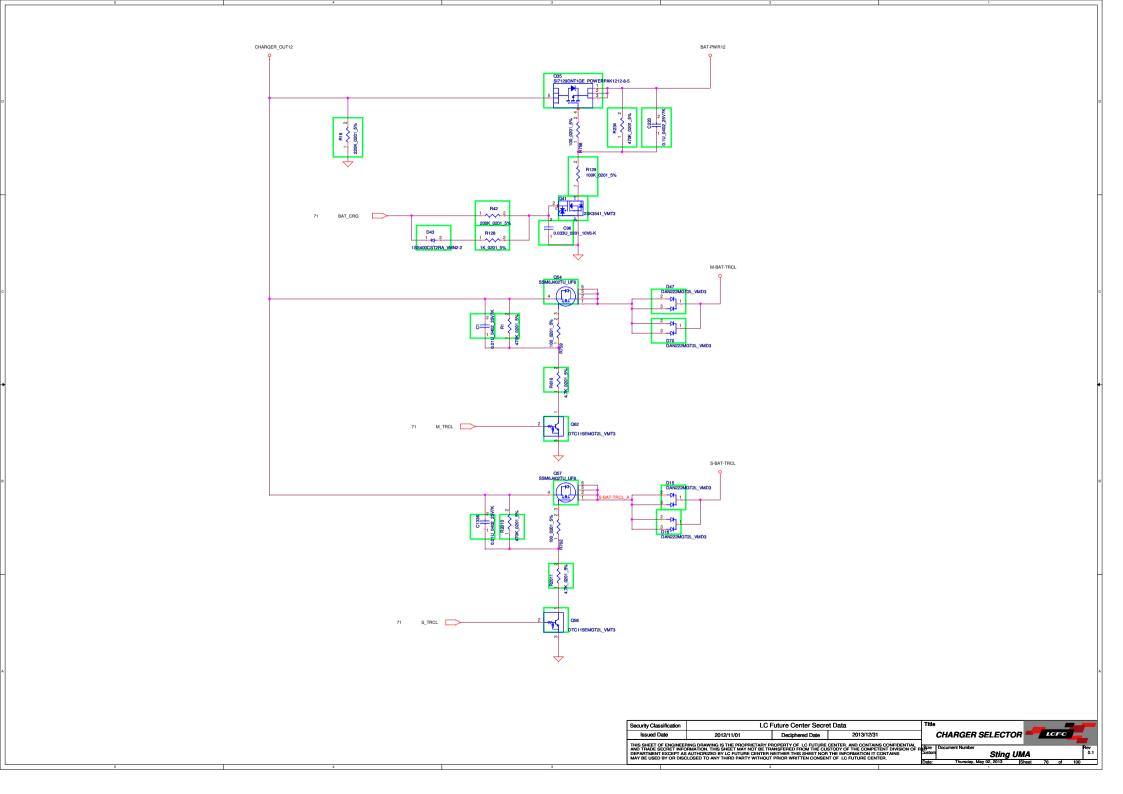


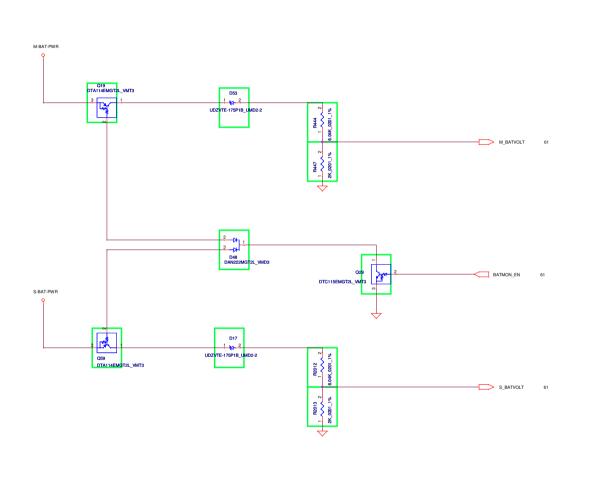




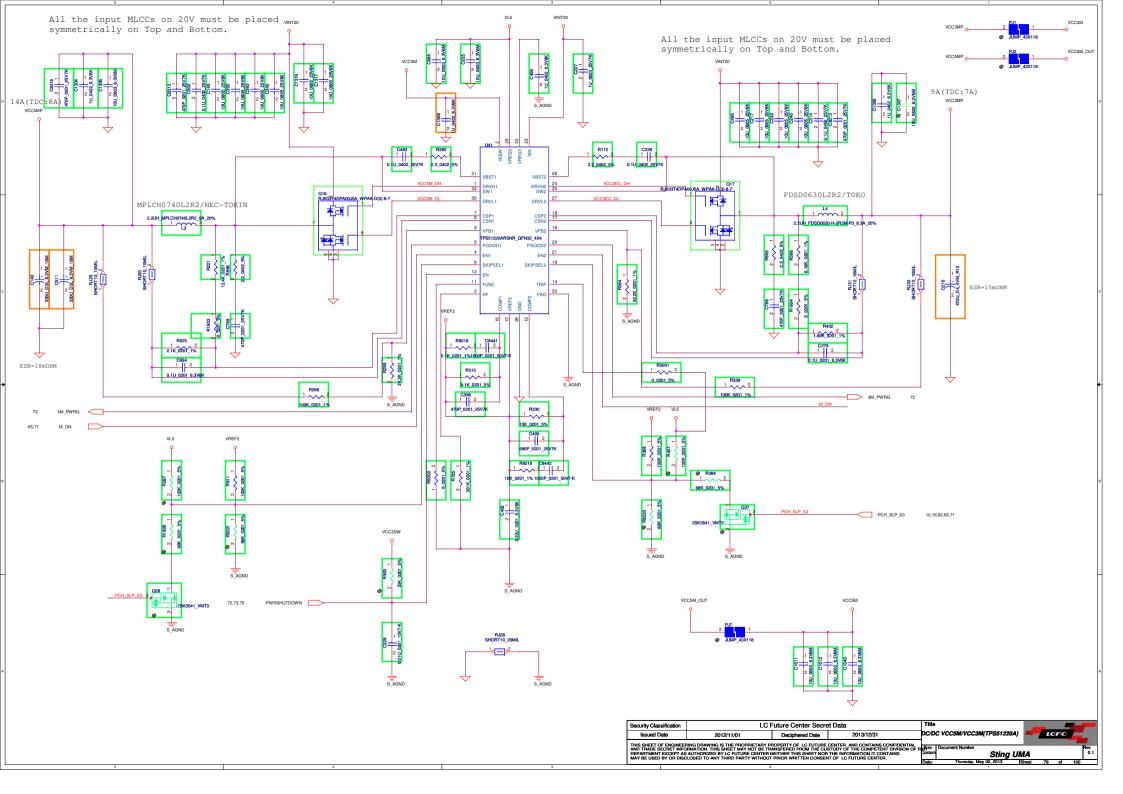


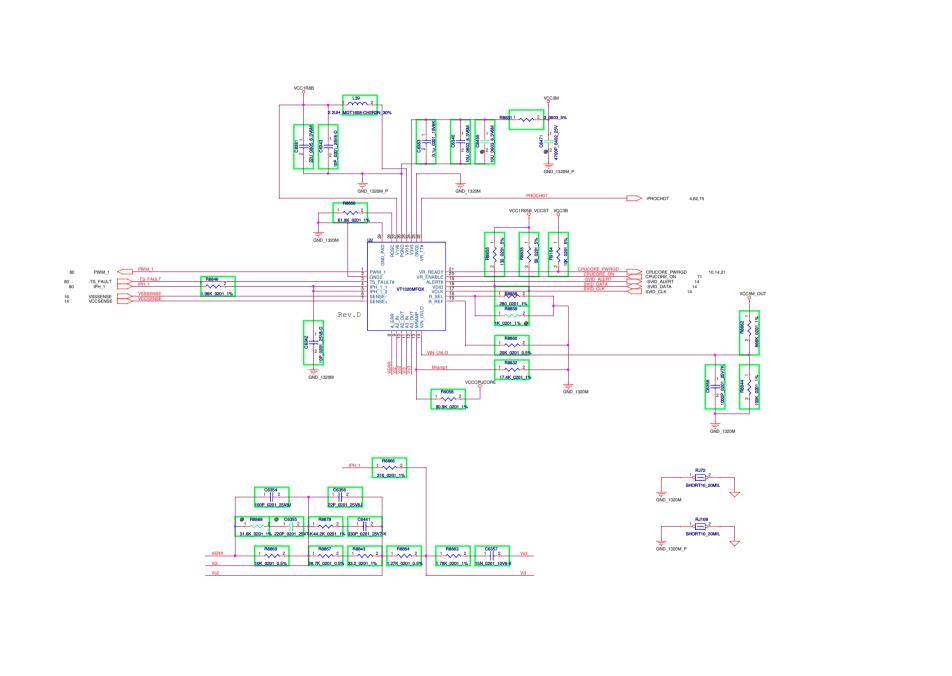




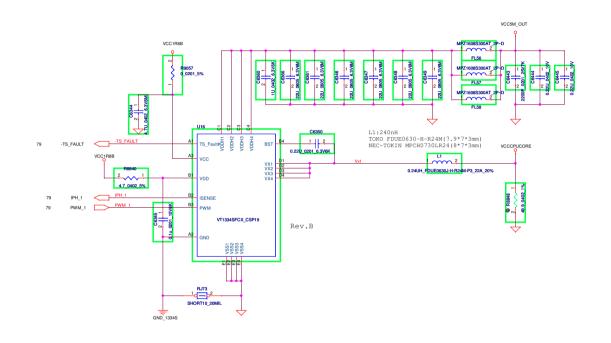


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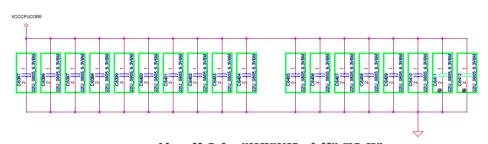
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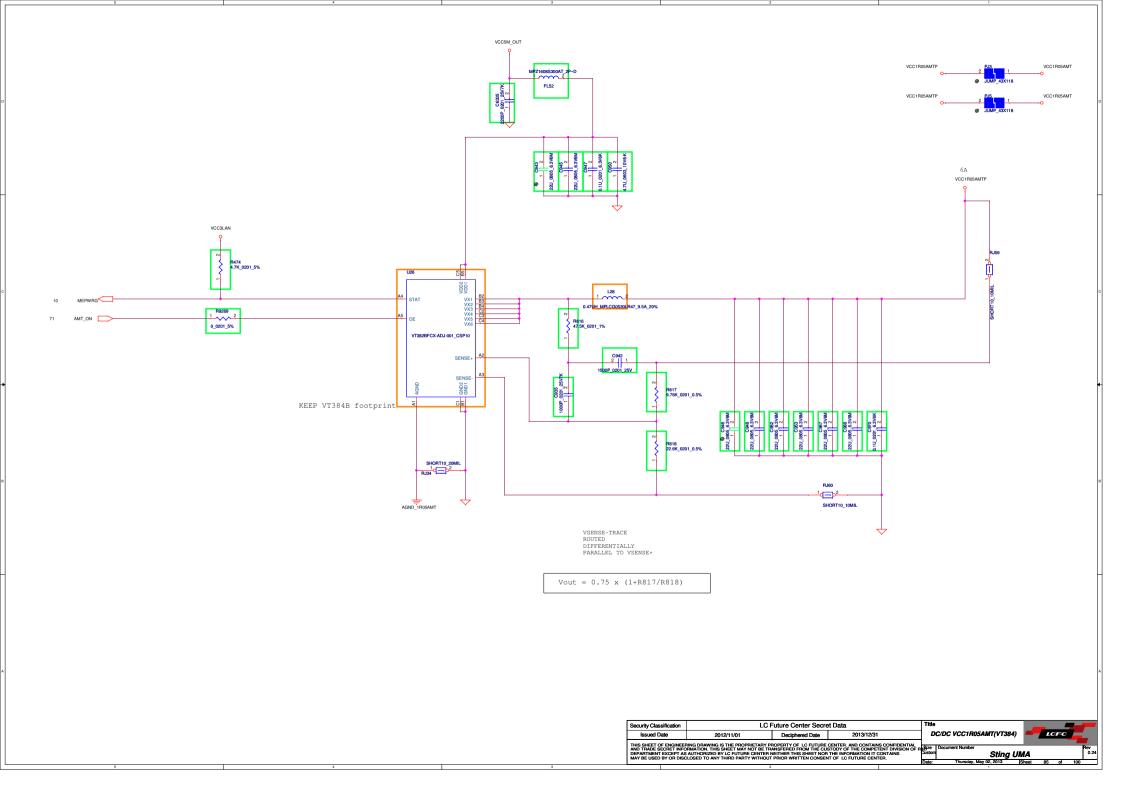
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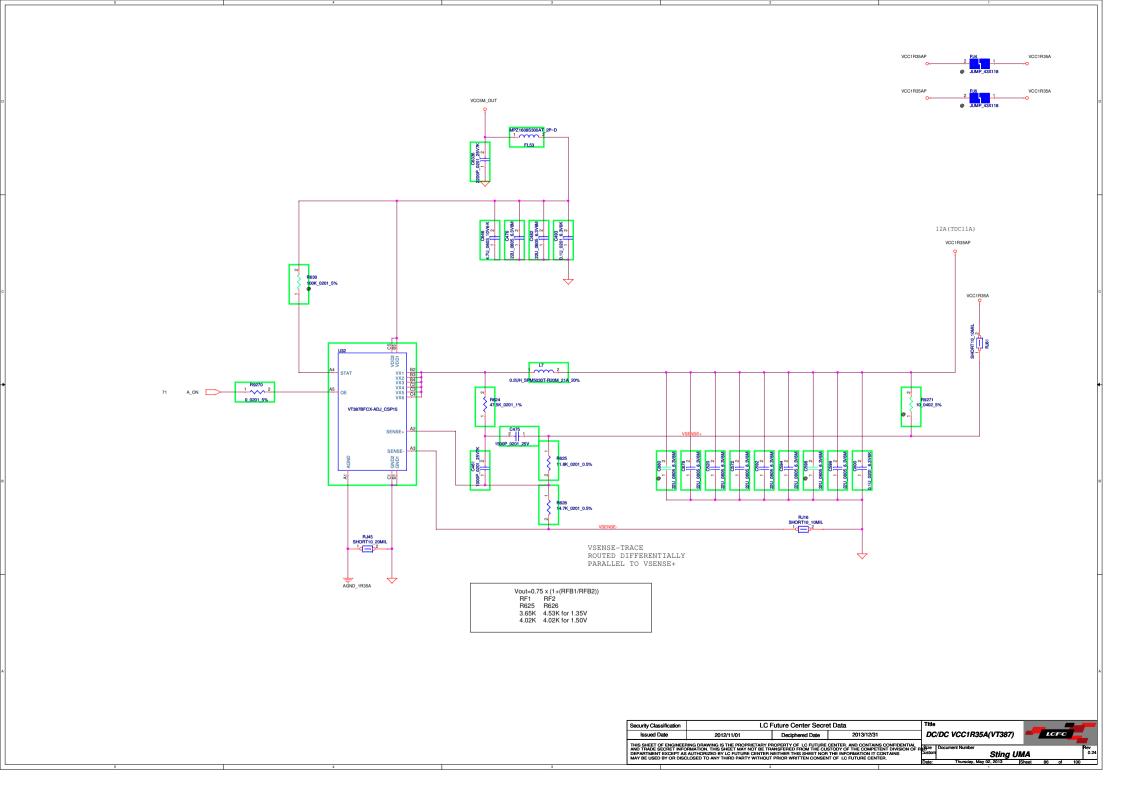
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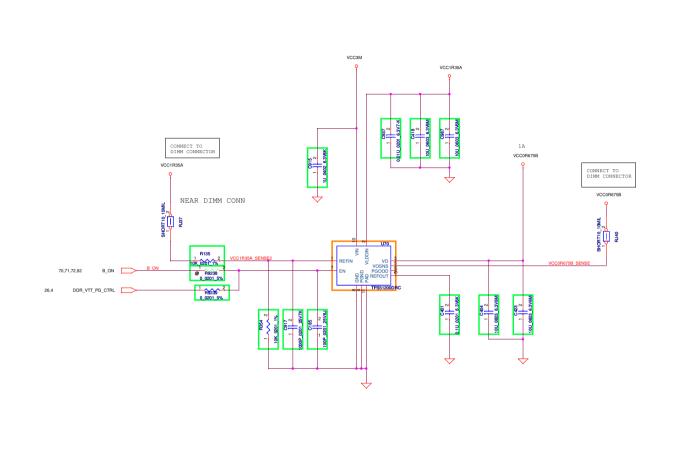
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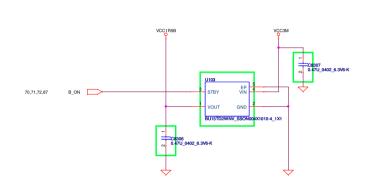
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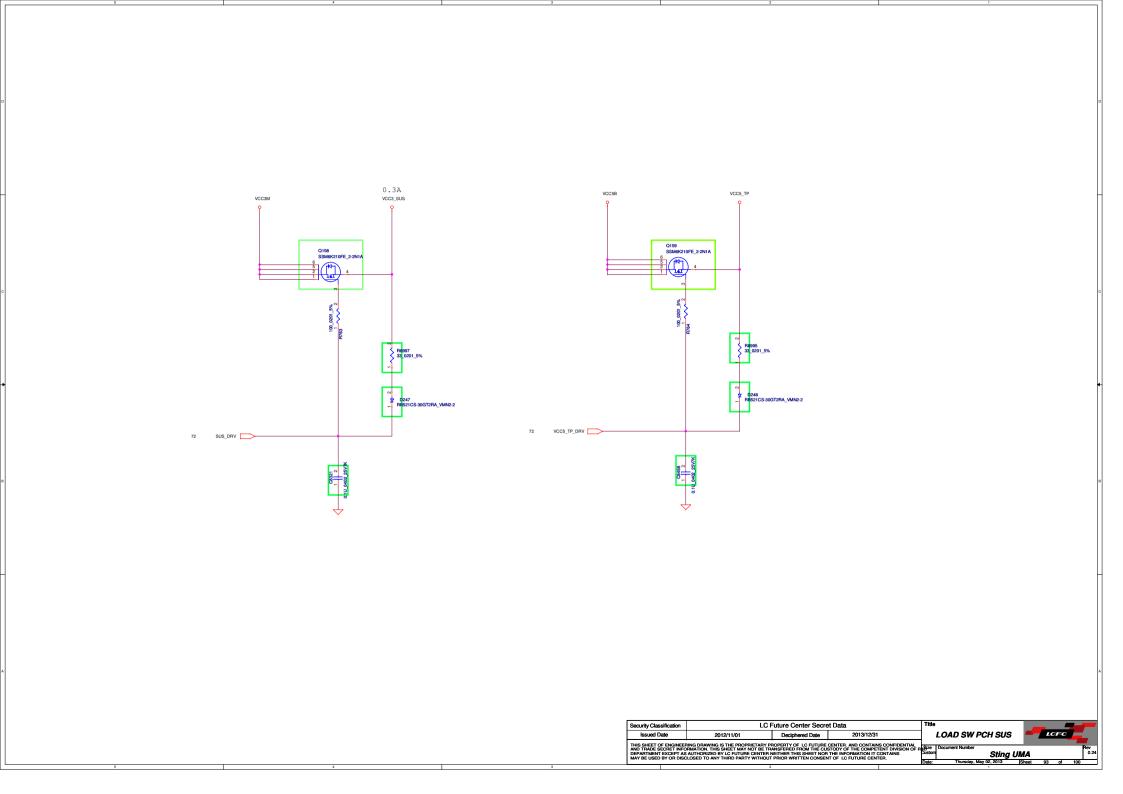
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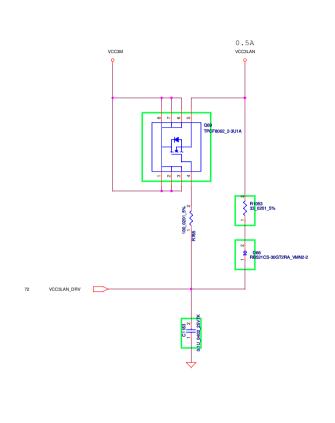
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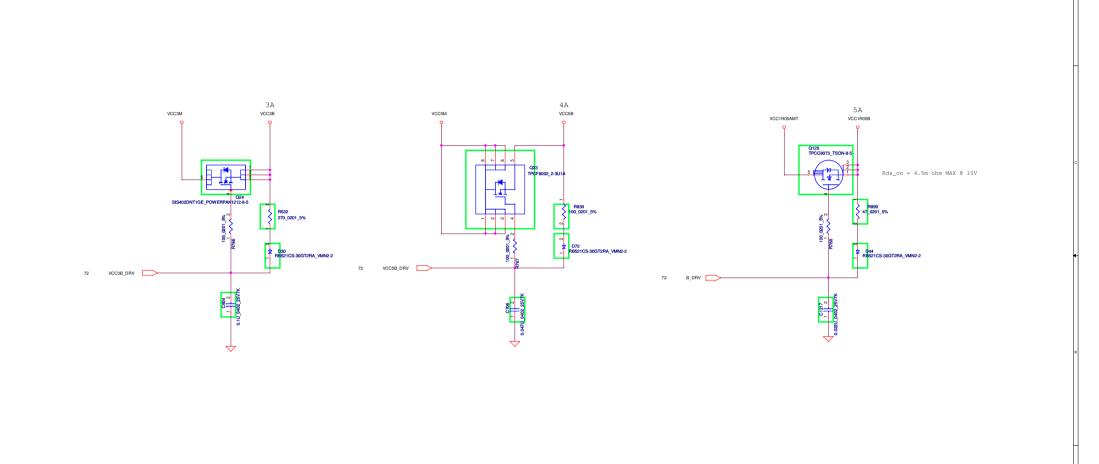
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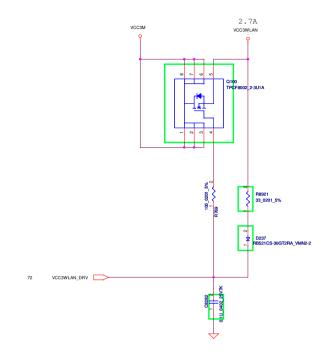
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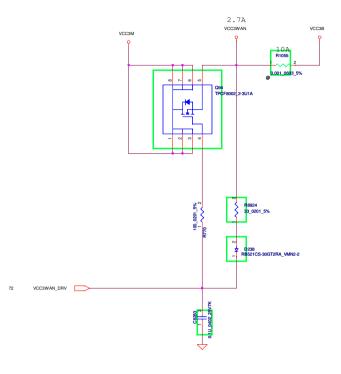
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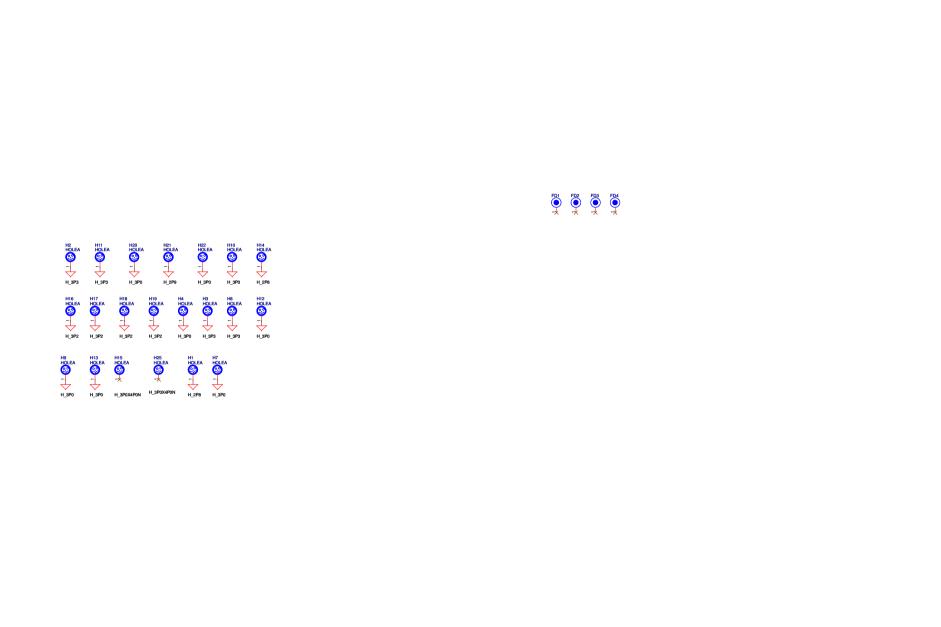
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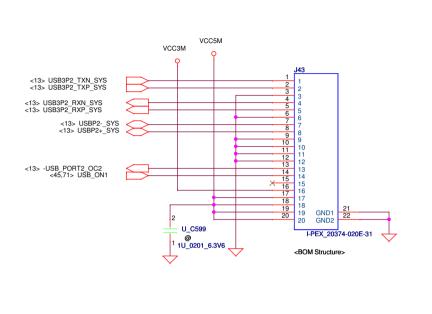


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