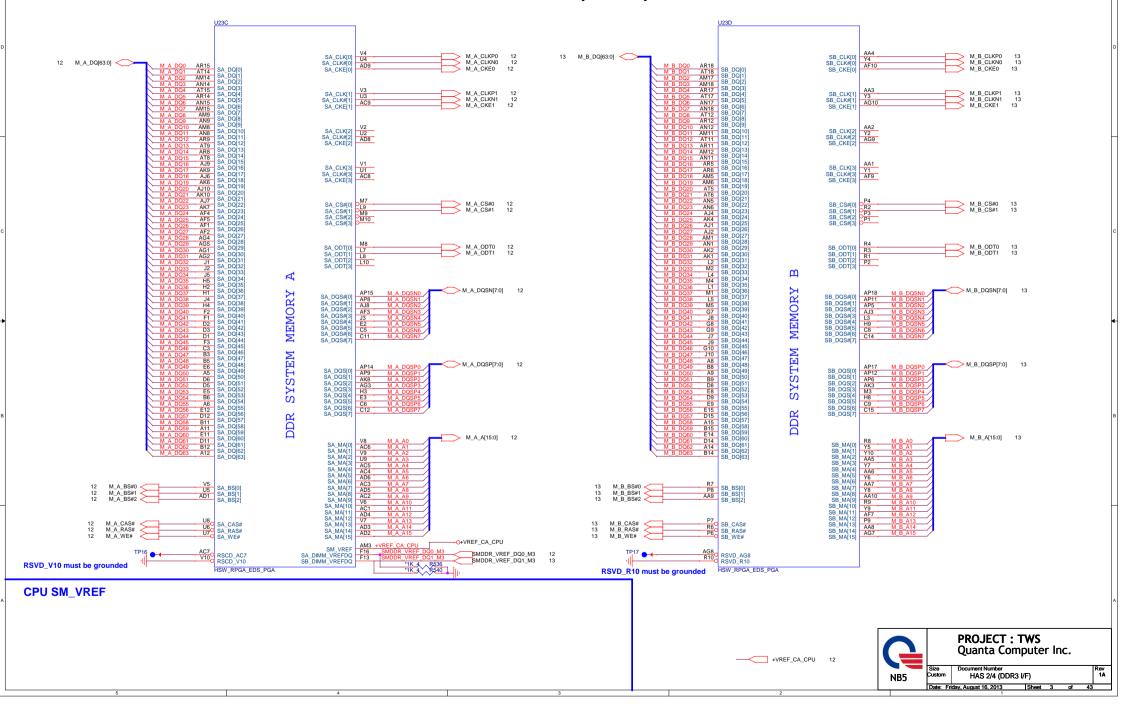
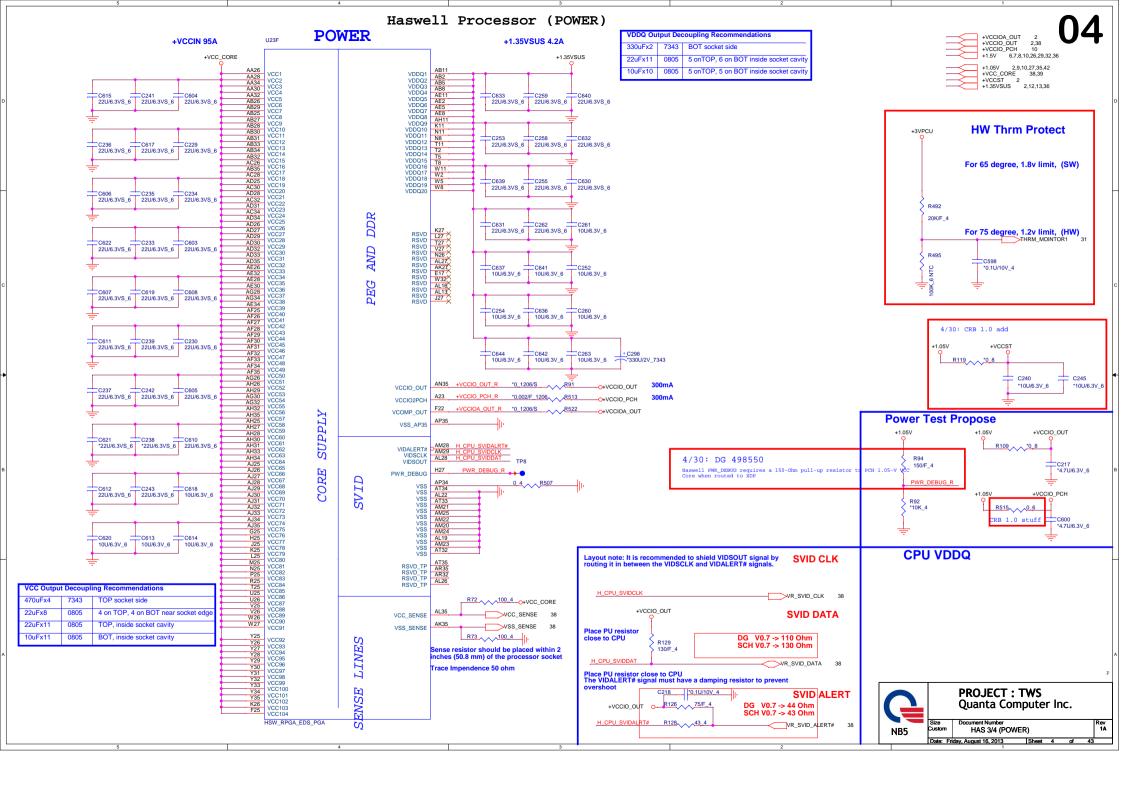
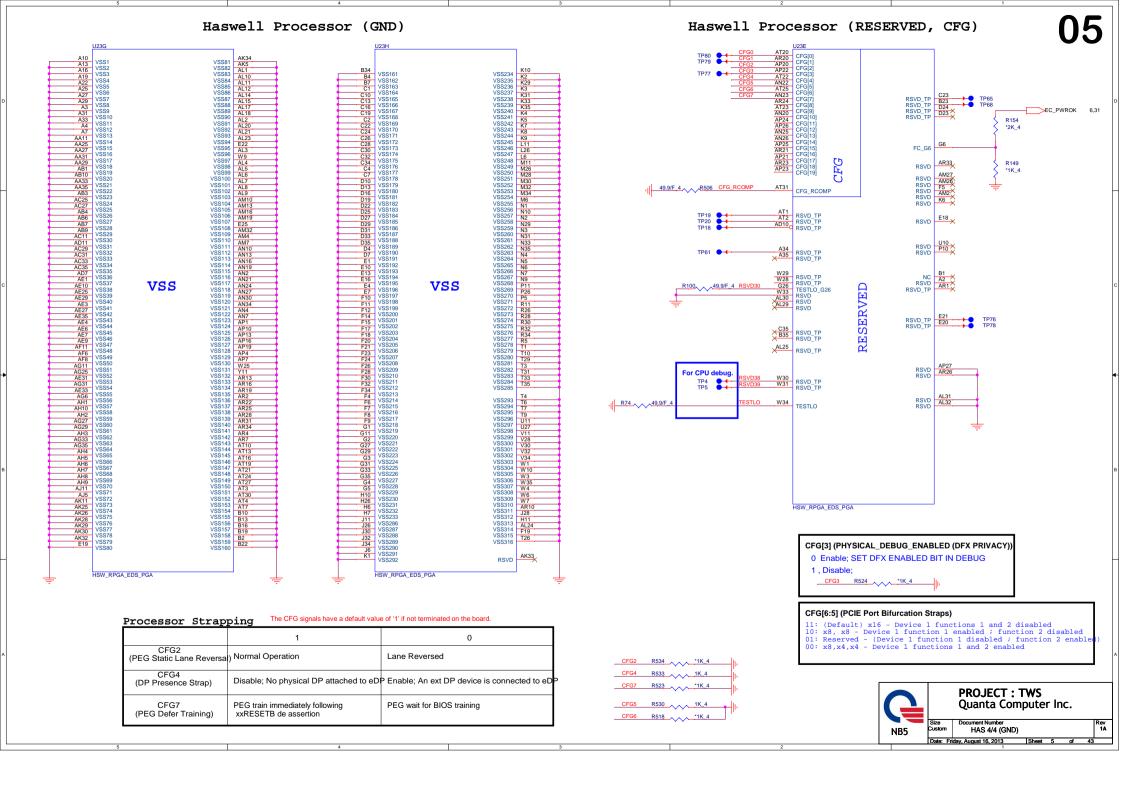
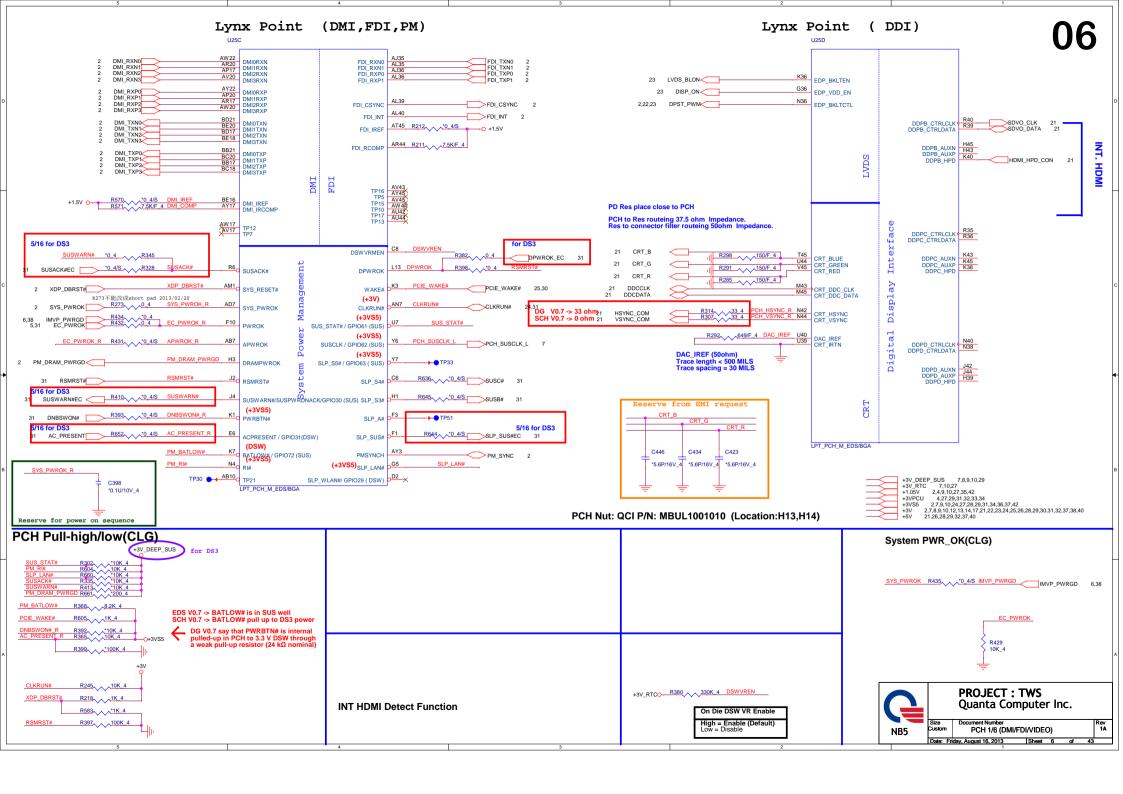


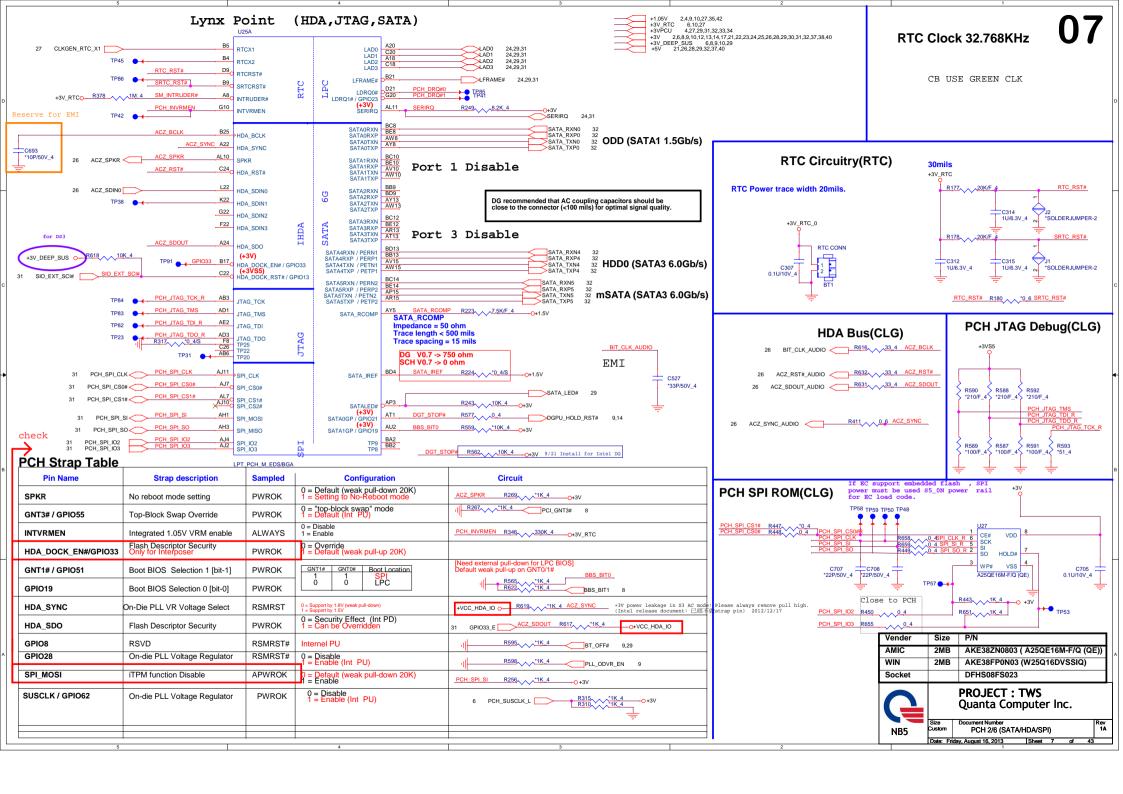
Haswell Processor (DDR3)

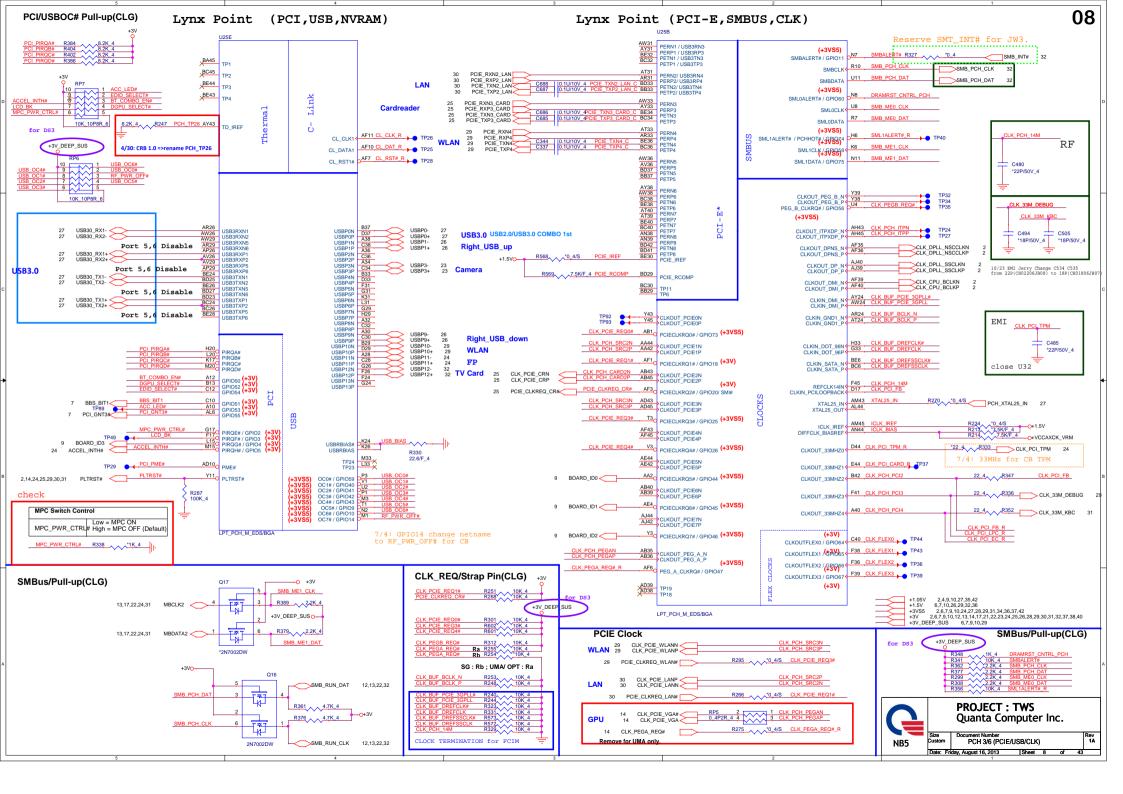


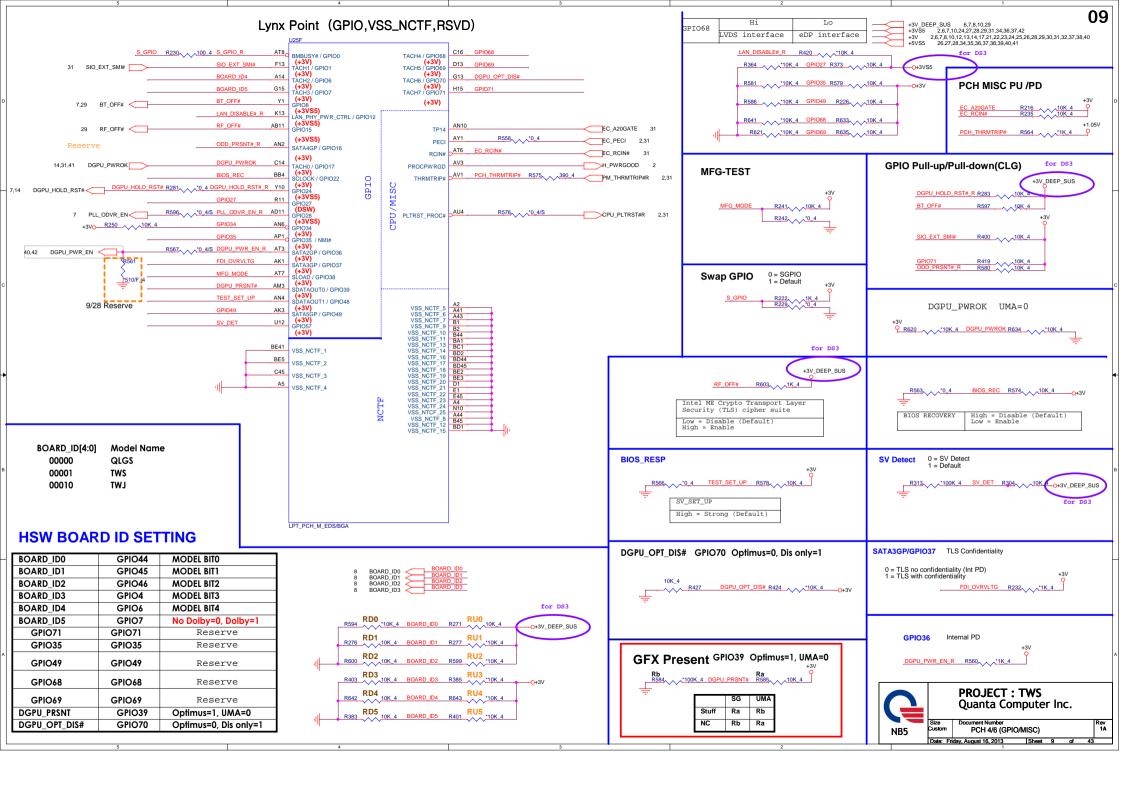


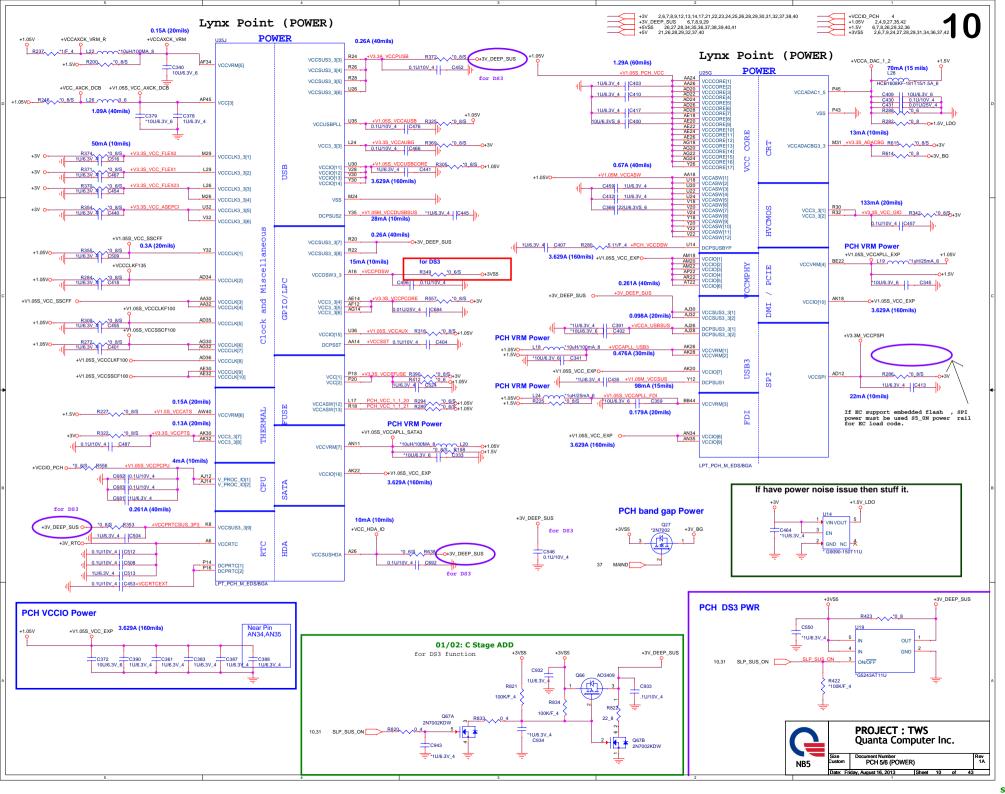


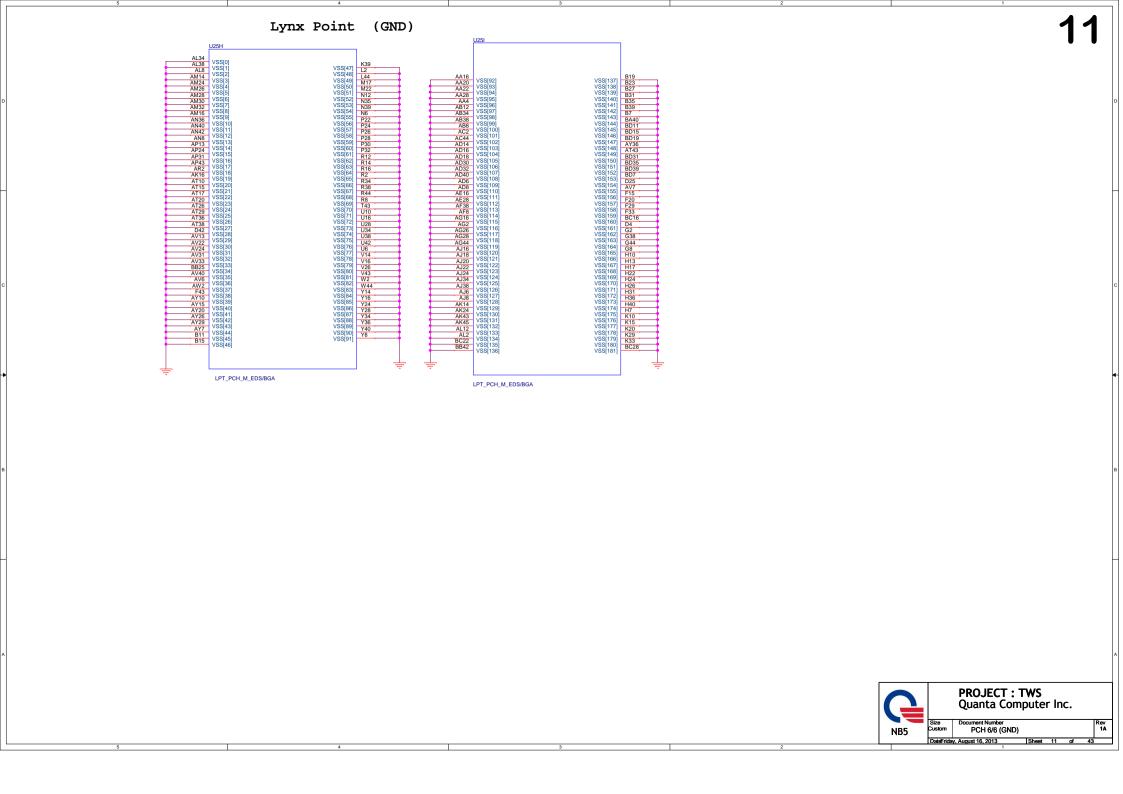


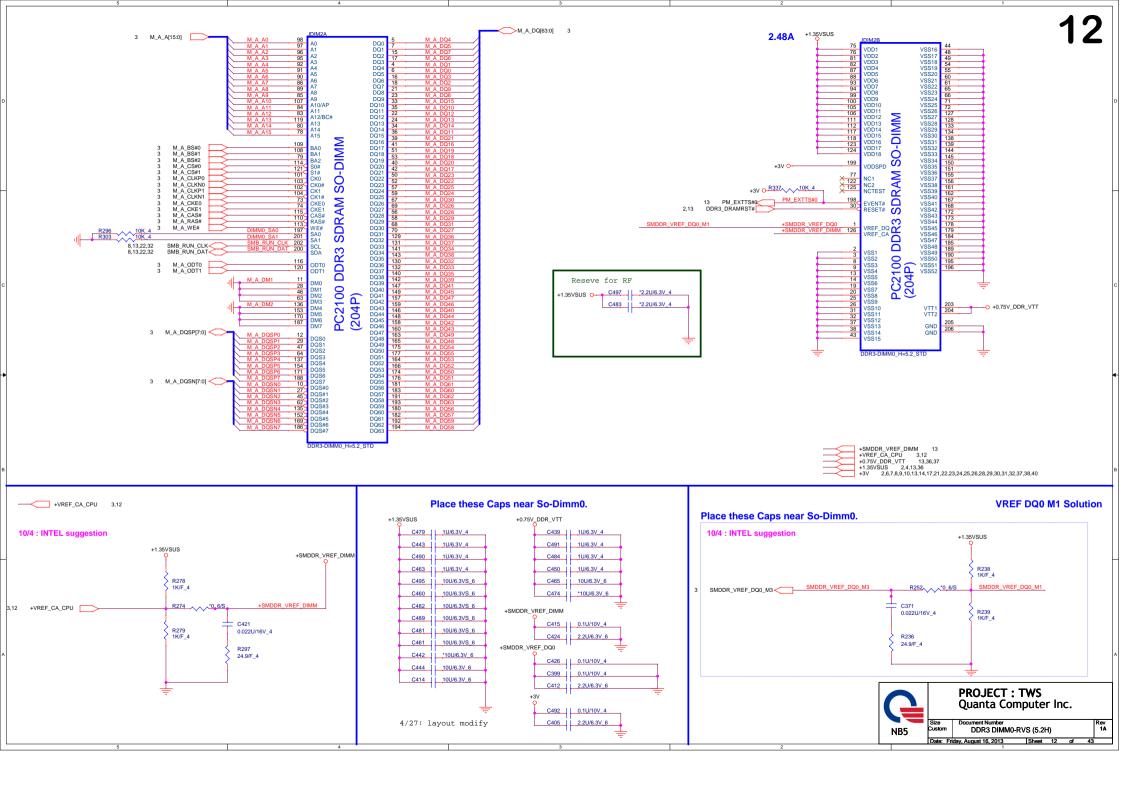


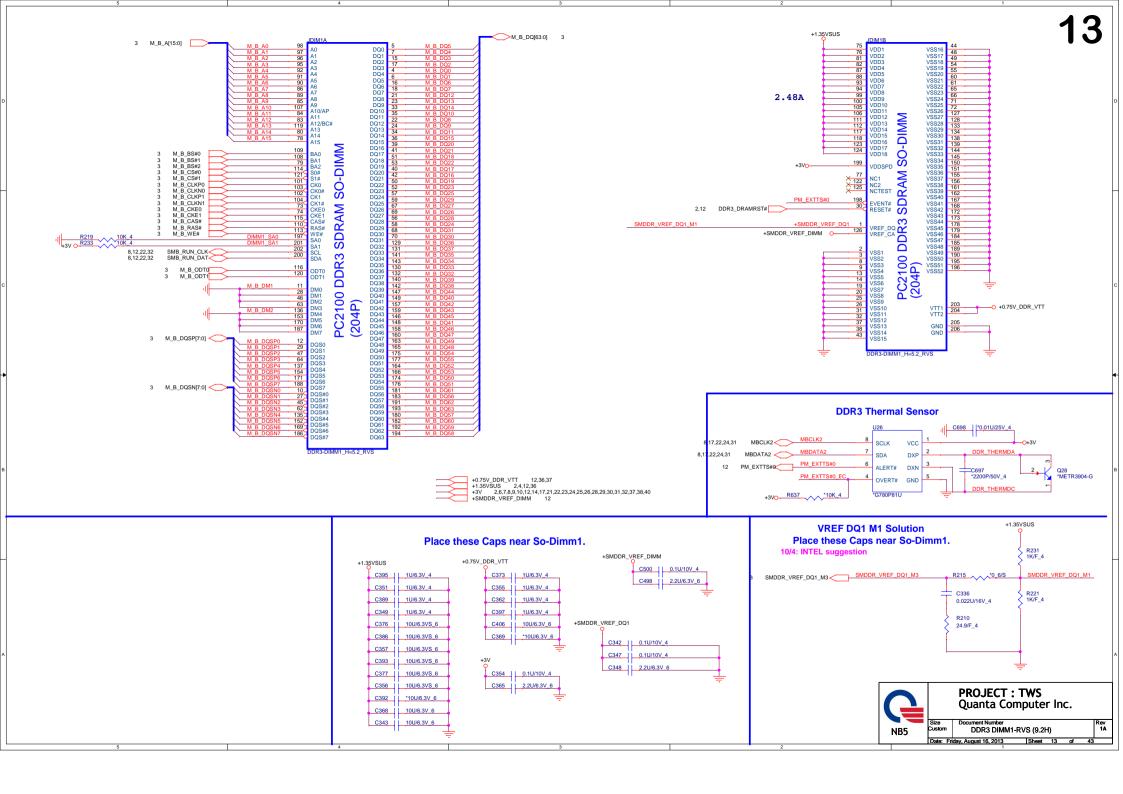


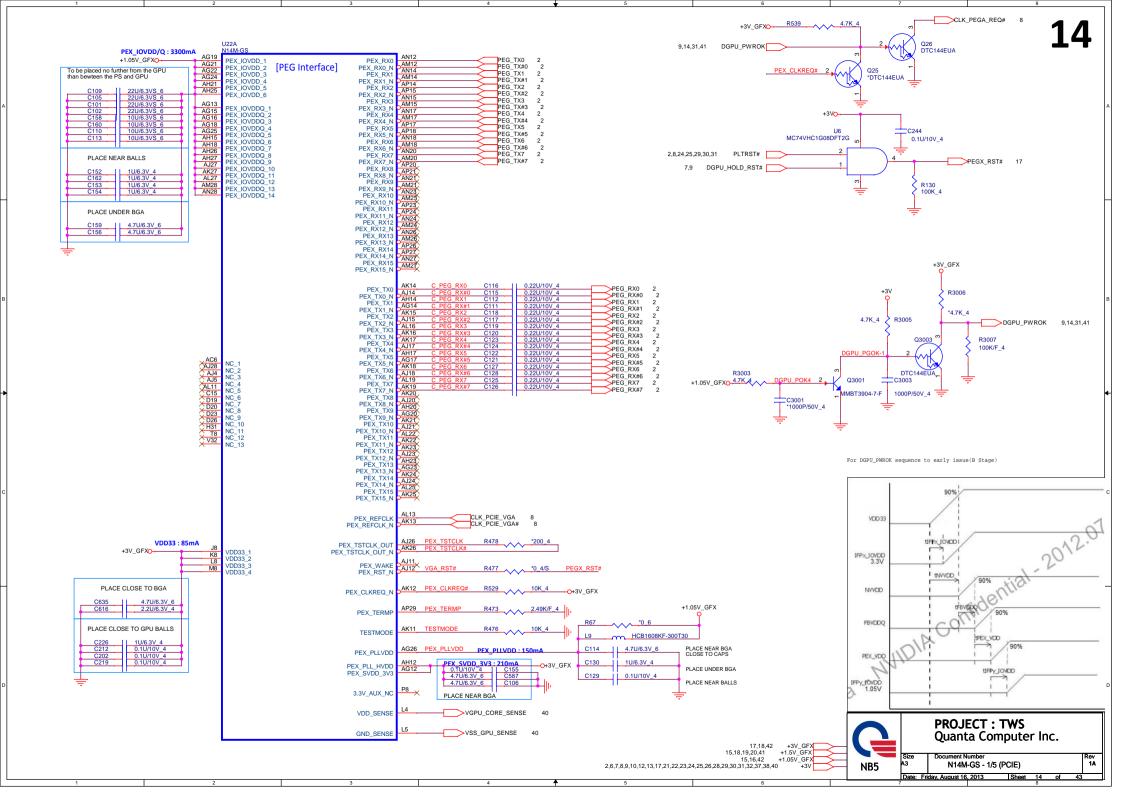


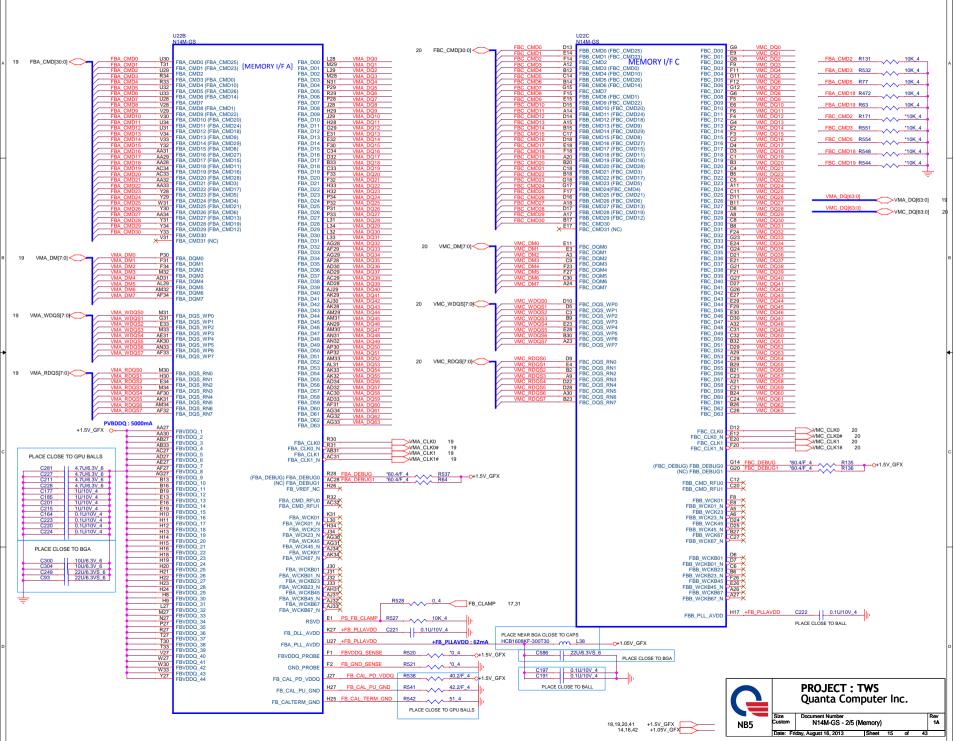












Configuration	Vendor	Strap	FBVDD/ FBVDDQ	Manufacturer Part Number	Max Speed CK (MHz)	Memory Date Code Minimum	Status
128M×16 DDR3	Samsung	0x7	1.5 V/ 1.5 V	K4W2G1646E-BC1A	1000	1204	Production Candidate
				K4W2G1646E-BC11	900	1204	Production Candidate
	Micron	0x5	1.5 V/ 1.5 V	MT41J128M16JT- 093G:K	1000	1234	Production Candidate
				MT41J128M16JT- 107G:K	900	1150	Production Candidate
	Hynix	0x6	1.5V/ 1.5V	H5TQ2G63DFR-N0C	1000	N/A	Production Candidate
				H5TQ2G63DFR-11C	900	N/A	Production Candidate
256Mx16 DDR3	Samsung	0x3	1.5 V/ 1.5 V	K4W4G1646B-HC11	900	N/A	Production Candidate
	Micron	0x1	1.5 V/ 1.5 V	MT41K256M16HA- 107G:E	900	N/A	Production Candidate

Table 4. N14M-GS/LP and N14P-GV2 DDR3L Recommended Memories 128Mx16 Configuration.

Configuration	Vendor	Strap	FBVDD/ FBVDDQ	Manufacturer Part Number	Max Speed CK (MHz)	Memory Date Code Minimum	Status
128M×16 DDR3L	Samsung	0xA	1.35 V/ 1.35 V	K4W2G1646E-BY11	900	1204	Production Candidate
	Micron	0x8	1.35 V/ 1.35 V	MT41K128M16JT- 107G:K	900	N/A	Production Candidate

Table 5. N14P-GS/LP/GE/GT DDR3 Recommended Memories 128Mx16 Configuration

Configuration	Vendor	Strap	FBVDD/ FBVDDQ	Manufacturer Part Number	Max Speed CK (MHz)	Memory Date Code Minimum	Status
128M×16 DDR3	Samsung	0x7	1.5 V/ 1.5 V	K4W2G1646E-BC1A	1000	1204	Production Candidate
				K4W2G1646E-BC11	900	1204	Production Candidate
	Micron	0x5	1.5 V/ 1.5 V	MT41J128M16JT- 093G:K	1000	1234	Production Candidate
				MT41J128M16JT- 107G:K	900	1150	Production Candidate
	Hynix	0x6	1.5V/ 1.5V	H5TQ2G63DFR-N0C	1000	N/A	Production Candidate
				H5TQ2G63DFR-11C	900	N/A	Production Candidate
256Mx16 DDR3	Samsung	0x3	1.5 V/ 1.5 V	K4W4G1646B-HC11	900	N/A	Production Candidate
	Micron	0x1	1.5 V/ 1.5 V	MT41K256M16HA- 107G:E	900	N/A	Production Candidate

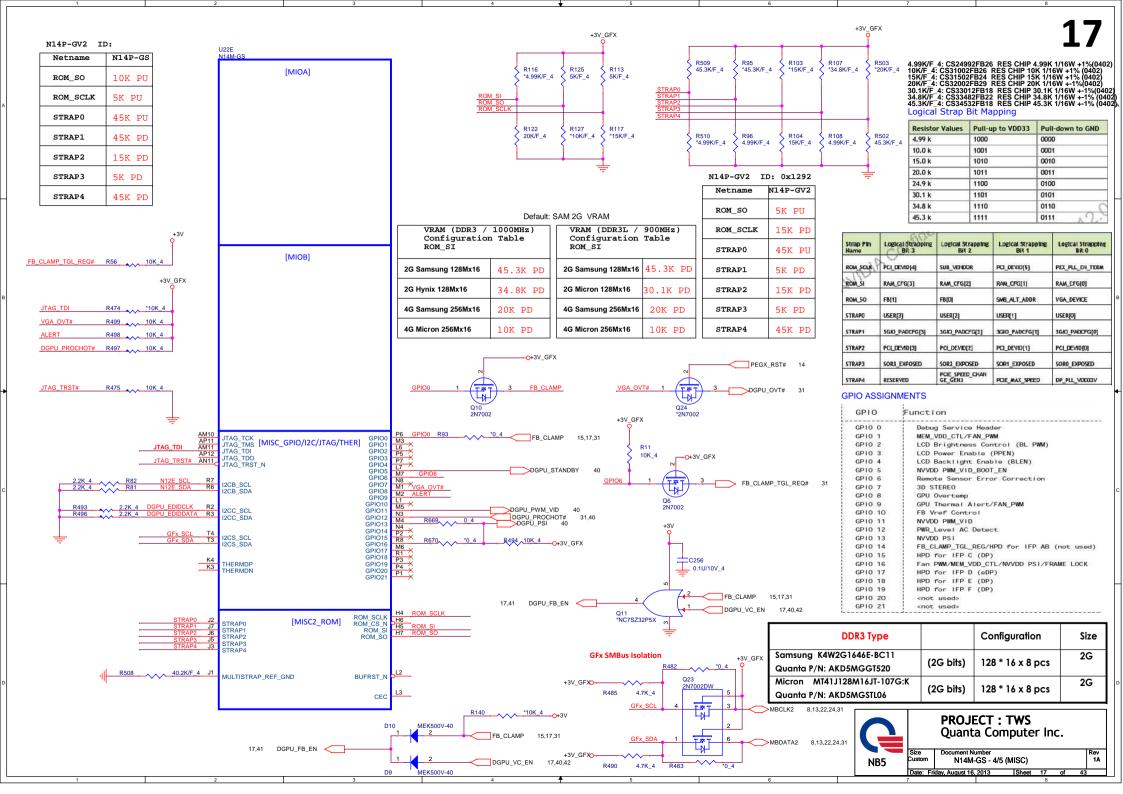
Table 6. N14P-GS/LP/GE/GT DDR3L Recommended Memories 128Mx16 Configuration.

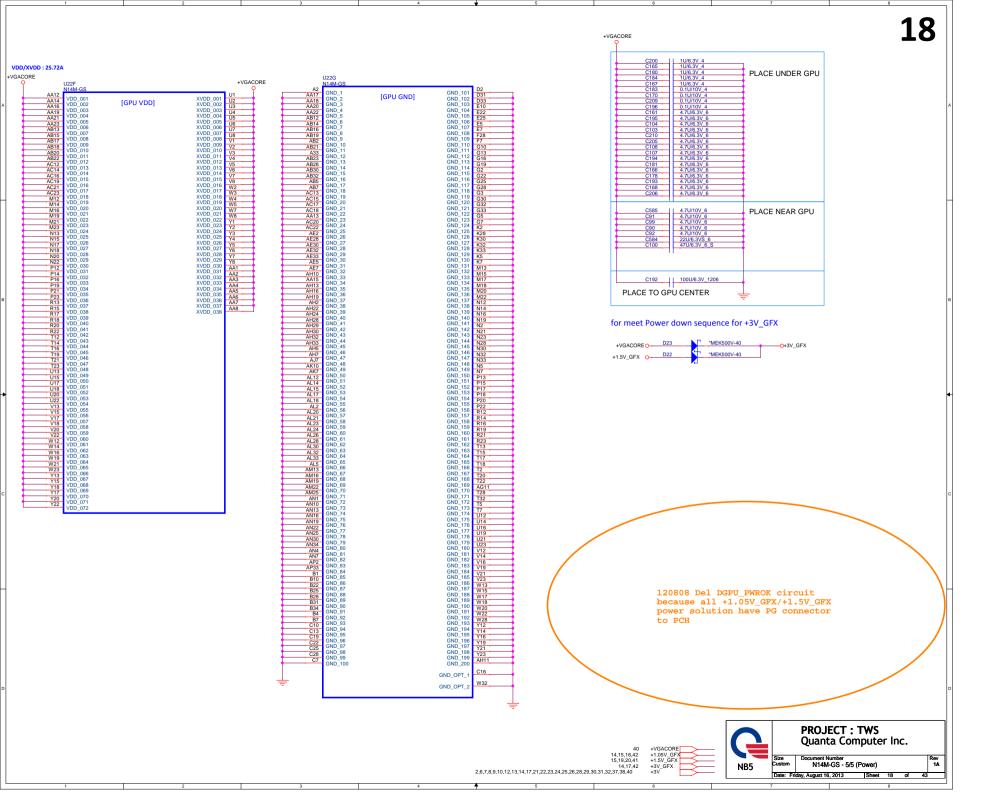
Configuration	Vendor	Strap	FBVDD/ FBVDDQ	Manufacturer Part Number	Max Speed CK (MHz)	Memory Date Code Minimum	Status	
128M×16 DDR3L	Samsung	0xA	1.35 V/ 1.35 V	K4W2G1646E-BY11	900	1204	Production Candidate	
	Micron	0x8	1.35 V/ 1.35 V	MT41K128M16JT- 107G:K	900	N/A	Production Candidate	

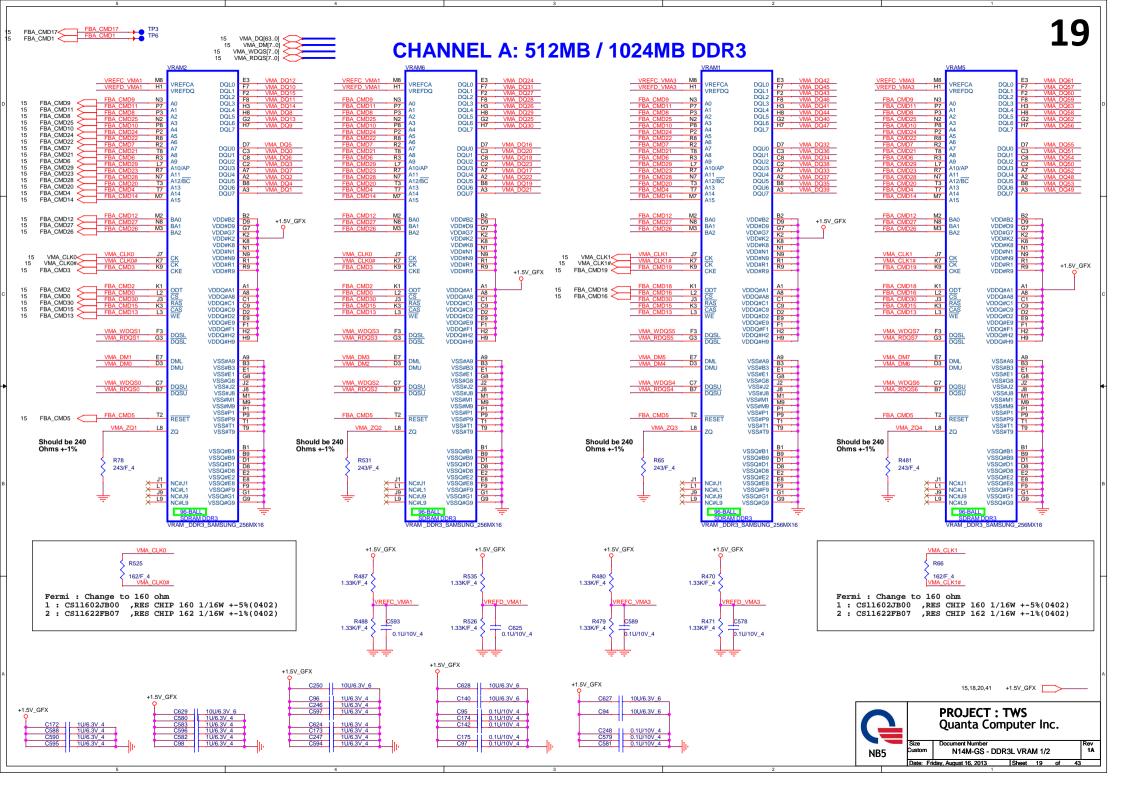
PROJECT: TWSQuanta Computer Inc. Document Number N14M-GS - 3/5 (Display) 14,17,18,42 +3V_GFX 14,15,42 +1.05V_GFX NB5

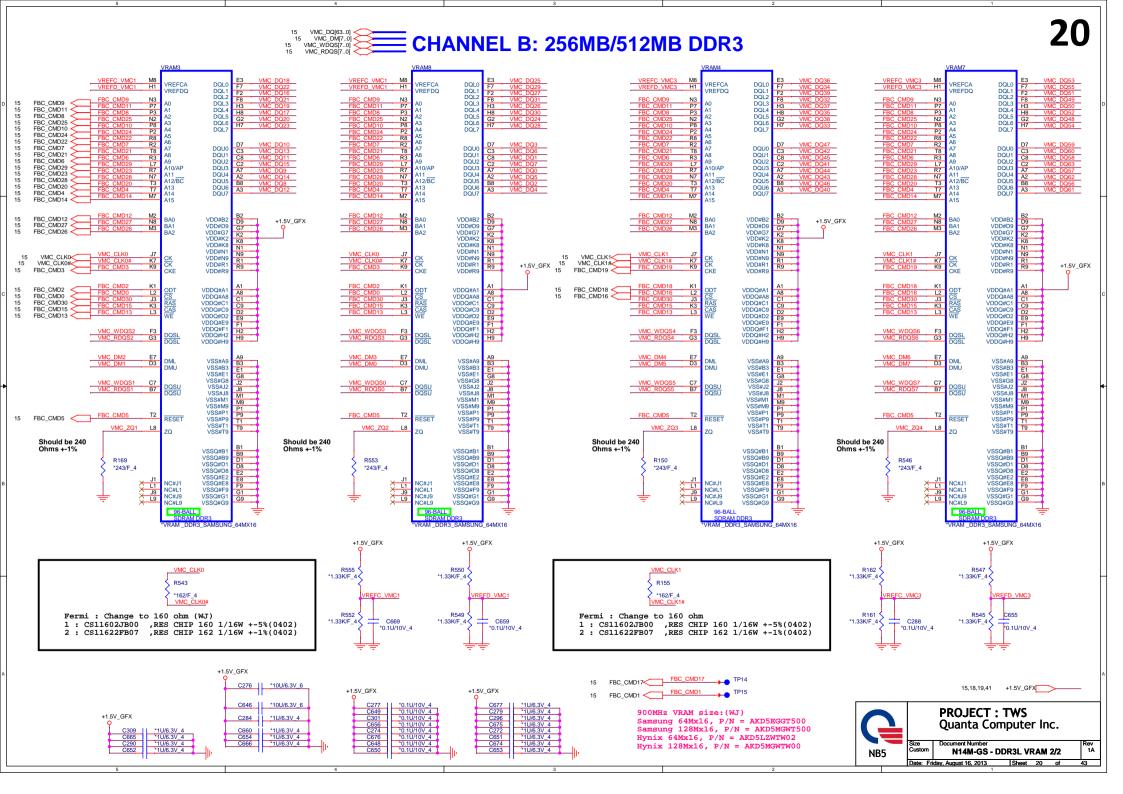
1	2		3			4		5	
		AH8	U22D N14M-GS			1 AM6			
		AG8	IFPAB_PLLVDD	[IFPA/B_LV	IFPA TXD0	AN6 AP3			Configura
		AG9	IFPA_IOVDD		IFPA_TXD0_N IFPA_TXD1 IFPA_TXD1_N	AN5 AM5			128M×16 E
		A 10	_		IFPA_TXD1 IFPA_TXD1_N IFPA_TXD2 IFPA_TXD2_N IFPA_TXD3	AK6			
		AJ8	IFPAB_RSET		IFPA_TXD3_N	O AHO X			
					IFPB_TXC IFPB_TXC_N IFPB_TXD4 IFPB_TXD4_N	AP6			
					IFPB_TXD5 IFPB_TXD5_N IFPB_TXD6	AL7 X			
					IFPB_TXD6_N IFPB_TXD7 IFPB_TXD7_N	AK8 X			
		AF7	IFPC_PLLVDD		IEDO ALIV IOOM OOL	VC3			256Mx16 E
		AG7	IFPD_PLLVDD	[IFPC/D_TMDS] _{FF}	PC_AUX_I2CW_SDA_N				
					IFPC_LO_N IFPC_L1_N IFPC_L1_N IFPC_L2_N IFPC_L2_N	AJ2 AH3			
		AF6	IEDO IOVED						
		AG6	IFPC_IOVDD IFPD_IOVDD		IFPC_L3_N IFPD_AUX_I2CX_SCL	AK3		1	
				IF.	PD_AUX_I2CX_SDA_N IFPD_L0 IFPD_L0_N	AM1 AM2 AM3			
		AF8 AN2	IFPC_RSET		IFPD_L1 IFPD_L1_N IFPD_L2 IFPD_L2_N	AM4			Configura 128M×16 E
			IFPD_RSET		IFPD_L2_N IFPD_L3 IFPD_L3_N				
		AB8	IFPEF_PLLVDD	[IEPE/E DP]	IFPE_AUX_I2CY_SCL PE_AUX_I2CY_SDA_N	AB3 AB4		ļ	
		AC7	IFPE_IOVDD IFPF_IOVDD	[1112/1_51] [F	IFPE_L0 IFPE_L0_N	AD1			-
		AD6			IFPE_L1 IFPE_L1_N IFPE_L2	AC2			
			IFPEF_RSET		IFPE_L2_N IFPE_L3 IFPE_L3_N				
				IF	IFPF_AUX_I2CZ_SCL PF_AUX_I2CZ_SDA_N	AF2 AE3			
					IFPF_L0_N IFPF_L1	AF4 AF5			
					IFPF_AUX_I2CZ_SCL PF_AUX_I2CZ_SDA_N IFPF_L0_N IFPF_L1_1 IFPF_L1_1 IFPF_L2_N IFPF_L2_N IFPF_L2_N	AD4 AD5 AG1			
					IFPF_L3_N	> ×			
		AG10	DACA_VDD	[DACA/B_CRT]	DACA_RED DACA_GREEN DACA_BLUE	ALIO			
		× ^{AP9}	DACA_VREF		DACA_HSYNC	AM9			
		× ^{AP8}	DACA_RSET		DACA_VSYNC	XIV9			
	PLLVDI	D : 200mA			I2CA_SCL I2CA_SDA	R4 I2CA R5 I2CA	SCL R83 SDA R84	2.2K_4 2.2K_4	
+1.05V_	GFXO- L10	LVDD AD8	PLLVDD				c 	CLK_27M_V	/GA_2
	R76 C169 C189 *0_4 22U/6.3VS_6 0.1U/10	N/ 4					9 9	DP/50V 1 Y3	
	220/03/3_0 0.10/10	V_4					C	27MHZ +	
	SP_PL	LVDD AE8	SP_PLLVDD				11	DP/50V_4	
L11	MCB1608KF-300T30 VID_PL	LVDD AD7			XTAL_IN	LIO VTAI	_27M_VGA_2 _OUT		
,			VID_PLLVDD	[XTAL IN]	XTAL_OUT XTAL_OUTBUFF XTAL_SSIN		14	DK_4 DK_4	
	22U/6.3VS_6 4.7U/6.3V_6 0.1U/10V_4 0.1U/10	V_4				_		₹	
	PLACE CLOSE TO GPU PLACE CLOSE TO BALLS								

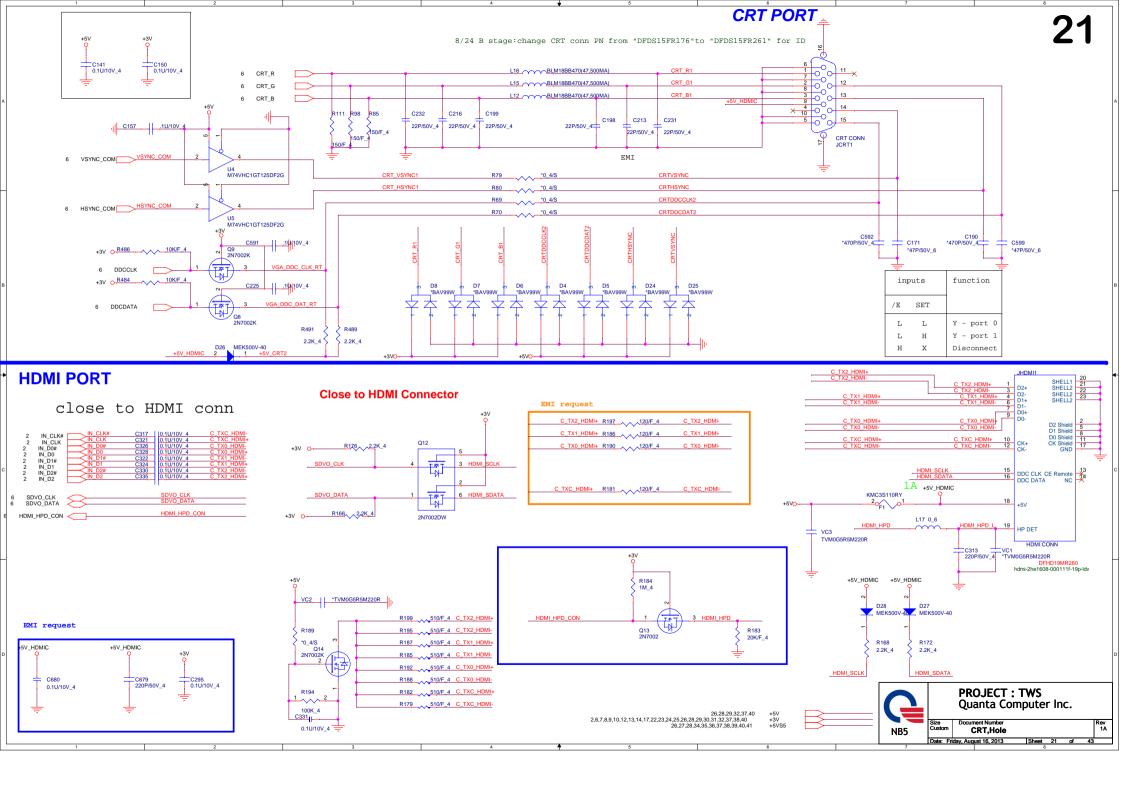
+1.05V_GFX0__L11______H¢B<u>1</u>608KF-300T30

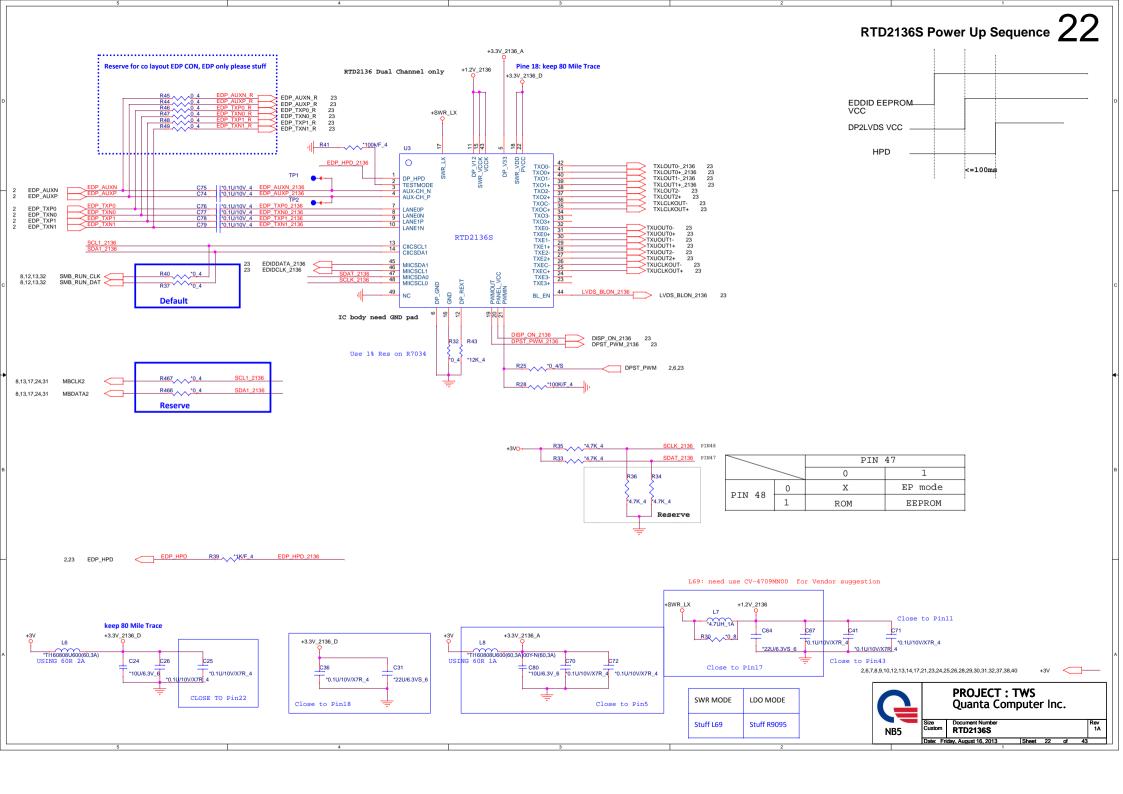


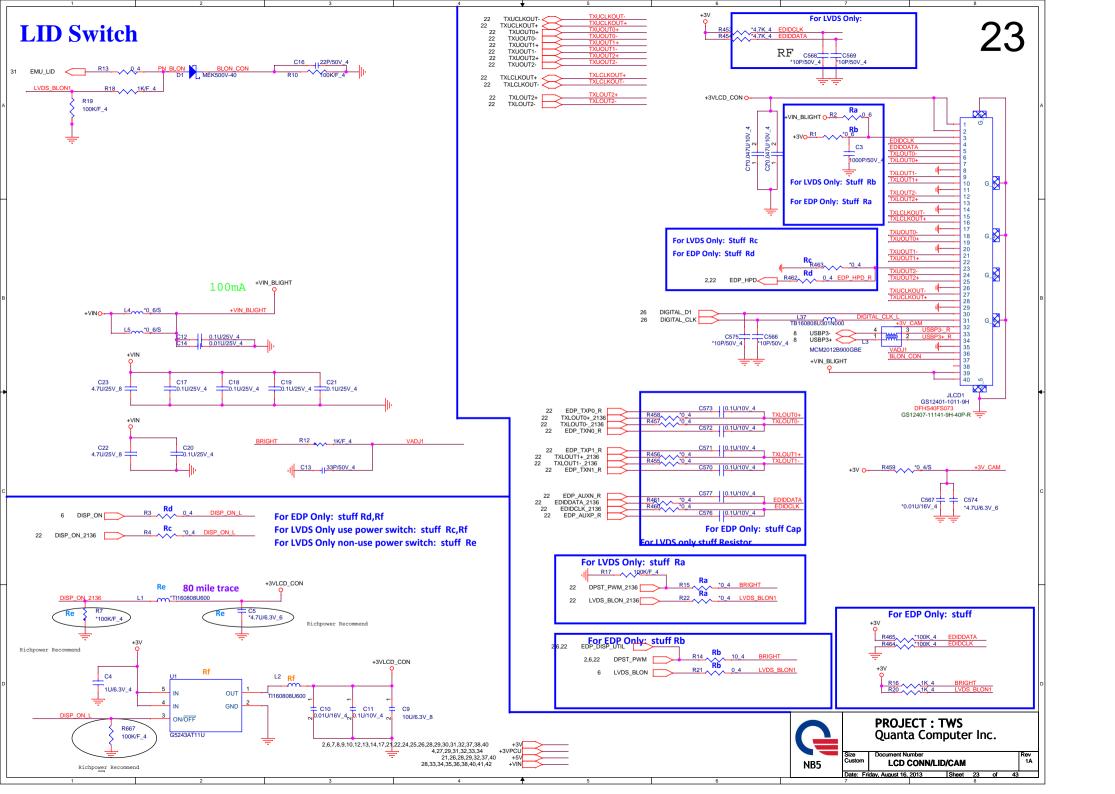


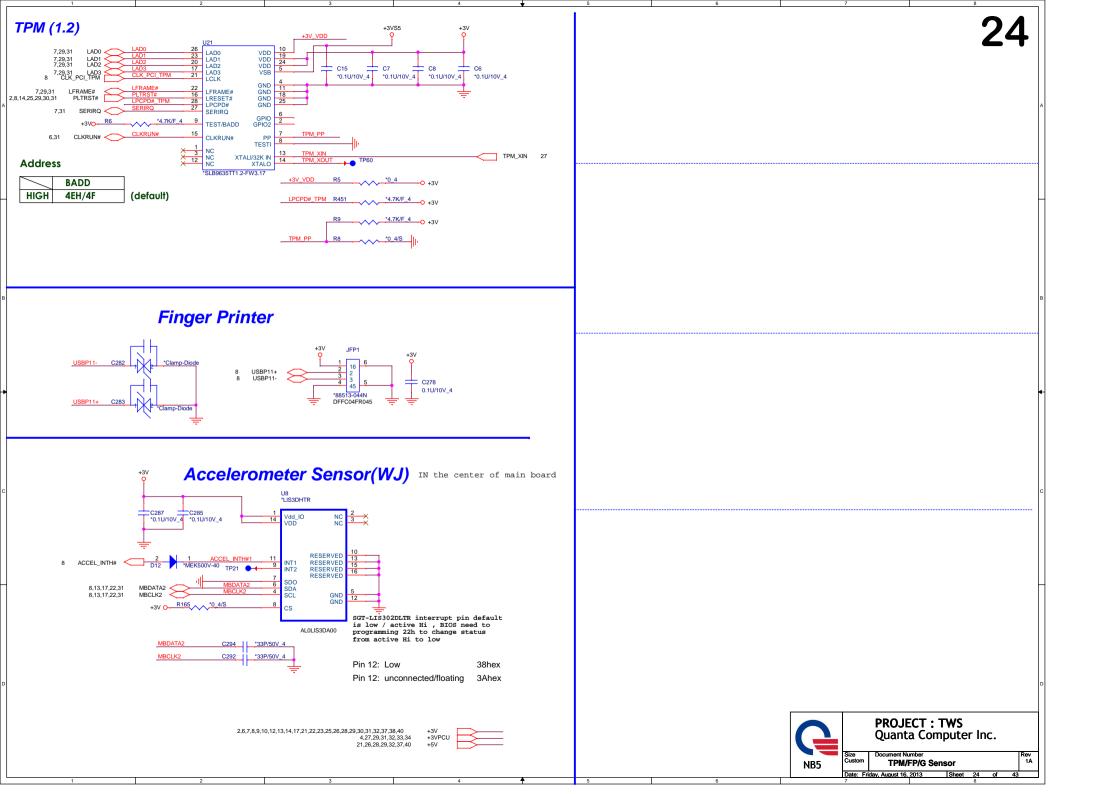


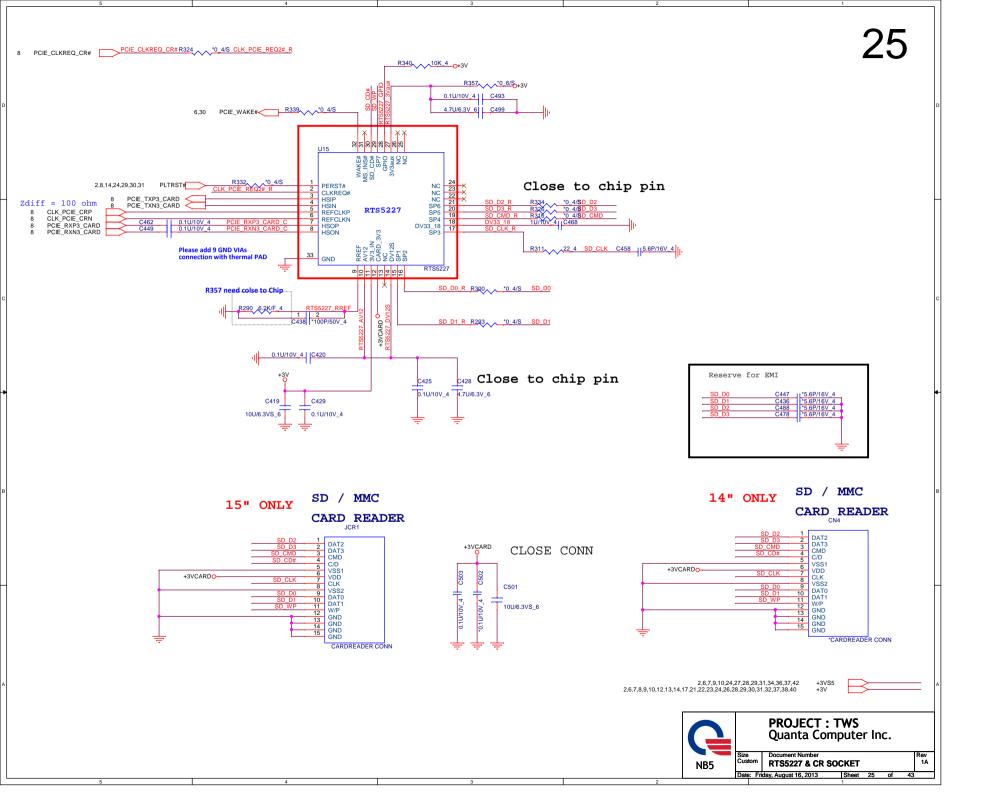


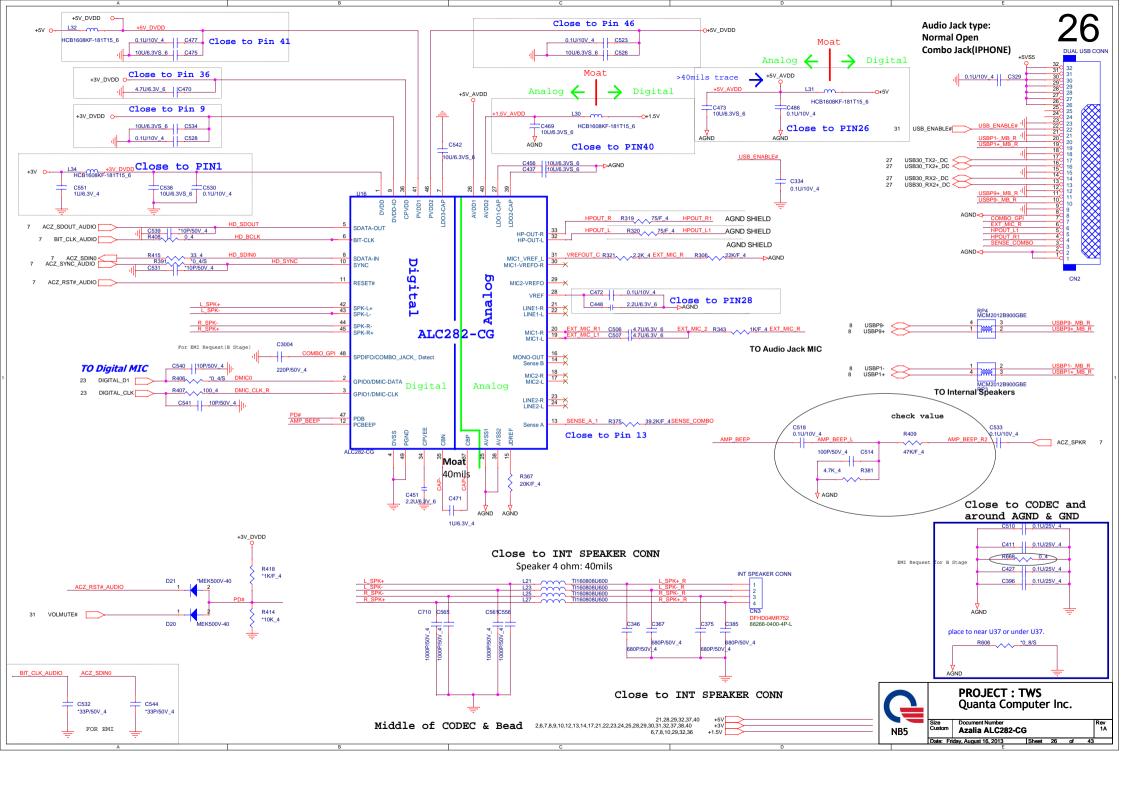


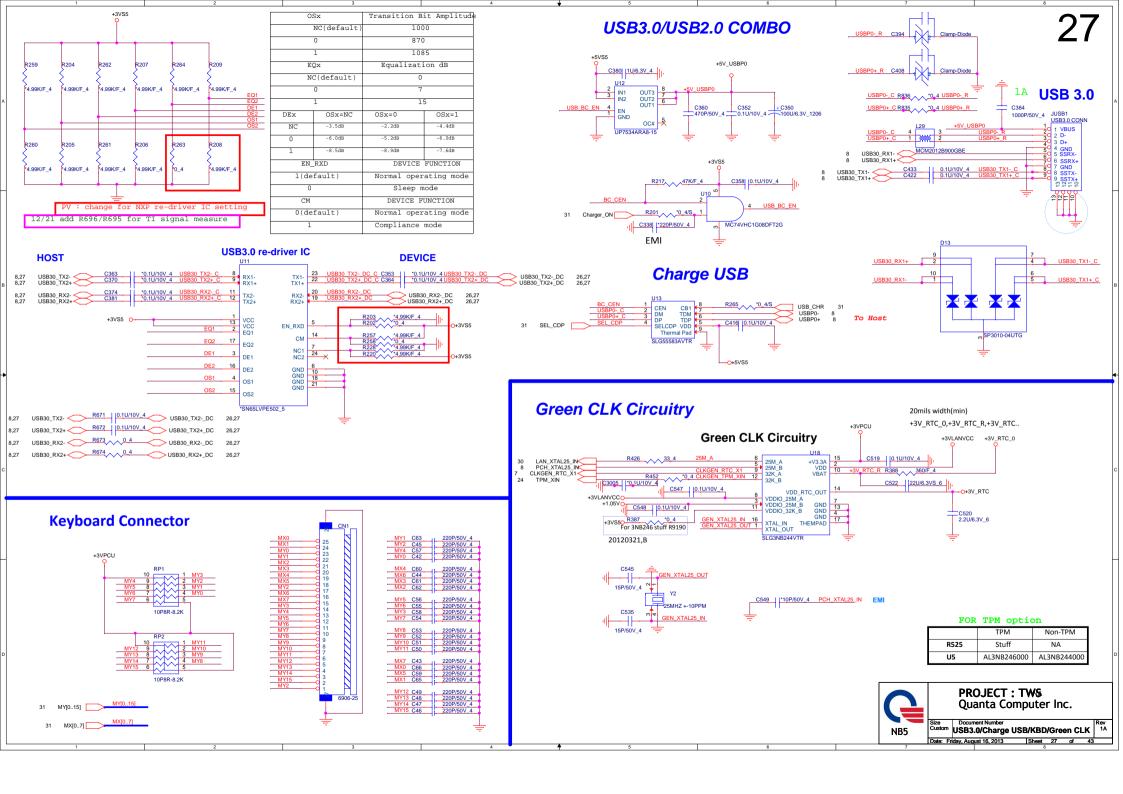




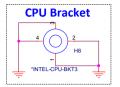








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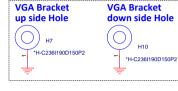










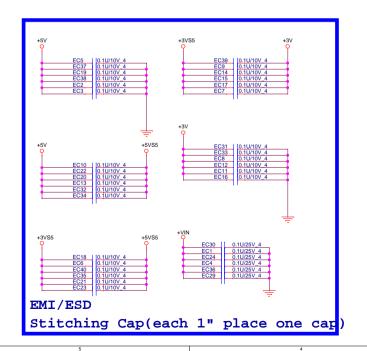


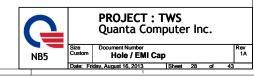


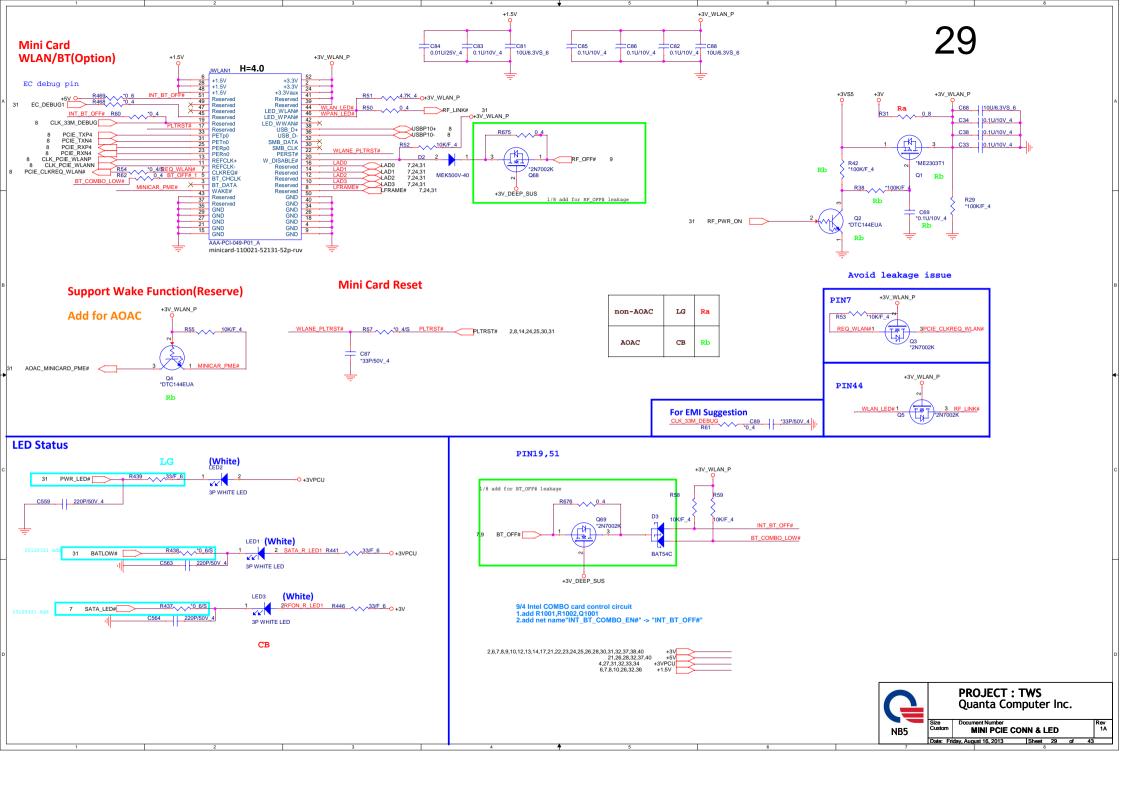


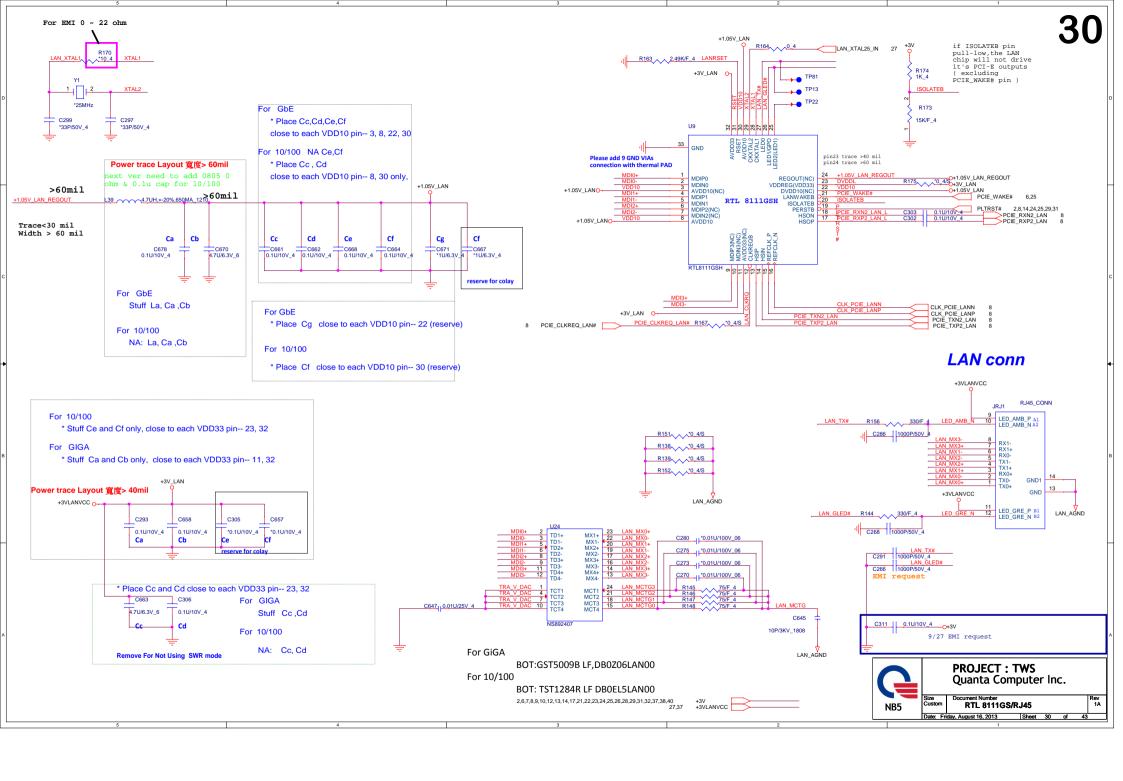


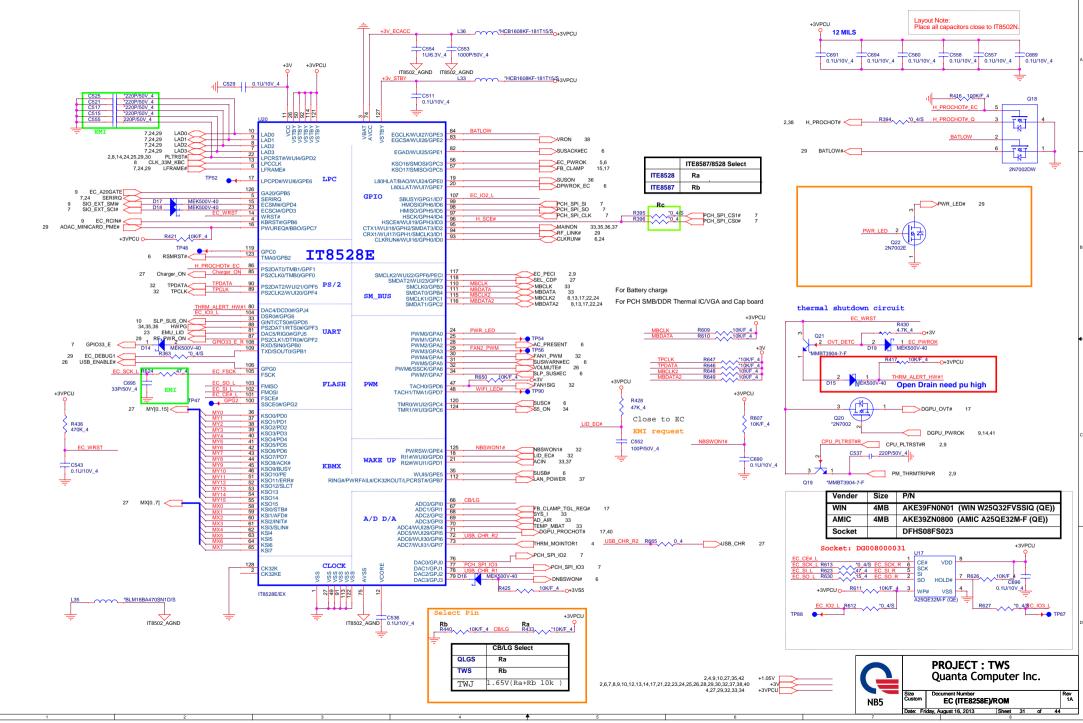


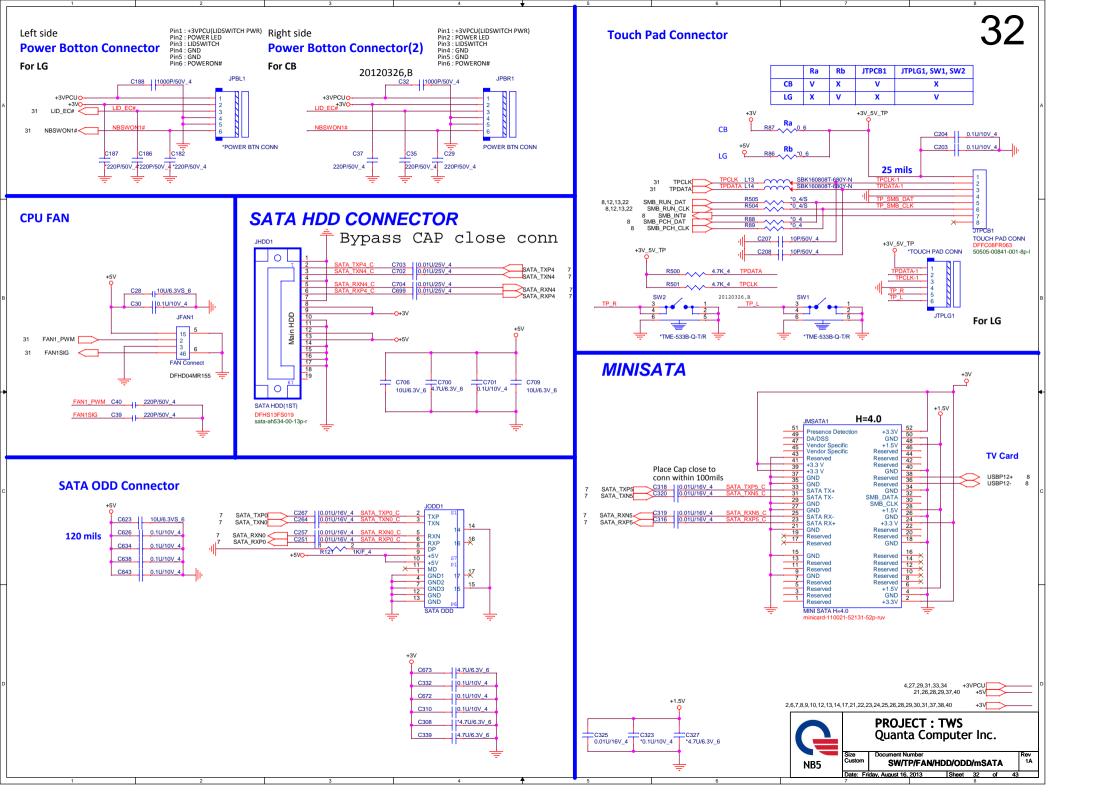


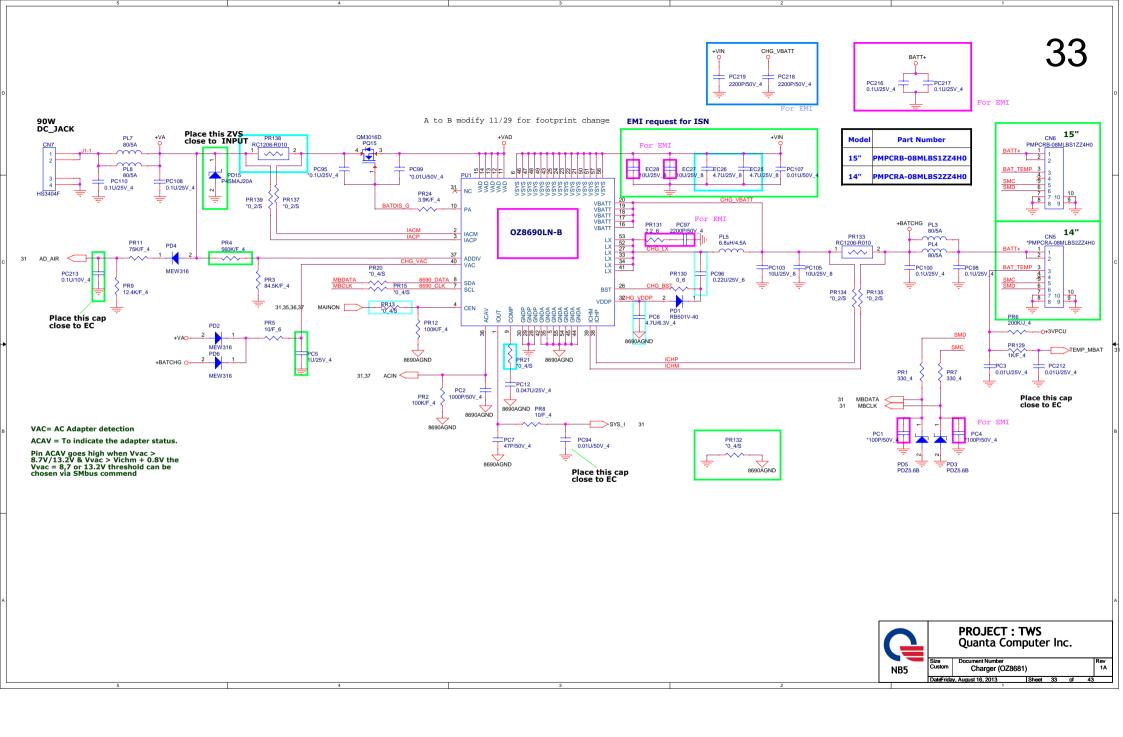


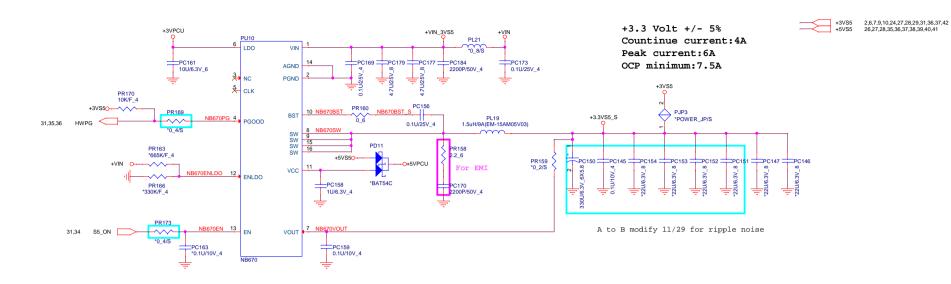


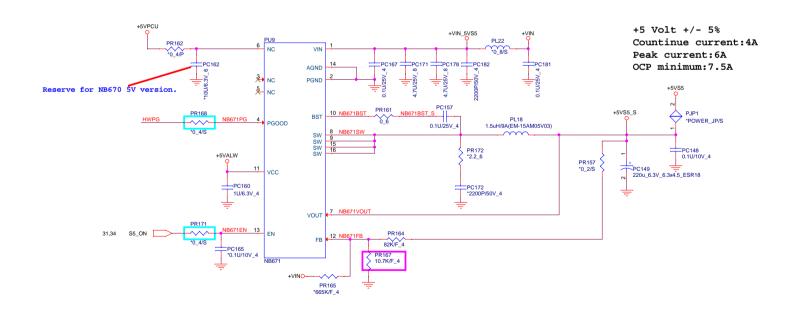


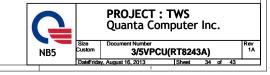


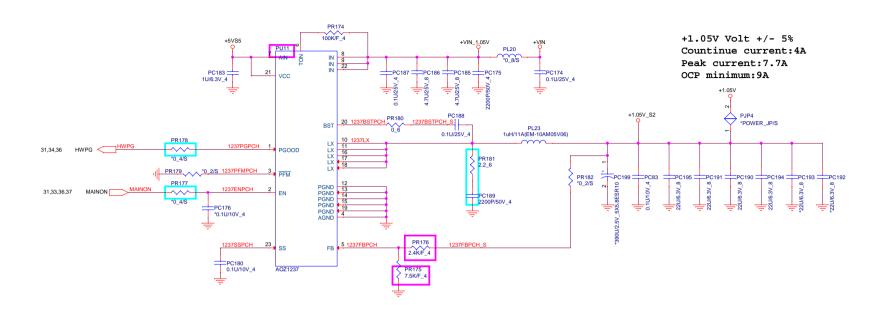












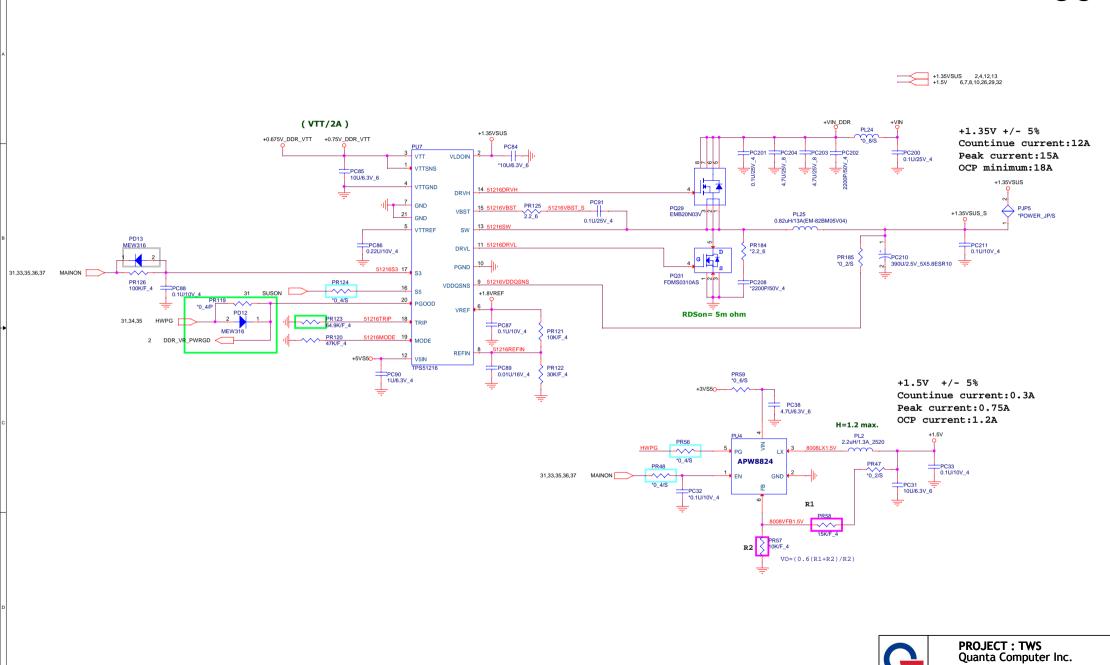


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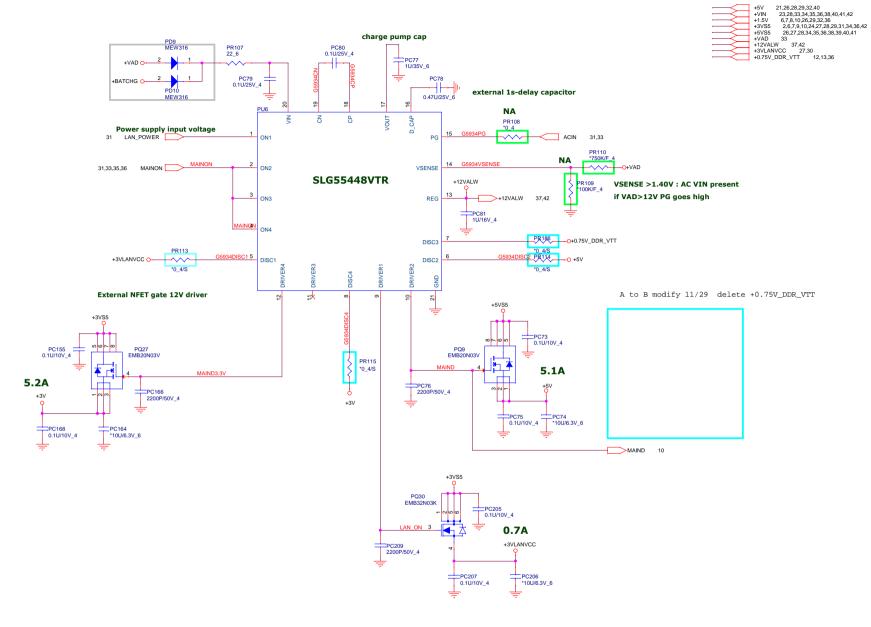
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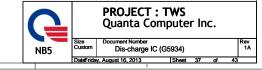
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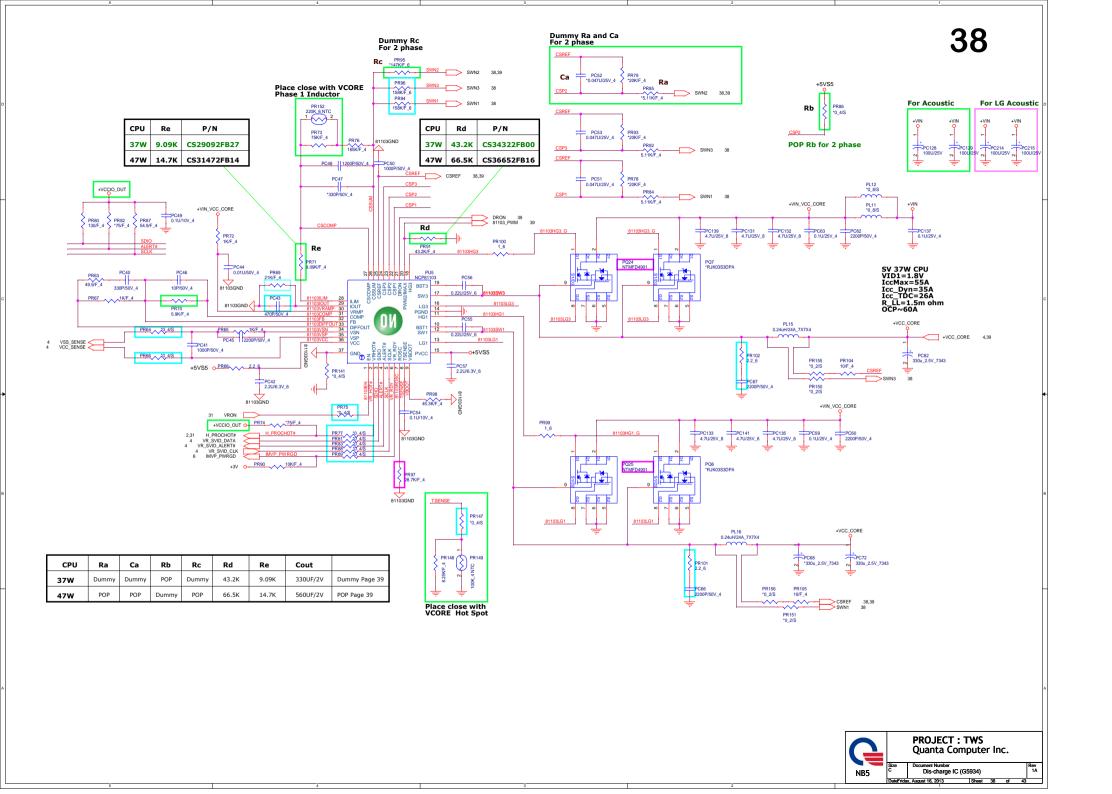


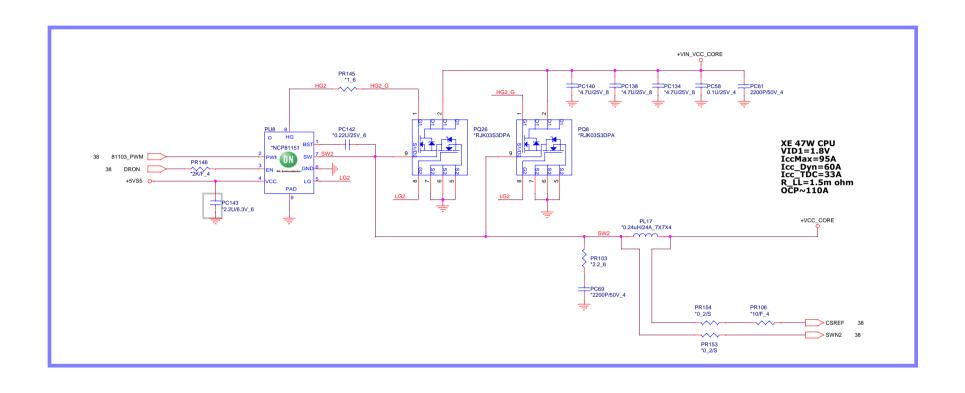




0/P will be change to shaort pad before PV







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Quanta Computer Inc.

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