

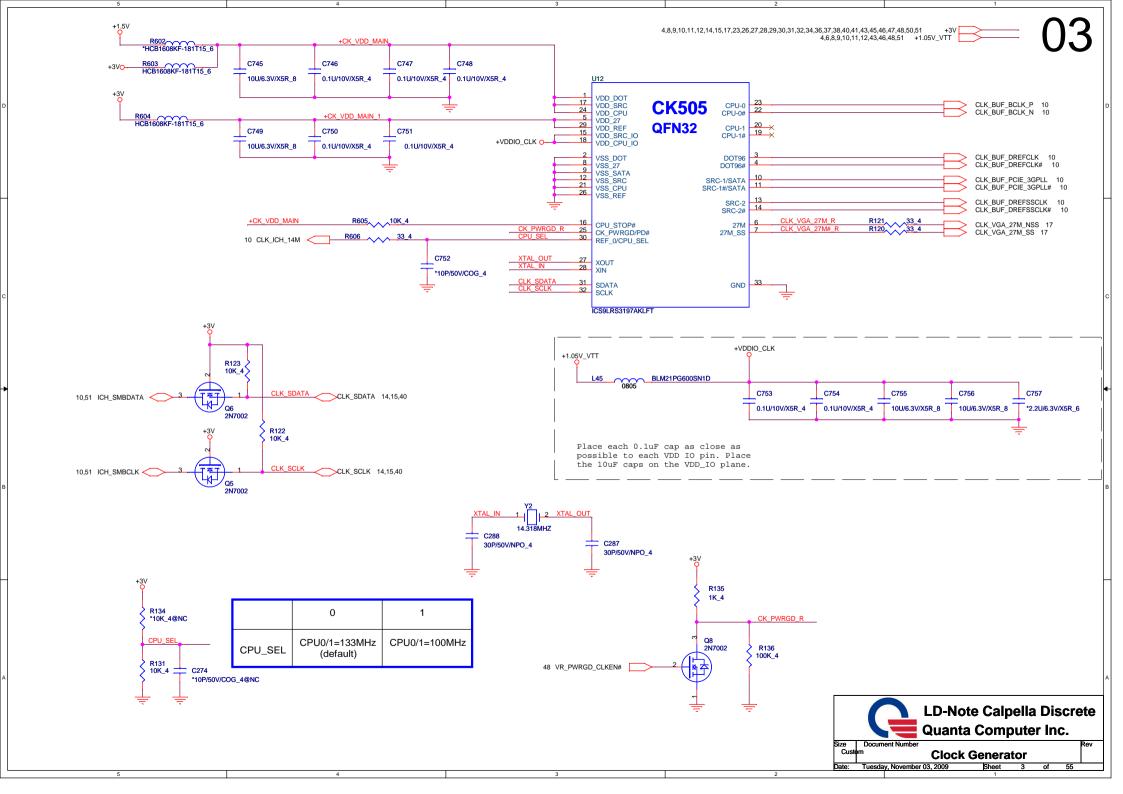
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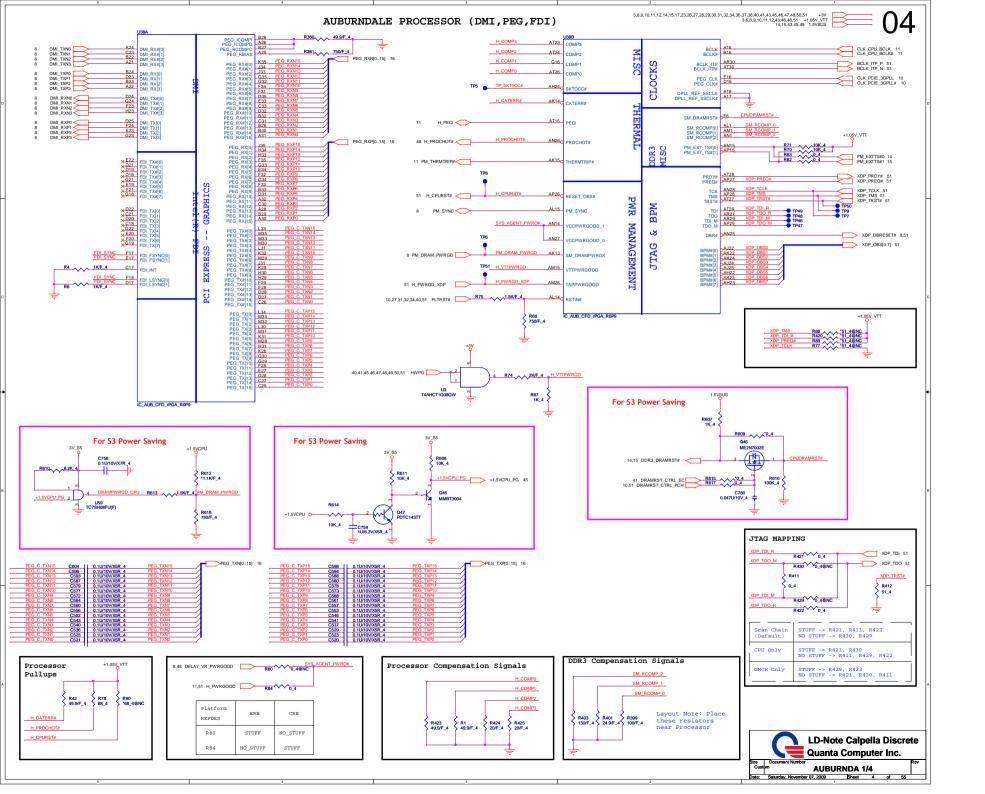
PAGE	DESCRIPTION					
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37	FAN & Thermal					
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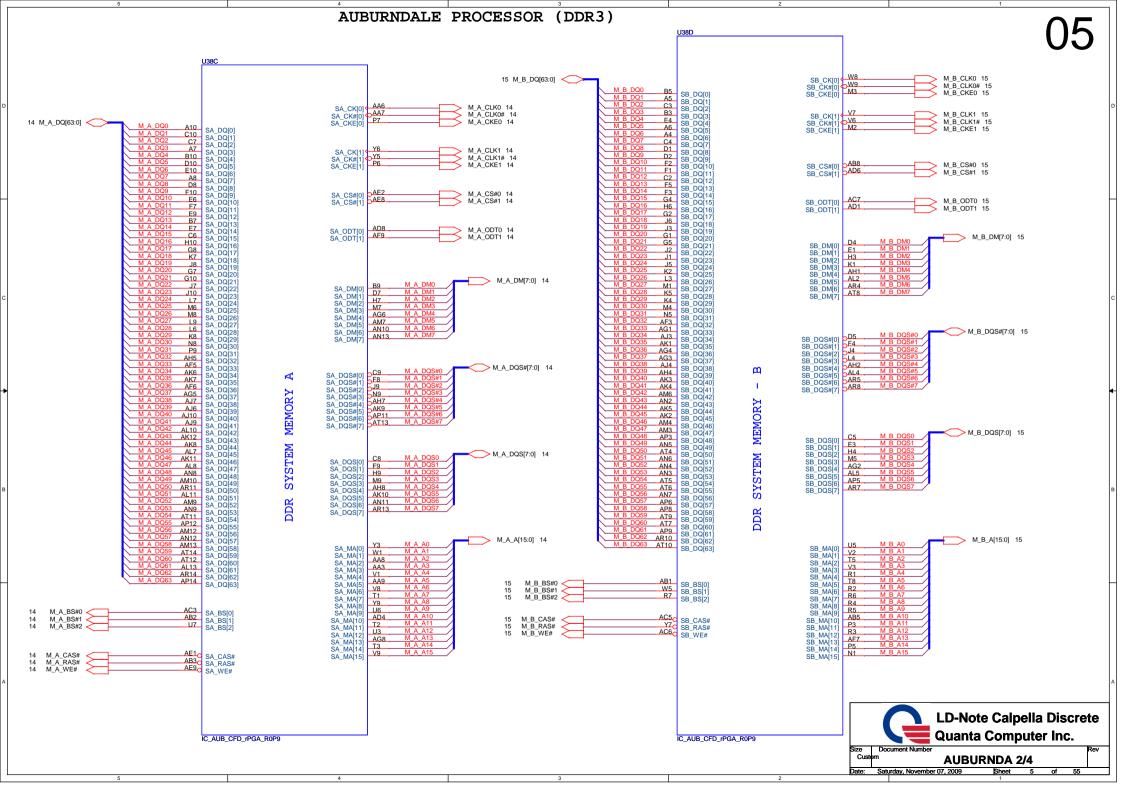
Power States

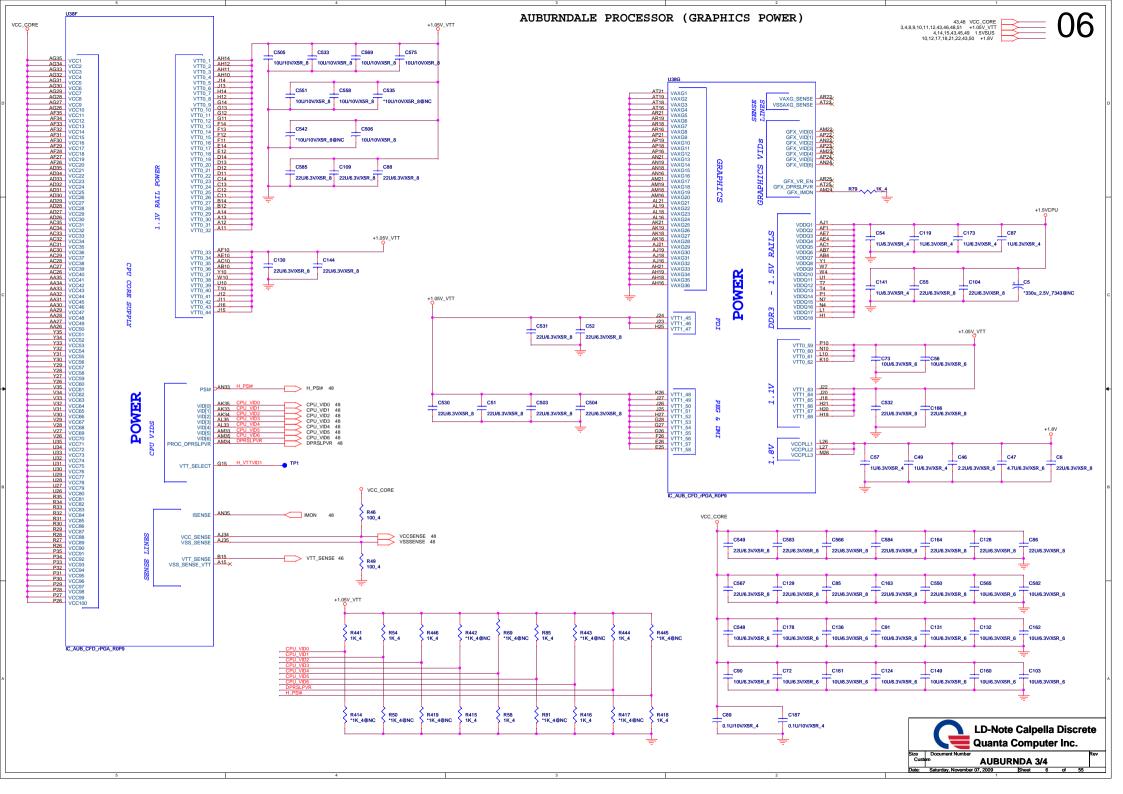
POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+20V	23,32,43,44,45,46,47,48,49,50	MAIN POWER		S0~S5
+3VRTC	+3.0V~+3.3V	9,12,41	RTC		S0~S5
3VPCU	+3.3V	9,23,27,30,32,35,39,41,43,44,47	ITE8052 POWER	3V5V_EN	S0~S5
5VPCU	+5V	14,43,44,45,46,47,49,50	DC/DC POWER IC SOURCE	3V5V_EN	S0~S5
+15V	+15V	23,38,43,45,46,47	LARGE POWER	3V5V_EN	S0~S5
LANVCC	+3.3V	27,43	LAN POWER	LAN_ON	
5V_S5	+5V	12,29,30,43	PCH SUS POWER	S5_ON	S0~S3
3V_S5	+3.3V	8,9,10,11,12,43,52	Sys Management,PCH Resume Well,Intel HD Audio,USB,WLAN WiMAX POWER	S5_ON	S0~S3
5VSUS	+5V	23,39,43,48	SLP_S4# CTRLD POWER	SUSON	S0~S3
3VSUS	+3.3V	14,15,30,34,41,43,49	SLP_S4# CTRLD POWER	SUSON	S0~S3
1.5VSUS	+1.5V	4,6,14,15,43,45,46,49,50	SODIMM POWER	SUSON	S0~S3
0.75VSMDDR_VTERM	+0.75V	14,15,43,45	DDR3 SODIMM REFERENCE POWER	MAIN_ON	S0
+5V	+5V	12,18,23,24,25,26,28,35,37,41,43,44	SLP_S3# CTRLD POWER	MAIN_ON	S0
+3V	+3.3V	3,4,8,9,10,11,12,14,15,17,23,25,26,27,28,29, 30,31,32,33,34,36,37,38,39,40,41,43,44,45,46 ,47,48,50,52	SLP_S3# CTRLD POWER	MAIN_ON	S0
+1.8V	+1.8V	6,12,17,18,21,22,33,43,50	LVDS,NVM POWER	MAIN_ON	S0
+1.5V	+1.5V	12,18,19,20,31,32,34,45,46	Mini PCIe,Express Card POWER	MAIN_ON	S0
+1.05V_VTT	+1.05V	4,6,11,12,43,46,48,52	AuBurndale VTT POWER	MAIN_ON	S0
+1.05V_PCH	+1.05V	3,10,12,43,46,52	PCH CORE POWER	1.05V_RUN_ON	S0
+VCC_GFX_CORE	+0.9V~+1.2V	18,21,43,49	VGA CORE POWER	GFXVR_EN	S0
VCC_CORE		6,43,48	CPU CORE POWER	VRON	S0
LCDVCC	+3.3V	23	LCD Power	ENVDD	S0
+5V_ODD	+5V	28	ODD Power	MAIN_ON	S0
+5V_HDD	+5V	28	HDD Power	MAIN_ON	S0
BAT-V	+10V~+17V	44	MAIN BATTERY	CHG_PBATT	S0~S5

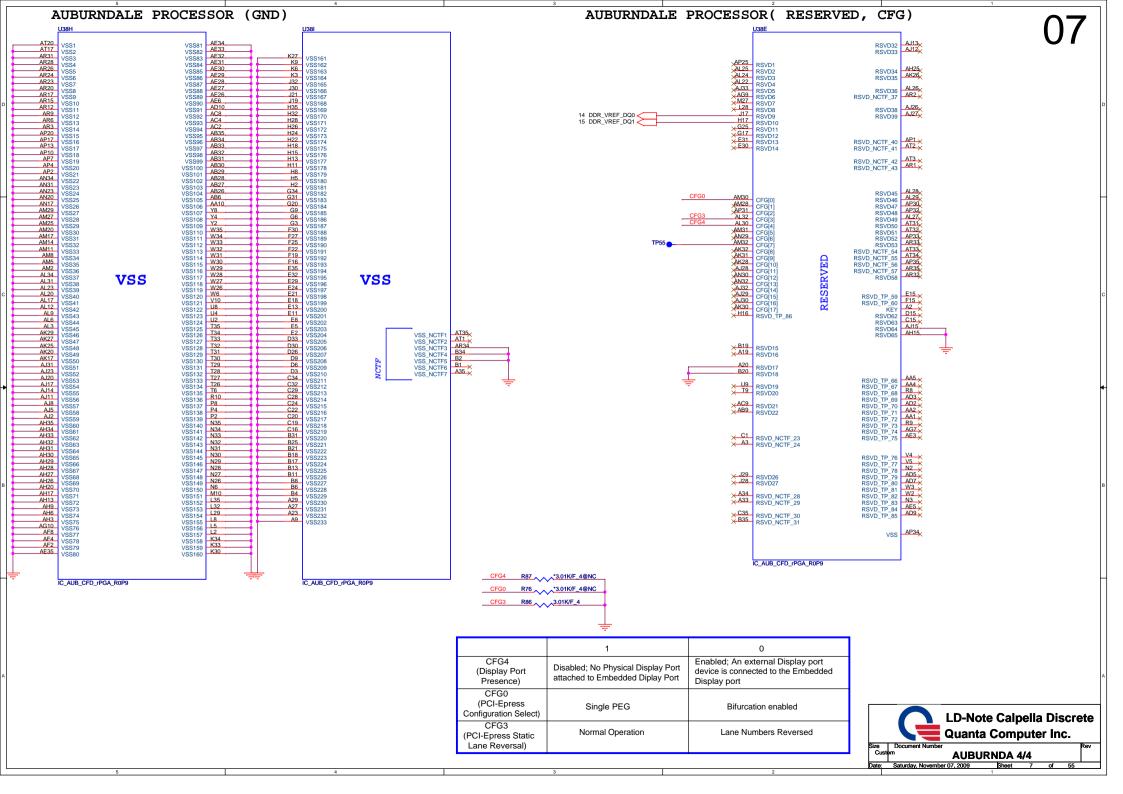


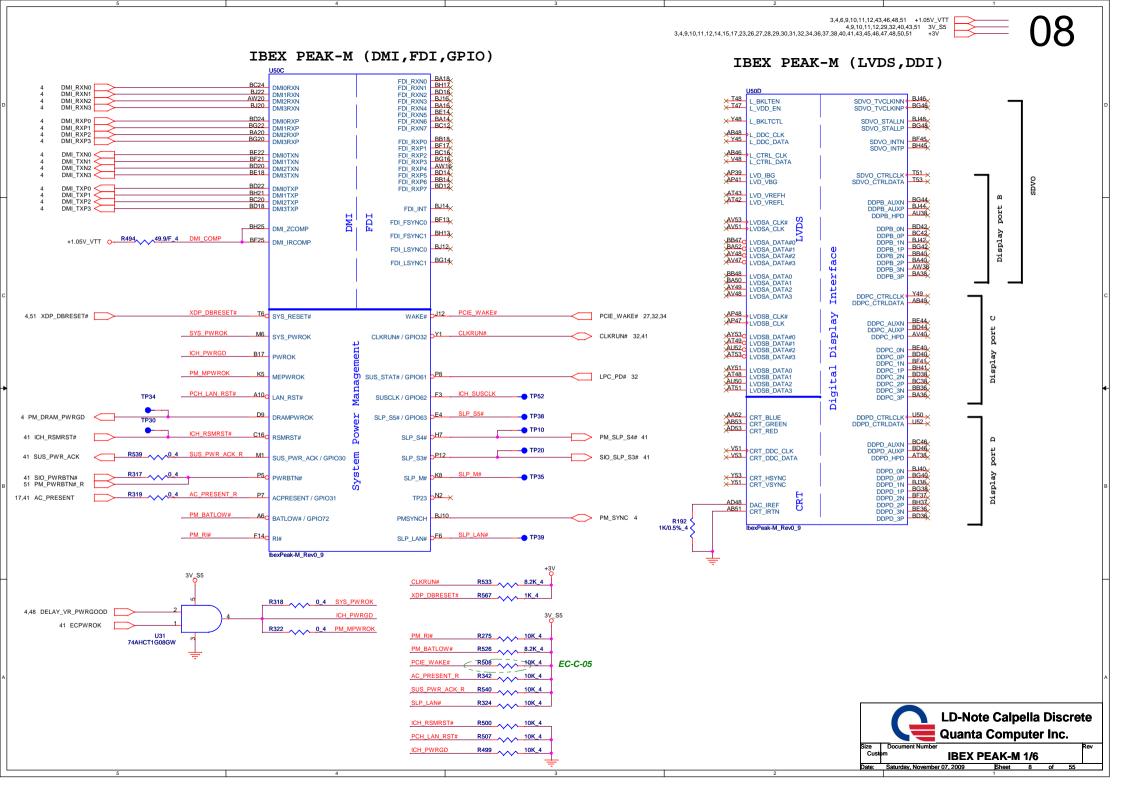


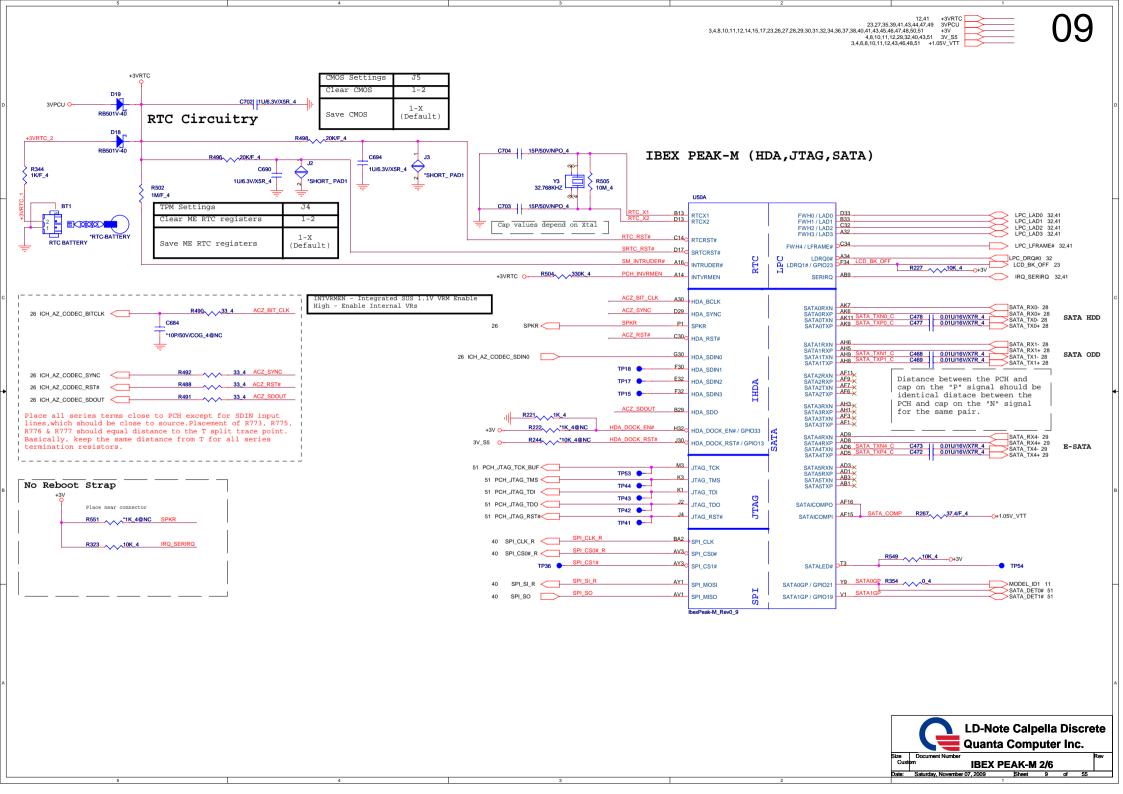


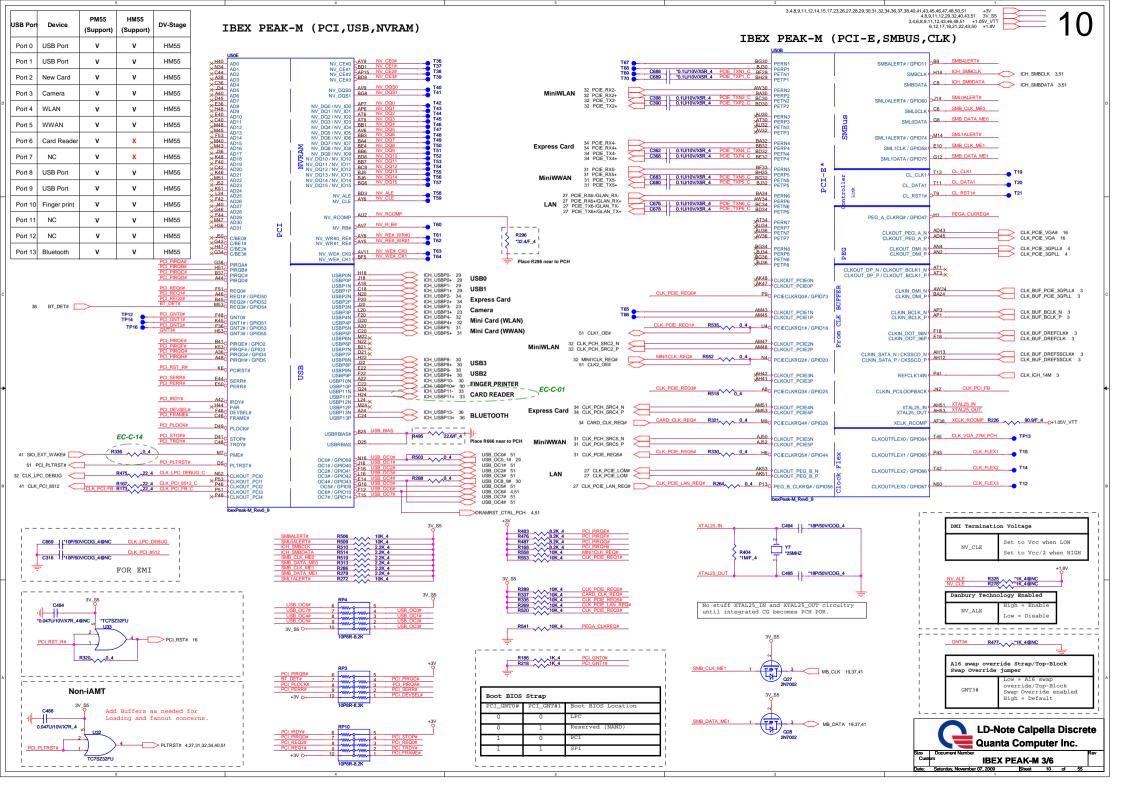


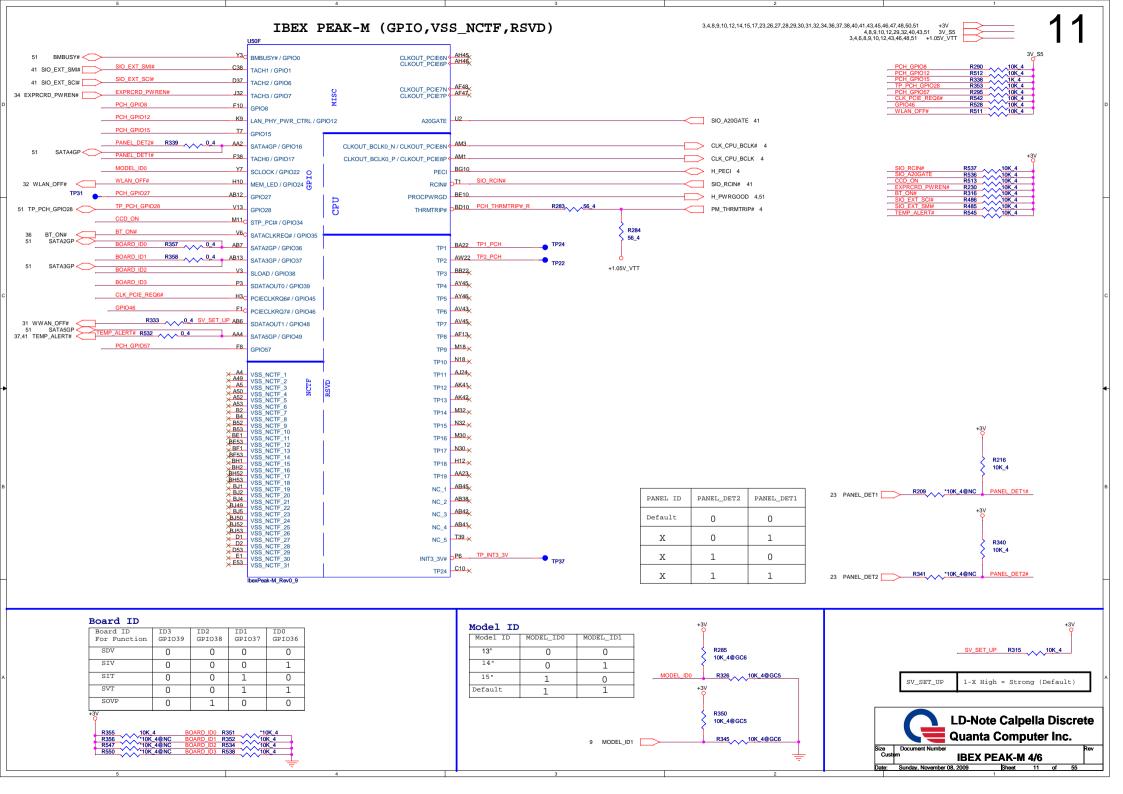


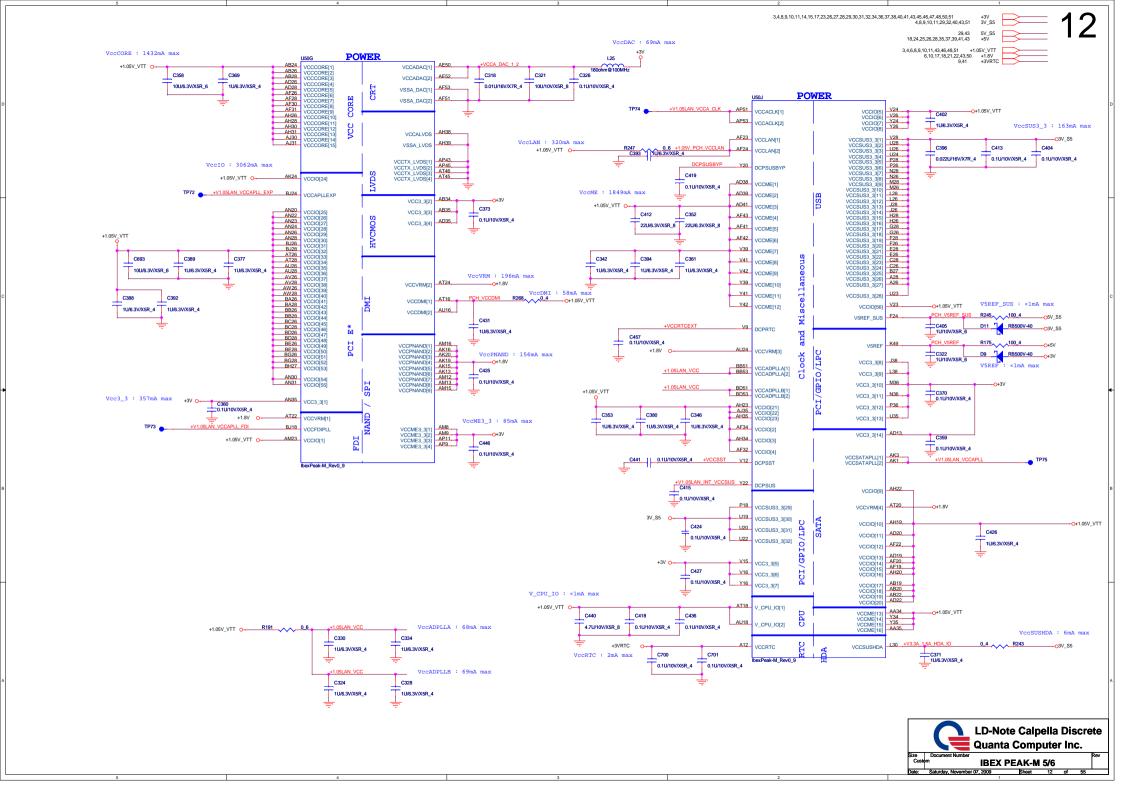


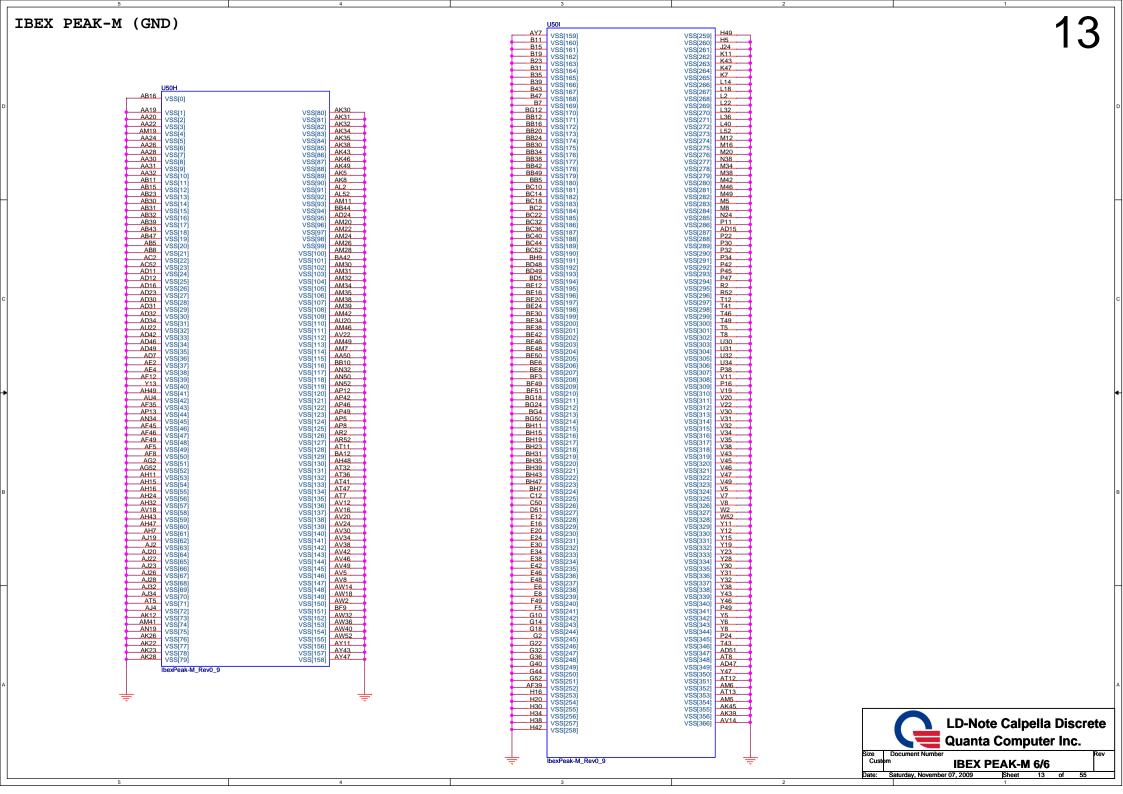


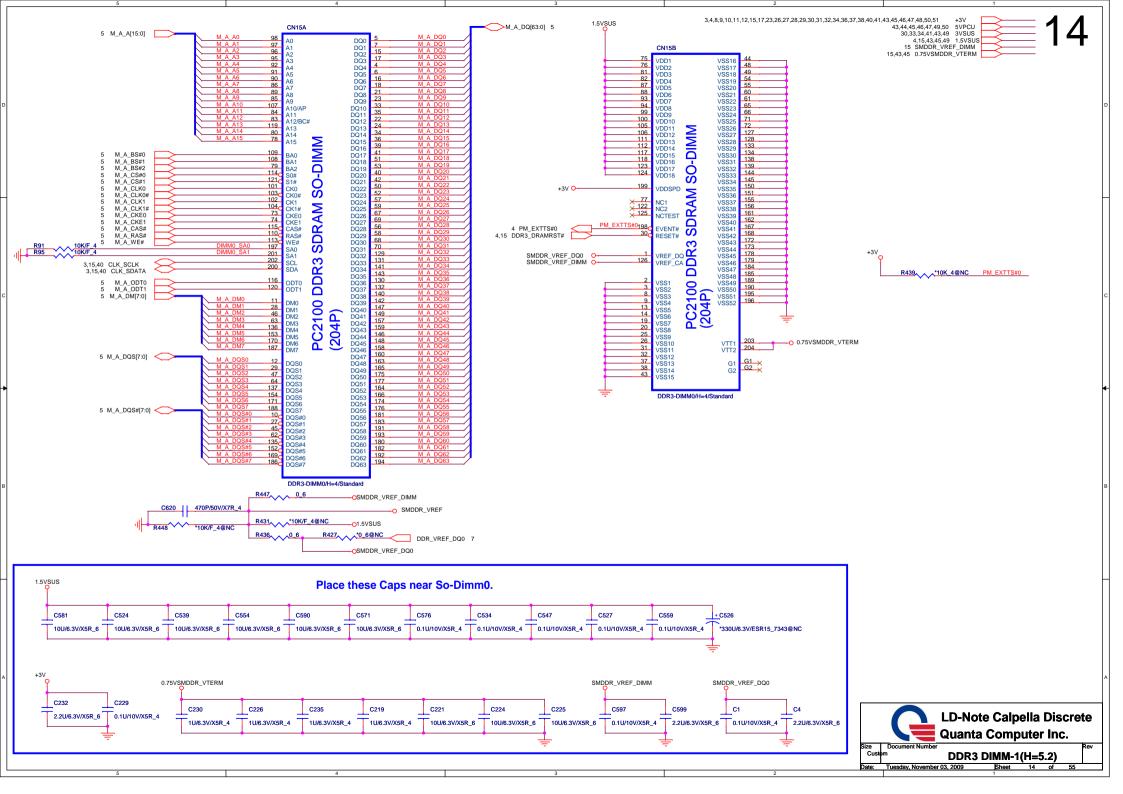


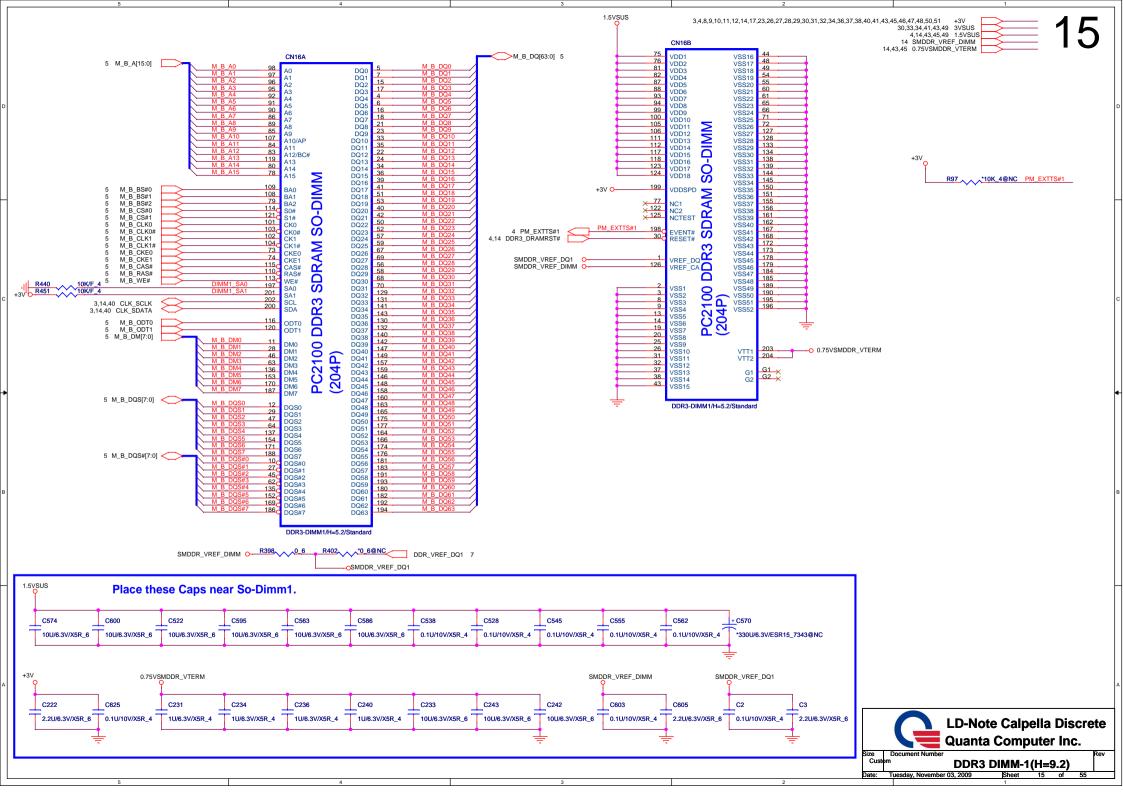


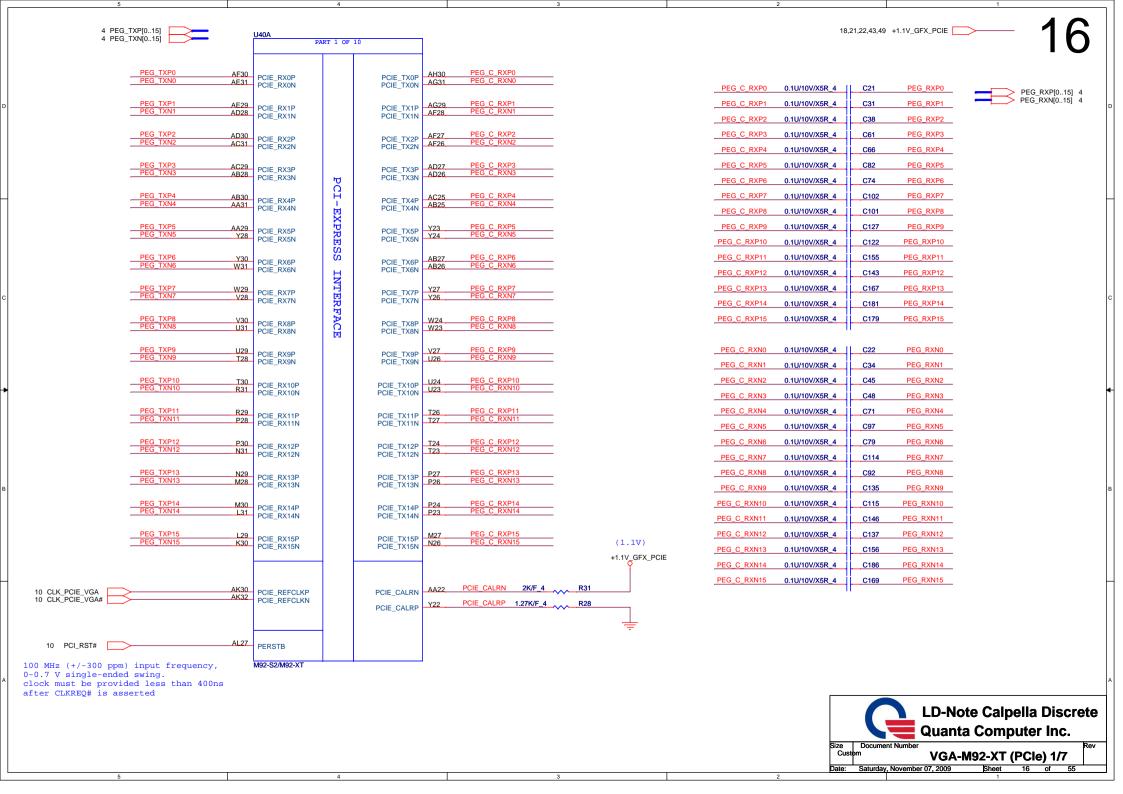


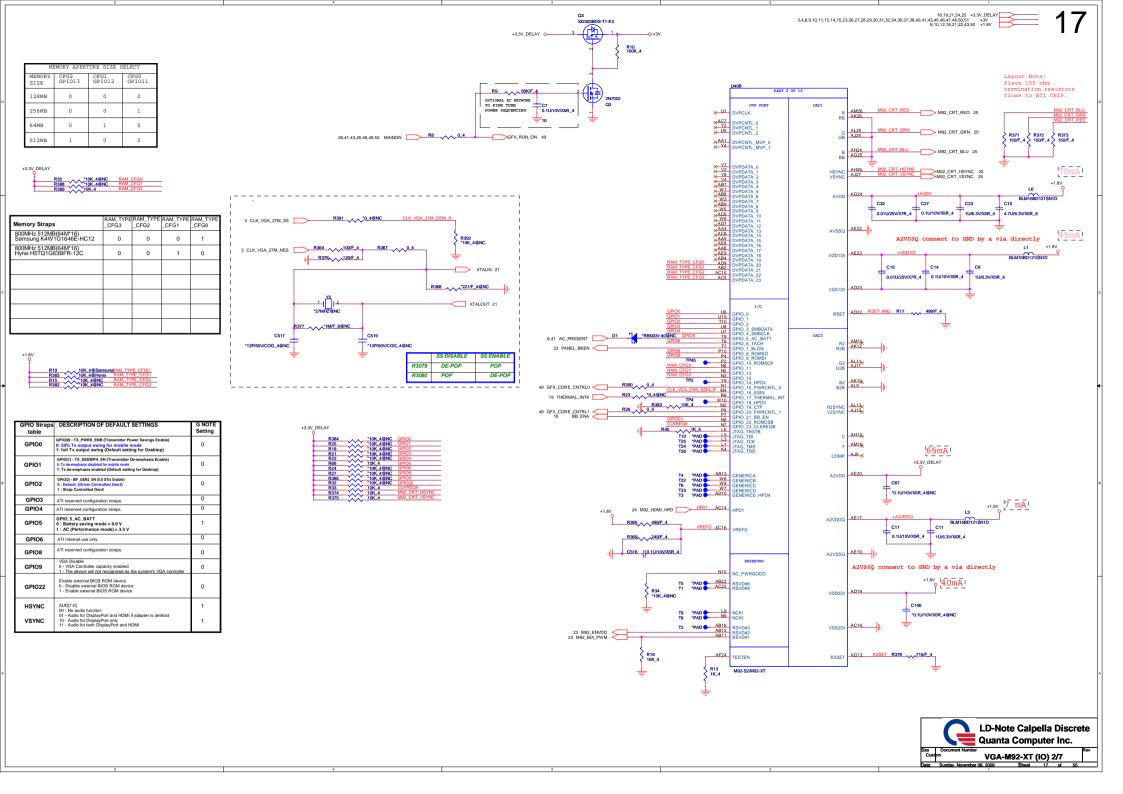


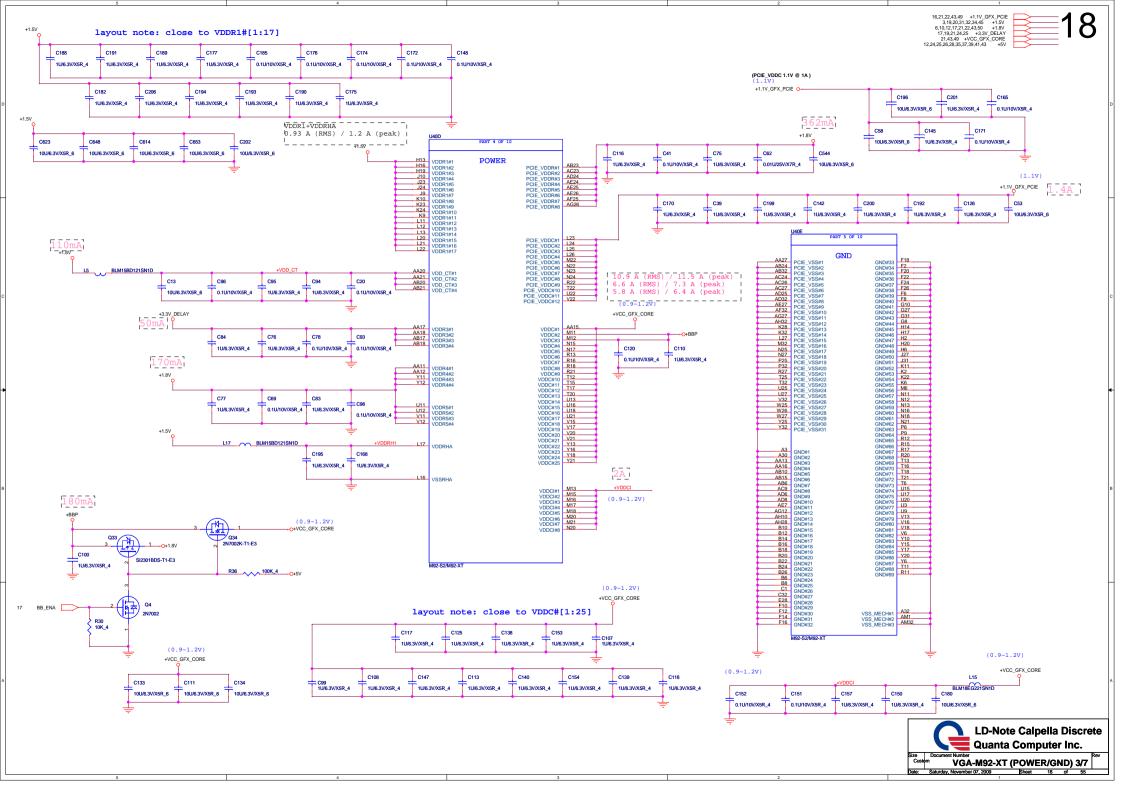


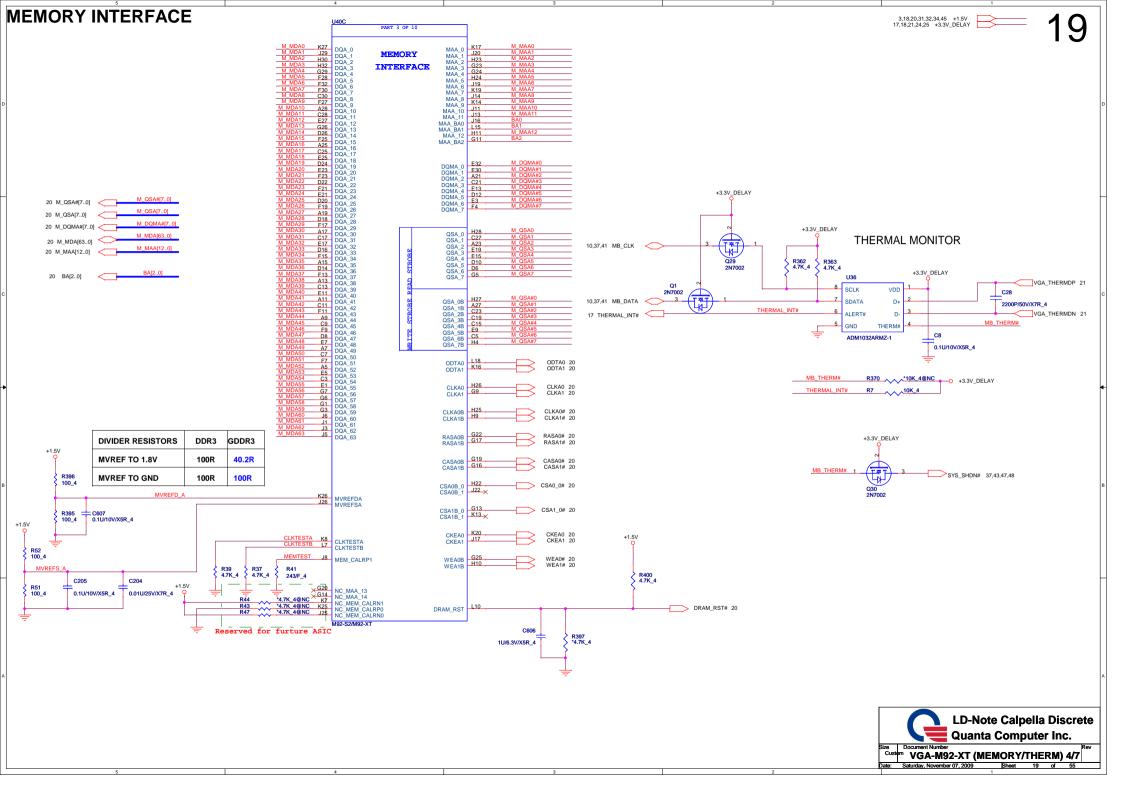


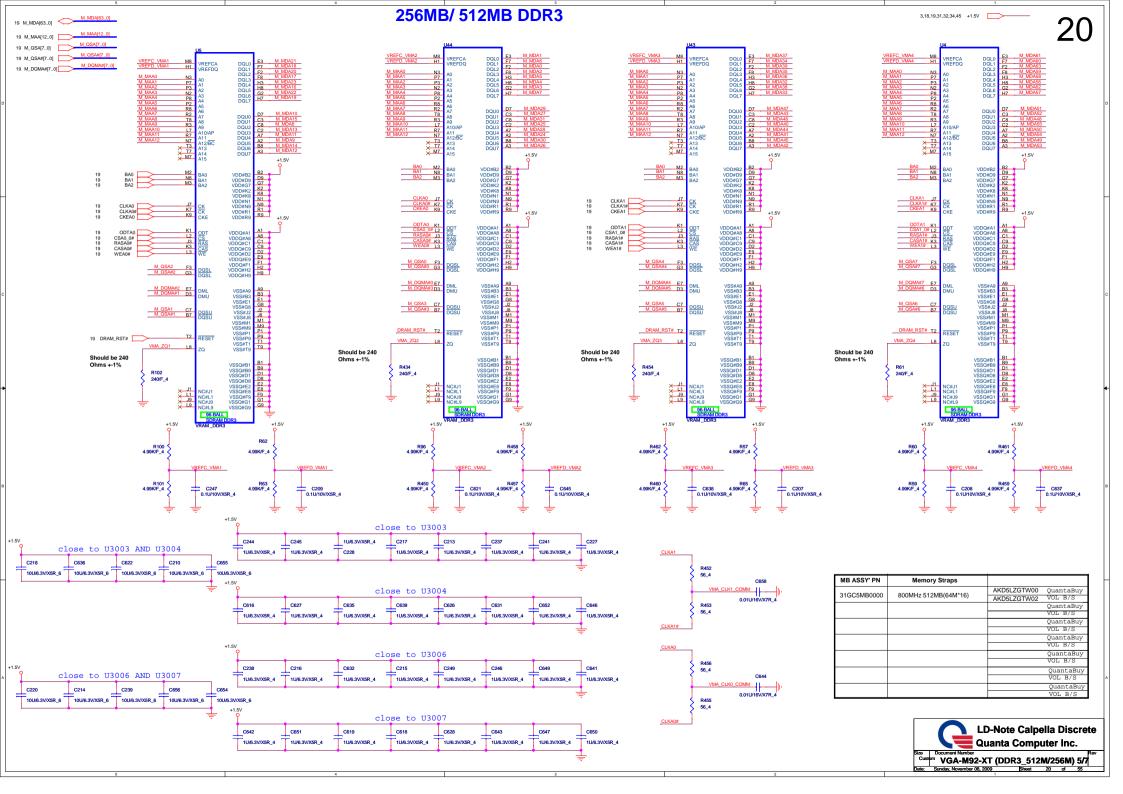


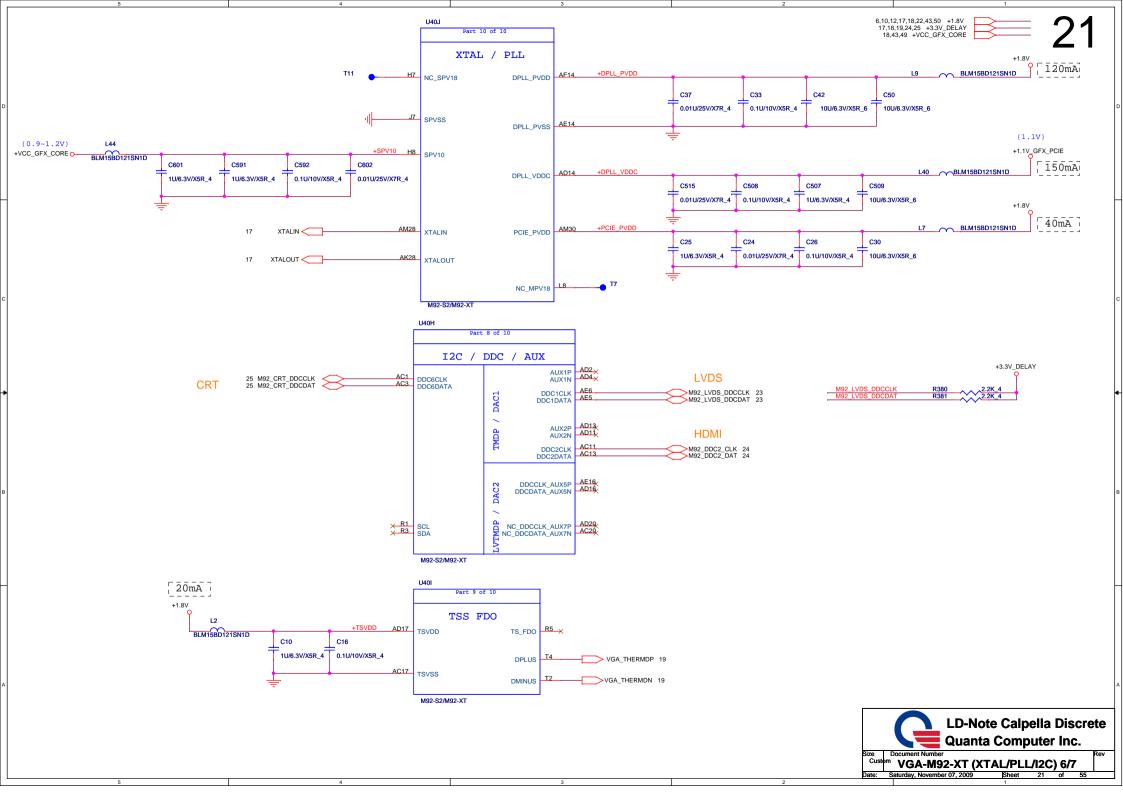










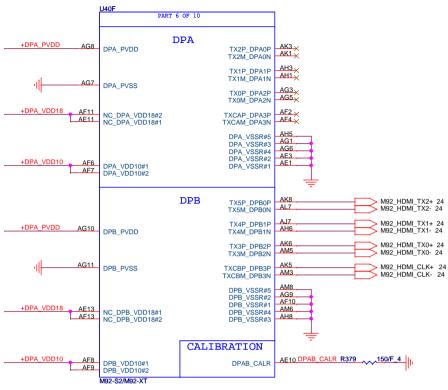


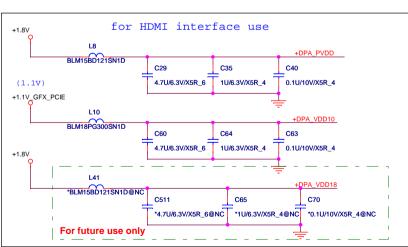
TMDP(HDMI) INTERFACE

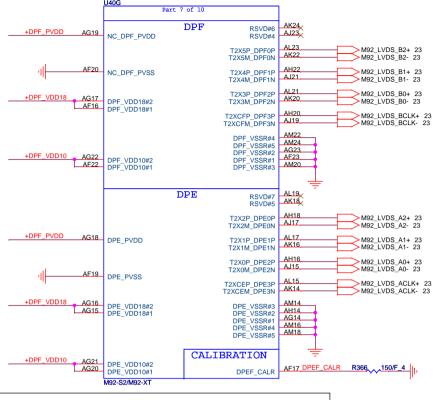
LVDS INTERFACE

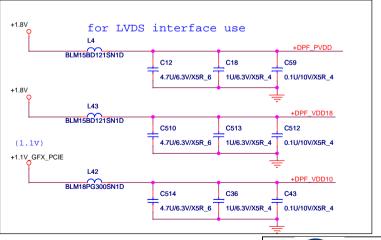
6,10,12,17,18,21,43,50 +1.8V

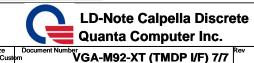
22





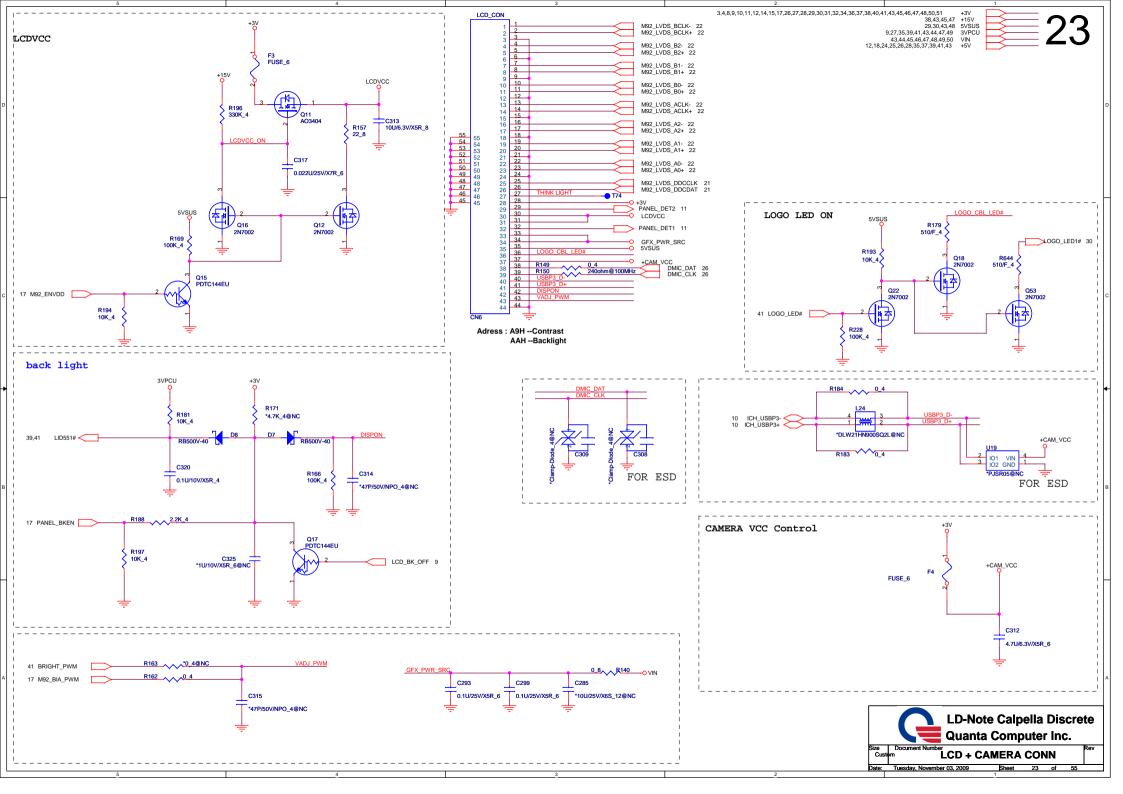


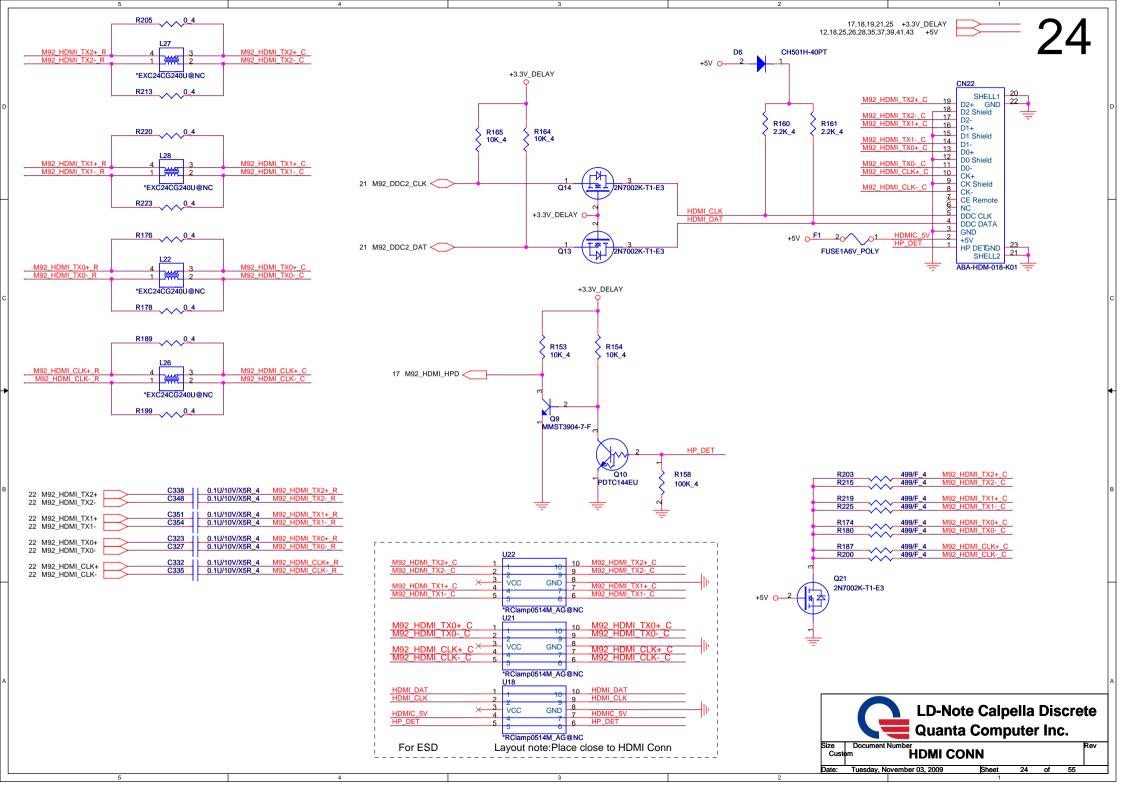


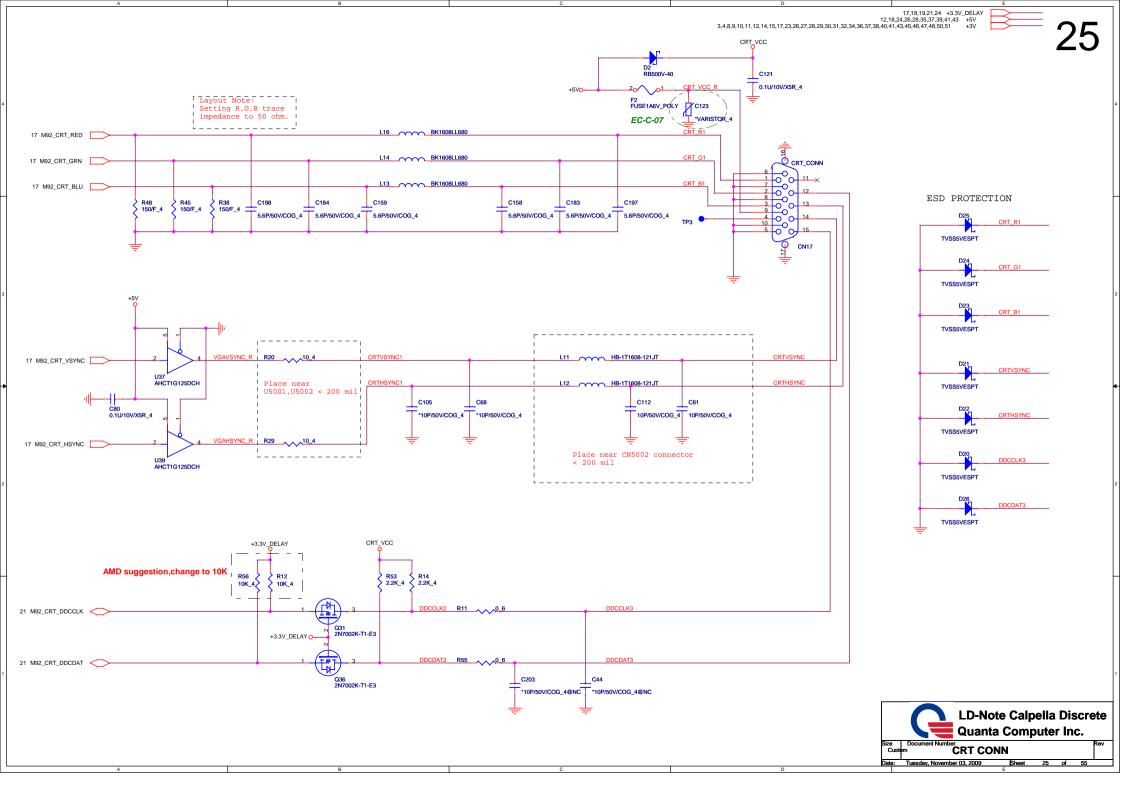


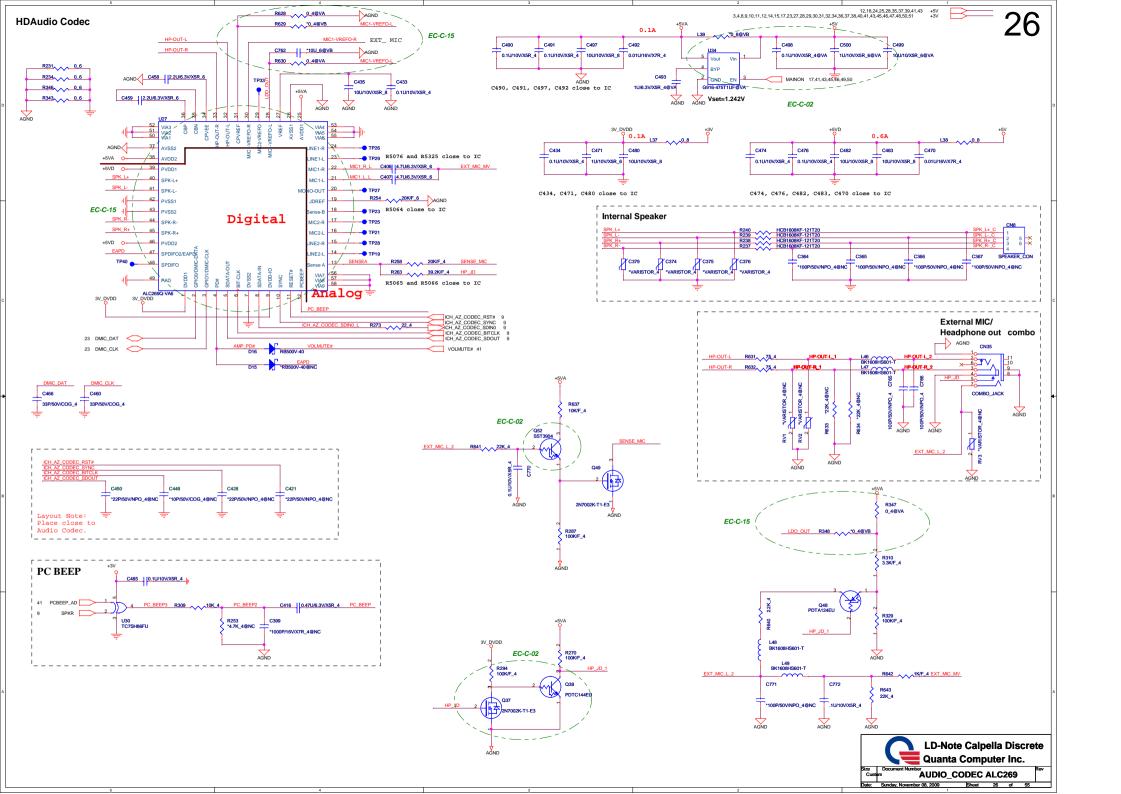
Custom VGA-M92-XT (TMDP VF) 7/7
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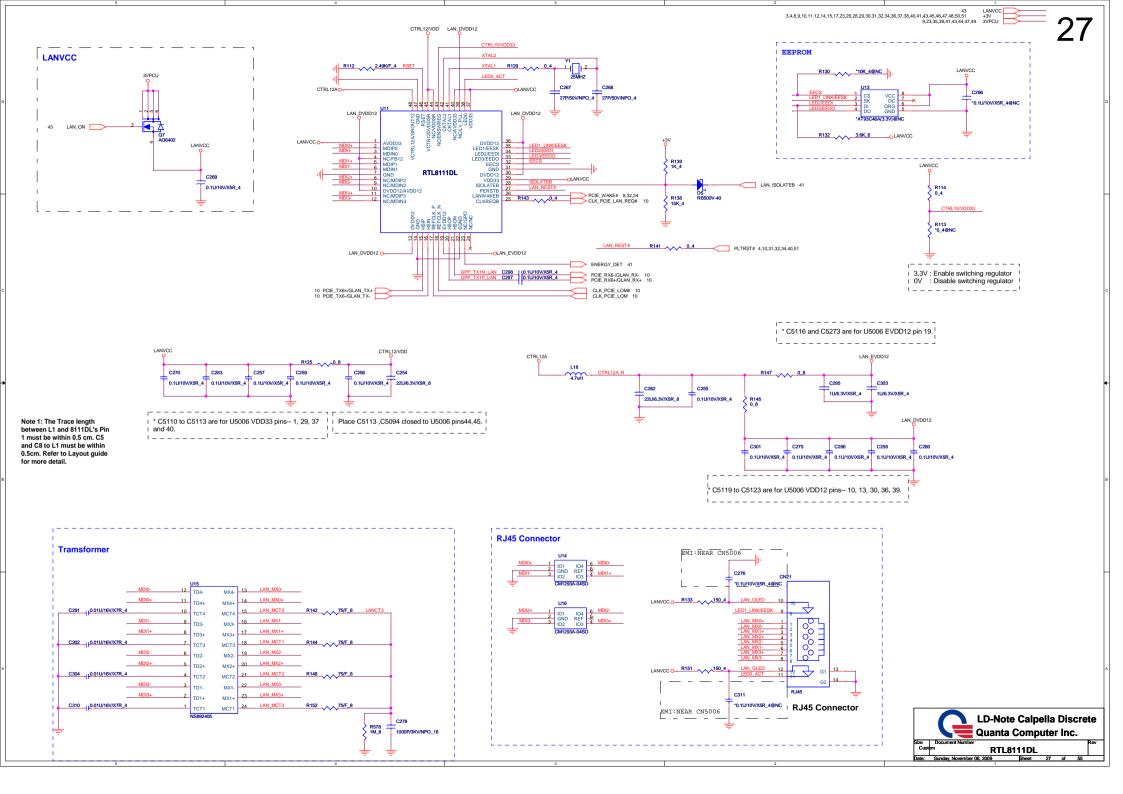
4 3 2

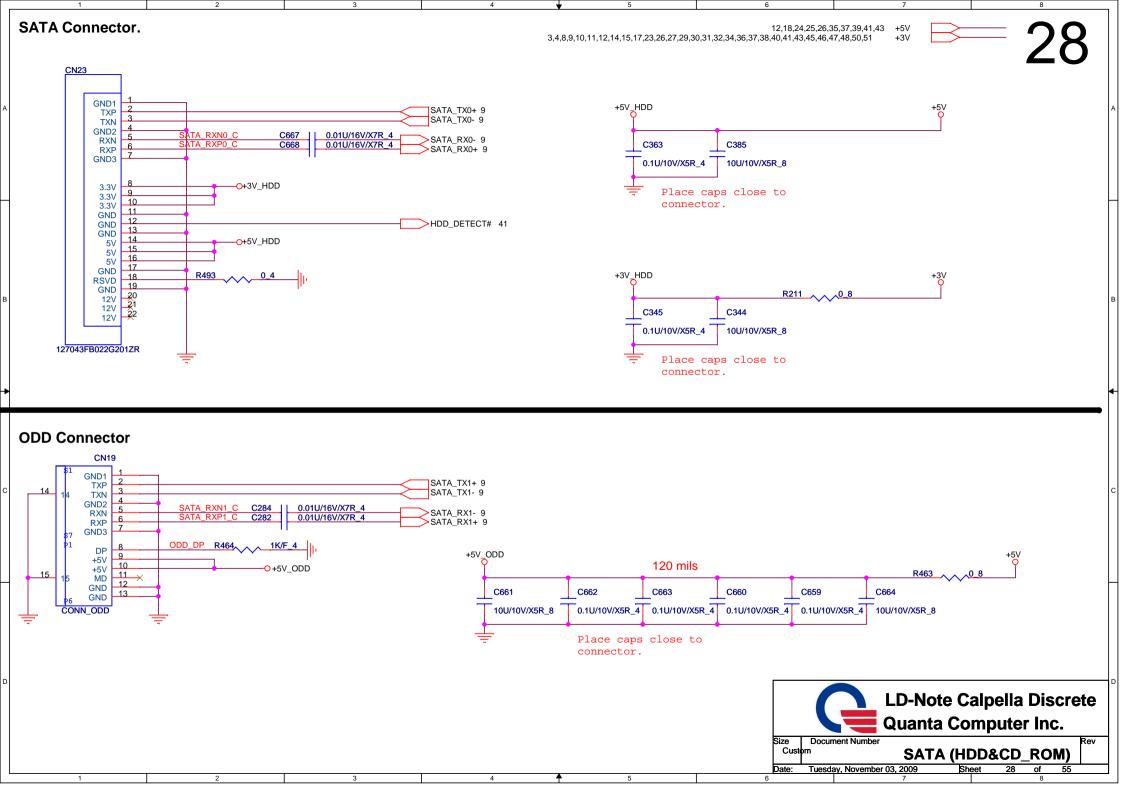


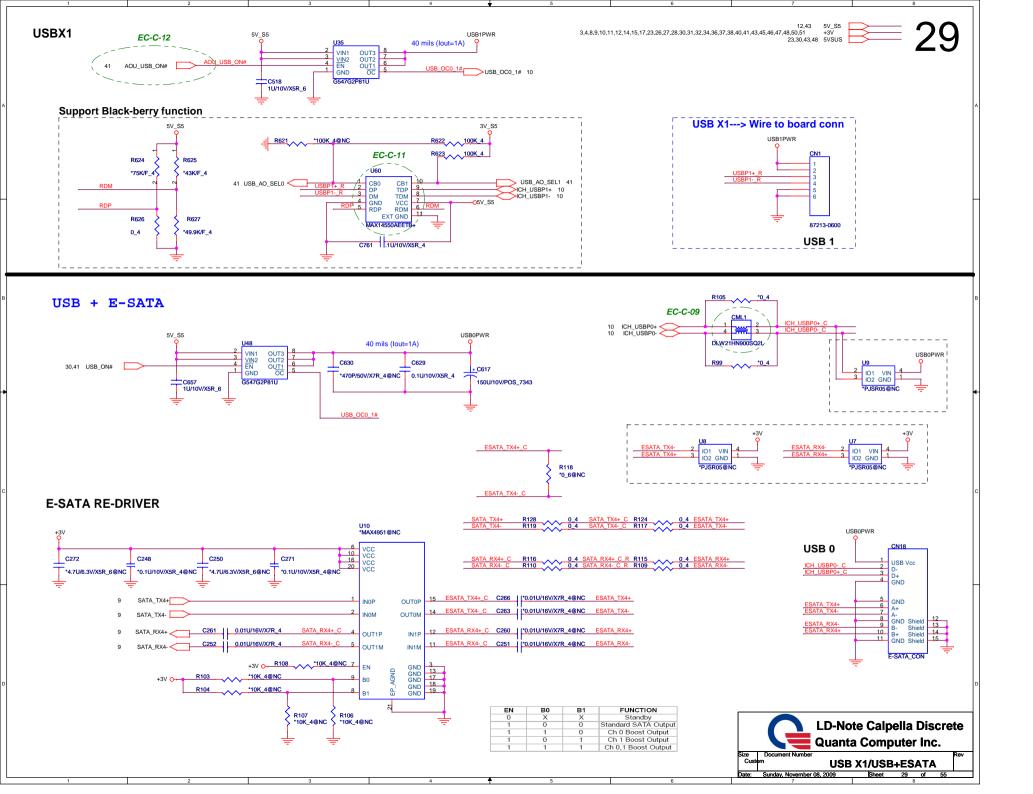


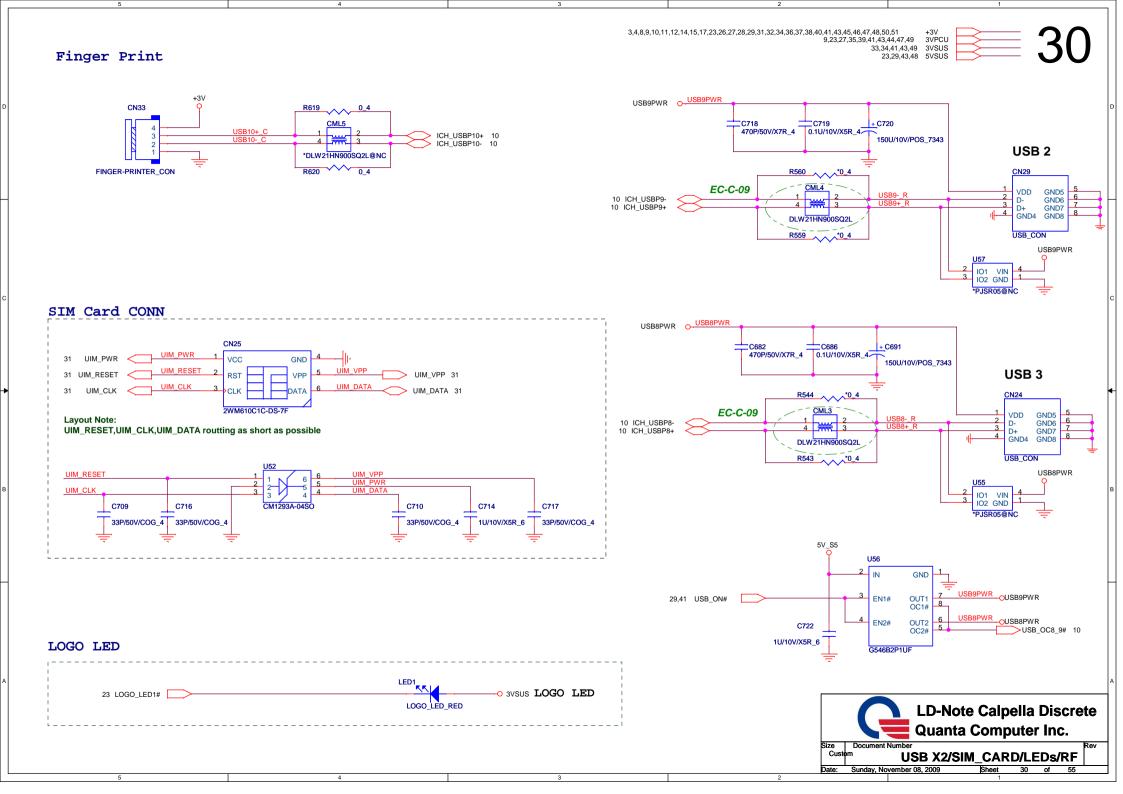


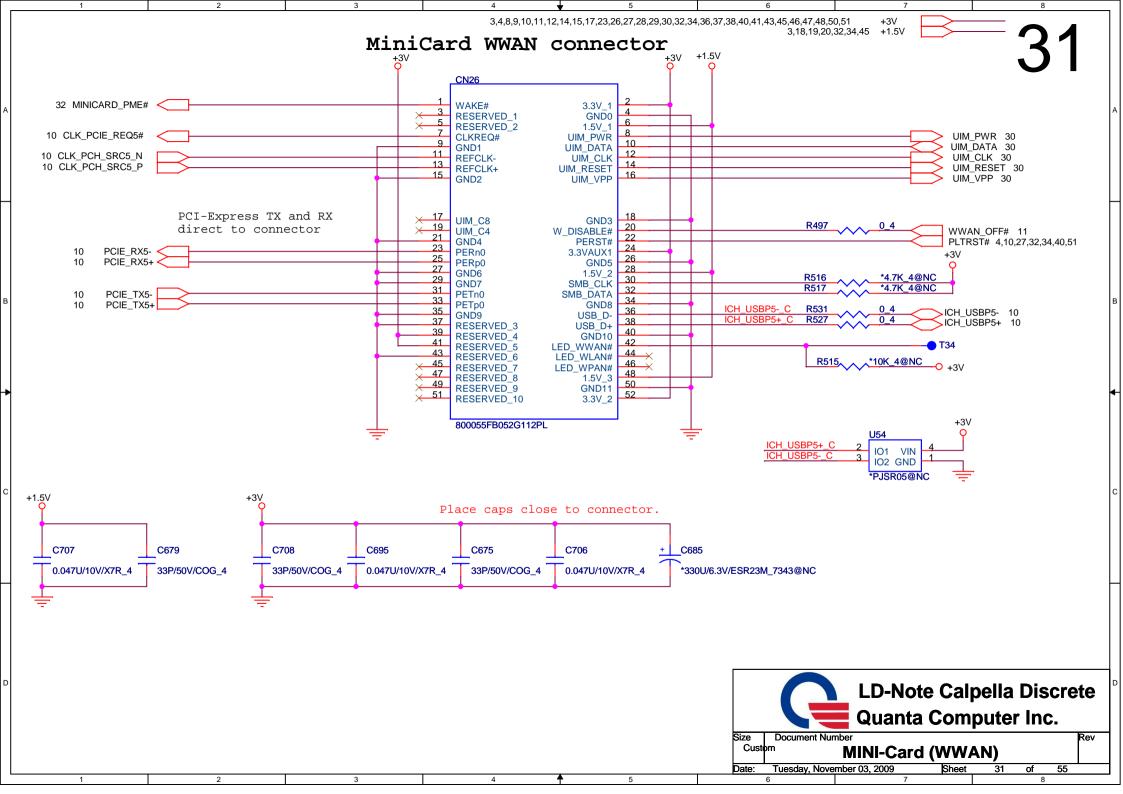






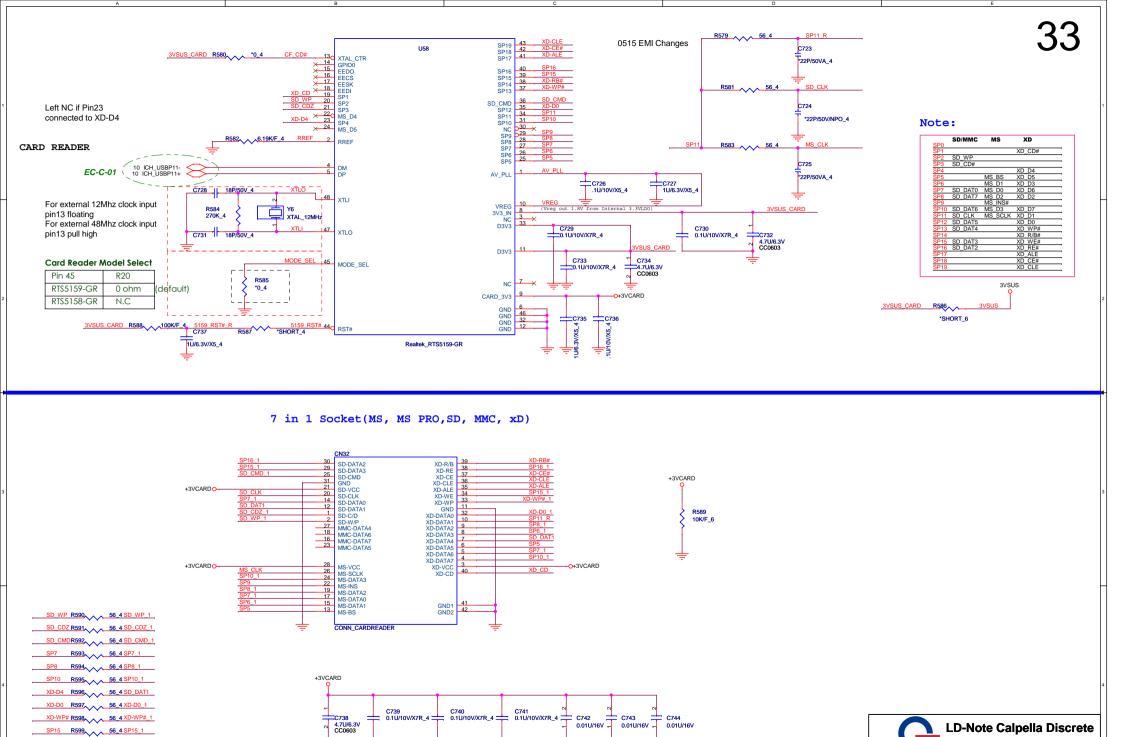






Quanta Computer Inc.

MINI-Card (WIAN)



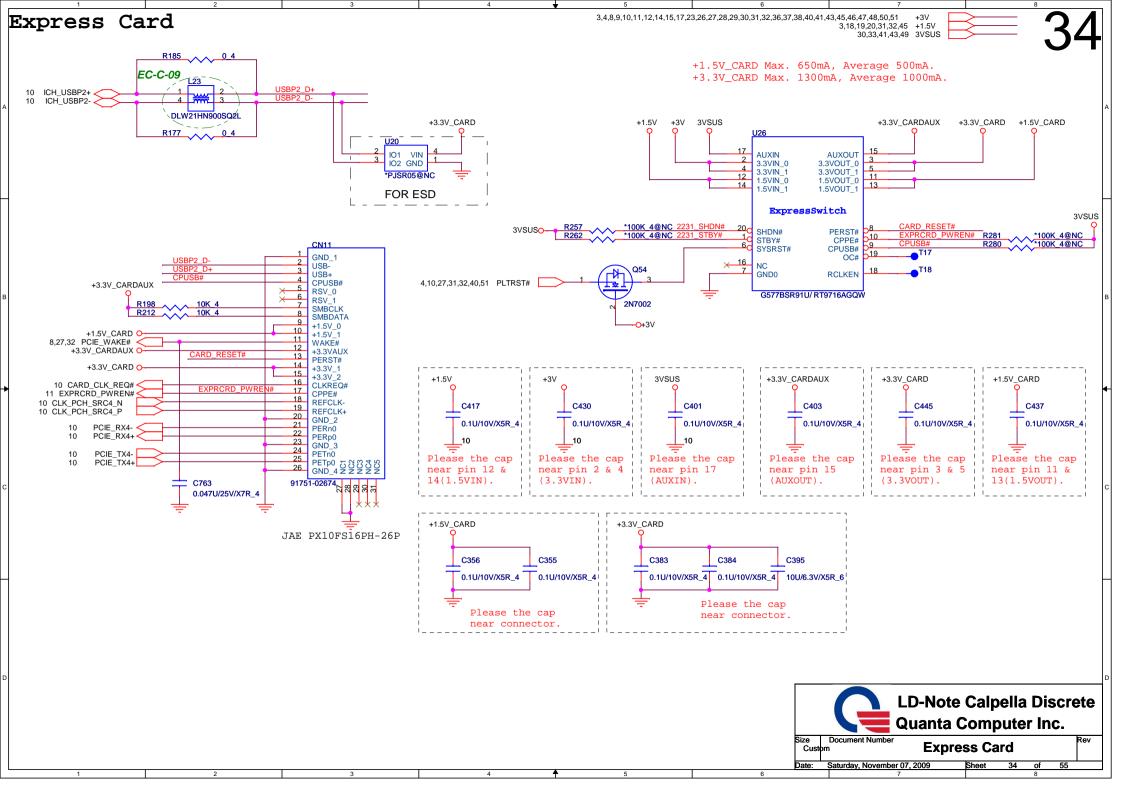
SP16 R600 56_4 SP16_1
SP6 R601 56_4 SP6_1

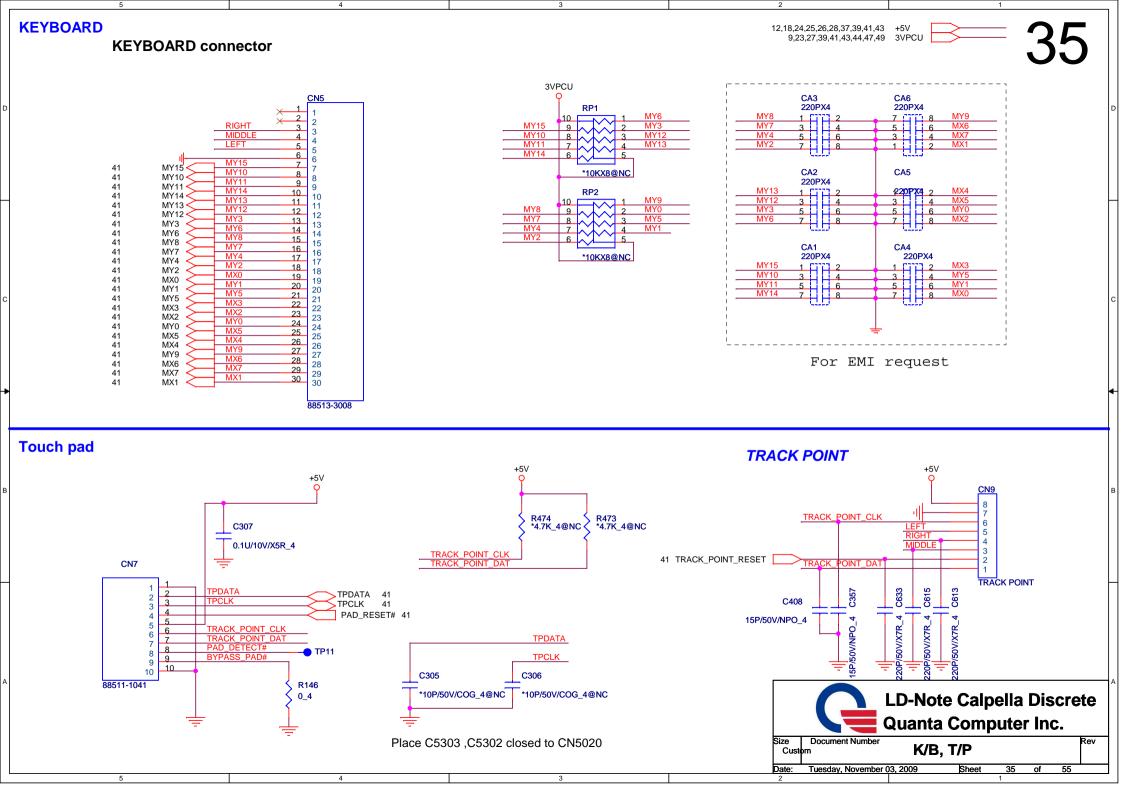
Quanta Computer Inc.

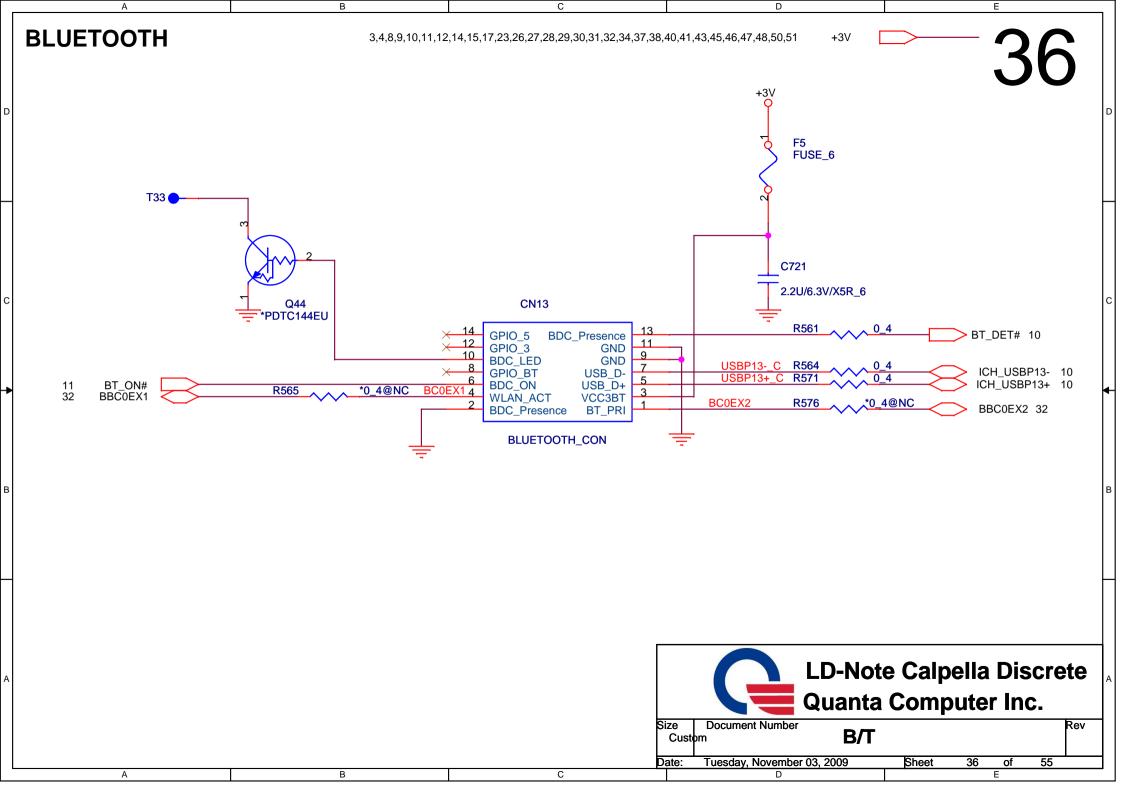
Tuesday, November 03, 2009

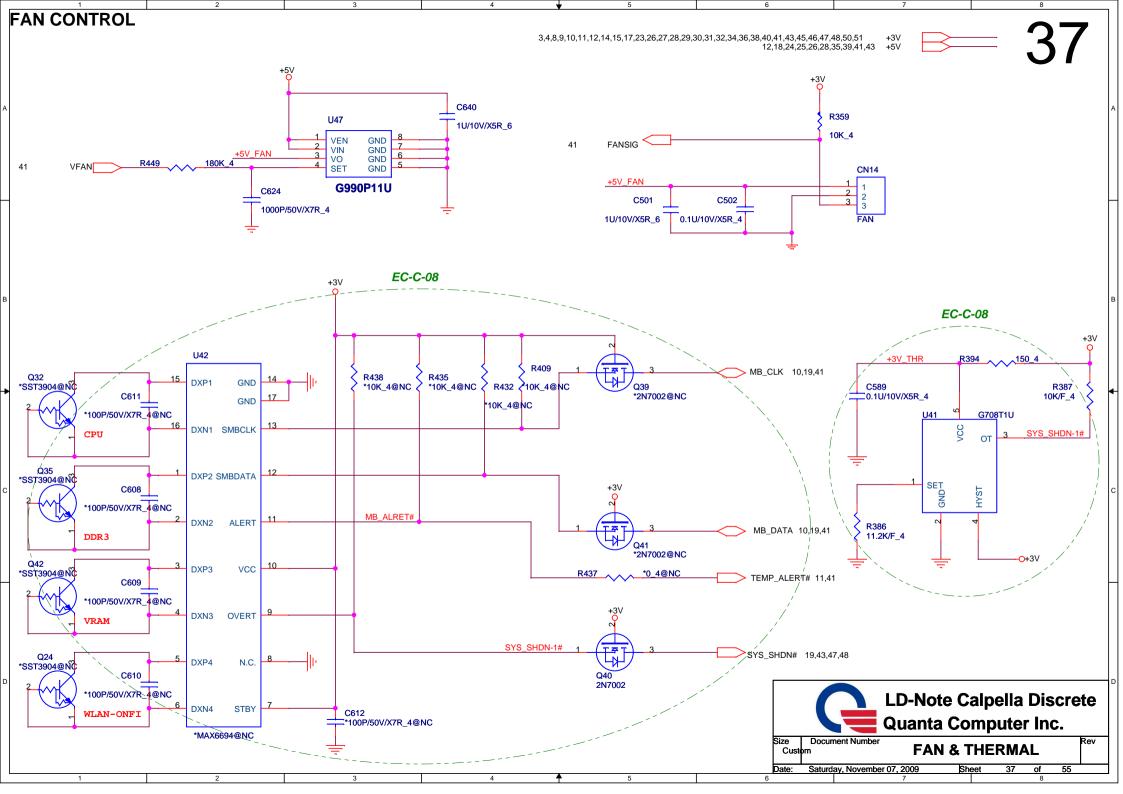
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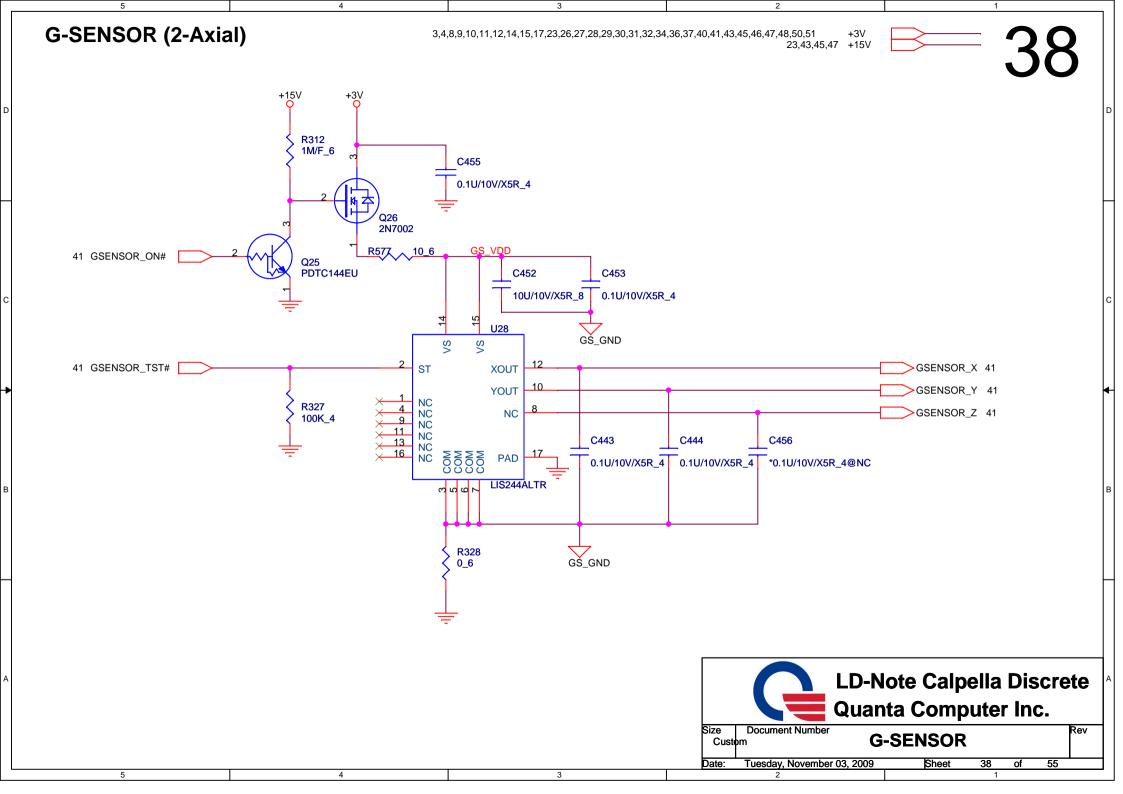
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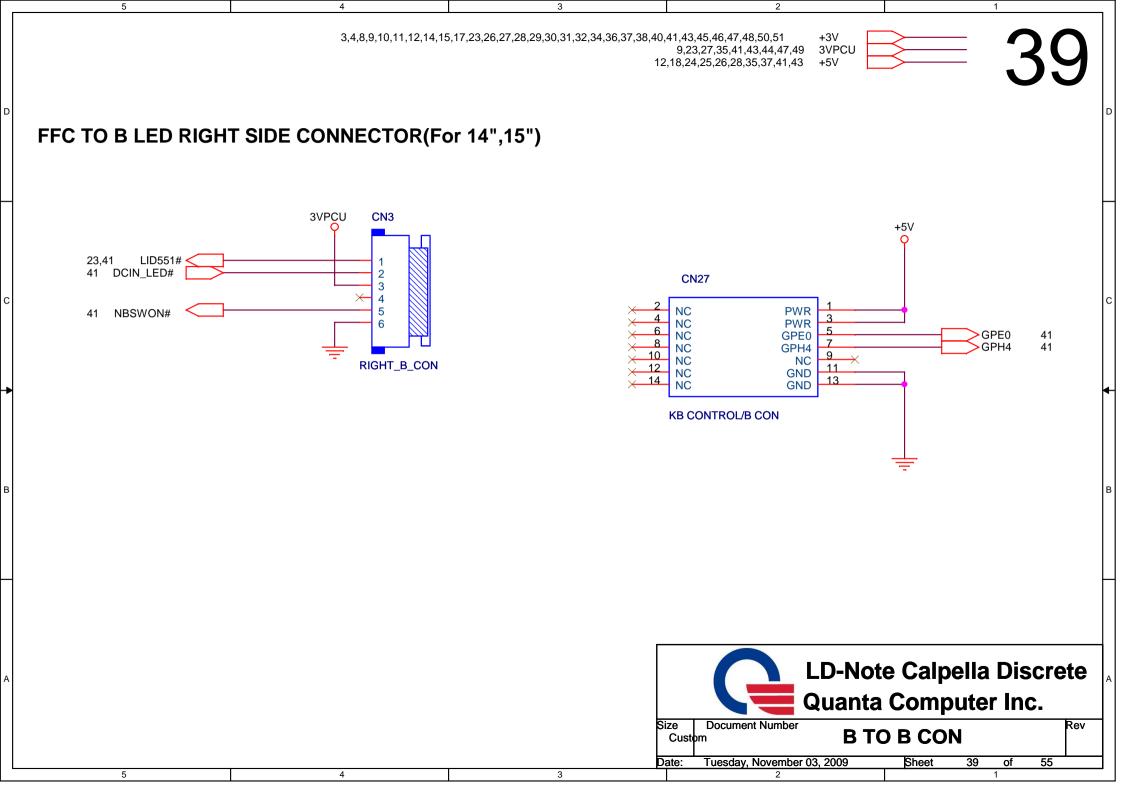


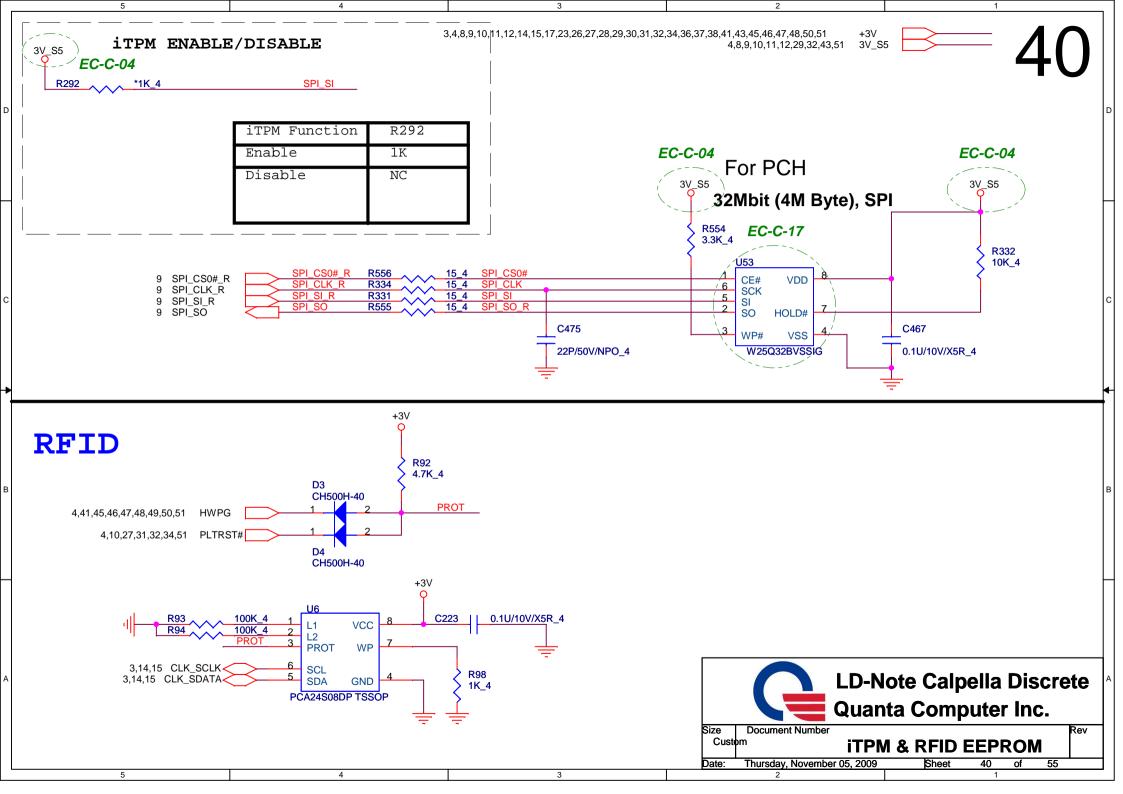


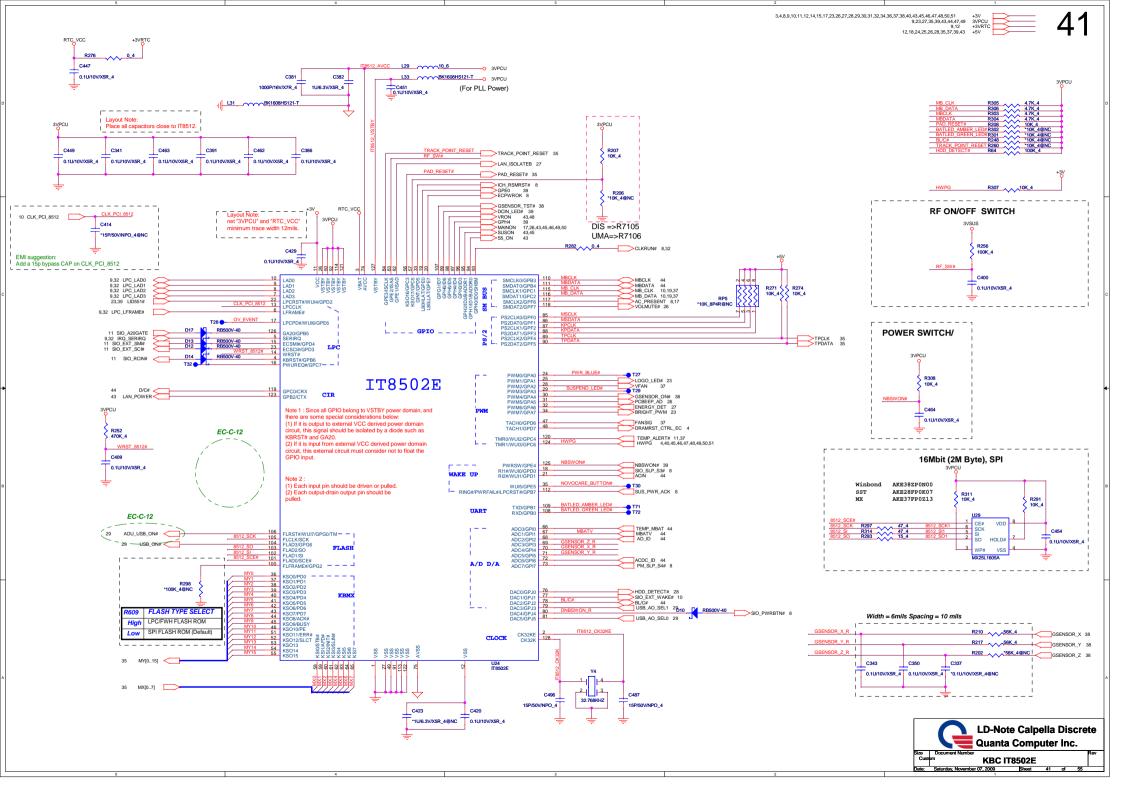


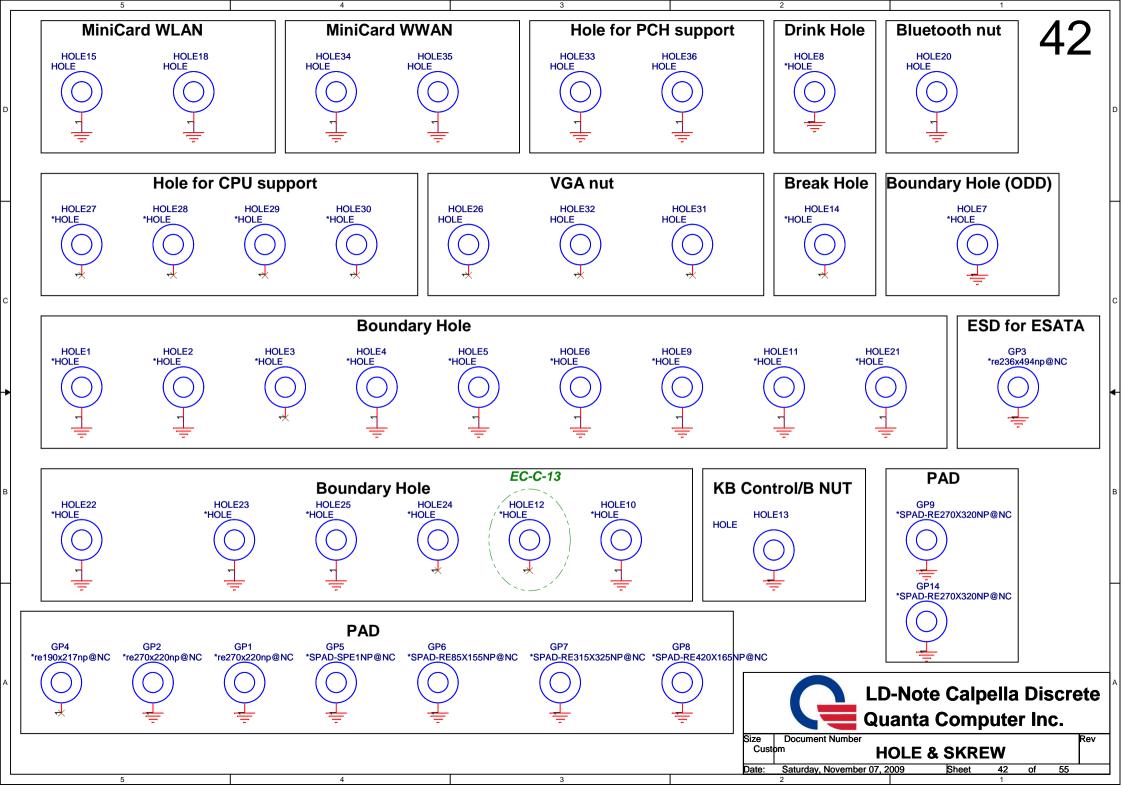


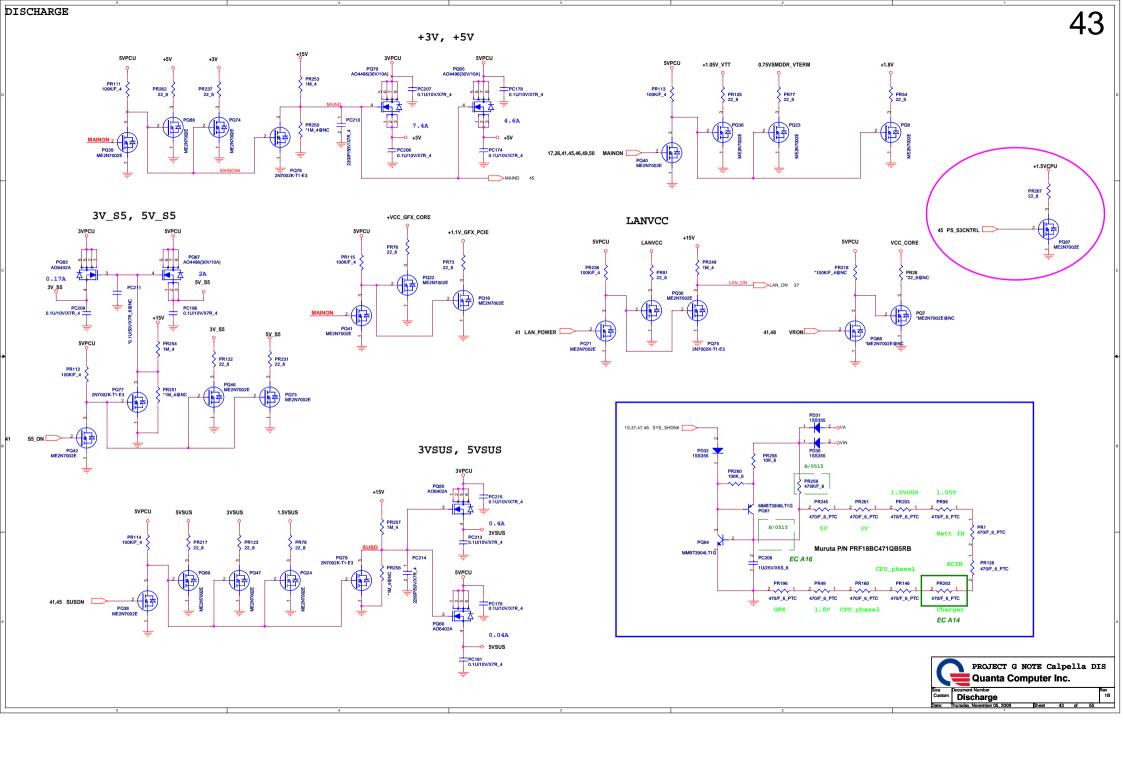


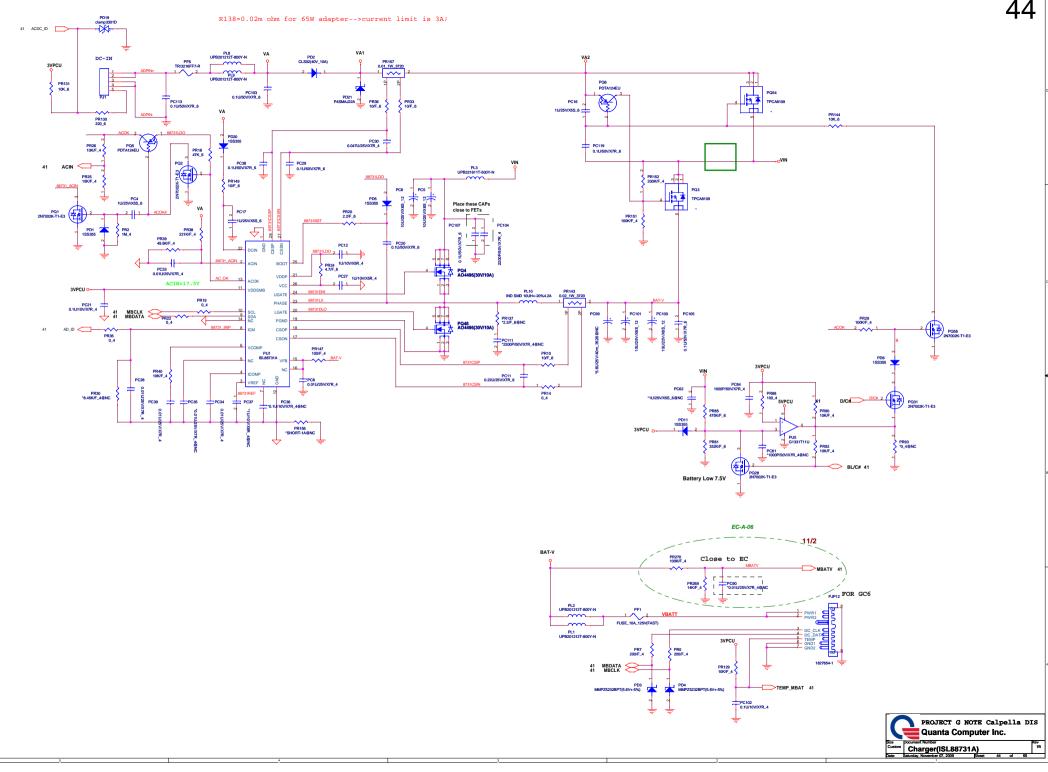


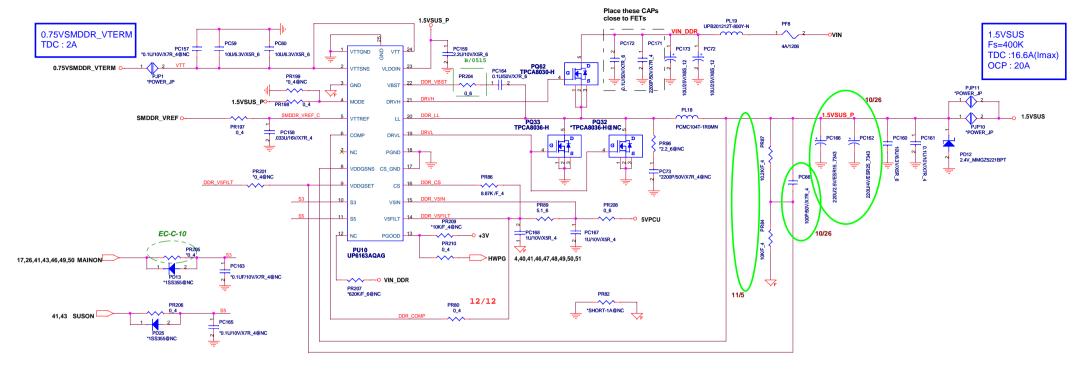


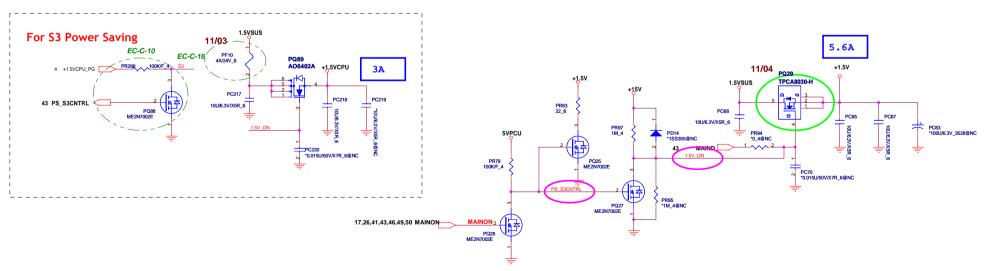


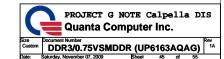


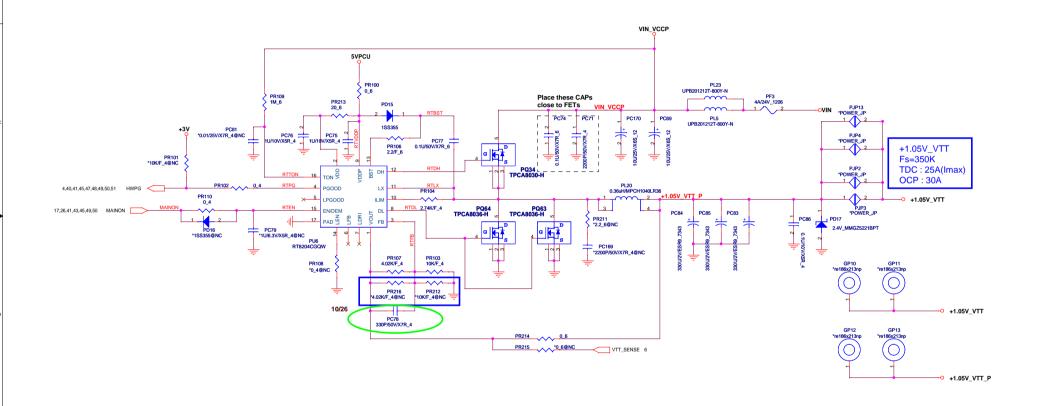


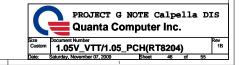


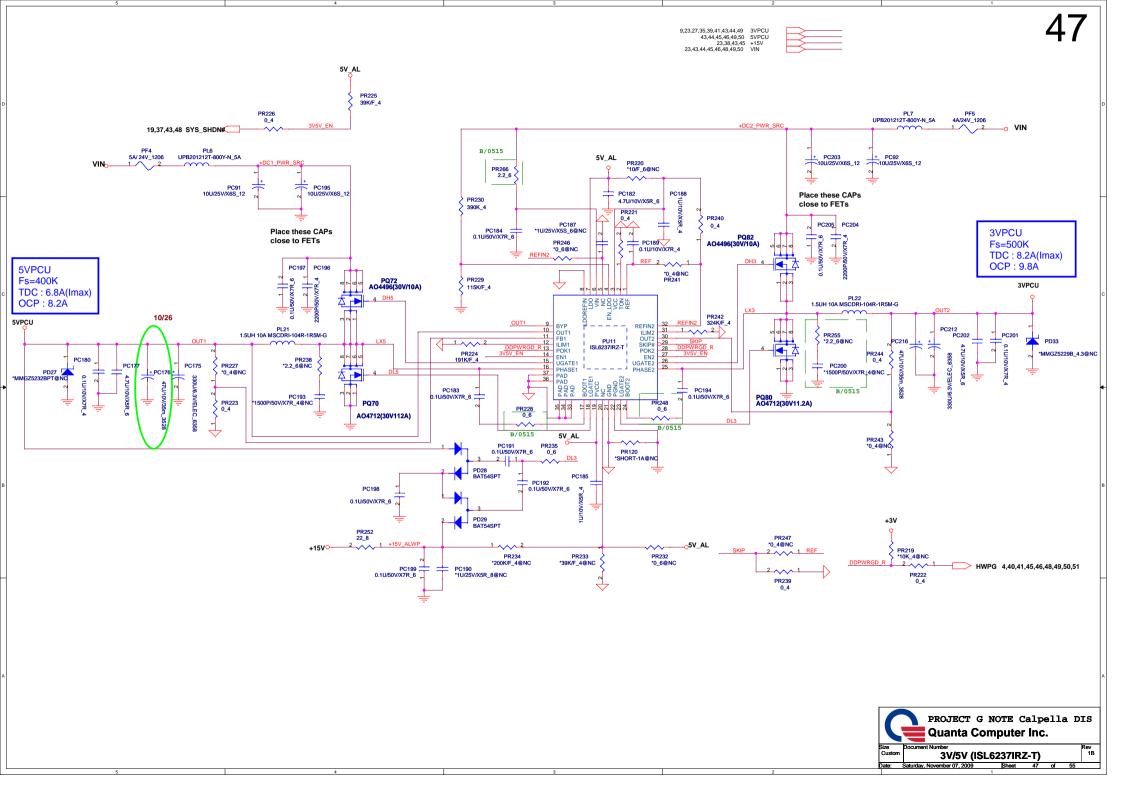


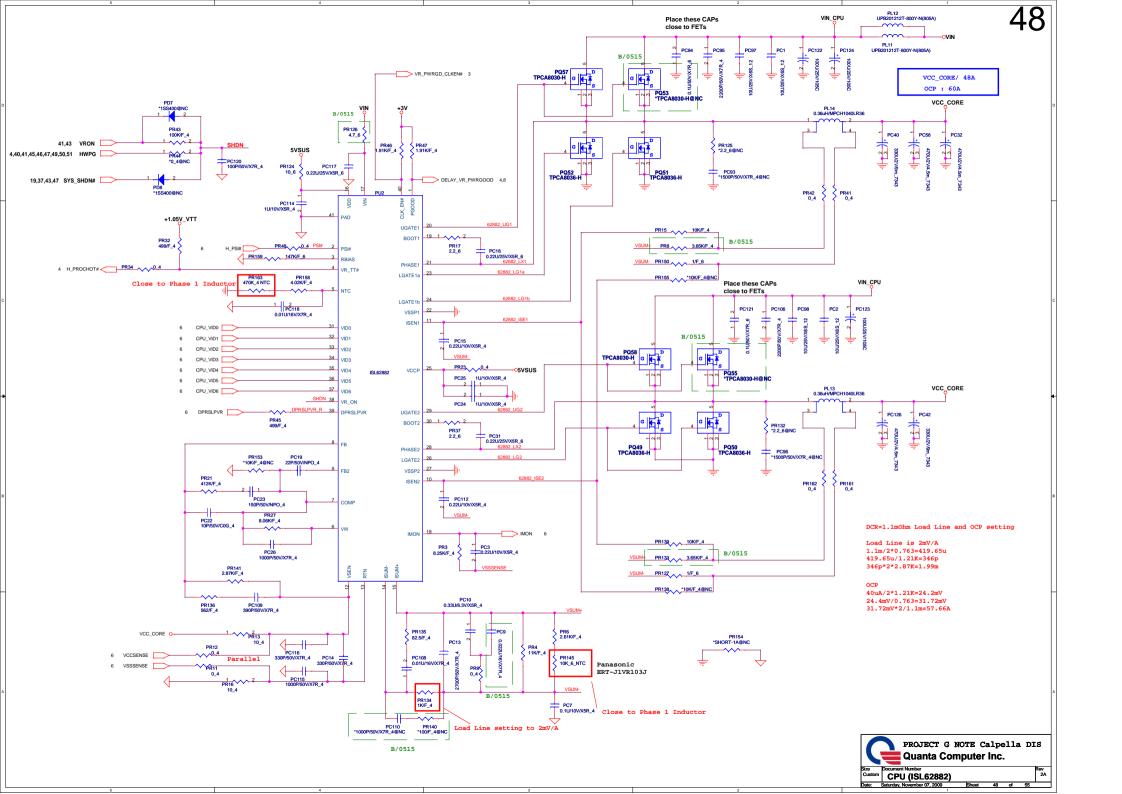


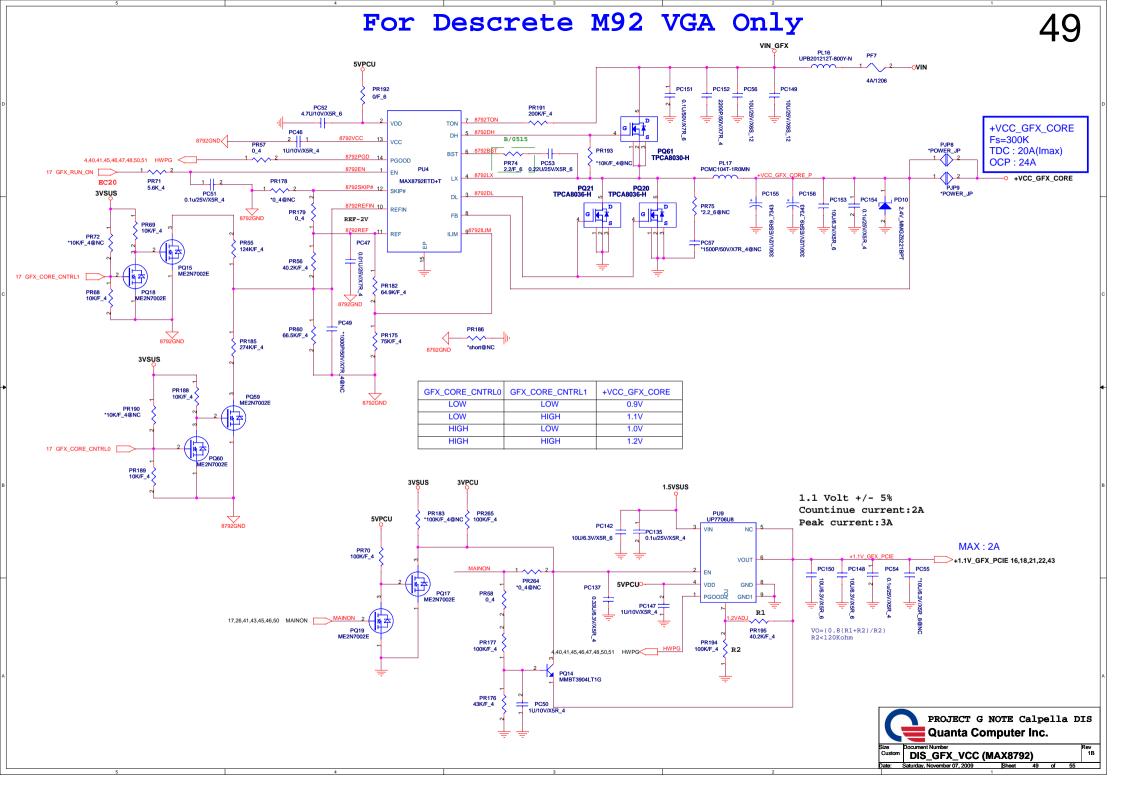


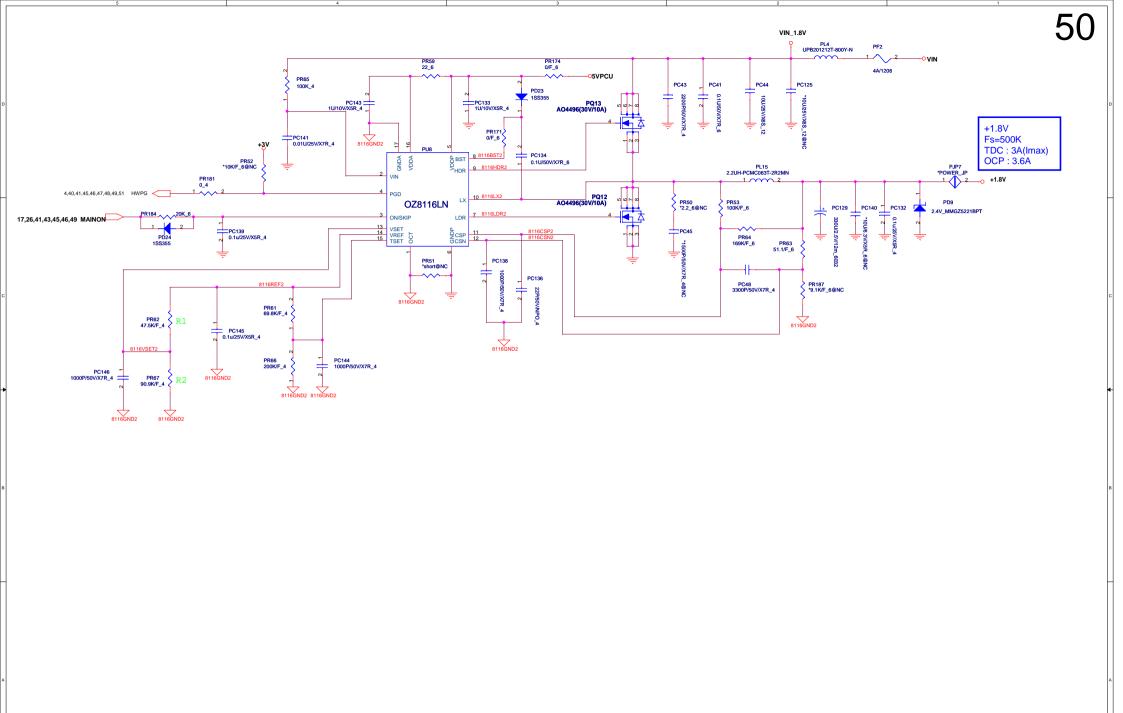






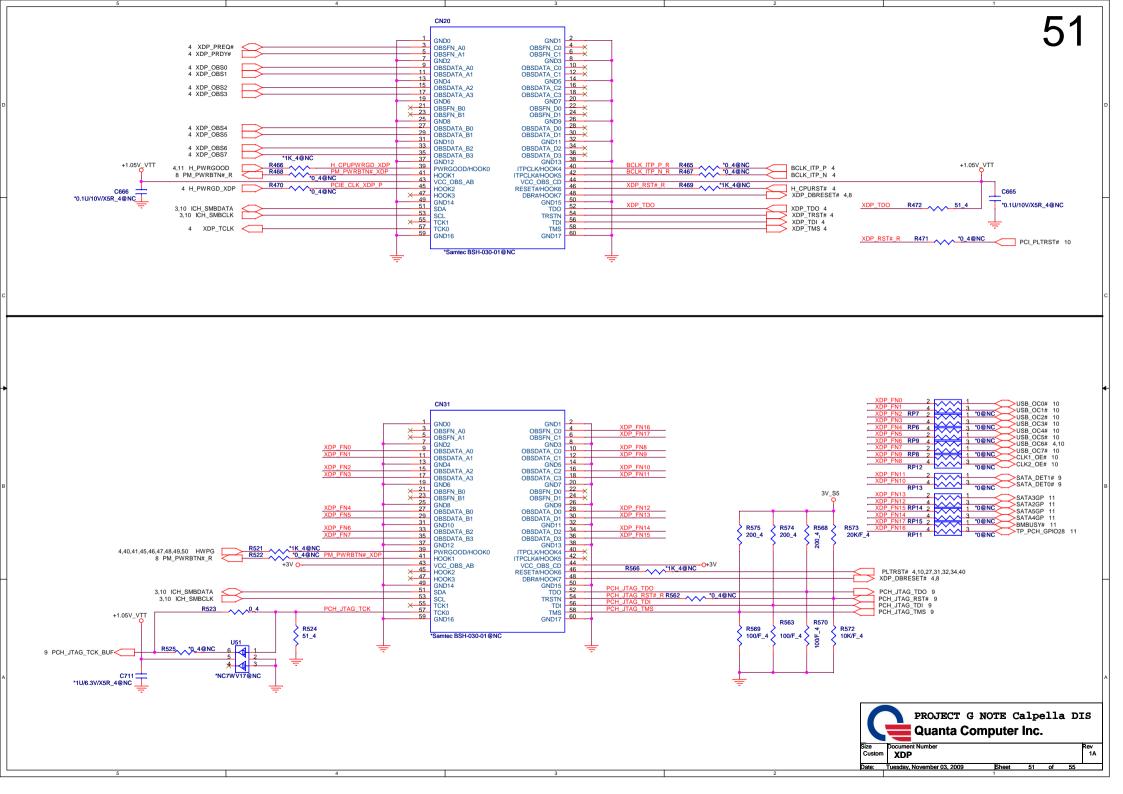






PROJECT G NOTE Calpella DIS
Quanta Computer Inc.

Size
Custom DIS_1.8V_RUN (OZ8116LN)
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Revision History

Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A		DV	Initial release		

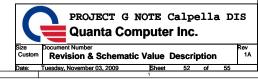
Schematic Value Explanation Description :

RESISTOR

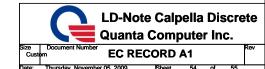
Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005)					DE POP	1K ohm 1% SMD 0402 package and DE POP
1K_6	5%		0603 (1608)				POP	1K ohm 5% SMD 0603 package and POP
1K_8	5%			0805 (2125)			POP	1K ohm 5% SMD 0805 package and POP
1K_12	5%				1206 (3216)		POP	1K ohm 5% SMD 1206 package and POP
1K_1210	5%					1210 (3225)	POP	1K ohm 5% SMD 1210 package and POP

CAPACITOR

Value	Voltage	Material	6		*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005)		DE POP	0.1UF 10V X5R SMD 0402 package DE POP
1U/25V/X7R_6	25V	X7R	0603 (1608)		POP	0.1UF 25V X7R SMD 0603 package POP



EC # Description Part Affected Page EC-C-01 10,33 U58,U50 Change card reader from port6 to port11 EC-C-02 Q52 Change Q52 from MOS to BJT to fix MIC function issue Q37,Q38 Change Q37 to MOS,Q38 to BJT to fix audio function issue U34,C498,C499,C500,L39 Add LDO circuit to fix audio noise; un-stuff 0 ohm RES EC-C-04 40 Change PCH SPI BIOS power supplier from +3V to 3V_S5 EC-C-05 R508 Change PCIE_WAKE# pull high RES to 10K ohm EC-C-06 44 PR269,PR270 Add net MBATV from charger to EC EC-C-07 25 C123 ESD suggest to move C123 EC-C-08 Un-stuff thermal IC MAX6694 relative circuits, suff G708 relative circuits Un-stuff U42,Q32,Q35,Q42,Q24,C611,C608,C609,C610,C612,Q39,Q41,Q40,R432,R409 Stuff U41.C589.R394.R386.R387 EC-C-09 Stuff common choke to fix EMI issue CML1 30 CML3.CML4 34 L23 EC-C-10 Solve S3 run in issue Stuff PR268.PQ88 Un-stuff PR205 EC-C-11 U60 Change black-berry charger IC to MAX14550AEETB+ EC-C-12 U35 Add enable pin AOU_USB_ON# to control black-berry charger function from EC 41 Remove CELL-SET pin and two select RES U24,299,R300 EC-C-13 42 ME modify NON-PTH hole HOLE12 EC-C-14 10 Reserve one 0 ohm RES to moniter leakage current in the future R336 EC-C-15 R628,R629,C762,R630,R347,R348 Reserve codec Rev. VA @ VB option schematic EC-C-16 45 PF10 Add one fuse to prevent from PQ89 burn out EC-C-17 Change PCH BIOS to 4M Byte U53



GC5 / G-Note

Schematic EC Tracking Record A (for DV --> DV1)Mar. 11, 2009