

# STM32MP157 EVAL

MB1262

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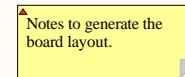
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## Legend

Compared to MB1262B :  
CAN FD & TT supported through MCP2561, NAND 8b, QSPI reset circuitry, SUB\_NRST, SAI2\_B implemented

Text to be added to silkscreen.

Warning text.



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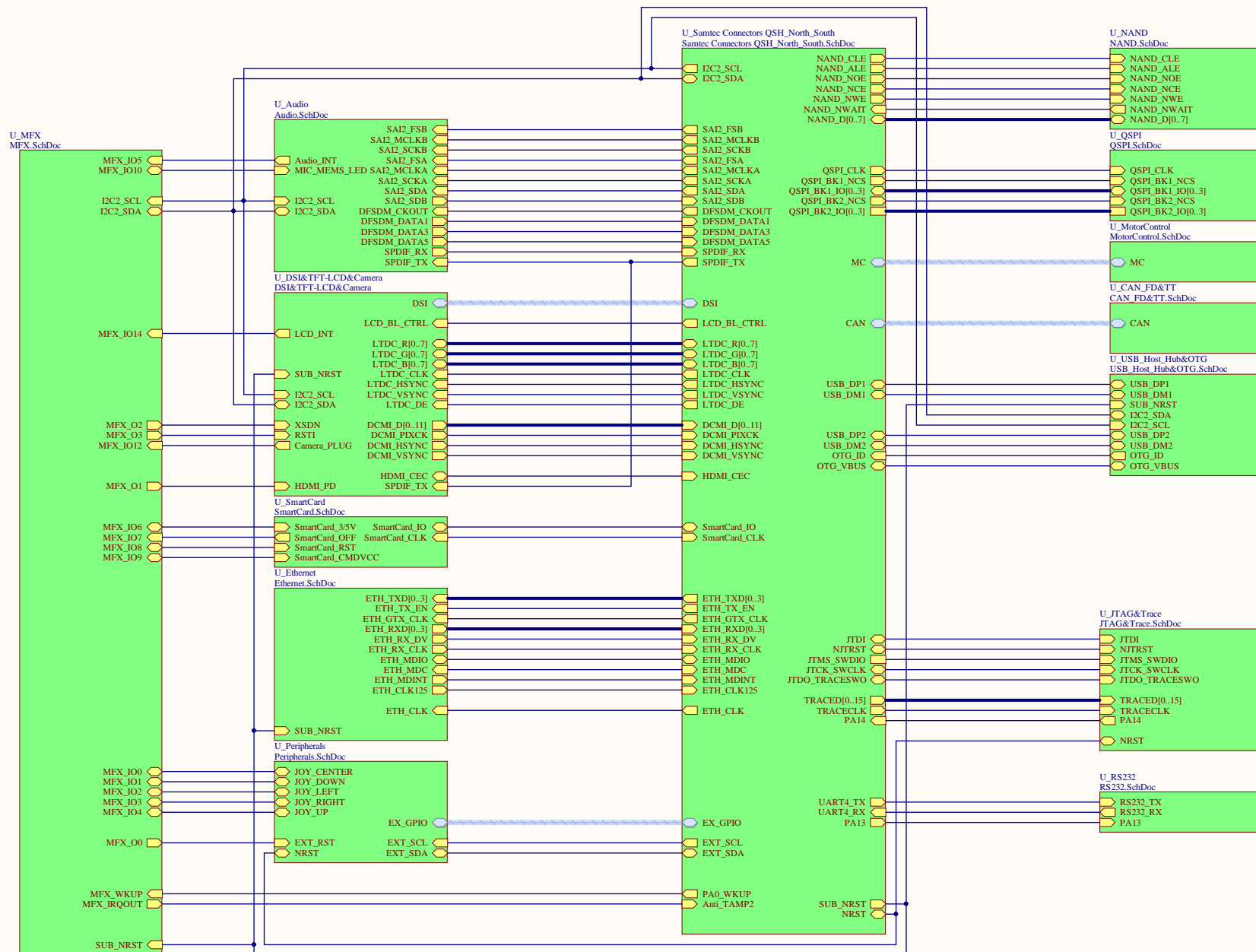
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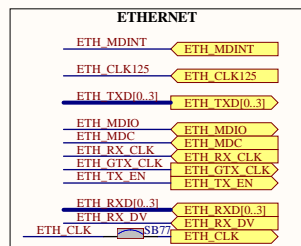
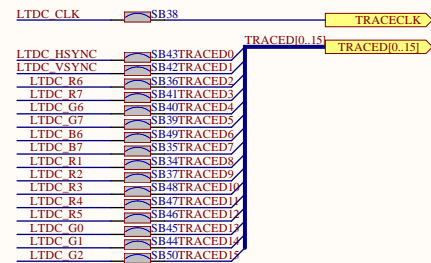
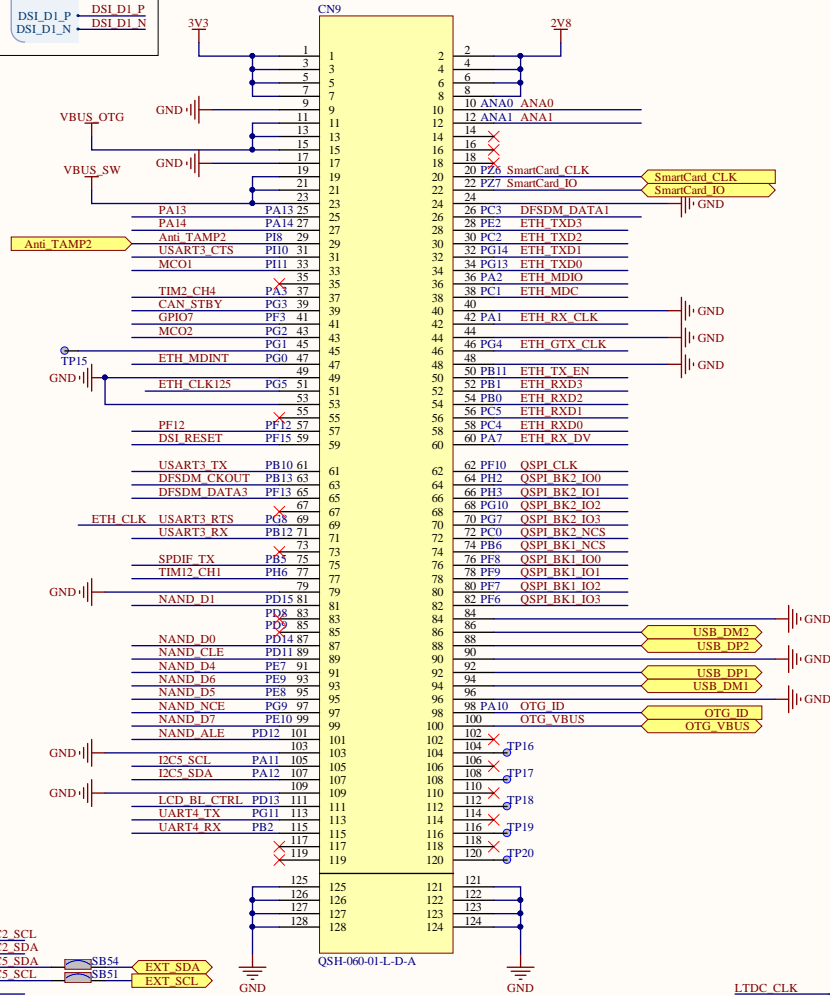
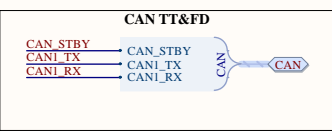
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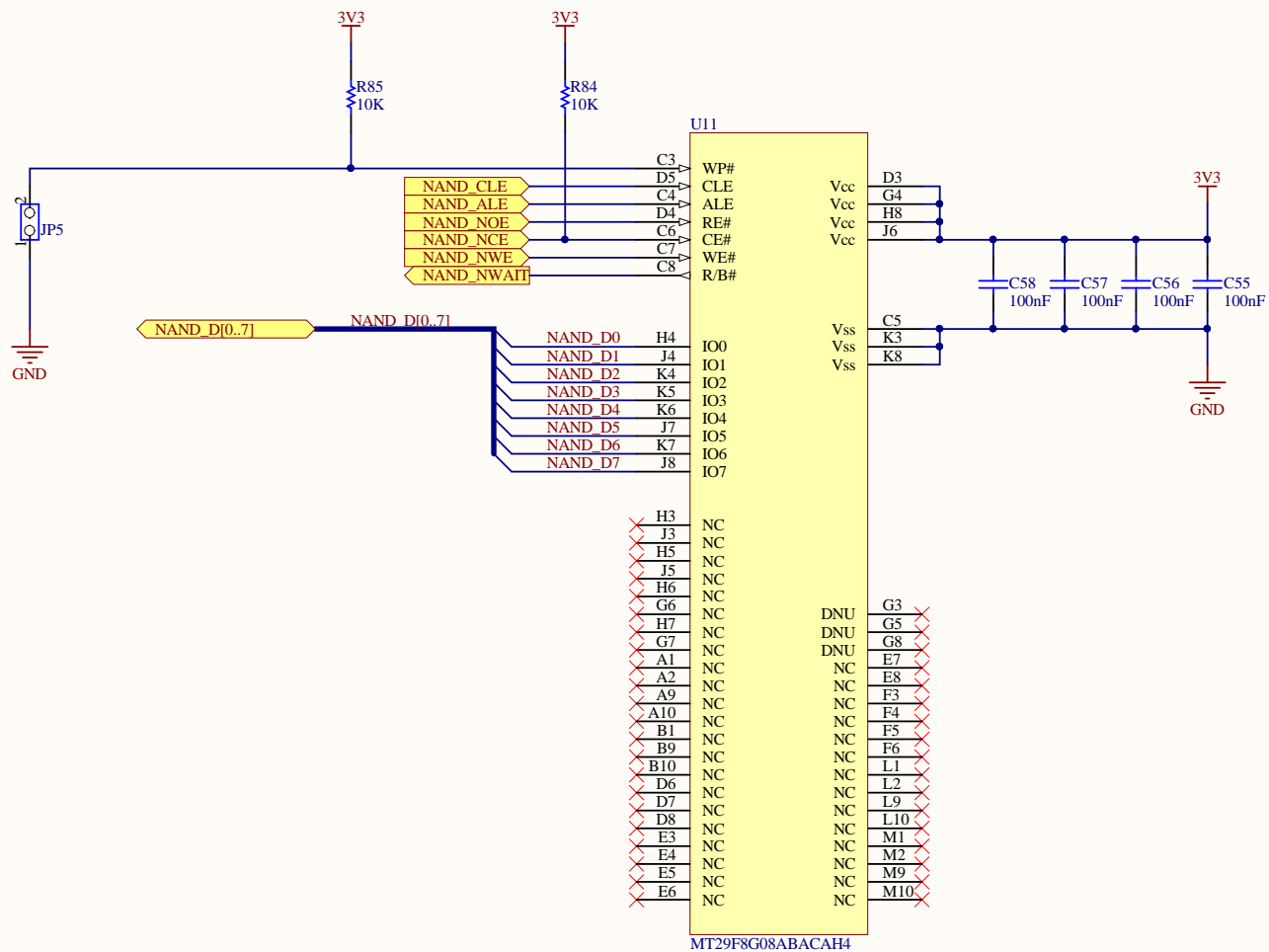
U\_MB1262-TOP  
MB1262-TOP.SchDoc



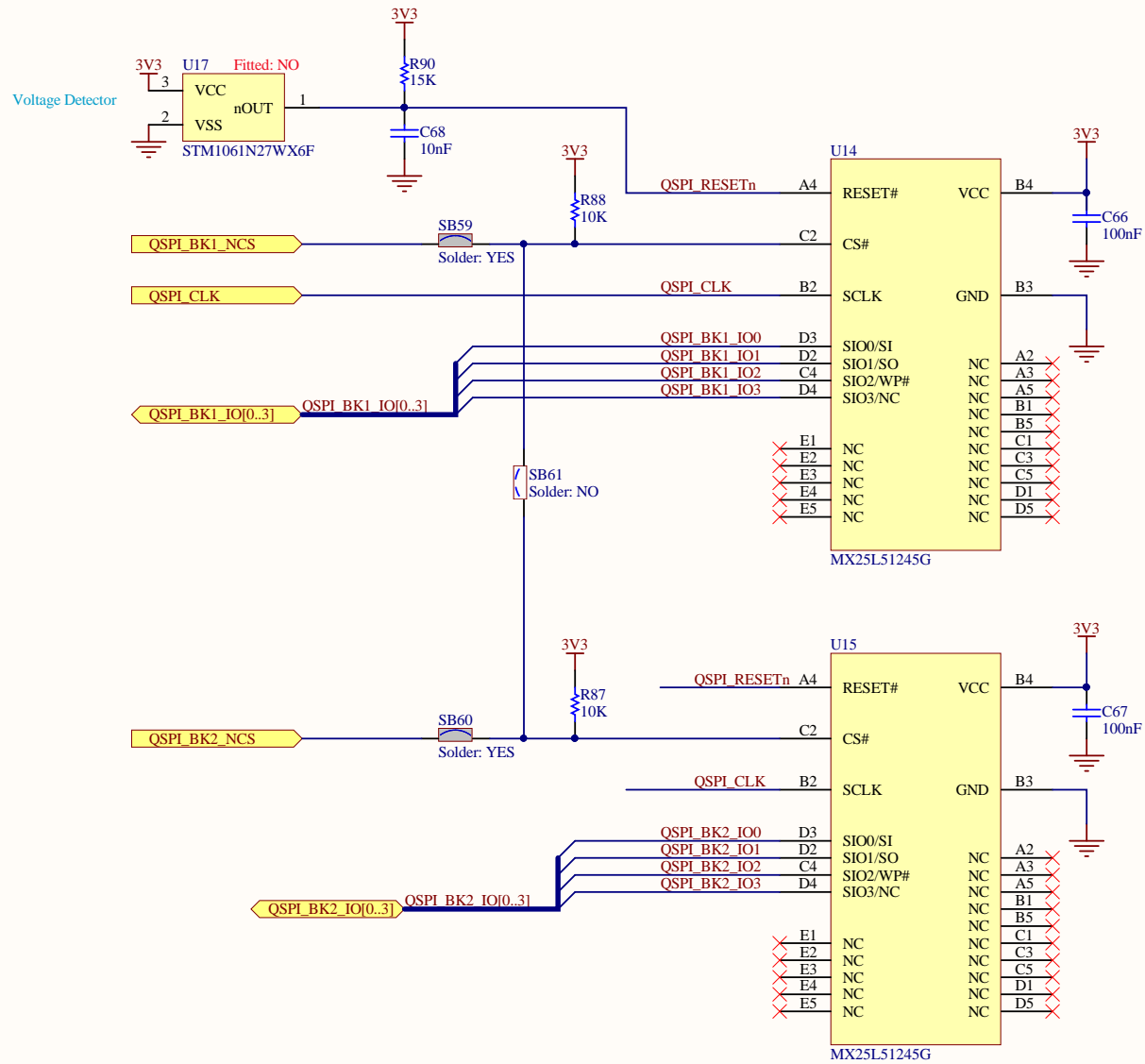
Title: <b>MB1262</b>			 life.aumentec
Project: <b>STM32MP157 EVAL</b>			
Variant: [No Variations]			
Revision: <b>C-01</b>		Reference: <b>MB1262</b>	
Size: <b>A4</b>	Date: <b>12-DEC-18</b>	Sheet: <b>1</b> of <b>17</b>	




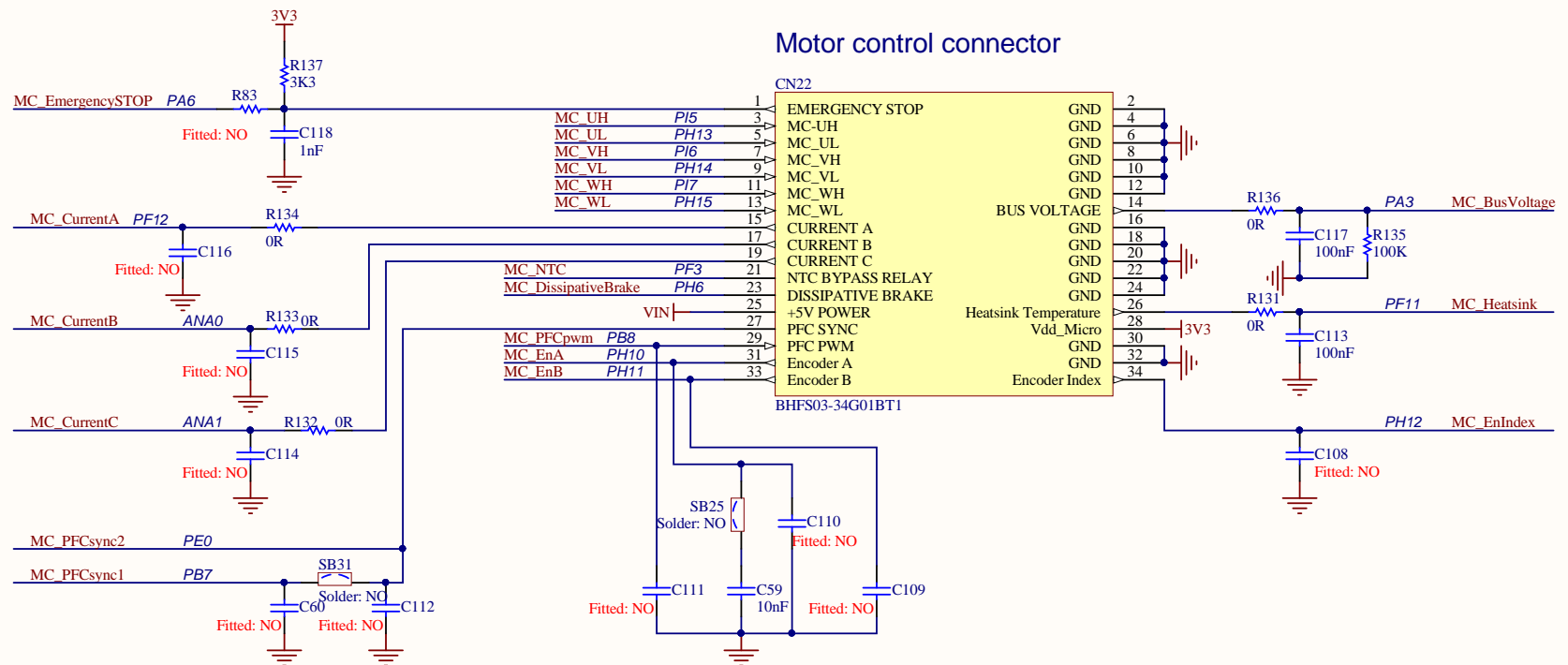
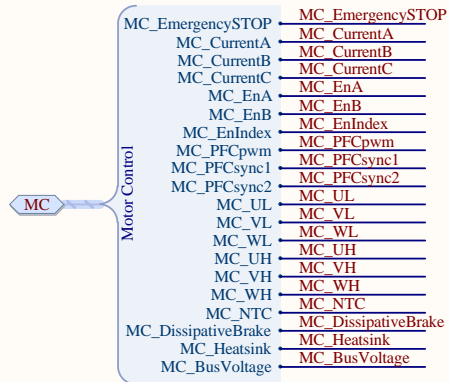


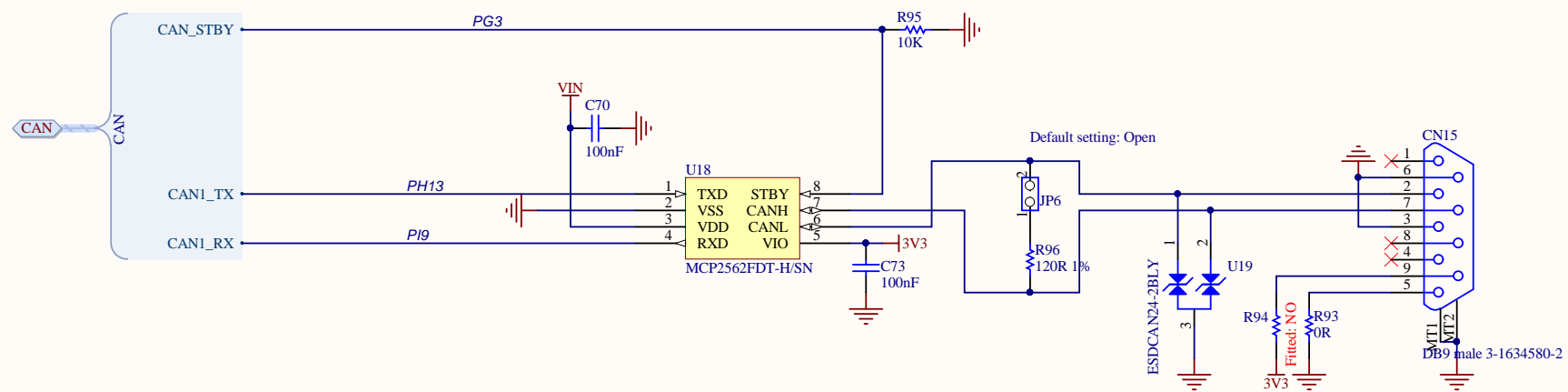


## Quad SPI Flash



Title: <b>QSPI</b>			 life.augmented
Project: <b>STM32MP15x-EVAL</b>			
Size: <b>A4</b>	Reference: <b>MB1262</b>	Revision: <b>B-01</b>	
Date: <b>3/26/2018</b>		Sheet: <b>5</b> of 17	

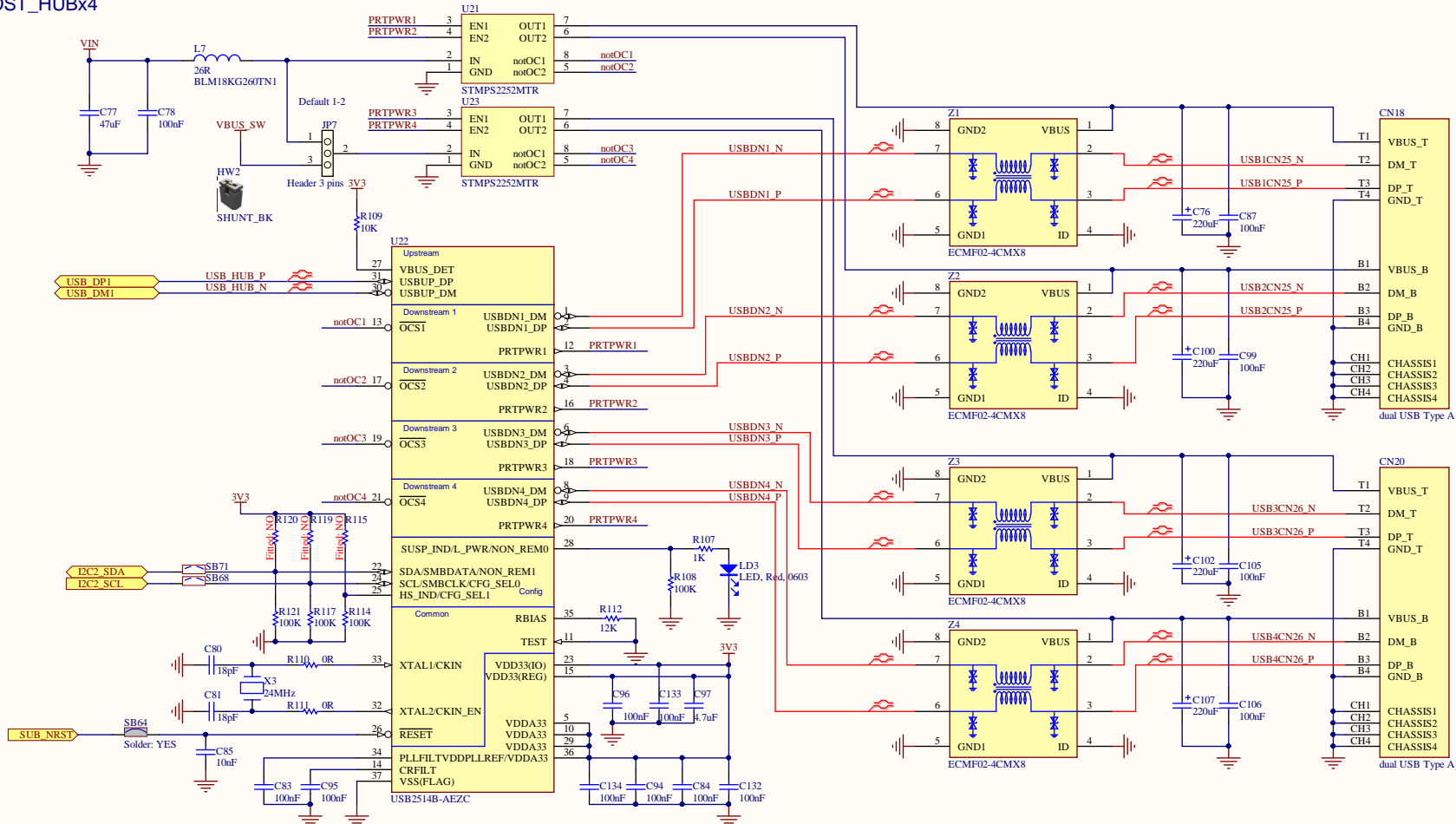




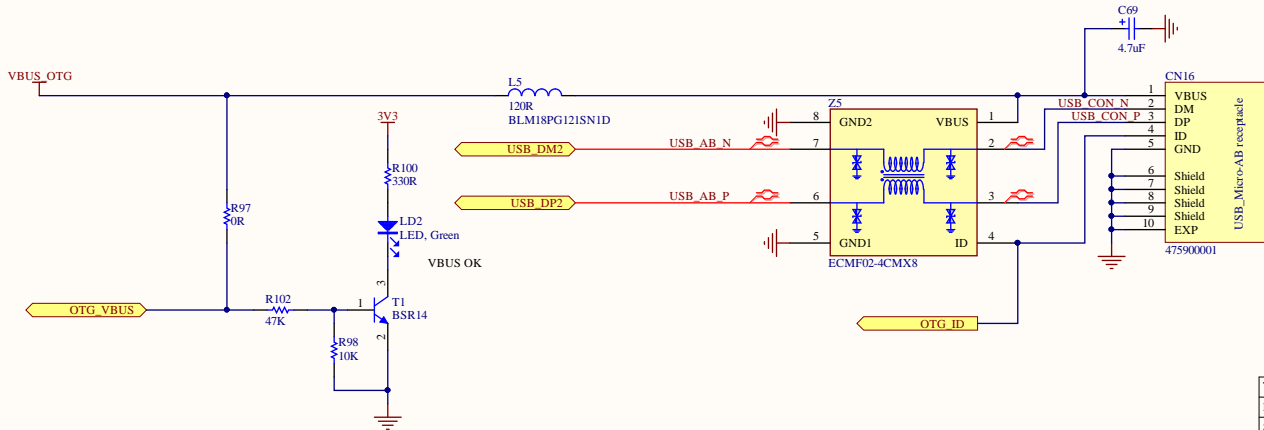
Title: <b>CAN FD&amp;TT</b>		
Project: <b>STM32MP15x-EVAL</b>		
Size: <b>A4</b>	Reference: <b>MB1262</b>	Revision: <b>C-01</b>
Date: <b>5/18/2018</b>	Sheet: <b>7</b> of <b>17</b>	



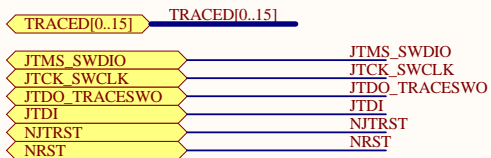
## USB\_HOST\_HUBx4



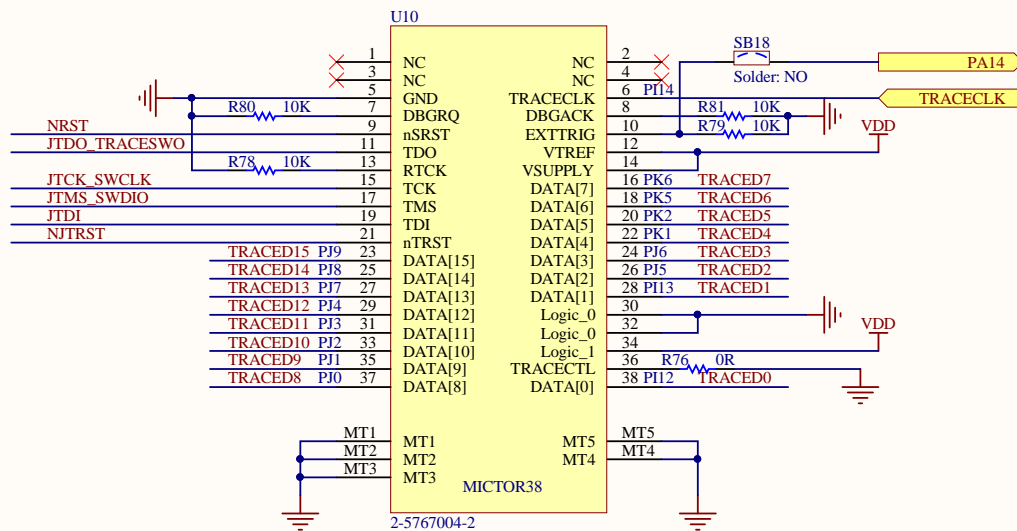
## USB\_OTG



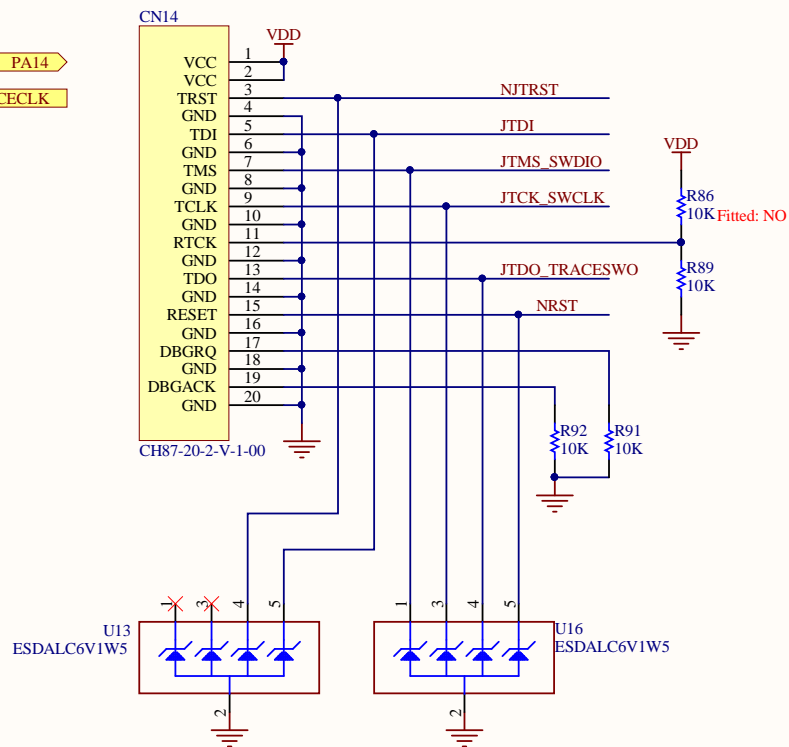




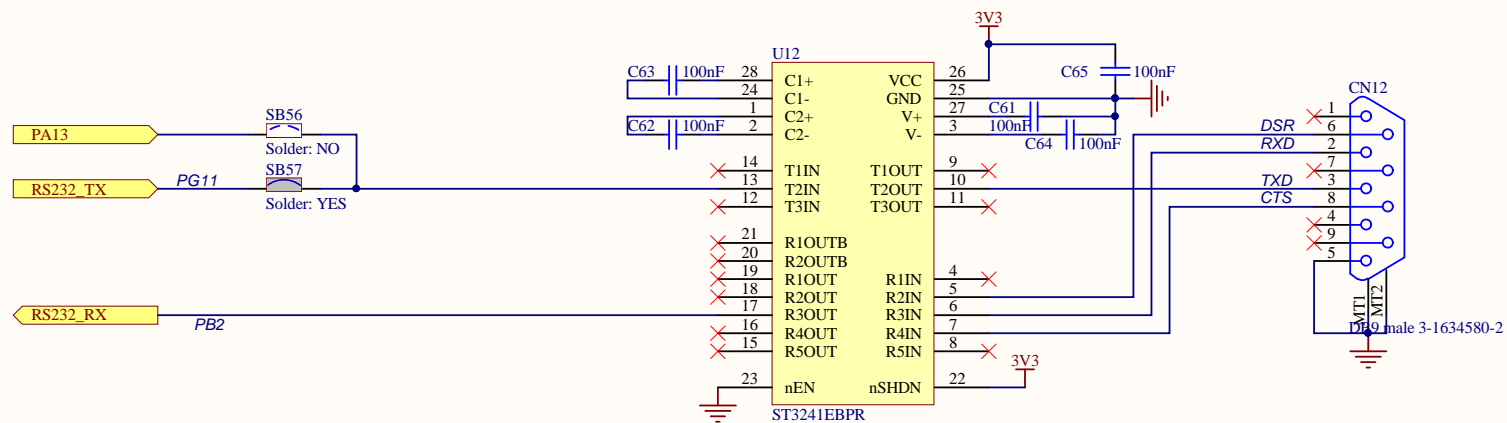
## Trace connector



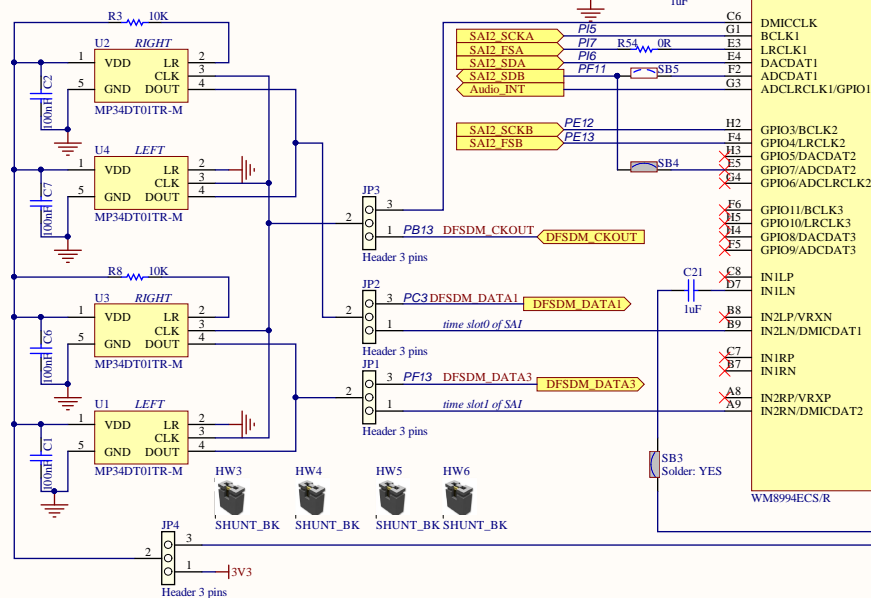
## JTAG connector



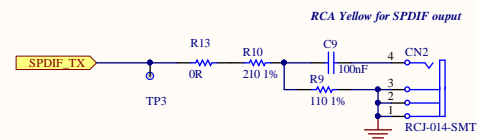
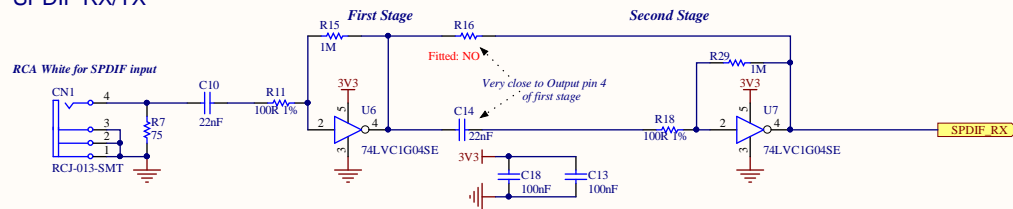
## RS232




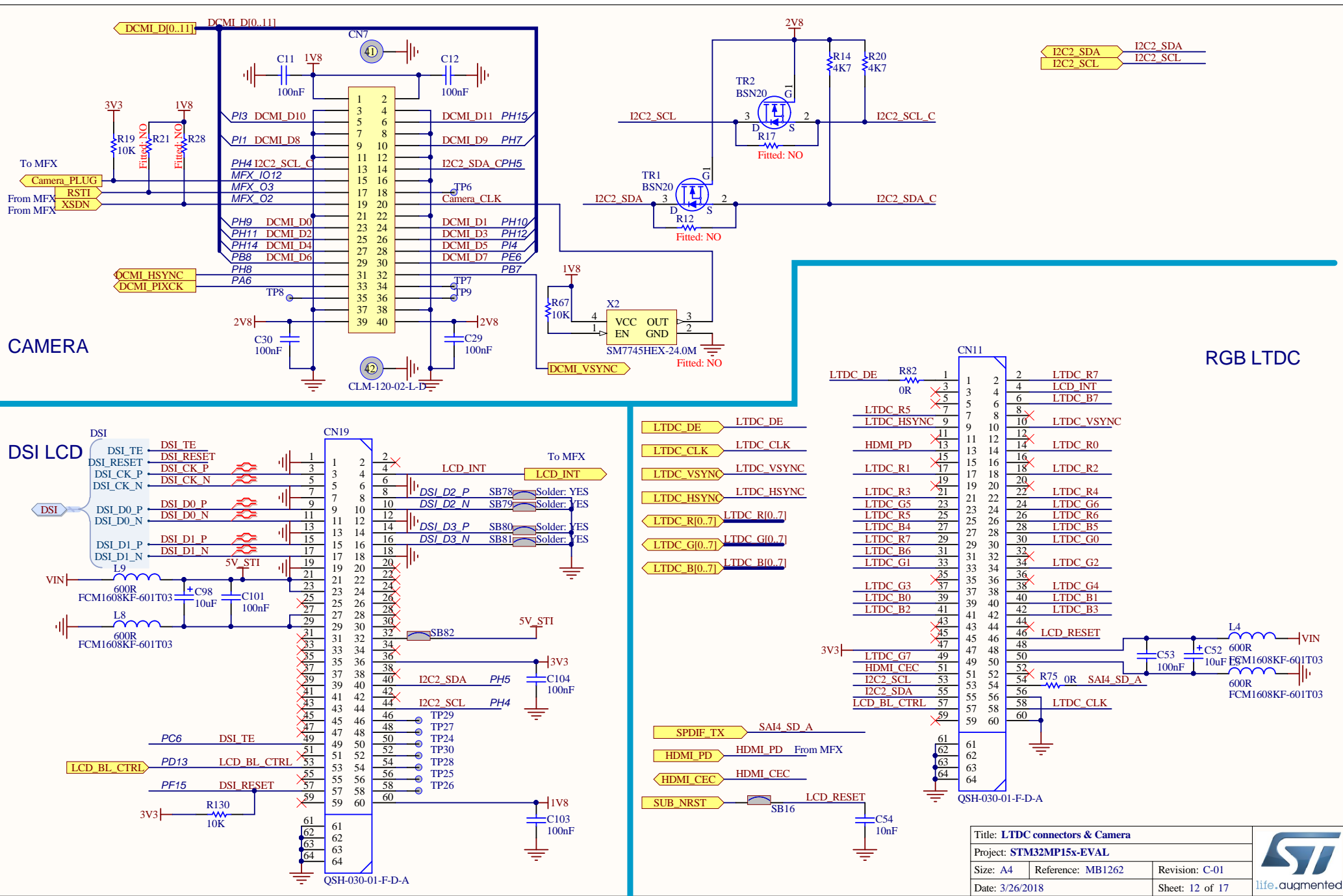
Pin connection diagram for the 10X2 TH VT 1.27mm receptacle. The diagram shows a 20-pin connector with pins numbered 1 to 20. Pin 1 is connected to SB2. Pin 2 is connected to 3V3. Pin 3 is connected to DFSDM\_CKOUT. Pin 4 is connected to DFSDM\_DATA3. Pin 5 is connected to DFSDM\_DATA5. Pin 6 is connected to DFSDM\_CKOUT. Pin 7 is connected to DFSDM\_DATA1. Pin 8 is connected to DETECTa. Pin 9 is connected to MIC\_MEMS\_LED. Pin 10 is connected to TP2. Pin 11 is connected to 3V3. Pin 12 is connected to 3V3. Pin 13 is connected to 3V3. Pin 14 is connected to 3V3. Pin 15 is connected to 3V3. Pin 16 is connected to 3V3. Pin 17 is connected to 3V3. Pin 18 is connected to 3V3. Pin 19 is connected to 3V3. Pin 20 is connected to 3V3. The diagram also shows a 3V3 supply and a ground connection.

[illegible]

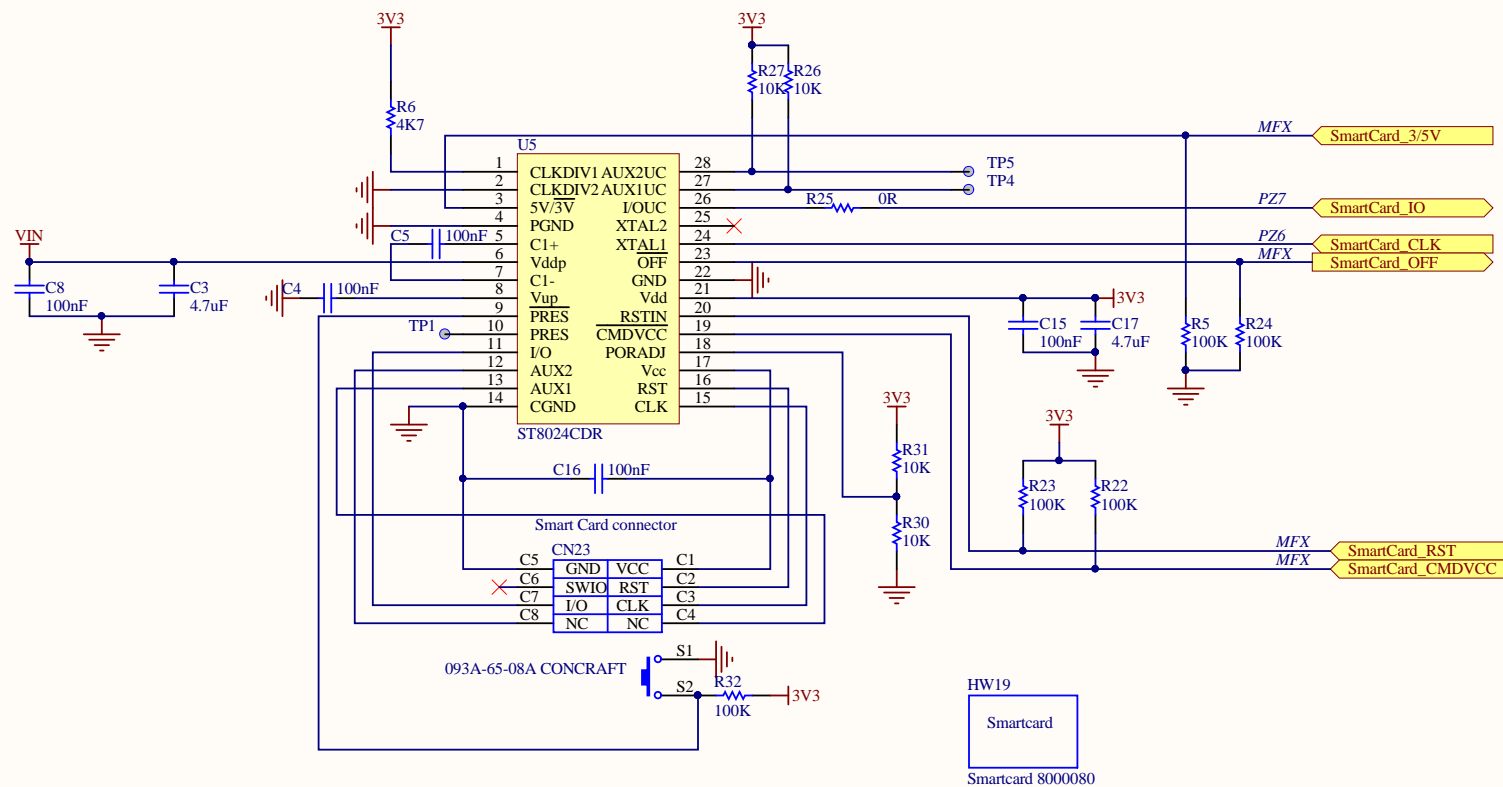
*RCA White for SPDIF input*

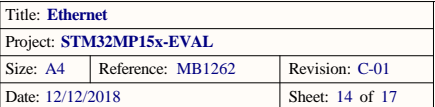


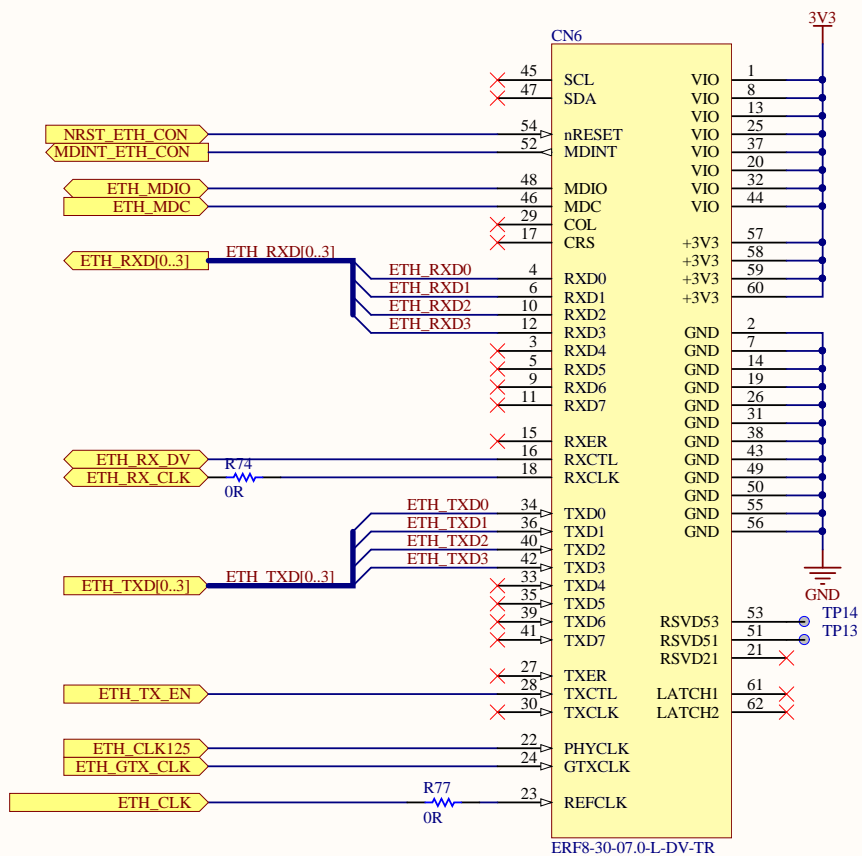
Title: <b>Audio</b>			 <b>life.augmented</b>
Project: <b>STM32MP15x-EVAL</b>			
Size: <b>A3</b>	Reference: <b>MB1262</b>	Revision: <b>C-01</b>	
Date: <b>12/12/2018</b>		Sheet: <b>11 of 17</b>	

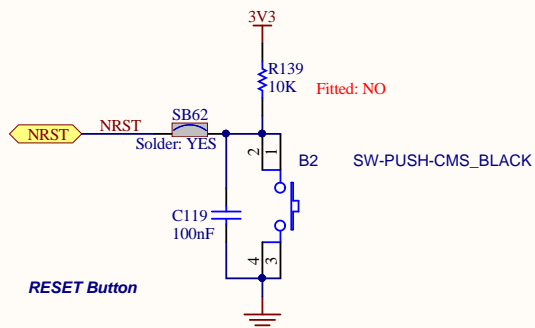


## SmartCard

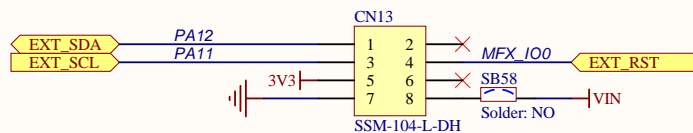




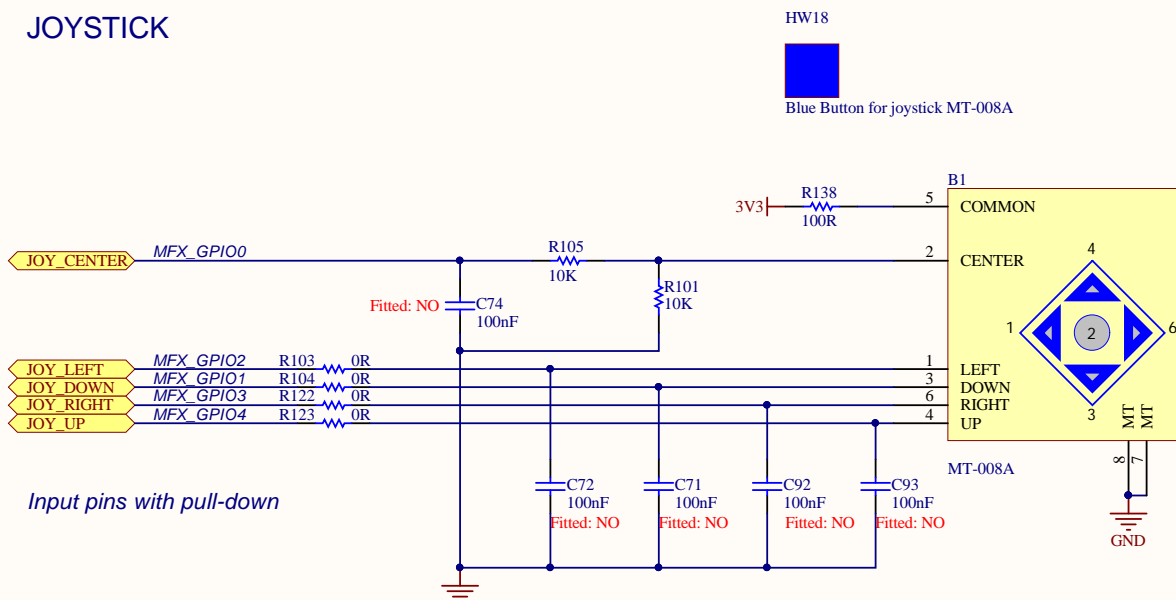




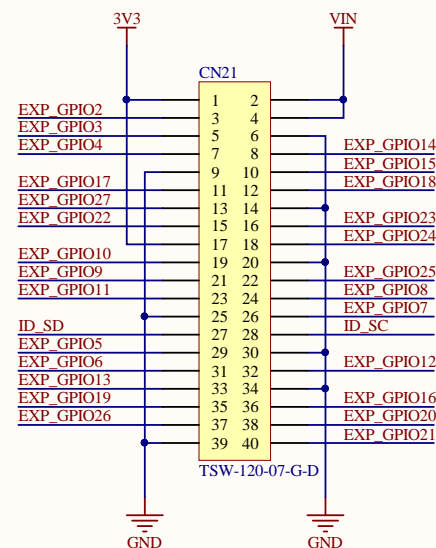
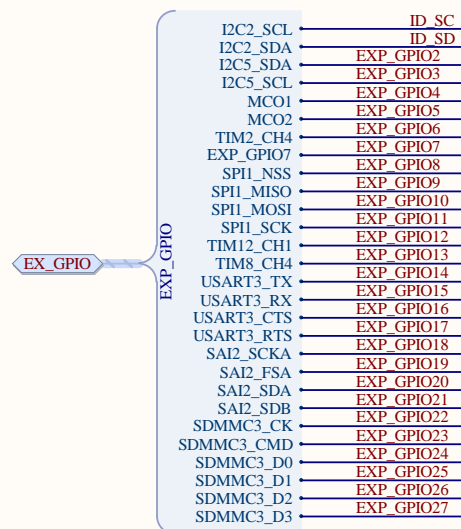
## EXT E2P Connector



## JOYSTICK

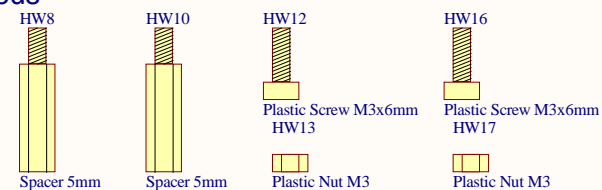


## GPIO EXPANSION

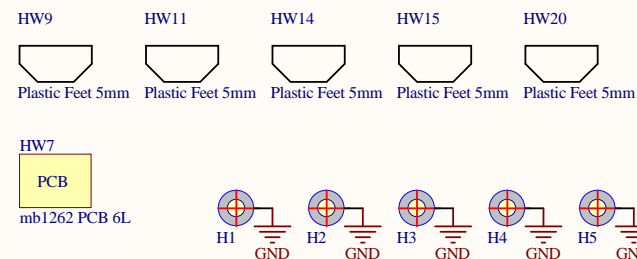


## Miscellaneous

### LCD



### Board



Title: <b>Peripherals</b>		
Project: <b>STM32MP15x-EVAL</b>		
Size: A4	Reference: MB1262	Revision: C-01
Date: 12/12/2018	Sheet: 16 of 17	





