

# QIAO KANG

<https://qiaokang.org>

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## EDUCATION

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<b>Rice University, USA</b> MS in Computer Science (early termination of PhD program)	Aug 2018 - Dec 2020
<b>Beihang University, China</b> MS in Software Engineering	Sep 2014 - Jun 2017
<b>Beihang University, China</b> BS in Software Engineering	Sep 2010 - Jun 2014

## WORK EXPERIENCE

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<b>OS kernel (ESXi) developer, VMware</b>	April 2017 - Jun 2018
My main job was Network Interface Card (NIC) driver development in the ESXi kernel: 1) Developed an ESXi NIC driver for the Virtio virtualization framework from scratch; it enables ESXi to run on top of the KVM hypervisor's Virtio virtual NIC. The project took 9 months and consists of 8000 LoC. 2) Collaborated with vendors from India to maintain the Solarflare 10G/40G NIC drivers.	

## SKILLS

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Proficient in C programming, familiar with C++, Python and Shell  
Familiar with OS kernel development, especially OS NIC drivers  
Familiar with P4-based network data plane programming

## RESEARCH EXPERIENCE

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At Rice University, my research work centers around emerging network hardware, such as P4-programmable switches and SmartNICs. My first two projects (Poise and NetWarden) seek to leverage P4 switches to build more secure networks. My third project (P4wn) contributes a novel profiling tool for stateful P4 programs. My fourth project (Clara) aims at providing performance prediction for SmartNIC offloading.

<b>Clara: Performance Clarity for SmartNIC Offloading</b>	April 2020 - Present
Offloading packet processing programs from CPUs to SmartNICs can bring significant performance benefits, but the developer has no easy way to understand the offloaded performance beforehand. We develop Clara, an automated tool to analyze a legacy NF in its unported form, identify acceleration opportunities, and predict its offloaded performance. Our experiments using Click NFs and a Netronome SmartNIC demonstrate that Clara can provide useful offloading hints and reasonable prediction accuracy. ( <b>HotNets'20</b> )	

<b>P4wn: Probabilistic Profiling of Stateful Data Planes (project lead)</b>	May 2019 - Present
There is a flurry of projects that develop networked systems in P4-programmable switches, but existing program profiling tools are unable to catch up. We develop P4wn, a program profiler that can analyze stateful program behaviors of recent P4 systems, as well as the behavior probabilities, in a scalable manner. We show P4wn is useful to discovering adversarial inputs to these P4 systems by distinguishing and stressing the program "edge cases". ( <b>CSET'19, ASPLOS'21</b> )	

<b>NetWarden: Mitigating Network Covert Channels without Performance Loss</b>	May 2019 - Nov 2019
Network covert channels are an advanced class of attacks to modern networks. Traditional solutions rely on general-purpose CPUs to detect and mitigate them, but they can incur high performance overhead. We propose	

NetWarden, which leverages P4 switches to mitigate network covert channels at switch data planes. Our experiments show that NetWarden can achieve similar detection accuracy and mitigation effectiveness compared with existing solutions, but without hurting end-to-end TCP performance. (**USENIX Security’20**)

**Poise: Programmable In-Network Security for BYOD Policies (project lead)** Aug 2018 - Sep 2019

We present a new security paradigm called programmable in-network security (Poise) for enforcing access control policies in enterprise networks. Administrators write security policies using an easy-to-use, high-level language, and our Poise compiler can generate P4 programs, which will be installed in programmable switches, to enforce these policies. Poise outperforms traditional solutions which are based on OpenFlow networks, and is resilient to control plane saturation attacks. (**CSET’19, USENIX Security’20**)

## RESEARCH PUBLICATIONS

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### **Probabilistic Profiling of Stateful Data Planes for Adversarial Testing**

**Qiao Kang\***, Jiarong Xing\*, Yiming Qiu, and Ang Chen

26th International Conference on Architectural Support for Programming Languages and Operating Systems (**ASPLOS’21**), Virtual, April 2021

### **Clara: Performance Clarity for SmartNIC Offloading**

Yiming Qiu\*, **Qiao Kang\***, Ming Liu, and Ang Chen

19th ACM Workshop on Hot Topics in Networks (**HotNets’20**), Virtual, Nov 2020

### **Mitigating Network Covert Channels while Preserving Performance**

Jiarong Xing, **Qiao Kang**, and Ang Chen

29th USENIX Security Symposium (**Security’20**), Virtual, Aug 2020

### **Programmable In-Network Security for Context-aware BYOD Policies**

**Qiao Kang\***, Lei Xue\*, Adam Morrison\*, Yuxin Tang, Ang Chen, and Xiapu Luo

29th USENIX Security Symposium (**Security’20**), Virtual, Aug 2020

### **Automated Attack Discovery in Data Plane Systems**

**Qiao Kang**, Jiarong Xing and Ang Chen

12th USENIX Workshop on Cyber Security Experimentation and Test (**CSET’19**), Santa Clara, CA, USA, Aug 2019

(\* indicates equal contributions)

## TALKS

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Qiao Kang, “Programmable In-Network Security for Context-aware BYOD Policies”  
USENIX Security’20 (online), Aug 2020

Qiao Kang, “Programmable In-Network Security for Context-aware BYOD Policies”  
Computer Systems Lab, University of Washington (online), Jul 2020

Qiao Kang, “Programmable In-Network Security for Context-aware BYOD Policies”  
P4 Expert Roundtable Series (online), Apr 2020

Qiao Kang, “Automated Attack Discovery in Data Plane Systems”  
CSET’19, Santa Clara, CA, USA, Aug 2019