Progress (2015-12-14)

Status
- Heuristics are mostly used in VLSI because they require less computing power. Partitioning problems are clustered to be solved in a realistic amount of time, then the solution is refined by uncoarsening the graph.
- Meta-heuristics in VLSI are barely a few years old.
- F-M heuritic is designed to be multi-weight and balance constrained, but not always fully implemented.

Highlights & Low points
- Nothing particular

TODO
- Check the heuristics used by the most popular soft implementations
- Find a complete implementation of F-M, or find an open source partial one, or develop a new one.

	Planned	Due Date	Status
Slide deck	Near future	23/11	In progress