

Progress (2015-11-16)

Status

- Algorithms to develop (min-cut):
 - Kernighan-Lin Algorithm
 - Fiduccia-Mattheyses Algorithm
 - Hagen-Kahng EIG Partitioning
- Implementation methods:
 - Linear Programming
 - SemiDefinite Programming

Highlights

- [1]

Low points

- Not enough time spent on slide deck
- Lack of background on graph theory

	Planned	Due Date	Status
Slide deck	23/11	23/11	Under construction

- [1] Sao-Jie Chen and Chung-Kuan Cheng.
Tutorial on VLSI Partitioning.
VLSI Design, 11(3):175–218, 2000.