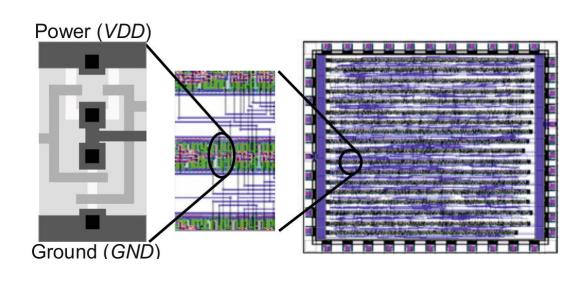
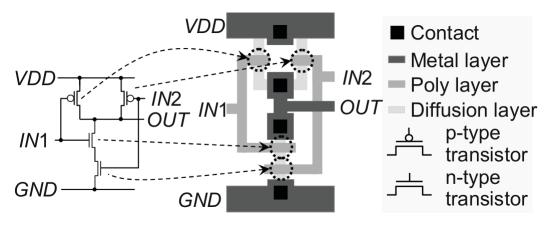
Automated 3D Integration

Research progress



What is in an IC?





2D is struggling and reforming

CMOS scaling

5nm in 2020, but at what cost?

ITRS restructed themselves to find alternatives

2D Future is manyfold

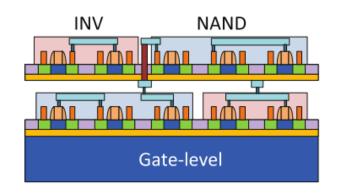
Keep scaling CMOS beyond its limits

3D integration

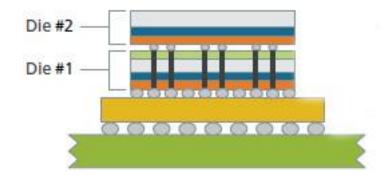
Quantum computing

3D wafer processing

Sequential: Monolithic

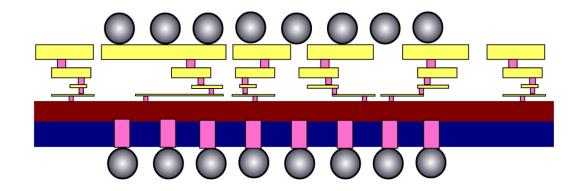


Parallel: Stacking



3D Stacking techniques

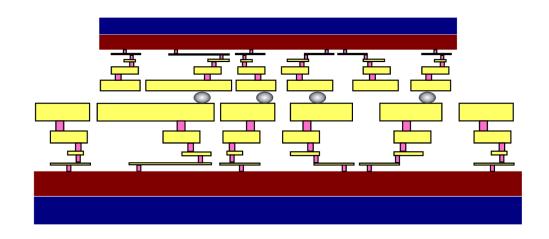
TSV, μ-bumps

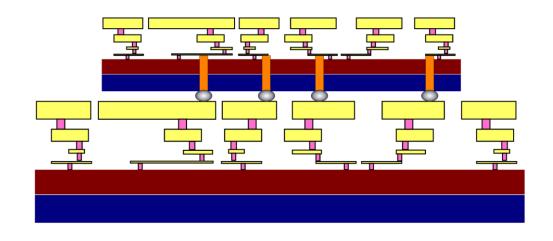


3D Stacking techniques

TSV, μ-bumps

F2F, F2B



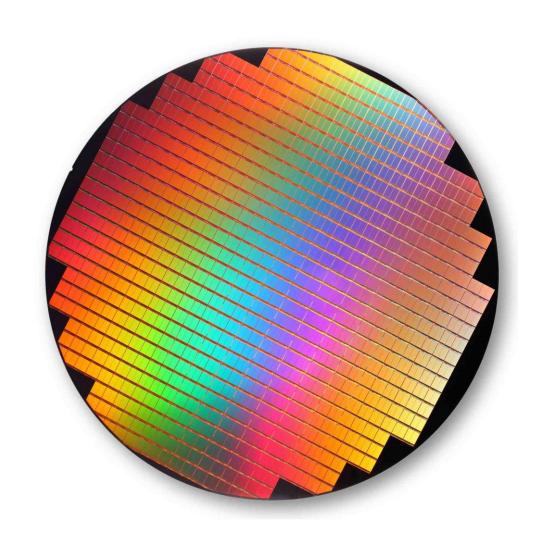


3D Stacking techniques

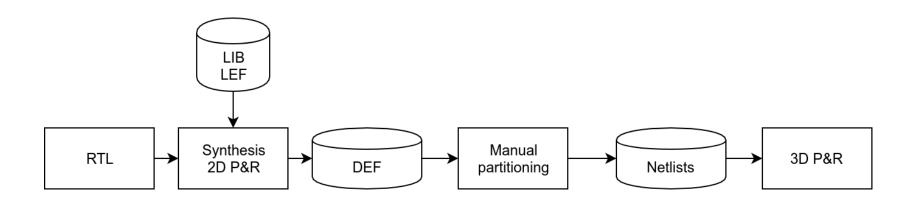
TSV, μ-bumps

F2F, F2B

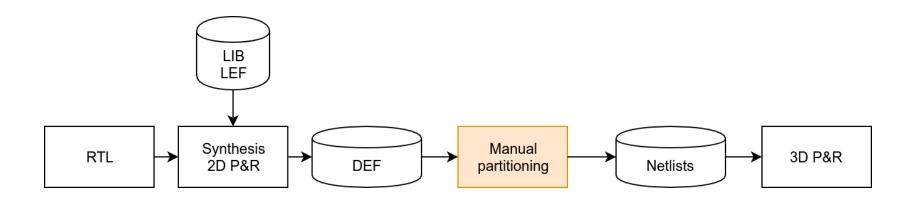
W2W, D2W, D2D

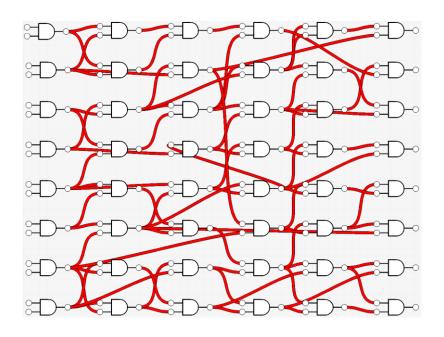


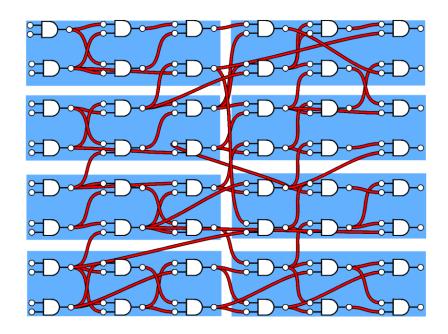
3D Flow



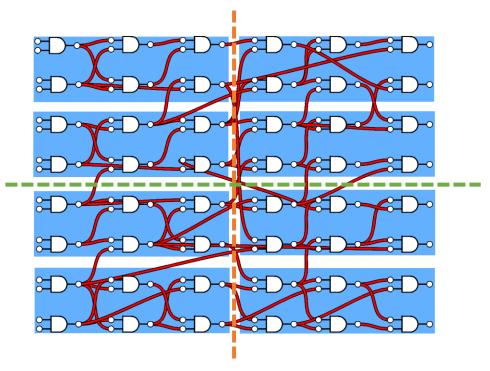
3D Flow



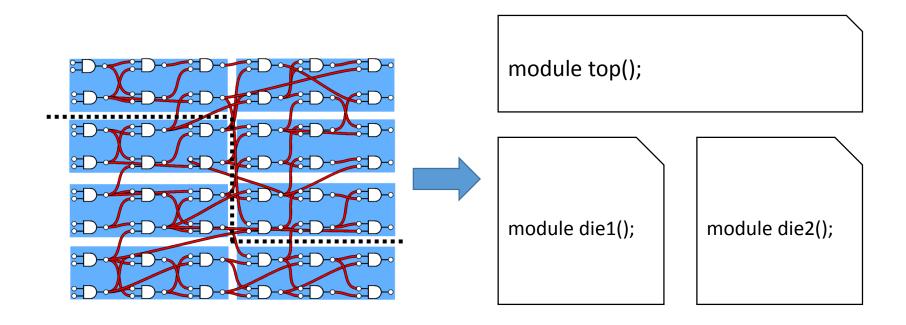




• How do we cluster?

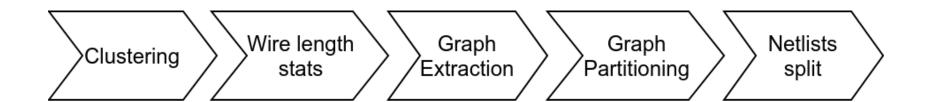


- How do we cluster?
- How do we partition?

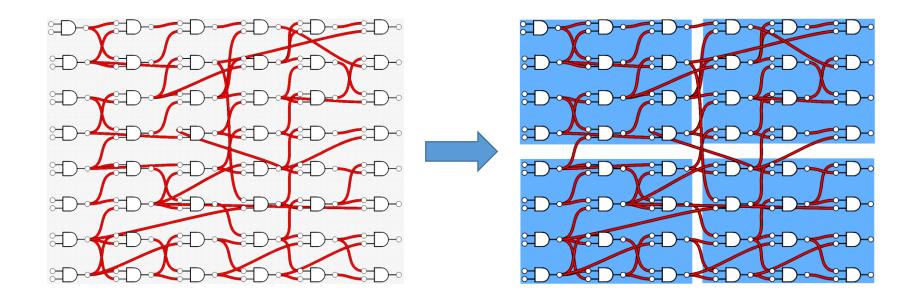


- How do we cluster?
- How do we partition?
- How do we generate the netlists?

Automate the partitioning



Choose the right clustering



Inter-cluster connectivity: 13 nets

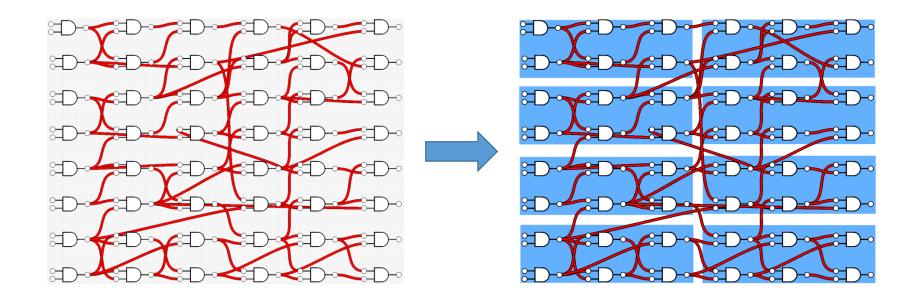
Intra-cluster connectivity: 27 nets

Wire length stats

Clustering

Graph Extraction Graph Partitioning Netlists split

Clustering



Inter-cluster connectivity: 18 nets

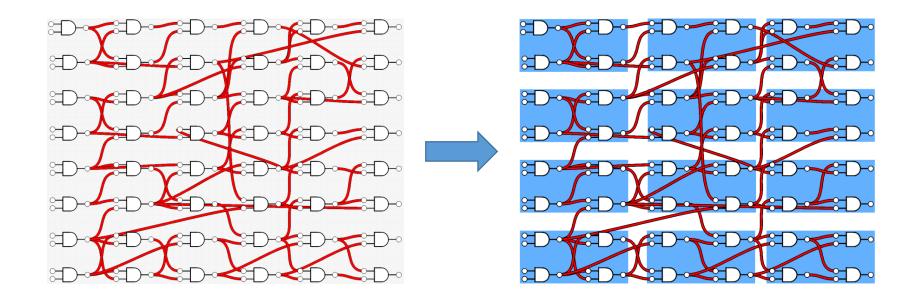
Intra-cluster connectivity: 22 nets

Wire length stats

Clustering

Graph Extraction Graph Partitioning Netlists split

Clustering

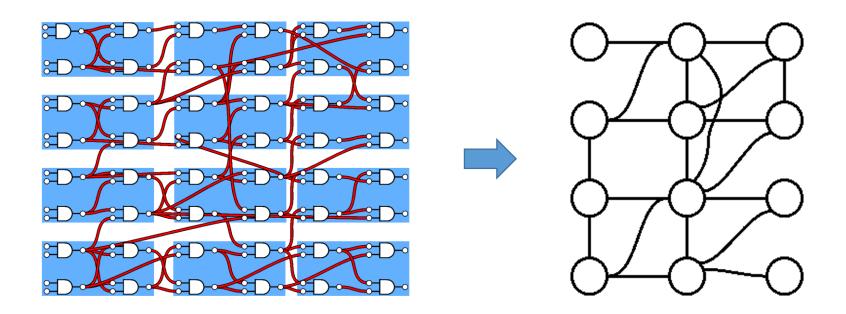


Inter-cluster connectivity: 27 nets

Intra-cluster connectivity: 13 nets

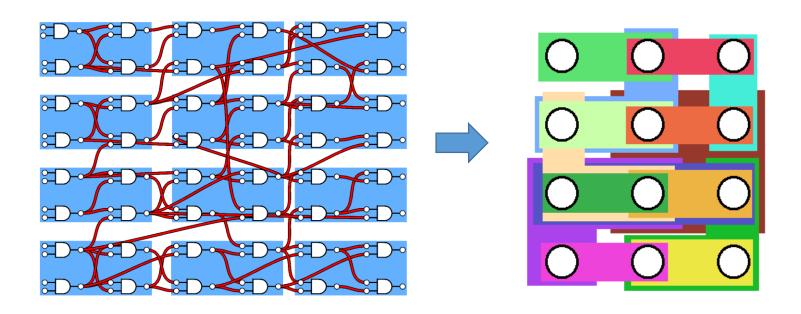
Clustering Wire length Stats Graph Graph Partitioning Split

Graph extraction



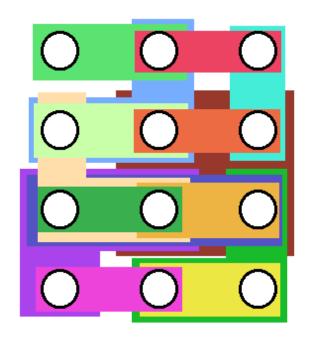
Clustering Wire length Stats Graph Partitioning Netlists Split

Hypergraph extraction

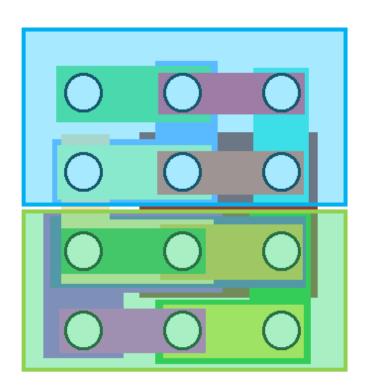


Clustering Wire length Graph Graph Stats Graph Extraction Partitioning Split

Partitioning





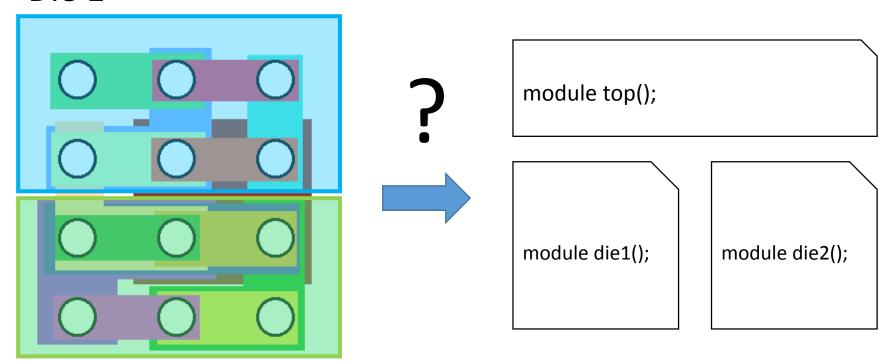


Clustering

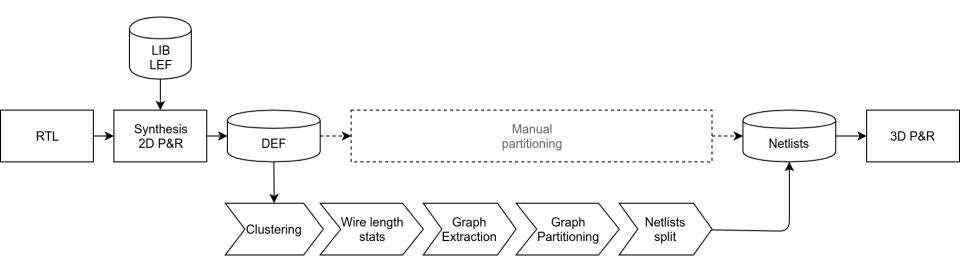
Split the netlist

Die 2

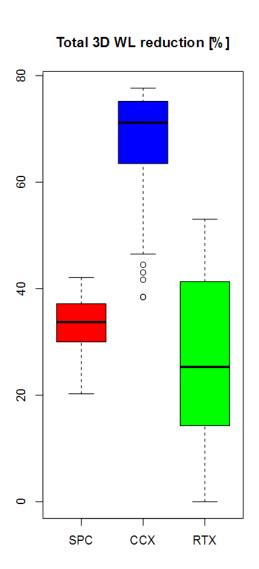
Die 1



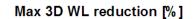
New 3D flow

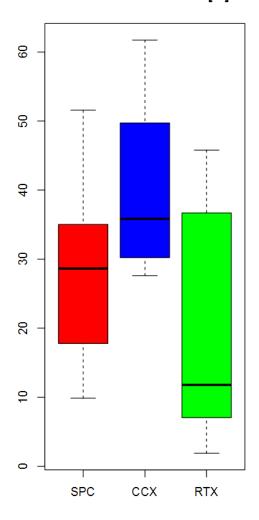


Up to 77% save in 3D wire length



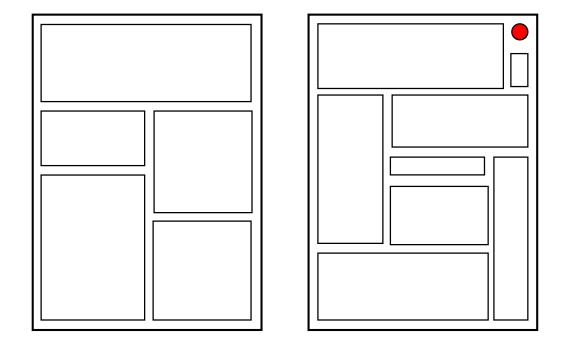
Up to 61% in critical path reduction





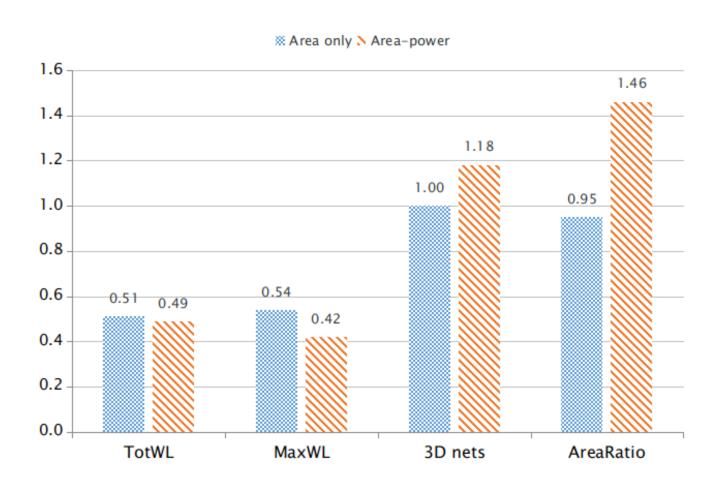
Asymmetric partitioning

How to trick the partitioner

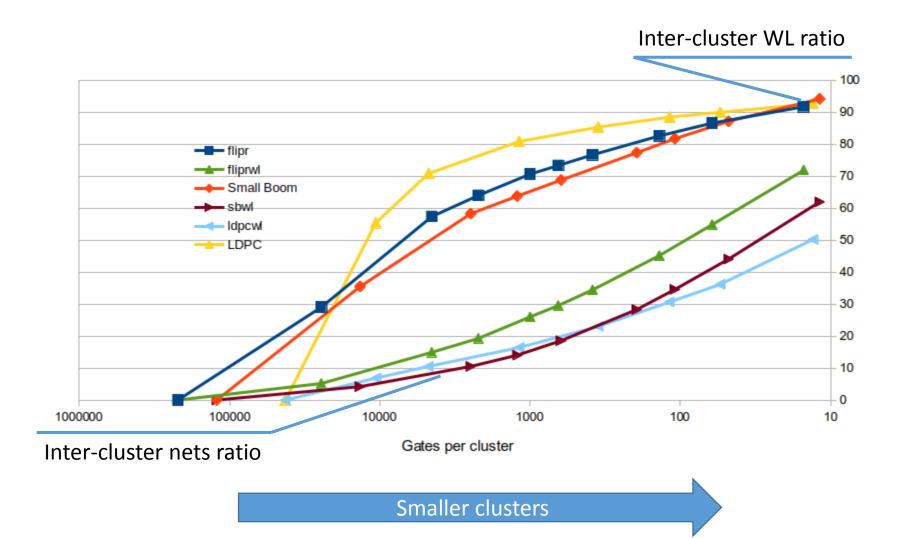


Dummy node with no area impact, but high power

Unwanted area disbalance



Clustering grain: Target 1000 gates



Next steps

Test other clustering methods

Netlist split

Various software improvements

Publication plan

December 2017

June 2018

Optimal cluster size for 3D integration

Clustering methods review: what is the best, should it be taken lightly?

Long term

Clustering and partitioning methods collection

Cadence integration is risky, \rightarrow alternative?

Netlist partitioning, how to publish?

3D integration comparison: monolithic vs stacking

Probabilité

Impact



	Faible	Moyenne	Elevée
Intolérable		Perte de licence	
Préoccupant	Perte du partenariat avec IMEC	Sujet obsolète	Outils libres inadéquats
Négligeable		Article refusé	