Quick review of EU-supported 3D-IC projects

April 12, 2017

$CoolCube^{TM}$

In the past few years, the CEA LETI has been working on a new way to integrate transistors in 3D: Clermidy et al. [2015], Michailos et al. [2016], Brunet et al. [2016], Vinet et al. [2016], Batude et al. [2015]. Their goal is to manufacture 3D monolithic chips using innovative stacking techniques. However, they still face the problem of partitioning and gates repartition on the tiers.

This project is partly funded by the Nano2017 program, the EQUIPEX FDSOI11 platform and the EU FP7 COMPOSE3 project¹.

FP7 Projects:

- FAB2ASM: Efficient and Precise 3D Integration of Heterogeneous Microsystems from Fabrication to $Assembly^2$.
- JEMSIP 3D: Joint Equipment and Materials for System-in-Package and 3D-Integration³.
- NANOPACK: Nano Packaging Technology for Interconnect and Heat Dissipation⁴.
- **ELITE**: Extended Large (3D) Integration TEchnology⁵.

Horizon 2020 projects:

- TAKE5: Technology Advances and Key Enablers for 5 nm⁶. This projects aims at the development of the 5nm node. Even though scaling is worth exploring, the 3D integration path is not to be left aside.
- NeuRAM3: NEUral computing aRchitectures in Advanced Monolithic 3D-VLSI nano $technologies^7$.
- METRO4-3D: Metrology for future 3D-technologies⁸.

 $^{^1}$ http://cordis.europa.eu/project/rcn/111144_fr.html

²http://cordis.europa.eu/project/rcn/94309_en.html

³http://cordis.europa.eu/project/rcn/201936_en.html

⁴http://cordis.europa.eu/project/rcn/85245_en.html

 $^{^{5}}$ http://cordis.europa.eu/project/rcn/85238_en.html

⁶http://cordis.europa.eu/project/rcn/203403_en.html ⁷http://cordis.europa.eu/project/rcn/199168_en.html

 $^{^8}$ http://cordis.europa.eu/project/rcn/199865_en.html

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