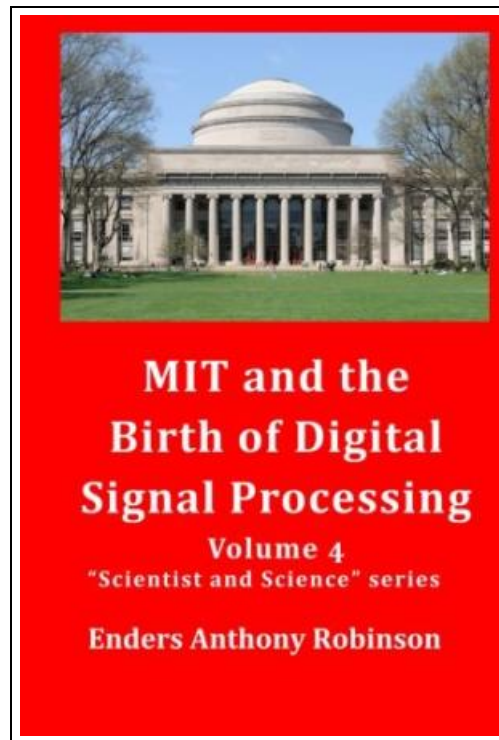


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Createspace, United States, 2014. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.In the late 1950s, the first transistorized digital computers were manufactured. As a result, the decade of the 1960s saw the conversion of the oil exploration industry from analog filtering to digital signal processing. Digital signal processing and deconvolution made it possible to explore successfully in all areas of the Earth including the areas under the seas. Deconvolution requires massive parallel processing. The computer industry accommodated and developed special-purpose digital signal processing (DSP) computers, which at the time were called array processors (also known as vector processors). As time went on, these DSP computers were replaced by microchips, as were all digital computers. In 1986 the National Academy of Engineering (NAE) of the United States of America elected Enders Robinson as member with the citation: For pioneering contributions that have led to the evolution of seismic processing from hand digitization of the 1950s to today's custom deconvolution chip. The hand digitization of the 1950s was the forerunner of what was to come. Practically all communications today are done with digital data. The telephone is digital, the television is digital, the cinema is digital, the music is digital, the camera is digital. Now in the second decade of the twenty-first century, DSP chips are everywhere. The deconvolution chip is the prototype digital signal processing (DSP) chip. Usually the NAE credits new members for such things as innovative research or revolutionary discoveries. Certainly such chips are vital, but why would the NAE mention a chip and not mention seemingly more important mathematical research? The National Academy foresaw what many others did not foresee. In 1986 the Academy realized that the era of mathematical numeral analysis was losing its position...

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