QI LING

https://qiling07.github.io ling102@purdue.edu | (734) 834-7174

2nd-year Ph.D. student in Computer Science, Purdue University

RESEARCH INTERESTS

I want to make computer systems more efficient, secure, and reliable. Currently, my project focuses on fine-grained and low-cost microarchitectural isolation techniques, enhancing performance and security for serverless computing. Still, my interest spans many other fields in computer architecture and operating systems.

EDUCATION

• Purdue University

Doctoral Degree in CS

Advisors: Prof. Kazem Taram and Prof. Pedro Fonseca

Aug. 2024 – Present

Overall GPA: 4.0/4

• University of Michigan
Bachelor's Degree in CS
Advisors: Prof. Baris Kasikci and Prof. Shuwen Deng

Sept. 2022 – May 2024
Overall GPA: 3.92/4

• Shanghai Jiao Tong University

Bachelor's Degree in ECE

Advisor: Prof. Yibo Pi

Sept. 2020 – Aug. 2024

Overall GPA: 3.74/4

AWARDS AND HONORS

• ACM Student Research Competition 2nd Place Award Presented a poster and gave a 10min talk at MICRO'23	2023
• ACM MICRO Student Travel Grant	2023
• Dean's Honor List	2022,2023
• Jackson and Murial Lum Scholarship 5 recipients each year	2022, 2023
• SJTU Undergraduate Excellence Scholarship	2021

PEER-REVIEWED WORKS

- Cache Partitioning for Performance in Serverless Computing Qi Ling, Ajay R. Rawat, Pedro Fonseca, Kazem Taram In submission to ISCA'26
- GadgetMeter: Quantitatively and Accurately Gauging the Exploitability of Speculative Gadgets

Qi Ling, Yujun Liang, Yi Ren, Baris Kasikci, Shuwen Deng Won 2nd Place at MICRO'23 Student Research Competition Network and Distributed System Security Symposium (NDSS'25)

• Towards Fine-Grained, High-Coverage Internet Monitoring at Scale Hongyu Wu, Qi Ling, Penghui Mi, Chaoyang Ji, Yinliang Hu, Yibo Pi The 7th Asia-Pacific Workshop on Networking (APNet'23)

• SecArch & RSSys, Purdue University Advisors: Kazem Taram and Pedro Fonseca Aug. 2024 - Present

Microarchitectural isolation for performance and security.

- Identified a new kind of cache contention in serverless computing.
- Discovered a new cache partitioning technique, without introducing new hardware.
- Achieved fine-grained, zero-cost cache partitioning across tens to hundreds of serverless functions.
- Improved the P50 latency of latency-critical functions by 30% on average.

• EFESLAB, University of Michigan Advisors: Baris Kasikci and Shuwen Deng

Dec. 2022 - Dec. 2023

Detecting and evaluating Spectre-PHT gadgets in programs.

- Identified the **Timing Condition** of Spectre-PHT gadget.
- Presented a systematic study and exploration of windowing power.
- Proposed and implemented a new approach, which describes the timing condition with a DAG graph, models the windowing power with graph algorithms, and quantifies the gadget's exploitability with runtime measurement.
- Validated our approach and evaluated it against SOTA scanners on 2 micro-benchmarks, 6 userspace programs, and the Linux kernel. Identify 503 gadgets reported by SOTA scanners as unexploitable.
- Network Measurement and System Lab, SJTU Advisor: Yibo Pi

Aug. 2021 - Sep. 2022

Optimizing the accuracy and coverage of large-scale network monitoring.

- Challenged two root assumptions of conventional network monitoring practice with experiments.
- Proposed and implemented a greedy end-to-end network monitoring approach.
- Evaluated the accuracy, coverage and overhead of our approach.

TEACHING EXPERIENCE

• Purdue University - Computer Security (CS426)
Teaching assistant

Spring, Fall 2025

 \bullet Purdue University - Computer Architecture (CS250)

Fall 2024

Teaching assistant

TECHNICAL SKILLS

- Linux kernel development
- LLVM compiler development
- Hardware: Verilog, Gem5