

# TANA: Fined-grained Side-channel Information Leakage Quantifications in Binaries

Anonymous

**Abstract**—Side-channel attacks allow attackers to infer some sensitive information based on non-functional characteristics. Existing works on address-based side-channel detection can provide a list of potential side-channels leakage sites. We observed that those works still have the following limitations: 1) Many software may have multiple information leakage sites. Some vulnerabilities could be more severe than others. But existing work couldnt tell the difference between those leakages. 2) An attacker could exploit multiple leakages at one time. However, no existing tool can report how much information is leaked in total.

To overcome the above limitations: we proposed a tool called TANA, which can not only find the side channels but can estimate how many bits are actually leaked through the leakage. TANA works in three phases. First, the application is executed to record the trace. Second, TANA runs the instruction level symbolic execution on the top of the execution trace. Tana will find side-channel information leakages and model each leakage as one unique math constraints. Finally, TANA will classify those constraints into independent multiple groups and run the multiple step monte carlo to estimate the information leakage. TANA will a very fined-grained result compared to existing tools.

We apply the tool on OpenSSL, MbedTLS and libjpeg and find several serious side channel vulnerabilities. We also evaluate the vulnerabilities from previous research. The result indicates most of the reported vulnerabilities are actually hard to exploit in practice.

## I. INTRODUCTION

Side channels are inevitable in computer systems. The attacker can infer the sensitive information by observing execution behaviours. The software-based attackers(e.g., Cache-based side channels, controlled side attacks) are especially common because these attacks typically dont need any physical access. We observe that the root cause is that the implementation of the program may have different memory access patterns during the execution. Attackers can observe the pattern and infer the sensitive information based on source code.

Various countermeasures have been proposed to defend the address-based side-channel attacks. The basic idea is to identify and eliminate secret-dependent memory access patterns. It is very tedious and hard to manually find every the leakage site. To address the problem, a lot of methods have been proposed to automatically detect information leakage. While those tools can typically report a list of potential leakages sites, they still face a couple of limitations.

First, the existing tools [1] [2] can discover a list of potential leakage sites. But they fail to report how severe each potential leakage site could be. Most of leakages found by those tools are typically hard to exploit or leak very little information (e.g., one bit of the length of the key). It is worthwhile to

have a tool to tell how much information is actually leaks. For example, many secret dependent memory access patterns in OpenSSL have been known and studied for years. But the authors still doesnt fix them all because they think they arent not severe. The tool based on static analysis with abstract interpretation, can give leakage upper bound, which is useful to justify the implementation is secure enough. But they cant indicate the leakage site is severe enough due to the over approximation. The dynamic method will take a concrete input and run the actual program. Those tools are typically very precise in term of true leakage. But none of those tools can actually report how many bits are actually leaked. For example, DATA reports more than 2000 potential leakage sites for the RSA implementation of OpenSSL. But most of them were dismissed by the author after some manual inspections.

Second, many open source libraries may have multiple information leakage sites. A side-channel attacker can exploit multiple leakage sites at one time. The attacker may retrieve some information from one site and some other information from another site. It is hard to model how much information is actually leaked in total. Adding those leakages simply can get only an upper bound estimate of the total information leakage if those leakages arent independent. No existing tools can practically estimate the total information leakage from multiple leakage sites in open source libraries.

In the paper, we presented a tool that can automatically identify sensitive information leakage sites in real-world applications. Compared to existing tools, our tool can provide a list of information leakage sites as well as how many bits they can leak if the attacker can observe one site, two sites or multiple sites. We defined the amount of leaked information as how much uncertainty about the sensitive information the attacker could reduce after launching the side-channel attack. It is interesting to mention that the definition here is different than the definition in several static analysis tools. We will explain the reason in the following sections. After that, we run the symbolic execution on the execution trace. We model each side-channel leakage as one logic formula. The only symbols in those constraints are sensitive input. Those constraints can unique model the side-channel vulnerability. In other words, if the application has a different sensitive input but still satisfy the formula, the code will still leak the same information about the input. Those information leakage sites may spread in the whole program and their leakages arent dependent. Simply adding them up can only give the upper bound estimate. In order to accurately calculate the total information leakage, we must know the dependent relationships among those multiple

leakages sites. Therefore, we introduce a monte carlo sampling method to estimate the total information leakage from the formula generated by previous symbolic execution.

We implement the proposed method as a tool named TANA that can precisely discover and quantify the information leakage vulnerabilities. We apply TANA on several crypto and non-crypto libraries including OpenSSL, MbedTLS and libjpeg. The experiments confirm that TANA can precisely identify the pre-known vulnerabilities, report how much information is leaked and which byte in the original sensitive buffer is leaked. The result shows that while those crypto libraries have a number of side-channels, most of them actually leak very little information. Also, we also use the tool to analysis the two reported side-channel attack in the libjpeg library. Finally, we also present new vulnerabilities. In summary, we make the following contributions:

- We propose a novel trace-based analysis method that can quantify fined-grained side-channel information leakages. We model each information leakage vulnerability as one formula. Therefore, mutiple side-channel vulnerabilities can be seen as the disjunctive normal form (DNF) formula, which precisely models the program semantics.
- We transfer the information quantification problem into a probabily distribution problem and use guided Monte Carlo sampling method to estimate the information leakage. Some initial results indicate the the sampling method suffers from the curse of dimensionality. We therefore design a guided sampling method and porvide the corresponding error esitimate.
- We implement the proposed method into a practical tool and apply it to a detect several software. TANA successfully identify the address-based side-channel vulnerabilities and provide the corresponding information leakage. The information leakage result provide the detailed information that help developers to fix the vulnerability.

## II. BACKGROUND

In this section, we first present a basic introduction about the several kinds of memory-based side-channel attack. Those attacks are exactly what we attempt to study in the paper. Then we present existing work on side-channel detection and quantification.

### A. Address-based Side-Channels

Address-based side-channels are information channels that can leak sensitive information unintended through the different behaviors when the program accesses different memory adrrsss. Fundamentally, those difrences were caused by the memory hierarchy design in modern computer systems. When the CPU fetches the data, it will first search the cache, which stores copies of the data from the frequently used main memory. If the data doesn't exist in the cache, the CPU will read the data from the main memory (RAM). Classified by the layer caused the side-channel, we introduce two kinds of commom side-channels: cache-based side-channel attacks and memory-based side-channel attacks.

1) *Cache-based Attack*: In general, the cached-based side-channel attacks seek information rely on the time differences between the cache miss and cache hit. Here we introduce two types of cache attacks: PRIME+PROBE, FLUSH+RELOAD.

**PRIME+PROBE** targets a single cache set. It has two phases. During the "prime" phase, the attacker fills the cache set with his own data. In the second "probe" phase, the attacker accesses the cache set again. If the victim accesses the cache set and evicts part of the data, the attacker will experience a slow measurement. If not, the measurement will be fast.

**FLUSH+RELOAD** targets a single cache line. It requires the attacker and victim share the same memory. It also have two phases. During the "flush" phase, the attacker will flush the "monitered memory" from the cache. Then the attacker wait for the victim to access the memory. In the third phase, the attacker reload the "monitered memory". If the time is short, which indicates there is a cache hit and the victim reolads the memory before. On the other hand, the time will be longer since the CPU need to reolad the memory into the cache line.

2) *Memory-based Attack*: Memory-based side-channel attack [] exploits the different behaviors when the program accesses different page tables. The controlled-channel attack [3], works in the kernel space can infer the sensitive data in the shielding systems by observing the page fault sequences by restricting some code and data pages.

After examining the memory-based side-channels attack. We find the fundamentally reason of those attacks are due to secret-dependent memory access and control flow transfers.

```

unsigned char* sample_encrypt_setup          1
    (encrypt_ctx *ctx,                       2
     const unsigned char *key)               3
{
    int i, j, k;                             4
    unsigned char *m, T[256];                5
    m = ctx->m;                               6
    for(i = 0 ; i < 256; i++, k++)           7
    {                                         8
        // Secret Dependent Memory Access   9
        m[i] = Table[key[k] % 256];         10
        // Secret Dependent Control Flow Transfer 11
        if(key[k] == 0)                     12
        {                                   13
            m[i] = k * key[i];              14
            m[i] = m[i] % 256;              15
        }                                   16
    }                                       17
}                                         18
                                         19
                                         20

```

Listing 1. Sample code shows secret-dependent memory access and secret-dependent control-flow transfer.

For exampls, the above code 1 show an simpple encrypt function that has the two kinds of side-channels. At the line 11, depending on the value of key, the code will access the different entry in the predefined table /textbfTable. At the line 13, the code will do a series of computation and determine if the code in the if branch is executed or not. Such vulnerabilities could leak to the memory-based side-channles. We indentify and quanify the leakage of the two kinds of vulnerabilities in the paper.

### B. Information Leakage Quantification

Given an event  $E$  which occurs with the probability  $P(E)$ , if the event  $E$  happens, then we receive

$$I = -\log(P(E)) \quad (1)$$

bits of information.

The above definition is obvious. Suppose a char variable  $a$  in C program has byte (8 bits) size, so the value in the variable can range from 0 - 255. We assume the  $a$  has the uniform distribution. If at one time we observe the  $a$  equals to 1, the probability will be  $1/256$ . So the information we get is  $-\log(1/256) = 8\text{bits}$ , which is exactly the size of the char variable.

One method to quantify the program is based on the channel capacity theorem. It shows that the max leakage of the program equals to the log of the number of possible distinct observations (NumOfObs) that an attacker can make.

$$\text{LeakedInformation} \leq -\log(\text{NumOfObs}) \quad (2)$$

### III. THREAT MODEL

We consider an attacker who shares the same hardware resource with the victim. The attacker attempt to retrieve sensitive information via memory-based side-channel attack. The attacker has no direct access to the memory or cache, but can probe the memory or cache at each program point. Similar to DATA, the attacker will face many noisy observations or can only observe a limited of memory or caches in practice. For the project, we assume the attacker can have noise-free observations. This threat model captures most of the cache-based and memory-based side channel attacks.

### IV. MOTIVATION

#### A. Technical Challenges

In this section, we articulate several challenges and existing problems in quantifying the side-channel vulnerability leakages. We describe the challenges and briefly describe the corresponding solutions.

1) *Information Leakage Definition*: Existing static-based side-channel quantification works either define the amount of leaked information corresponds to the number of possible side-channel observations or the min-entropy. However, the definition has some limitations. Consider the following examples,

```
unsigned char key = input();
//4 bits sensitive information
unsigned char temp = key/2;
if (temp == 0xb)
//branch 0
else
//branch 1
```

If we use the number of possible observations to quantify the amount of leakage, the code will have two different observations (branch 1 and branch 2). Those observations can be represent by one bit number. Therefore, the total information leakage here will be 1 bit. The above estimation, however, are

less useful in practice. We want to have a way that can very precisely estimate how much information are leaked for each execution. Apparently, the existing method fails to achieve our requirement. Suppose an attacker can observe which branch the program executes (e.g., flush reload, controlled attack), the above code may leak different amount of information depending on the value of input. For example, if the attacker knows the code executes the branch 0, then he will know the key equals  $11 \times 2 = 22$ . As the key has 4 bits information in total, we think the amount of information leakage here is 4 bits. On the other hand, if the code executes the branch 2, the only thing that an attacker can know is that the key doesn't equal 22, which leaks very little information in this case.

Solution: We model each leakage as one unique formula. We come to the original information content definition and use the probability to mark the information leakage.

#### B. Multiple Leakage Sites

Real-world software usually can have multiple side-channel vulnerabilities. Those vulnerabilities may spread in the whole program. An adversary may exploit more than one side-channel vulnerabilities to achieve the attack. For example, the controlled-side channel attack [3] [4], the author demonstrate an attack against a popular spell checking tool, Hunspell. By observing four sets of secret-dependent memory accesses sites in two functions *HashMgr :: addword* and *HashMgr :: lookup*, the author can recover the word that Hunspell checks.

For the Hunspell, the attacker manually study the source code of Hunspell, figure out the relation of those vulnerabilities and launch the attack. In order to precisely quantify the total information leakage, we need to know the relation of those leakage sites.

Suppose one program have two side-channel vulnerabilities A and B, which leaks  $L_A$  and  $L_B$  bits during the execution. The total information leakage is noted as  $L_{Total}$ . The relation between A and B has the following two cases.

1) *Independent Leakages*: If A and B are independent leakages, the total information leakage will be:

$$L_{total} = L_A + L_B \quad (3)$$

2) *Dependent Leakages*: If A and B are dependent leakages, the total information leakage will be:

$$\max\{L_A, L_B\} \leq L_{total} < L_A + L_B \quad (4)$$

#### C. Scalability

We use the mutual information (MI) to quantify the information leakages. For a program P with sensitive information K as the input, the attacker may have some observations O during the execution. The information leakage is defined as the mutual information  $I(O; K)$  between O and K.

$$I(O; K) = H(O) - H(O|K) \quad (5)$$

$I(O; K)$  represent how much uncertainty about K can be reduced if the attacker has the observation O. For a deterministic program, the program will have the same memory access

behavior as long as the input is fixed. As the observation of the attacker correlate to the memory access behavior, we can have the following formula.

$$O = f(K) \quad (6)$$

The function  $f$  is determined by the program  $P$ . For our project, we can calculate the function  $f$  via symbolic execution.

$$I(O; K) = H(O) - H(f(K)|K) = H(O) = H(f(K)) \quad (7)$$

So the mutual information between  $O$  and  $S$  equals to the self information of  $O$ .

$$H(O) = p(O_i) \log(p(O_i)) \quad (8)$$

For a determinist program, we can calculate the distribution of  $O$  as long as we know the distribution of input  $K$ . So we can calculate how much information is leaked.

For examples, given a program  $P$ , we have the sensitive input  $K$ . The  $K$  should be a value in a memory cell or a sequential buffer (e.g., an array). We use  $k_i$  to denote the sensitive information, where  $i$  is the index of the byte in the original buffer. We can have the following equations. The  $t_1, t_2, t_3$ , is the temporary values during the execution.

$$t_1 = f_1(k_1, k_2, k_3 \dots k_n) t_2 = f_2(k_1, k_2, k_3 \dots k_n) t_3 = f_3(k_1, k_2, k_3 \dots k_n) \quad (9)$$

The attacker can retrieve the sensitive information by observing the different patterns in control-flows and data access when the program process different sensitive information. We refer them as the secret-dependent control flow and secret-dependent data access accordingly.

#### D. Secret-dependent Control Flow

Here is an example of the secret-dependent control-flows. Consider the code snippet in List 1. Here the key is the confidential data. The code will have different behaviours (time, cache access) depending on which branch is actually executing. By observing the behaviour, the attacker can infer which branch actually executed and know some of the sensitive information. One of the famous leakage example is the square and multiply in many RSA implementations.

For example, the attacker knows the key equals to zero if he observes the code run the branch1. Because key has 256 different possibilities. The original key has  $\lg 256 = 8$  bits information. If the attacker can observe the code run branch 1. Then he will knows the key equals to zero. If the code run branch 2, the attacker can infer the key doesnt equal to zero.

```
Branch 1 temp = 0xb; 0 = key; key = 256; temp = key/2;
Information Leakage =  $-\log(1/p) = -\log(1/256) = 8$  bits
Branch 2 temp != 0; 0 = key; key = 256; temp = key/2;
Information Leakage =  $-\log(255/256)$  bits
```

#### E. Seret-dependent Memory Access

```
T[64]; // Lookup tables with 64 entries
index = key % 63;
temp = T[index];
// Secret-dependent memory access
```

The simple program above is an example of secret dependent memory access. Here  $T$  is a precomputed tables with sixty-four entries. Depending on the values of key, the program may access any values in the array. Those kind of code patterns may wildy exist in many crypto and media libraries.

Suppose the attackers observe the code accesses the first entry of the lookup tables. We can have the following formulas.

$$\text{key} \bmod 63 = 0 \Rightarrow \text{key} = 256$$

So the key can be one of the following values: 1 64 127 190 253

$$\text{Information leakages} = -\log(5/256) = 5.6 \text{ bits}$$

#### V. DESIGN

In this section, we will explain the design decisions to realize A.

##### A. Trace Logging

The trace information can be logged via some emulators (e.g., QEMU) or Binary Instrumentation Tools. For our project, we write an Intel Pin Tool to record the execution traces. The trace data has the following information: Each instruction mnemonics and its memory address The operands of each instruction and their values The memory address of sensitive information and its length The value of eflags register Most software developers stores sensitive information in an array, a variable or a buffer, which means that those data is stored in a contiguous area in the memory. We use the symbol information in the binary to track the address in the memory.

##### B. Instruction Level Symbolic Execution

The main purpose of the this step is to generate constraints of the input sensitive information for the execution trace. If we give the target program a new input which is different from the origin input that was used to generate the execution trace but still satisfies those constraints, the new execution trace will still have the same control flow and data access patterns.

The tool runs the symbolic execution on the top of the execution traces. At the beginning of the symbolic execution, the tool creates fresh symbols for each byte in the sensitive buffer. For other data in the register or memory at the beginning of the symbolic execution, we use the concrete value from the runtime information collected in the previous step. During the symbolic execution, the tool will maintain a symbol and a concrete value for every variables in the memory and registers. The formula is made up with concrete values and the input key as the symbols calculated through the symbolic execution. For each formula, the tool will check weather it can be reduced into a concrete values. If so, the tool will only use the concrete values in the following symbolic execution.

##### C. Verification and Optimization

We run the symbolic execution on the top of x86 instructions to achieve the better performance and accuracy of the memory model. In other words, we dont rely on any intermediate languages to simplify the symbolic execution. While the implementation itself has a lot of benefits, we need to implement the symbolic execution rules for more than one thousand

x86 instructions. However, due to the complexity of X86, it is inevitable to make mistakes. Therefore, we verify the correctness of the symbolic execution. The tool will collect the runtime information (Register values, memory values) and compare them with the values generated from the symbolic execution. Whenever the tool finishes the symbolic execution of each instruction, the tool will compare the formula for each symbol and its actual value. If the two values don't match, we check the code and fix the error. Also, if the formula doesn't contain any symbols, the tool will use the concrete value instead of symbolic execution.

#### D. Secret-dependent control-flows

An adversary can infer sensitive information from secret-dependent control-flows. There are two kinds of control-transfer instructions: the unconditional control transfer instructions and the conditional transfer instructions. The unconditional instructions, like CALL, JUMP, RET transfer control from one code segment location to another. Since the transfer is independent from the input sensitive information, an attacker was not able to infer any sensitive information from the control-flow. So the unconditional control transfer doesn't leak any information based on our threat model. During the symbolic execution, we just update the register information and memory cells with the new formulas.

The conditional transfer instructions, like conditional jump, may or may not transfer control, depending on the CPU state. For any conditional jump, the CPU will test if certain condition flag (e.g., CF = 0, ZF = 1) is true or false and jump to the certain branches respectively. So the symbolic engine will compute the flag and represent the flag in a symbol formula. Because we are running on a symbolic execution on an execution trace, we know which branch executes. If a conditional jump uses the CPU status flag, we will generate the constraint accordingly.

For examples,

```
...
0x0000e781    add dword [local_14h], 1
0x0000e785    cmp dword [local_14h], 4
0x0000e789    jne 0xe7df
0x0000e78b    mov dword [local_14h], 0
```

At the beginning of the instruction segment, the value at the address of local14h can be written as  $F(K)$ . At the address e785, the value will be updated with  $F(K)+1$ . Then the code compares the value with 4 and uses the result as a conditional jump. Based on the result, we can have the following constraint:

$$F(K) + 1 = 4$$

#### E. Secret-dependent data access

Like control-flows, an adversary can also infer sensitive information from the data access pattern as well. We try to find this kind of leakages by checking every memory operand of the instruction. We generate the memory addressing formula. As discussed before, every symbol in the formula is the input key. If the formula doesn't contain any symbols, the memory access

is independent from the input sensitive information and won't leak any sensitive information according to our threat model. Otherwise, we will generate the constraint for the memory addressing.

**Monte Carlo Volume Sampling** From the above steps, we already have the constraints from the execution trace. The only variables in those constraints are the sensitive data. An adversary who wants to infer the sensitive data based on side-channel attacks can't observe the sensitive information directly. The adversary, however, can observe the memory access pattern of the software.

We use the Monte Carlo Sampling to calculate the ratio of input that satisfies those constraints. We will estimate how much information is actually leaked from each input key.

**Normalizations** The goal of the normalizations is to simplify the constraints. Each constraint will be evaluated multiple times during the following sampling, we would like to make those formulas simpler to reduce the whole execution time. Each formula is implemented as an abstract syntax tree. We apply a series of normalization rules (e.g.  $\text{key1} \oplus \text{key1} = 0$ ) to simplify the formula.

$$\text{Key1} + 1 + 2 + \text{key3} ; \text{key2} =; \text{key1} + 3 ; \text{key2}$$

Split the independent constraints into multiple groups. Each constraint may have multiple symbols as the input. If each of two formulas has completely different inputs, then those leakages modeled by the constraints are independent.

**Multiple Stage Monte Carlo Sampling** After the above steps, each side channel leakage vulnerability is transferred into a formula. The only symbol in the original formula is the key. An adversary who wants to infer the sensitive data based on side-channel attacks can't observe the sensitive information directly.

The adversary, however, can observe the memory access pattern of the software. If the attacker can observe one leakage site, the attack is modeled as one formula. If he observes multiple information leakages, the attack is modeled as a DNF formula. Calculating the total information leakage can be reduced to the problem of approximating the number of solutions. One intuition way is to use the Monte Carlo method. However, the number of satisfying keys could be exponentially small. Imagine an attacker who can recover the one unique key after the attacks, in such case, the DNF formula  $F = (f(k))$  will only have one solution.

In order to estimate how much information is actually leaked from each input key, we have to calculate the ratio of input that satisfies those constraints. One intuition is to use the Monte Carlo method to approximate the number of solutions.

## VI. DISCUSSION ON IMPLEMENTATION

### A. Binary code vs source code

Many existing works find side-channels vulnerabilities from source code level or intermediate languages (e.g., LLVM IR). Those approaches will have the following questions. First, many compilers can translate the operator into branches. For example, the GCC compiler will translate the `!` operator into conditional branches. If the branch is secret-dependent, the attacker could learn some sensitive information. But those

source-based methods will fail to detect those vulnerabilities. Second, some if else in the source code can be converted into single conditional instructions (e.g., `cmov`). Source-based methods will still regard them as potential leakages, which will introduce false positives.

### B. X86 vs Intermediate Languages

The tool takes in an unmodified binary and the marked sensitive information as the input. The tool will first start with logging the execution trace. Then the tool will symbolizes the sensitive information into multiple symbols and start with the symbolic execution. During the symbolic execution, the tool will find any potentials leakage sites as well as the path constraints. For each leakage site, whether it is the tool will also generate the constraint. Then, the tool will split the constraints into multiple group. After that, the tool will run monte carlo sampling to estimate the total information leakages.

### REFERENCES

- [1] S. Wang, P. Wang, X. Liu, D. Zhang, and D. Wu, "Cached: Identifying cache-based timing channels in production software," in *26th USENIX Security Symposium (USENIX Security 17)*. Vancouver, BC: USENIX Association, 2017, pp. 235–252. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity17/technical-sessions/presentation/wang-shuai>
- [2] S. Weiser, A. Zankl, R. Spreitzer, K. Miller, S. Mangard, and G. Sigl, "DATA – differential address trace analysis: Finding address-based side-channels in binaries," in *27th USENIX Security Symposium (USENIX Security 18)*. Baltimore, MD: USENIX Association, 2018, pp. 603–620. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity18/presentation/weiser>
- [3] Y. Xu, W. Cui, and M. Peinado, "Controlled-channel attacks: Deterministic side channels for untrusted operating systems," in *2015 IEEE Symposium on Security and Privacy*, May 2015, pp. 640–656.
- [4] D. Gruss, R. Spreitzer, and S. Mangard, "Cache template attacks: Automating attacks on inclusive last-level caches," in *24th USENIX Security Symposium (USENIX Security 15)*. Washington, D.C.: USENIX Association, 2015, pp. 897–912. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity15/technical-sessions/presentation/gruss>