

CleverHans: Precise Fine-grained Side-channel Information Leakage Quantification in Binaries

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Abstract—Side-channel attacks allow adversaries to infer sensitive information based on non-functional characteristics. Existing work on address-based side-channel detections can identify many tentative side-channel leakage sites. However, no existing tools can precisely report the number of leaked bits for each leakage site, for production systems.

To overcome the limitation, we propose a novel method to precisely quantify the leaked information from side-channel vulnerabilities. We model each leakage as a constraint and use those constraints to divide the input space set into several subsets such that only one subset satisfies every constraint. The cardinality of the subset corresponds to the amount of leaked information from those leakages. We use symbolic executions to generate math constraints. It is widely believed that symbolic execution is expensive in terms of performance. We systematically analyze the bottlenecks of the current dynamic symbolic execution approach and make it scalable to real-world cryptosystems. In the final step, we run Monte Carlo sampling to estimate the number of leaked information for each detected vulnerabilities. With the help of the Central Limit Theorem (CLT), we can also give the error estimation.

We have implemented the above technique in a tool called CleverHans, which can not only find the side-channel vulnerabilities but also estimate how many bits are leaked. CleverHans outperforms existing dynamic side-channel detection tools in terms of performance and accuracy. Also, CleverHans can report a very fine-grained vulnerability leakage information. Compared to existing abstract interpretation approaches that can only give upper leakage bounds, those information are more useful for developers to identify the severity level of each side-channel vulnerabilities.

We apply CleverHans on OpenSSL, MbedTLS, and found several side-channel severe vulnerabilities. Compared with previous research, the results are surprisingly different. First, it shows that most of the reported vulnerabilities are hard to exploit in practice. Second, we also find several sensitive vulnerabilities that are neglected before CleverHans. We confirmed those vulnerabilities with manual checks and software developers.

I. INTRODUCTION

Side channels are inevitable in modern computer systems as the sensitive information may be leaked by many kinds of inadvertent behaviors, such as power, electromagnetic radiation and even sound []. Among them, software-based side-channel channels (e.g., cache attacks, memory page attacks, and controlled-channel attacks) are especially common and have been studied for years []. Those vulnerabilities result from vulnerable software and shared hardware components. By observing the outputs or hardware behaviors, attackers can infer the program execution flow that manipulate secrets and guess the secrets such as encryption keys [].

Various countermeasures have been proposed to defend against software-based side-channel attacks. Hardware level

solutions, including reducing shared resources, adopting oblivious RAM, and using transnational memory [1]–[3] need to change the hardware and modern complex computer systems, which is impractical and hard to adopt in reality. Therefore, a more promising and universal direction are software countermeasures, detecting and eliminating side channels vulnerabilities from source code base.

After examining the root cause of many software-based side channels, we believe that many of them are caused by the following two specific types of disaster: data flow from secrets to load addresses and data flow from secrets to branch conditions. We call them secret-dependent control-flow and memory-access correspondingly. Therefore, a central problem is identifying those two code patterns automatically. Recent works [2] adopt static and dynamic analysis to detect side-channels. They can find many potential leak sites in real-world software, but fail to report how severe a potential leakage could be. Many of the reported vulnerabilities are typically hard to exploit and leak very little information. For example, DATA [] reports 2246 potential leakage site for the RSA implementation in OpenSSL. After some inspections, 1510 are dismissed, but it still leaves 278 control-flow and 460 data-access patterns. For software developers, it is hard for them to fix all those vulnerabilities. While some vulnerabilities can be used to recover the full secret keys [], many other vulnerabilities prove to be less serious in reality.

To assess the sensitive level of side-channel vulnerabilities, we need a proper quantification metric. Static methods, usually with the abstract interpretation, can give a leakage upper bound, which is useful to justify the implementation is secure when they report zero leakage. However, they cannot indicate how serious the leakage is because of the over-approximation [] they make during the analysis. For example, CacheAudit [] reports that the upper bound leakage of AES-128 exceeds the original key size. The dynamic methods take another way with a concrete input and run the program in real environment. Although they are very precise in term of true leakages, no existing tool can precisely assess the vulnerabilities they discover.

To overcome the above limitations, we propose a novel method to quantify information leakage more precisely. Different from previous works, which only consider the “average” information leakage, we study the problem based on real attack scenarios. The average information assumes that the target program will have **variable** sensitive information when the attacker launches the attack. However, for real-world attacks, an adversary may run the target problem again and over again

with **fixed** unknown sensitive information such as the key. Therefore, the previous threat model cannot catch real attack scenarios. In contrast, our method is more precise and fine-grained. We quantify the amount of leaked information as the cardinality of the set of possible inputs based on attackers' observations.

Before an attack, an adversary has a big but finite input space. Every time when the adversary observes one leakage site, he can eliminate some potential inputs and reduce the size of the input space. The smaller the input space is, the more information is actually gained. In an extreme case, if the size of the input space reduces to one, the adversary can determine the input information uniquely, which means all information (e.g., the whole secret key) is leaked. By counting the number of distinct inputs, we can quantify the information leakage more precisely.

We use math constraints to model the relation between the original sensitive input and each leakage site. We run the instruction level symbolic execution on the whole execution trace to generate the constraints. Symbolic execution can provide the fine-grained information but is usually believed to be an expensive operation in terms of performance. Therefore, existing dynamic symbolic execution based works [] either only analyze small programs or apply some domain knowledges to simplify the execution. We systematically analyze the bottleneck of the symbolic execution and optimize it scalable to real-world cryptosystems.

We apply the above technique and build a tool called *CleverHans*,¹ which could discover potential information leakage sites as well as estimating how many bits they can leak for each leakage site. We assume that adversaries can exploit secret-dependent control-flow transfers and data-access patterns when the program processes different sensitive data. First, we collect the dynamic execution trace for each input of the target libraries and then run symbolic execution on the traces. In this way, we model each side-channel leakage as a math formula. The sensitive input is divided into several independent bytes and each byte is regarded as a unique symbol. Those formulas can precisely model every the side-channel vulnerability. Then we extend the problem to multiple leakages and related leakages and introduce a monte carlo sampling method to estimate the total information leakage. In fact, if the application has a different sensitive input but still satisfies the formula, the code can still leak the same information.

We apply *CleverHans* on both symmetric and asymmetric ciphers from real-world crypto libraries including OpenSSL, mbedTLS. The experiment result confirms that *CleverHans* can precisely identify the pre-known vulnerabilities, reporting how much information is leaked and which byte in the original sensitive buffer is leaked. Although some of the analyzed crypto libraries have a number of side-channels, they actually leak very little information. Also, we perform the analysis

of widely deployed software countermeasures against side channels. Finally, we present new vulnerabilities. With the help of *CleverHans*, we confirm those vulnerabilities are easily to be exploited. Our results are superisngly different compared to previous results and much more useful in practice.

In summary, we make the following contributions:

- We propose a novel method that can quantify fine-grained leaked information from side-channel vulnerabilities. We model each side-channel vulnerabilities as math formulas and mutple side-channel vulnerabilities can be seen as the conjunction of those formulas, which precisely models the program semantics.
- We transfer the information quantification problem into a probabily distribution problem and use the Monte Carlo sampling method to estimate the information leakage. Some initial results indicate the the sampling method suffers from the curse of dimensionality problem. We therefore design a guided sampling method and provide the corresponding error esitmate.
- We implement the proposed method into a practical tool and apply it on several real-world software. *CleverHans* successfully identify the address-based side-channel vulnerabilities and provide the corresponding information leakge. The information leakage result provide the detailed information that help developers fix the vulnerability.

II. BACKGROUND

In this section, we first present an introduction to software-based side-channel attacks. Moreover, we analyze the root cause of many software-based side-channels. We find many of them are caused by two specific side-channel vulnerabilities, secret-dependent control-flow transfers and secret-dependent memory accesses. Therefore, we will focus on identifying and quantifying those leakages in the paper. After that, we discuss existing methods on side-channel detection and quantification.

A. Software-based Side-channels

Side-channels are information channels that can leak sensitive information unconsciously through different execution behaviors. Fundamentally, those differences were caused by shared hardware components (e.g., the CPU cache, the TLB and DRAM) in modern computer systems. Depending on the layer causing side-channels, we can classify them into the following types of side-channel attacks.

For example, cached-based side-channels rely on the time differences between cache miss and cache hit. We introduce two common attack strategies, namely Prime+Probe and Flush+Reload. Prime+Probe targets a single cache set. The attacker preloads cache set with its own data and wait until the victim execute the program. If the victim accesses the cache set and evicts part of the data, the attacker will experience a slow measurement. If not, it will be fast. By knowing which cache set the target program accesses, the attacker can infer locations of the sensitive information. Flush+Reload targets a single cache line. It requires the attacker and the victim share

¹ *CleverHans* is a horse that can "count". Our tool uses an advanced method to count the number of leaked bits from side channels.

```

unsigned long long r;
int secret[32];
while(i>0){
    r = (r * r) % n;
    if(secret[--i] == 1){
        r = (r * x) % n;
    }
}

```

Figure 1: Secret-dependent control-flow transfers

```

static char Fsb[256] = {...}
...
uint32_t a = *RK++ ^ \
(Fsb[(secret)) ^ \
(Fsb[(secret >> 8)] << 8 ) ^ \
(Fsb[(secret >> 16)] << 16 ) ^ \
(Fsb[(secret >> 24)] << 24 );
...

```

Figure 2: Secret-dependent memory accesses

the same memory address space. During the “flush” stage, an attacker flushes the “monitored memory” from the cache. Then the attacker waits for the victim to access the memory. In the next phase, the attacker reload the “monitored memory”. By measuring the time difference, the attacker can infer the sensitive information.

There are some other types of side-channels which target different hardware layers other than CPU cache as well. For example, the controlled-channel attack [4], where an attacker works in the kernel space, can infer sensitive data in the shielding systems by observing the page fault sequences after restricting some code and data pages.

The key intuition is that each side-channels above happens when the program accesses different memory addresses if the program has different sensitive inputs. More specifically, if a program show different patterns in control transfers or data accesses when the program processes different sensitive inputs, the program could possibly have side channels vulnerabilities. Different kinds of side-channels can be exploited to retrieve information in various granularities. For example, many cache channels can observe cache accesses at the level of a cache line. For most CPU, one cache line holds 64 bytes of data. So the low 6 bits the address is irrelevant in causing those cached-based side-channels.

B. Information Leakage Quantification

FIXME: Section (II.B), this subsection: Clarify mutual information, min-entropy, and maximal leakage. How many types? maybe use subsubsection

Given an event e which occurs with the probability $P(e)$, if the event e happens, then we receive

$$I = -\log_2 P(e)$$

bits of information by knowing the event e . Consider a char variable a in a C program, which has the size of one byte (8 bits). It ranges from 0 – 255. Assume a has the uniform distribution. If at one time we observe that a equals 1, the probability of this observation is $\frac{1}{256}$. So the information we get is $-\log(\frac{1}{256}) = 8$ bits, which is exactly the size of the char variable in the C program.

Some existing work on information leakage quantification is based on mutual information or min-entropy [5]. In these frameworks, the input sensitive information K is viewed as a

random variable. Let k_i be one of the possible value of K . The Shannon entropy $H(K)$ is defined as

$$H(K) = - \sum_{k_i \in K} P(k_i) \log_2 P(k_i)$$

The Shannon entropy can be used to quantify the initial uncertainty about the sensitive information. Suppose a program with some sensitive input K , an adversary has some observations (O) through some side channel. In this work, the observations are referred as the secret-dependent control-flows and secret-dependent data-access patterns. **FIXME:** See the comment above, we need to explain these patterns before this point. The conditional entropy $H(K|O)$ is

$$H(K|O) = - \sum_{o_j \in O} P(o_j) \sum_{k_i \in K} P(k_i|o_j) \log_2 P(k_i|o_j)$$

Intuitively, the conditional information marks the uncertainty about K after the adversary has gained some observations (O).

Some previous work uses the mutual information $I(K; O)$ to quantify the leakage which is defined as follows:

$$Leakage = I(K; O) = \sum_{k_i \in K} \sum_{o_j \in O} P(k_i, o_j) \log_2 \frac{P(k_i, o_j)}{P(k_i)P(o_j)}$$

where $P(k_i, o_i)$ is the joint discrete distribution of K and O . Alternatively, the mutual information can also be computed with the following equation:

$$Leakage = I(K; O) = H(K) - H(K|O) = H(O) - H(O|K)$$

For a deterministic program, once the input K is fixed, the program will have the same control-flow transfers and data-access patterns. As a result, $P(k_i, o_j)$ will always equals to 1 or 0. So the conditional entropy $H(O|K)$ will equal to zero. So the leakage defined by the mutual information can be simplified into:

$$Leakage = I(K; O) = H(O)$$

In other words, once we know the distribution of those memory-access patterns. We can calculate how much information is actually leaked.

Another common method is based on the maximal leakage [1], [5], [6]. The formal information theory can prove that the maximal leakage is the upper bound of the mutual information (Channel Capacity).

$$Leakage = \log(C(O))$$

Here $C(O)$ represents the number of different observations that an attacker can have.

III. THREAT MODEL

We consider an attacker who shares the same hardware resource with the victim. The attacker attempts to retrieve sensitive information via memory-based side-channel attacks. The attacker has no direct access to the memory or cache but can probe the memory or cache at each program point. In reality, the attacker will face many possible obstacles, including the

noisy observations, limited observations on memory or cache. However, for this project, we assume the attacker can have noise-free observations. The threat model captures most of the cache-based and memory-based side-channel attacks. We only consider the deterministic program for the project.

IV. METHOD

In the section, we provide a concrete example to show how we quantify the amount of leaked information from side-channel vulnerabilities. After that, we present the terminology in the paper and introduce the definition of the amount of leaked information.

A. A Toy Example

B. Definitions

V. CHALLENGES

In this section, we articulate several challenges and existing problems in quantifying the side-channel vulnerability leakages. We describe the challenges and then briefly present the corresponding solution.

A. Challenge I: Information Leakage Definition

Existing static-based side-channel quantification works [1] define information leakage as the mutual information or the maximal leakage. These definitions provide a strong security guarantee when trying to prove a program is secure enough if their methods calculate the program leaks zero bits of information.

```

1 unsigned char key = input();
2 // key = [0 ... 255]
3 if(key = 128)
4     A(); // branch 1
5 else if (key < 64)
6     B(); // branch 2
7 else if (key < 128)
8     C(); // branch 3
9 else
10    D(); // branch 4

```

Figure 3: Side-channel leakage

However, the above definition is less useful to justify the sensitive level of leakage sites. Considering the example code 3, if an attacker knows the code executes branch A by some observations, the attacker can know the key actually equals to 128. Suppose it is a dummy password checker, in which case the attacker can fully retrieve the password. Therefore, the total information leakage should be 8 bits, which equals to the size of unsigned char. According to the mutual definition, however, the leakage will be 1.7 bits. **FIXME:** use footnote or list the calculation process. The maximal information leakage is 2 bits. **FIXME:** the same with previous. Both approaches fail to tell how much information is leaked during the execution precisely.

The problem with the existing methods is that they are static-based and the input values are neglected by the previous definition. They assume the attacker runs the program multiple

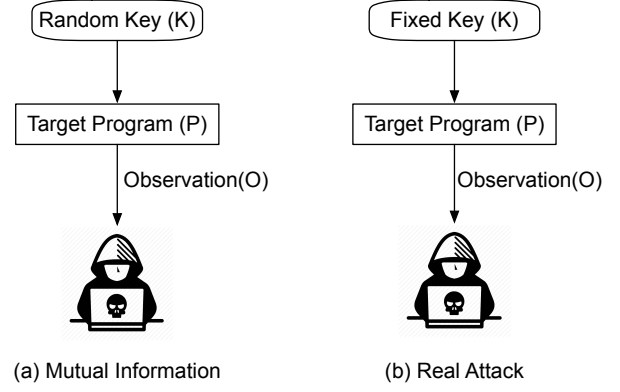


Figure 4: some caption here. **FIXME:** redraw the figure. i will give you the instruction.

times with many different sensitive information as the input. Both the mutual information and the max-leakage give an “average” estimate of the information leakage. However, it isn’t the typical scenario for an adversary to launch an side-channel attack. When a side-channel attack happens, the adversary wants to retrieve the sensitive information, in which case the sensitive information is fixed (e.g. AES keys). The adversary will run the attack over and over again and guess the value bit by bit. Like the previous example, the existing static method does not work well in those situations.

Our Solution to Challenge I: In the project, we hope to give a very precise definition of information leakages. Suppose an attacker run the target program multiple times with one fixed input, we want to know how much information he can infer by observing the memory access patterns. We come to the simple slogan [5] that

$$\text{Information leakage} = \text{Initial uncertainty} - \text{Remaining uncertainty}.$$

If an adversary has zero knowledge about the input before the attack. The initial uncertainty equals to the size the input. As for the remaining uncertainty, we come to the original definition of the information content. We quantify the information leakage with the following definition.

Definition 1. Given a program P with the input set K , an adversary has the observation o when the input $k \in K$. We denote it as

$$P(k) = o.$$

The leakage $L_{P(k)=o}$ based on the observation is

$$L_{P(k)=o} = \log_2 |K| - \log_2 |K^o|$$

where

$$K^o = \{k' | k' \in K \text{ and } P(k') = o\}$$

With the new definition, if the attacker observes that the code 3 runs the branch 1, then the $K^{o^1} = \{128\}$. Therefore, the information leakage $L_{P(k)=o^1} = \log_2 256 - \log_2 1 = 8$ bits, which means the key is totally leaked. If the attacker observes

the code runs other branches, the leaked information is shown in the following table.

Branch	1	2	3	4
K^o	1	64	64	127
$L_{P(k)=o}(\text{bits})$	8	2	2	1

Table I: The leaked information by the definition 1

With the same definition 1, if the attacker observes that the code run branch 2, the information leakage will be 2 bits. The conclusion is consistent with the intuition. Because if the branch 2 was executed, we can know the key is less than 64. So we know the most and the second significant digits of the value key equals to 128.

B. Challenge II: How to Combine the Leakage Information from Multiple Leak Sites

Real-world software can have various side-channel vulnerabilities. Those vulnerabilities may spread in the whole program. An adversary may exploit more than one side-channel vulnerabilities to gain more information [4], [7]. In order to precisely quantify the total information leakage, we need to know the relation of those leakage sites.

```

1 unsigned char k1, k2, k3;
2 ...
3 t1 = T[k1 % 8];          // Leakage 1
4 t2 = T[k2 % 8];          // Leakage 2
5 t3 = T[k3 % 8];          // Leakage 3
6 bool flag = foo(t1, t2, t3);
7 if(flag) {
8     if(k1 > 128)           // Leakage 4
9         A();
10 }
11 else {
12     if(k2 > 128)           // Leakage 5
13         B();
14 }
15 if(k1 + k2 + k3 > 128) // Leakage 6
16     C();

```

Listing 1: Multiple leakages

Consider the running example in 1, in which $k1$, $k2$ and $k3$ are the sensitive key. The code has six different leakage. Leakage 1, 2, 3 are the secret-dependent data accesses and leakage 4, 5, 6 are the secret-dependent control-flow transfers. The attacker can infer the last three digits of $k1$, $k2$, $k3$ from leakage 1, 2, 3. So those leakages are independent. For leakage 1, 4, 6, however, we have no idea about the total information leakage.

Suppose one program has two side-channel vulnerabilities A and B, which can leak L_A and L_B bits respectively according to the definition 1. Depending on the relation between A and B, the total leaked information L_{Total} will be:

1) *Independent Leakages*: If A and B are independent leakages, the total information leakage will be:

$$L_{total} = L_A + L_B$$

2) *Dependent Leakages*: If A and B are dependent leakages, the total information leakage will be:

$$\max\{L_A, L_B\} \leq L_{total} < L_A + L_B$$

3) *Mutual Exclusive Leakages*: If A and B are mutual exclusive leakages, then only A or B can be observed for one fixed input.

$$L_{total} = \begin{cases} L_A & \text{only A} \\ L_B & \text{only B} \end{cases}$$

According to above definition, leakage 1, 2, 3 are independent leakages. Leakage 4, 5 are mutual exclusive leakages. For real-world applications, it is hard to estimate the total leaked information for the following reasons. First, the real-world applications have more than thousands of lines of code. One leakage site leaks the temporary value. But the value contains some information about the original buffer. It is hard to know how the sensitive value affects the temporary value. Second, some leakages sites may be dependent. The occurrence of the first affects the occurrence of the second sites. We can't simply add them up. Third, leakage sites are in the different blocks of the control-flow graph, which means that only one of the two leakages site may be executed during the execution.

Our Solution to Challenge II: Given a program P with k as the sensitive input, we use k_i to denote the sensitive information, where i is the index of the byte in the original buffer. We can represent each temporal values with a formula. There are two types of values in the formula: the concrete value and the symbolic value. We use the runtime information to simplify the formula. In other words, we only use symbolic values to represent the sensitive input. For other values that are independent from the sensitive input, we use the concrete value from the runtime information.

After that, we model each leakage sites as a math formulas. The attacker can retrieve the sensitive information by observing the different patterns in control-flows and data access when the program process different sensitive information. We refer them as the secret-dependent control flow and secret-dependent data access accordingly. For secret-dependent control transfers, we model the leakage using the path conditions that cause the control transfer. For secret-dependent memory accesses, we use a symbolic formula $F(\vec{K})$ to represent the memory address and check if different sensitive inputs can lead to different memory accesses. As long as we model each leakage with a formula. We can regard multiple leakages as the conjunction of those formulas.

C. Challenge III: Scalability and Performance

After we transfer each potential leaks sites into formula. We can group several formulas together to estimate the total information leakage. One naive way is to use the Monte Carlo sampling estimate the number of input keys. With the definition 1, we can estimate the total information leakage.

However, some pre-experiments show that above approach suffers from the unberable cost, which impede its usage to detect and quantify side-channel leakages in real-world applications. We systematically analyze the performance bottlenecks of the whole process. In general, the performance suffers from the two following reasons.

- Symbolic Execution (Challenge III(a))
- The Monte Carlo Sampling (Challenge III(b))

1) *Symbolic Execution*: Symbolic execution interprets each instruction and update the memory cells and registers with a formula that captured the semantics of the execution. Unfortunately, the number of machine instructions are huge and the semantics of each instruction is complex. For example, the Intel Developer Manual [8] introduces more than 1000 different X86 instructions. It is tedious to manually implement the rules for every instructions.

Therefore, existing binary analysis tools [9], [10] will translate machine instructions into intermediate languages (IR). The IR typically has fewer instructions compared to the original machine instructions. The IR layer designs, which significantly simplify the implementations, also introduce significant overhead as well [11].

Our Solution to Challenge III(a): We adopt the similar approach from [11] and implement the symbolic execution directly on the top X86 instructions.

2) *Monte Carlo Sampling*: For an application with m bytes secret, there are total 2^{8m} possible inputs. Of the 2^{8m} possible inputs, we want to estimate the number of inputs that satisfy those formulas. Then we can use the definition /refdef to calculate the information leakage.

A Monte Carlo method for approximating the number of $|K_o|$ is to pick up M random values and check how many of them satisfy those constrains. If l values satisfy those constrains, then the approximate result is $\frac{l \cdot 2^{8m}}{M}$.

However, the number of satisfying values could be exponentially small. Consider the formula $F = k_1 = 1 \wedge k_2 = 2 \wedge k_3 = 3 \wedge k_4 = 4$, k_1, k_2, k_3 and k_4 each represent one byte in the original sensitive input, there is only one possible solution of 2^{32} possible values, which requires exponentially many samples to get a tight bound. The naive Monte Carlo Method also suffers from the curse of dimensionality. For example, the libjpeg libraries can transfer the image from one format into another format. One image could be 1kMB. If we take each byte in the original buffer as symbols, the formula can have at most 1024 symbols.

Our Solution to Challenge III(b): We adopt the Markov Chain Monte Carlo to estimate the number of possible input that satisfies the logic formula groups. The key idea is that we have one group of input that satisfies the logic formula constrains. We will introduce the method in the following sections.

VI. DESIGN

In this section, we describe the design of CleverHans by focusing on how our design solves the three challenges discussed in the previous section.

A. Workflow

The shortcomings of the existing work inspire us to design a new tool to detect and quantify information leakage vulnerabilities in binaries. The tool has three steps. First, we run the target program with the concrete input (sensitive information)

under the dynamic binary instrumentation (DBI) frameworks to collect the execution traces. After that, we run the symbolic execution to capture the fined-grained semantic information of each secret-dependent control-flow transfers and data-accesses. Finally we run the Markov Chain Monte Carlo (MCMC) to estimate the information leakage.

- 1) *Execution trace generation*. The design goal of CleverHans is to estimate the information leakage as precisely as possible. Therefore, we sacrifice the soundness for the precision in terms of program analysis. Previous works [2], [3] have demonstrated the effectiveness of the dynamic program analysis. We run the target binary under the dynamic binary instrumentation (DBI) to record the execution trace and the runtime information.
- 2) *Instruction level symbolic execution*. We model the attacker's observation about the side-channel vulnerabilities with math formula. Each formula capture the fined-grained information between the input secrets and the leakage site. For the consideration of precision and performance, we remove the intermediate language(IR) layer of the symbolic execution. Also, the engine only symbolically execute the instruction that might be affected the input key. The above design significantly reduces the overhead of the symbolic execution, which make the tool scales to real-world programs.
- 3) *Leakage estimation*. We transfer the information leakage quantification problem into the problem of counting the number of assignments that satisfy the formulas which models the observations from attacker. We propose a markov monte carlo method to estimate the number of satisfied solutions. With the help of Chebyshev's Inequality, we also give the an error estimate with the probability.

B. Trace Logging

The trace information can be logged via some emulators (e.g., QEMU) or dynamic binary instrumentation tools (DBI). We run a program with the concrete input under the DBI to record the execution trace. The trace data has the following information:

- Each instruction mnemonics and its memory address.
- The operands of each instruction and their concrete values during the runtime.
- The value of eflags register.
- The memory address and the length of the sensitive information. Most software developers stores sensitive information in an array, a variable or a buffer, which means that those data is stored in a contiguous area in the memory. We use the symbol information in the binary to track the address in the memory.

C. Instruction Level Symbolic Execution

The main purpose of the step is to generate constraints of the input sensitive information from the execution trace. If we give the target program a new input which is different from the origin input that was used to generate the execution trace

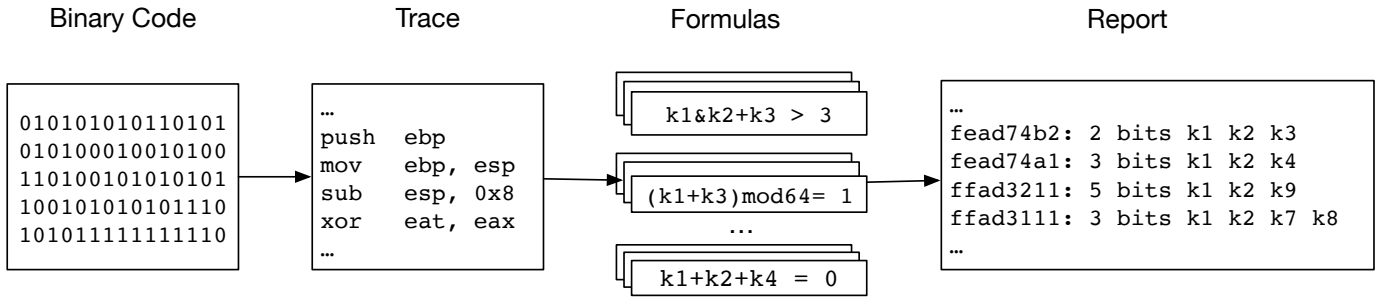


Figure 5: The workflow of CleverHans. **FIXME:** redraw the figure. i will give you the instruction.

but still satisfies those constraints, the new execution trace still have the same control flow and data access patterns.

The tool runs the symbolic execution on the top of the execution traces. At the beginning of the symbolic execution, the tool creates fresh symbols for each byte in the sensitive buffer. For other data in the register or memory at the beginning, we use concrete values from the runtime information collected during the runtime. During the symbolic execution for each instruction, the tool updates every variable in the memory and registers with a math formula. The formula is made up with concrete values and the input key as the symbols accumulated through the symbolic execution. For each formula, the tool will check whether it can be reduced into a concrete values (e.g., $k_1 + 12 - k_1 = 12$). If so, the tool will only use the concrete values in the following symbolic execution.

1) *Verification and Optimization:* We run the symbolic execution (SE) on the top of x86 instructions. In other words, we don't rely on any intermediate languages to simplify the implementation of symbolic execution. While the implementation itself has a lot of benefits (Better performance, accurate memory model), we need to implement the symbolic execution rules for each X86 instruction. However, due to the complexity of X86, it is inevitable to make mistakes. Therefore, we verify the correctness of the SE engine during the execution. The tool will collect the runtime information (Register values, memory values) and compare them with the formula generated from the symbolic execution. Whenever the tool finishes the symbolic execution of each instruction, the tool will compare the formula for each symbol and its actual value. If the two values don't match, we check the code and fix the error. Also, if the formula doesn't contain any symbols, the tool will use the concrete value instead of symbolic execution.

2) *Secret-dependent control-flows:* An adversary can infer sensitive information from secret dependent control-flows. There are two kinds of control-transfer instructions: the unconditional control-transfer instructions and the conditional transfer instructions. The unconditional instructions, like CALL, JUMP, RET transfer control from one code segment location to another. Since the transfer is independent from the input sensitive information, an attacker was not able to infer any sensitive information from the control-flow. So the unconditional control-transfer doesn't leak any information based

on our threat model. During the symbolic execution, we just update the register information and memory cells with new formulas accordingly.

The conditional control-flow transfer instructions, like conditional jumps, depending on CPU states, may or may not transfer control flows. For conditional jumps, the CPU will test if certain condition flag (e.g., CF = 0, ZF = 1) is met and jump to the certain branches respectively. The symbolic engine will compute the flag and represent the flag in a symbol formula. Because we are running on a symbolic execution on a execution trace, we know which branch is executed. If a conditional jump uses the CPU status flag, we will generate the constraint accordingly.

<pre> 1 804841c: movzx edx, byte ptr [ebp+var_D]; 2 8048420: movzx eax, byte ptr [ebp+var_C]; 3 8048424: add eax, edx; 4 8048426: mov byte ptr [ebp+var_B], al; 5 8048429: movzx edx, byte ptr [ebp+var_C]; 6 804842d: movzx eax, byte ptr [ebp+var_A]; 7 8048431: sub edx, eax; 8 8048433: mov eax, edx; 9 8048435: mov byte ptr [ebp+var_9], al; 10 8048438: movzx eax, byte ptr [ebp+var_B]; 11 804843c: cmp al, byte ptr [ebp+var_9]; 12 804843f: jnb 0x8048451; 13 8048441: sub esp, 0xc; </pre>	<pre> var_D = k1; edx = zeroext(k1) var_C = k2; eax = zeroext(k2) eax = zeroext(k1) + zeroext(k2); edx = zeroext(k1) var_B = k1 + k2 var_C = k2; edx = zeroext(k2) var_A = k3; eax = zeroext(k3) eax = zeroext(k3); edx = zeroext(k2) - zeroext(k3) var_9 = k2 - k3 eax = k1 + k2 CF = (k1 + k2) > (k2 - k3) Formula: k1 ≤ k3 esp = C1 - 0xc </pre>
---	--

Figure 6: The workflow of CleverHans

For examples,

```

...
0x0000e781  add dword [local_14h], 1
0x0000e785  cmp dword [local_14h], 4
0x0000e789  jne 0xe7df
0x0000e78b  mov dword [local_14h], 0
...

```

At the beginning of the instruction segment, the value at the address of local14h can be written as $F(\vec{K})$. At the address e785, the value will be updated with $F(\vec{K}) + 1$. Then the code compares the value with 4 and use the result as a conditional jump. Based on the result, we can have the following formula:

$$F(\vec{K}) + 1 = 4$$

The formula, together with the memory address (0xe789) is store as a *formula tuple (address, formula)*. Each formula tuple represents one leakage site.

3) *Secret-dependent data access*: Like input-dependent control-flow transfers, an adversary can also infer sensitive information from the data access pattern as well. We try to find this kind of leakages by checking every memory operand of the instruction. We generate the memory addressing formulas. As discussed before, every symbols in the formula is the input key. If the formula doesn't contain any symbols, the memory access is independent from the input sensitive information and won't leak any sensitive information according to our threat model. Otherwise, we will generate the constraint for the memory addressing. We model the memory address with a symbolic formula $F(\vec{K})$. Because we also have the concrete value of the memory address $Addr1$. Inspired by the work from [2], the formula can be written as:

$$F(\vec{K}) \gg L = Addr1 \gg L$$

The L represents the minimum memory address granularity that an attacker can observe. For example, Flush and Reload can distinguish between different cache lines, which means the value of L is 6.

4) *Information Flow Check*: CleverHans is designed to help software developers find and understand the side-channel vulnerabilities. To ease the procedure of fixing the bug, we also track the information flow for each byte of the input buffer. The step can be seen as the multiple-tag taint analysis. With the help of the information from symbolic execution, we can implement a relative simple but relatively precise information flow track. At the beginning of the analysis, CleverHans keep a track for each byte in the original buffer. When CleverHans symbolically executes each instruction in the trace, it will check every value read from registers or memory. If the value is concrete, it means the instruction has nothing to do with the original buffer. If the value is a formula, it means the original information pass through the instruction. Since each byte in the sensitive buffer is represented as a symbol with a unique ID, CleverHans can know which byte in the origin buffer actually goes through the instruction.

D. Information Leakage Estimation

In this section, we present the algorithm to calculate the information leakage based on the definition 1.

1) *Problem Statement*: From the above step VI-C, we can generate the constraint for each unique leakage site on the execution trace. The only variables in those constraints are the sensitive data represented with k_1, k_2, \dots, k_n . Suppose the address of the leakage site is $addr_i$, we use C_{addr_i} to denote the constraint. For multiple leakage sites, as each leakage site has one constraint, we use the conjunction of those constraints to represent those leakage sites.

According to the definition 1, to calculate the amount of leaked information, the key is calculating $\frac{|K|}{|K^o|}$. K^o represents the set that contains every input keys that satisfy the constraint. As the cardinality of K is known, the key problem is to know the cardinality of K^o . Suppose an attacker can observe n leakage sites, and each leakage site has the following

constraints: $C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}$ respectively. The total leakage has the constraint $F_{c_{addr_1}, c_{addr_2}, \dots, c_{addr_n}} = C_{addr_1} \wedge C_{addr_2} \wedge C_{addr_3} \wedge \dots \wedge C_{addr_n}$. The problem of estimating the total leaked information can be reduced to the problem of counting the number of different solutions that satisfies the constraint $F_{c_{addr_1}, c_{addr_2}, \dots, c_{addr_n}}$.

A native method for approximating the result is to pick k elements from K and check how many of them are also contained in K^o . If q elements are also in K^o . In expectation, we can use $\frac{k}{q}$ to approximate the value of $\frac{|K|}{|K^o|}$.

However, as the discussion in V-C2, the above sampling method will typically fail in practice due to the following two problems:

- 1) The curse of dimensionality. $F_{c1...cn}$ is the conjunction of many constraints. Therefore, the input variables of each constraints will also be the input variables of the $F_{c1...cn}$. The sampling method will fail as n increases. For example, when n equals to 2, the whole search space is a 256^2 cube. If we want the sampling distance between each point still to 1, we need 256^2 points. When n equals to 10, we need 256^{10} points if we still want the distance between each points equals to 1.
- 2) The number of satisfying assignments could be exponentially small. According to Chernoff bound, we need exponentially many samples to get a tight bound. On an extreme situation, if the constraint only has one unique satisfying solution, the simple Monte Carlo method can't find the satisfying assignment after sampling many points.

However, despite the two problems. We also observe the constrain $F_{(c_{addr_1}, c_{addr_2}, \dots, c_{addr_n})}$ has the following characteristics:

- 1) $F_{(c_{addr_1}, c_{addr_2}, \dots, c_{addr_n})}$ is the conjunction of several short constraints $F_{c_{addr_i}}$. The set containing the input variables of $F_{c_{addr_i}}$ is the subset of the input variables of $F_{(c_{addr_1}, c_{addr_2}, \dots, c_{addr_n})}$. Some constraints have completely different input variables from other constraints.
- 2) For each constraint $F_{c_{addr_i}}$, the satisfying assignments are close to each other, which means if we find one satisfying assignment, we are more likely to find other satisfying assignments nearby than randomly pick one point in the whole searching space.

In regard to the above problems, we present our methods. First, we split $F_{(c_{addr_1}, c_{addr_2}, \dots, c_{addr_n})}$ into several independent constraint groups. After that, we run random-walk based sampling method on each constraint.

2) *Maximum Independent Partition*: For a constraint C_i , we define the function S , which maps the constraint into the set consisting of input symbols. For example, $S(k1 + k2 > 128) = \{k1, k2\}$.

Definition VI.1. Given two constraints C_m and C_n , we call them independent iff

$$S(C_m) \cap S(C_n) = \emptyset$$

Based on the definition, we can split the constraint $F_{c_{addr_1}, c_{addr_2}, \dots, c_{addr_n}}$ into several independent constraints.

There are many partitions. For our project, we are interested in the following one.

Definition VI.2. For the constraint $F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}}$ and the constraint group $C_{g1}, C_{g2}, \dots, C_{gm}$, we call $C_{g1}, C_{g2}, \dots, C_{gm}$ is the maximum independent partition of $F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}}$ iff

- 1) $C_{g1} \wedge C_{g2} \wedge \dots \wedge C_{gm} = F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}}$
- 2) $\forall i, j \in \{1, 2, 3, \dots, m\}$ and $i \neq j$, $S(C_{gi}) \cap S(C_{gj}) = \emptyset$
- 3) For any other partitions $C_{g1}, C_{g2}, \dots, C_{gn}$ satisfy 1) and 2), $m \geq n$

The reason we want a good partition of the constraints is that we want to reduce the dimensions. Consider the example in the previous section,

$$F : k_1 = 1 \wedge k_2 = 2 \wedge k_3 = 3 \wedge k_4 = 4$$

The good partition of F would be

$$C_{g1} : k_1 = 1 \quad C_{g2} : k_2 = 2 \quad C_{g3} : k_3 = 3 \quad C_{g4} : k_4 = 4$$

So instead of sampling in the four dimension space, we can sample each constraint in the one dimension space and combine them together with VI-D2.

Theorem VI.1. $C_{g1}, C_{g2}, \dots, C_{gm}$ is a maximum independent partition of $F_{C_{c1}C_{c2}\dots C_{cn}}$. K_c is the input set that satisfies constrain c . We can have the following equation in regard to the size of K_c

$$|K_{F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}}}| = |K_{C_{g1}}| * |K_{C_{g2}}| * \dots * |K_{C_{gn}}|$$

With the VI.1, we can transfer the problem of counting the number of solutions in a large constraint with high dimensions into counting solutions of several small constraints. We apply the following algorithm to get the Maximum Independent Partition of the $F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}}$.

Algorithm 1: The Maximum Independent Partition

```

input :  $F_{C_{addr_1}, C_{addr_2}, \dots, C_{addr_n}} =$ 
 $C_{addr_1} \wedge C_{addr_2} \wedge \dots \wedge C_{addr_m}$ 
output: The Maximum Independent Partition of
 $G = \{C_{g1}, C_{g2}, \dots, C_{gm}\}$ 
1 for  $i \leftarrow 1$  to  $n$  do
2    $S_i \leftarrow S(C_{addr_i})$ 
3   for  $C_{gi} \in G$  do
4      $S_{gi} \leftarrow S(C_{gi})$ 
5      $s \leftarrow S_i \cap S_{gi}$ 
6     if  $s \neq \emptyset$  then
7        $C_{gi} = C_{gi} \wedge C_{addr_i}$ 
8       break
9   end
10  Insert  $C_{addr_i}$  to  $G$ 
11 end
12 end

```

3) *Multiple Step Monte Carlo Sampling:* After we split those constraints into several small constraints, we count the number of solutions for each constraint. Even though the dimension has been reduced greatly after the previous step, this is still a #P problem. For our project, we apply the approximate counting instead of exact counting for two reasons. First, we don't need to have a very precise result of the exact number of total solutions. The information is defined with a logarithmic function. We don't need to distinguish between a constraint having 10^{10} and $10^{10} + 10$ solutions. Second, as the constraint could be very complicated. The exact model counting approaches, like DPLL search, have difficulty scaling up to large problem sizes.

We apply the "counting by sampling" method. The basic idea is as follows. For the constraint $C_{gi} = C_{i1} \wedge C_{i2} \wedge \dots \wedge C_{ij} \wedge \dots \wedge C_{im}$, if the solution satisfies C_{gi} , it also satisfies any constraint from C_{i1} to C_{im} . In other words, $K_{C_{gi}}$ should be the subset of $K_{C_{i1}}, K_{C_{i2}}, \dots, K_{C_{im}}$. We notice that C_i usually has less numbers of input compared to C_{gi} . For example, if C_{ij} has only one input variable, we can find the exact solution set $K_{C_{ij}}$ of C_{ij} by trying every possible 256 solutions. After that, we can only generate random input numbers for the rest input variables in constraint C_{gi} . With the simple trick, we can reduce the number of input while still ensure the accuracy.

Algorithm 2: Metropolis Sampling

Input: The constraint $C_{gi} = C_{i1} \wedge C_{i2} \wedge \dots \wedge C_{im}$
Output: The number of assignments that satisfy C_{gi}
 $|K_{C_{gi}}|$

```

1  $n$ : the number of sampling times
2  $P$ : a probability generator
3  $k$ : the input assignment
4  $n_s$ : the number of satisfying assignments
5  $SN_k$ : the satisfying number of  $k$ 
6 Initialization:
7  $SN_{k_0} \leftarrow \sum_{j=1}^m C_{ij}(k_0)$ 
8 for  $t \leftarrow 1$  to  $n$  do
9    $p \leftarrow P$ 
10  if  $p \geq 0.5$  then
11     $v \leftarrow \text{RandomWalk } v$ 
12  end
13  else
14     $v \leftarrow \text{MetropolisMove } v$ 
15  end
16  if  $v$  satisfies  $C_{gi}$  then
17     $n_s \leftarrow n_s + 1$ 
18  end
19 end
20  $|K_{C_{gi}}| \leftarrow n_s |K| / n$ 

```

4) *Error Estimation:* In this part, we analyze the accuracy of Monte Carlo approximate result. We use central limit theorem (CLT) and propagation of uncertainty to estimate errors of the number of leaked bits for each site.

Let n be the number of samples and n_s be the number of samples that satisfy the constraint C . Then we can get $\hat{p} = \frac{n_s}{n}$. If we run the same experiment multiple times, each time we can get a p . As each experiment is independent from others, according to central limit theorem, the sample mean \bar{p} should satisfy normal distribution.

$$\frac{\bar{p} - E(p)}{\sigma\sqrt{n}} \rightarrow N(0, 1)$$

Here $E(p)$ is the mean value of p and σ is the standard variance of p . If we use the observed value \hat{p} to the represent standard deviation. We can claim that we have 95% confidence that the error $\Delta p = \bar{p} - E(p)$ falls in the interval:

$$|\Delta p| \leq 1.96\sqrt{\frac{\hat{p}(1-\hat{p})}{n}}$$

Since we use $L = \log_2 p$ to estimate the amount of leaked information, we can have the following error propagation formula $\Delta L = \frac{\Delta p}{p \ln 2}$ by differentiation. For CleverHans, we want the error of estimated leaked information (ΔL) is less than 1 bit. So we can get $\frac{\Delta p}{p \ln 2} \leq 1$. As long as $n \geq \frac{1.96^2(1-p)}{p(\ln 2)^2}$, we have 95% confidence the error of estimated leaked information is less than 1 bit. During the simulation, if n and p satisfy the above equation, the Monte Carlo Simulation will terminate.

VII. IMPLEMENTATION

We implement the CleverHans with 14320 lines of code in C++11. It has three components, Intel Pin tool that can collect the execution trace, the instruction-level symbolic execution engine, and the backend that can estimate the information leakage. The tool can also report the memory address of the leakage site. To assist the developers fix the bugs, we also have several Python scripts that can report the leakage location in the source code with the debug information and the symbol information. A sample report can be found at the appendix.

Component	Lines of code (LOC)
Trace Logging	101 LOC of C++
Symbolic Execution	11547 LoC of C++
Data Flow	451 LoC of C++
Monte Carlo Sampling	603 LOC of C++
Others	FIXME: xxx LOC of Python
Total	FIXME: xxx LOC

Table II: CleverHans’s main components and their LoC

Our current implementation supports part of the Intel 32-bit instructions, including bitwise operations, control transfer, data movement, and logic instructions which are essential in finding memory-based side-channel vulnerabilities. For other instructions the current implementation does not support, the tool will use the real values to update the registers and memory cell. Therefore, the tool may miss some leakages but will not give us any new false positives with the implementation.

VIII. EVALUATION

We evaluate CleverHans with the real-world crypto libraries and non-crypto libraries. For crypto libraries, we choose

OpenSSL, mbedTLS and NaCl. OpenSSL and mbedTLS are the two most commonly used crypto libraries in today’s software. NaCl (pronounced "salt") is a new software library for encryption, decryption and signatures, etc. NaCl is designed to have no data flow from secrets to load address and no data flow from secrets to branch conditions. Therefore, NaCl should have no leakage under our attack model.

We build the source code into 32-bit x86 Linux executables with the GCC 8.0 on Ubuntu 14.04. Although we use use symbol information to track back leakage sites in the source code, our tool can work on stripped binaries as well. We use Intel Pin version 3.7 to record the execution trace. We run our experiments on a 2.90GHz Intel Xeon(R) E5-2690 CPU with 128GB RAM memory. During our evaluation process, we are interested in the following two aspects:

- 1) Is CleverHans effective to detect side-channels in real-world crypto systems?
- 2) Can CleverHans precisely report the number of leaked bits in open source libraries?
- 3) Recent work has reported a number of side-channel vulnerabilities in open source libraries. Is the number of leaked bits reported by CleverHans useful to justify the sensitive level of side-channel vulnerabilities?

A. Evaluation Result Overview

In this section, we present an overview of the evaluation result. CleverHans find 883 leakages in total from real-world crypto system libraries. Among those 883 leak points, 205 of them are leaked due to secret-dependent control-flow transfers and 678 of them are leaked due to secret-dependent memory accesses.

For crypto libraries, CleverHans finds that secret-dependent memory accesses cause most leakages. CleverHans also identifies that most side-channel vulnerabilities leak very little information in practice, which confirms our initial assumptions. However, we do find some sensitive leakages. Some of them have been confirmed by existing research that those vulnerabilities can be exploited to realize real attacks.

All the symmetric key implementations in OpenSSL and mbedTLS all yield significant leakages due to the implementation of the lookup table to speed up the computation. Every leakages found during the evaluation belongs to the type of secret-dependent memory accesses. We believe that the secret-dependent control-flow transfers have been widely studied in the past few years, and developers have patched most of those leakages. One method to address the leakage is to use bit-slicing. We will analyse the corresponding countermeasure in the following sections.

CleverHans find several leakage sites for both the implementation of DES and AES in OpenSSL and mbedTLS. CleverHans confirm that all those leakages come from table lookups implementations. mbed TLS 2.15 and 2.5 have the same implementation of DES and AES so they have the same leakage report. One proper fix would be a scalar bit-sliced implementations. However, we don’t see the bit-sliced

Table III: Result

Algorithm	Implementation	Leakage Sites	CF	DF	# Instructions	Max Leakage	Sym. Exe.	Monte Carlo
						bits	ms	ms
AES	MBED TLS 2.5	68	0	68	39,855	8	570	850
AES	MBED TLS 2.15	68	0	68	39,855	8	550	829
AES	openssl 0.9.7	75	0	75	1,704	10	319	7,720
AES	openssl 1.0.2f	88	0	88	1,350	12	72	1,500
AES	openssl 1.0.2k	88	0	88	1,350	11	83	1,441
AES	openssl 1.1.0f	88	0	88	1,420	12	87	1,454
AES	openssl 1.1.1	88	0	88	1,586	8	91	1,250
DES	MBED TLS 2.5	15	0	15	4,596	1	114	144
DES	MBED TLS 2.15	15	0	15	4,596	1	106	137
DES	openssl 0.9.7	6	0	6	2,976	7	149	4,193
DES	openssl 1.0.2f	8	0	8	2,593	9	239	5,311
DES	openssl 1.0.2k	8	0	8	2,593	9	235	5,080
DES	openssl 1.1.0f	8	0	8	4,260	9	256	5,027
DES	openssl 1.1.1	6	0	6	8,272	7	235	4,584
							minutes	minutes
RSA	MBED TLS 2.5	6	6	0	22,109,246	9	37m 51s	20m 10s
RSA	MBED TLS 2.15	12	0	12	24,484,441	9	39m 17s	4h 1m 16s
RSA	openssl 0.9.7	105	103	2	16,980,109	13	28m 26s	4h 26m 33s
RSA	openssl 1.0.2f	38	27	11	14,468,307	10	28m 11s	2h 40m 17s
RSA	openssl 1.0.2k	36	27	9	15,285,210	12	39m 13s	4h 42m 43s
RSA	openssl 1.1.0f	31	22	9	16,390,750	13	32m 48s	4h 21m 58s
RSA	openssl 1.1.1	26	20	6	18,207,020	12	7m 3s	7h 35m
Total		883	205	678	128,042,089		213m	1688m

implementation of AES and DES in various versions of OpenSSL and mbedTLS.

B. Information Leakage Quantification

C. Analysis of Software Countermeasures

- 1) *Bit-slicing*;
- 2) *Scatter and Gather*;

IX. DISCUSSION

A. Binary code vs source code

Many existing works find side-channels vulnerabilities from source code level or intermediate languages (e.g., LLVM IR). Those approaches will have the following limitations. First, many compilers can translate the operator into branches. For example, the GCC compiler will translate the `!` operator into conditional branches. If the branch is secret-dependent, the attacker could learn some sensitive information. But those source-based methods will fail to detect those vulnerabilities. Second, some if else in the source code can be converted into single conditional instructions (e.g., `cmov`). Source-based methods will still regard them as potential leakages, which will introduce false positives.

B. X86 vs Intermediate Languages

The tool takes in an unmodified binary and the marked sensitive information as the input. The tool will first start with logging the execution trace. Then the tool will symbolizes the sensitive information into multiple symbols and start with the symbolic execution. During the symbolic execution, the tool will find any potentials leakage sites as well as the path constraints. For each leakage site, whether it is the tool will also generate the constraint. Then, the tool will split the constraints into multiple group. After that, the tool will run monte carlo sampling to estimate the total information leakages.

X. RELATED WORK

A. Side-channel vulnerabilities detection

There are a large number of works on side-channel vulnerability detections in recent years. CacheAudit [1] uses abstract domains to compute the overapproximation of cache-based side-channel information leakage upper bound. However, due to over approximation, the leakage given by CacheAudit can indicate the program is side-channel free if the program has zero leakage. However, it is less useful to judge the sensitive level of the side-channel leakage based on the leakage provided by CacheAudit. CacheS [12] improves the work of CacheAudit by proposing the novel abstract domains, which only precisely track secret related code. Like CacheAudit, CacheS can't provide the information to indicate the sensitive level of side-channel vulnerabilities.

The dynamic approach, usually comes with taint analysis and symbolic execution, can perform a very precise analysis. CacheD [2] takes a concrete program execution trace and run the symbolic execution on the top of the trace to get the formula of each memory address. During the symbolic execution, every value except the sensitive key uses the concrete value. Therefore, CacheD is quite precise in term of false positives. We adopted the similar idea to model the secret-dependent memory accesses. DATA [3] detects address-based side-channel vulnerabilities by comparing execution traces under various tests.

B. Quantification

Quantitative Information Flow (QIF) aims at providing an information leakage estimate for the sensitive information given the public output. If zero bits of the information is leaked, the program is called non-interference. McCamant [13] quantify the information leakage as the network flow capacity.

Phan [14] propose symbolic QIF. The goal of their work is to ensure the program is non-interference. They adopt an over approximation way of estimating the total information leakage and their method doesn't work for secret-dependent memory access side-channels. CHALICE [15] quantifies the information for a given cache behaviours. But CHALICE can only work on PICA code (a MIPS like architecture), which limits its usage to X86 binary. CHALICE can only scale to small programs, which limits its usage in real-world applications.

XI. CONCLUSION

In this paper, we present CleverHans for identifying and quantifying memory-based side-channel leakages. We show that CleverHans is effective at finding and quantifying the side-channel leakages. With the new definition of information leakage that imitates an real side-channel attacker, the number of leaked bits is useful to justify the understand the sensitive level of side-channel vulnerabilities. The evaluation results confirm our design goal and show CleverHans is useful to estimate the amount of leaked information in real-world applications.

REFERENCES

- [1] G. Doychev, D. Feld, B. Kopf, L. Mauborgne, and J. Reineke, "Cacheaudit: A tool for the static analysis of cache side channels," in *Presented as part of the 22nd USENIX Security Symposium (USENIX Security 13)*. Washington, D.C.: USENIX, 2013, pp. 431–446. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity13/technical-sessions/paper/doychev>
- [2] S. Wang, P. Wang, X. Liu, D. Zhang, and D. Wu, "CacheD: Identifying cache-based timing channels in production software," in *26th USENIX Security Symposium (USENIX Security 17)*. Vancouver, BC: USENIX Association, 2017, pp. 235–252. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity17/technical-sessions/presentation/wang-shuai>
- [3] S. Weiser, A. Zankl, R. Spreitzer, K. Miller, S. Mangard, and G. Sigl, "DATA – differential address trace analysis: Finding address-based side-channels in binaries," in *27th USENIX Security Symposium (USENIX Security 18)*. Baltimore, MD: USENIX Association, 2018, pp. 603–620. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity18/presentation/weiser>
- [4] Y. Xu, W. Cui, and M. Peinado, "Controlled-channel attacks: Deterministic side channels for untrusted operating systems," in *2015 IEEE Symposium on Security and Privacy*, May 2015, pp. 640–656.
- [5] G. Smith, "On the foundations of quantitative information flow," in *Foundations of Software Science and Computational Structures*, L. de Alfaro, Ed. Berlin, Heidelberg: Springer Berlin Heidelberg, 2009, pp. 288–302.
- [6] B. Kopf, L. Mauborgne, and M. Ochoa, "Automatic quantification of cache side-channels," in *Computer Aided Verification*, P. Madhusudan and S. A. Seshia, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2012, pp. 564–580.
- [7] D. Gruss, R. Spreitzer, and S. Mangard, "Cache template attacks: Automating attacks on inclusive last-level caches," in *24th USENIX Security Symposium (USENIX Security 15)*. Washington, D.C.: USENIX Association, 2015, pp. 897–912. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity15/technical-sessions/presentation/gruss>
- [8] Intel Corporation, *Intel® 64 and IA-32 Architectures Software Developer's Manual*, December 2009, no. 253669-033US.
- [9] Y. Shoshitaishvili, R. Wang, C. Salls, N. Stephens, M. Polino, A. Dutcher, J. Grosen, S. Feng, C. Hauser, C. Kruegel, and G. Vigna, "SoK: (State of) The Art of War: Offensive Techniques in Binary Analysis," in *IEEE Symposium on Security and Privacy*, 2016.
- [10] D. Brumley, I. Jager, T. Avgerinos, and E. J. Schwartz, "Bap: A binary analysis platform," in *Computer Aided Verification*, G. Gopalakrishnan and S. Qadeer, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2011, pp. 463–469.
- [11] I. Yun, S. Lee, M. Xu, Y. Jang, and T. Kim, "QSYM: A practical concolic execution engine tailored for hybrid fuzzing," in *27th USENIX Security Symposium (USENIX Security 18)*. Baltimore, MD: USENIX Association, 2018, pp. 745–761. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity18/presentation/yun>
- [12] "Identifying cache-based side channels through secret-augmented abstract interpretation," in *28th USENIX Security Symposium (USENIX Security 19)*. Santa Clara, CA: USENIX Association, 2019. [Online]. Available: <https://www.usenix.org/conference/usenixsecurity19/presentation/wang-shuai>
- [13] S. McCamant and M. D. Ernst, "Quantitative information flow as network flow capacity," in *PLDI 2008: Proceedings of the ACM SIGPLAN 2008 Conference on Programming Language Design and Implementation*, Tucson, AZ, USA, Jun. 2008, pp. 193–205.
- [14] Q.-S. Phan, P. Malacaria, O. Tkachuk, and C. S. Pășăreanu, "Symbolic quantitative information flow," *SIGSOFT Softw. Eng. Notes*, vol. 37, no. 6, pp. 1–5, Nov. 2012. [Online]. Available: <http://doi.acm.org/10.1145/2382756.2382791>
- [15] S. Chattopadhyay, M. Beck, A. Rezne, and A. Zeller, "Quantifying the information leak in cache attacks via symbolic execution," in *Proceedings of the 15th ACM-IEEE International Conference on Formal Methods and Models for System Design*, ser. MEMOCODE '17. New York, NY, USA: ACM, 2017, pp. 25–35. [Online]. Available: <http://doi.acm.org/10.1145/3127041.3127044>