

## EE445M/EE380L Lab 6 Documentation

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## Chapter 1

## Todo List

Global `exec_elf` (const char \*path, const `ELFEnv_t` \*env)

Error information



## Chapter 2

# Module Index

### 2.1 Modules

Here is a list of all modules:

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## Chapter 3

# Data Structure Index

### 3.1 Data Structures

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## Chapter 4

# File Index

### 4.1 File List

Here is a list of all documented files with brief descriptions:

inc/ <a href="#">ADC.h</a>	ADC driver for the TM4C123G. Provides interfaces for collecting single samples or a series at a given sampling frequency. Does not allow for sampling of more than one channel at any given time. Timer 2 is reserved for this driver . . . . .	23 ??
inc/ <a href="#">asmdefs.h</a>		??
inc/ <a href="#">diskio.h</a>	Low level disk interface module include file (C)ChaN, 2014 converted to TM4C123 Jonathan Valvano, January 13, 2015 . . . . .	25 ??
inc/ <a href="#">elf.h</a>		??
inc/ <a href="#">ff.h</a>	FatFs - FAT file system module include file R0.10c (C)ChaN, 2014 FatFs module is a generic FAT file system module for small embedded systems. This is a free software that opened for education, research and commercial developments under license policy of following terms. Copyright (C) 2014, , all right reserved. The FatFs module is a free software and there is NO WARRANTY. No restriction on use. You can use, modify and redistribute it for personal, non-profit or commercial product UNDER YOUR RESPONSIBILITY. Redistributions of source code must retain the above copyright notice . . . . .	28 ??
inc/ <a href="#">ffconf.h</a>		??
inc/ <a href="#">FIFO.h</a>		??
inc/ <a href="#">heap.h</a>	Implements memory heap for dynamic memory allocation. Follows standard malloc/calloc/realloc/free interface for allocating/unallocating memory. modified 8/31/08 Jonathan Valvano for style modified 12/16/11 Jonathan Valvano for 32-bit machine modified August 10, 2014 for C99 syntax This example accompanies the book "Embedded Systems: Real Time Operating Systems for ARM Cortex M Microcontrollers", ISBN: 978-1466468863, Jonathan Valvano, copyright (c) 2014 Implementation Notes: This is a Knuth Heap. Each block has a header and a trailer, which I shall call the meta-sections. The meta-sections are each a single int32_t that tells how many int32_ts/words can be stored between the header and trailer. If the block is used, the meta-sections record the room as a positive number. If the block is unused, the meta-sections record the room as a negative number. Copyright 2014 by Jonathan W. Valvano, <a href="mailto:valvano@mail.utexas.edu">valvano@mail.utexas.edu</a> You may use, edit, run or distribute this file as long as the above copyright notice remains THIS SOFTWARE IS PROVIDED "AS IS". NO WARRANTIES, WHETHER EXPRESS, IMPLIED OR STATUTORY, INCLUDING, BUT NOT LIMITED TO, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE APPLY TO THIS SOFTWARE. VALVANO SHALL NOT, IN ANY CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, FOR ANY REASON WHATSOEVER. For more information about my classes, my research, and my books, see <a href="http://users.ece.utexas.edu/~valvano/">http://users.ece.utexas.edu/~valvano/</a> . . . . .	34

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inc/hw_emac.h	??
inc/hw_epi.h	??
inc/hw_ethernet.h	??
inc/hw_fan.h	??
inc/hw_flash.h	??
inc/hw_gpio.h	??
inc/hw_hibernate.h	??
inc/hw_i2c.h	??
inc/hw_i2s.h	??
inc/hw_ints.h	??
inc/hw_lcd.h	??
inc/hw_lpc.h	??
inc/hw_memmap.h	??
inc/hw_nvic.h	??
inc/hw_peci.h	??
inc/hw_pwm.h	??
inc/hw_qei.h	??
inc/hw_shamd5.h	??
inc/hw_ssi.h	??
inc/hw_sysctl.h	??
inc/hw_sysexc.h	??
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inc/ST7735_lab3.h	
This is a library for the Adafruit 1.8" SPI display	58
inc/Switch.h	??
inc/timeMeasure.h	??
inc/tm4c123gh6pm.h	??

inc/[UART.h](#)

Runs on LM4F120/TM4C123 Use UART0 to implement bidirectional data transfer to and from a computer running HyperTerminal. This time, interrupts and FIFOs are used . . . . . 69



## Chapter 5

# Module Documentation

### 5.1 ELF Loader

#### Data Structures

- struct [ELFSymbol\\_t](#)
- struct [ELFEnv\\_t](#)

#### Enumerations

- enum [ELFSecPerm\\_t](#) { [ELF\\_SEC\\_WRITE](#) = 0x1, [ELF\\_SEC\\_READ](#) = 0x2, [ELF\\_SEC\\_EXEC](#) = 0x4 }

#### Functions

- int [exec\\_elf](#) (const char \*path, const [ELFEnv\\_t](#) \*env)

#### 5.1.1 Detailed Description

#### 5.1.2 Enumeration Type Documentation

##### 5.1.2.1 enum [ELFSecPerm\\_t](#)

Protection flags of memory

Enumerator

***ELF\_SEC\_WRITE*** Enable for write  
***ELF\_SEC\_READ*** Enable for read  
***ELF\_SEC\_EXEC*** Enable for execution (instruction fetch)

#### 5.1.3 Function Documentation

##### 5.1.3.1 int [exec\\_elf](#) ( const char \* *path*, const [ELFEnv\\_t](#) \* *env* )

Execute ELF file from "path" with environment "env"

**Parameters**

<i>path</i>	Path to file to load
<i>env</i>	Pointer to environment struct

**Return values**

<i>0</i>	On successful
<i>-1</i>	On fail

**Todo** Error information

## Chapter 6

# Data Structure Documentation

### 6.1 \_pcb\_s Struct Reference

#### Data Fields

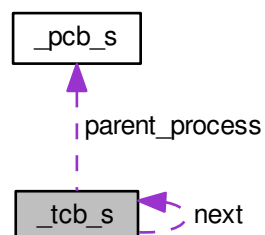
- unsigned long **num\_threads**
- void \* **text**
- void \* **data**

The documentation for this struct was generated from the following file:

- [inc/OS.h](#)

### 6.2 \_tcb\_s Struct Reference

Collaboration diagram for \_tcb\_s:



## Data Fields

- long \* **sp**
- struct [\\_tcb\\_s](#) \* **next**
- uint32\_t **wake\_time**
- unsigned long **id**
- uint8\_t **priority**
- uint32\_t **period**
- unsigned long [magic](#)  
*magic field must contain TCB\_MAGIC for TCB to be valid*
- void(\* **task** )(void)
- char \* **task\_name**
- [pcb\\_t](#) \* **parent\_process**

The documentation for this struct was generated from the following file:

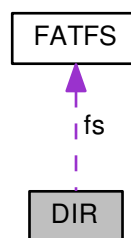
- inc/[OS.h](#)

## 6.3 DIR Struct Reference

Directory object structure ([DIR](#))

```
#include <ff.h>
```

Collaboration diagram for DIR:



## Data Fields

- [FATFS](#) \* **fs**
- WORD **id**
- WORD **index**
- DWORD **sclust**
- DWORD **clust**
- DWORD **sect**
- BYTE \* **dir**
- BYTE \* **fn**



### 6.3.1 Detailed Description

Directory object structure ([DIR](#))

The documentation for this struct was generated from the following file:

- [inc/ff.h](#)

## 6.4 Elf32\_Dyn Struct Reference

### Data Fields

- Elf32\_Sword **d\_tag**
- union {
  - Elf32\_Word **d\_val**
  - Elf32\_Addr **d\_ptr**
- } **d\_un**

The documentation for this struct was generated from the following file:

- [inc/elf.h](#)

## 6.5 Elf32\_Ehdr Struct Reference

### Data Fields

- unsigned char **e\_ident** [EI\_NIDENT]
- Elf32\_Half **e\_type**
- Elf32\_Half **e\_machine**
- Elf32\_Word **e\_version**
- Elf32\_Addr **e\_entry**
- Elf32\_Off **e\_phoff**
- Elf32\_Off **e\_shoff**
- Elf32\_Word **e\_flags**
- Elf32\_Half **e\_ehsize**
- Elf32\_Half **e\_phentsize**
- Elf32\_Half **e\_phnum**
- Elf32\_Half **e\_shentsize**
- Elf32\_Half **e\_shnum**
- Elf32\_Half **e\_shstrndx**

The documentation for this struct was generated from the following file:

- [inc/elf.h](#)

## 6.6 Elf32\_Phdr Struct Reference

### Data Fields

- Elf32\_Word **p\_type**
- Elf32\_Off **p\_offset**
- Elf32\_Addr **p\_vaddr**
- Elf32\_Addr **p\_paddr**
- Elf32\_Word **p\_filesz**
- Elf32\_Word **p\_memsz**
- Elf32\_Word **p\_flags**
- Elf32\_Word **p\_align**

The documentation for this struct was generated from the following file:

- inc/elf.h

## 6.7 Elf32\_Rel Struct Reference

### Data Fields

- Elf32\_Addr **r\_offset**
- Elf32\_Word **r\_info**

The documentation for this struct was generated from the following file:

- inc/elf.h

## 6.8 Elf32\_Rela Struct Reference

### Data Fields

- Elf32\_Addr **r\_offset**
- Elf32\_Word **r\_info**
- Elf32\_Sword **r\_addend**

The documentation for this struct was generated from the following file:

- inc/elf.h

## 6.9 Elf32\_Shdr Struct Reference

### Data Fields

- Elf32\_Word **sh\_name**
- Elf32\_Word **sh\_type**
- Elf32\_Word **sh\_flags**
- Elf32\_Addr **sh\_addr**
- Elf32\_Off **sh\_offset**
- Elf32\_Word **sh\_size**
- Elf32\_Word **sh\_link**
- Elf32\_Word **sh\_info**
- Elf32\_Word **sh\_addralign**
- Elf32\_Word **sh\_entsize**

The documentation for this struct was generated from the following file:

- inc/elf.h

## 6.10 Elf32\_Sym Struct Reference

### Data Fields

- Elf32\_Word **st\_name**
- Elf32\_Addr **st\_value**
- Elf32\_Word **st\_size**
- unsigned char **st\_info**
- unsigned char **st\_other**
- Elf32\_Half **st\_shndx**

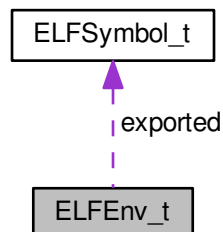
The documentation for this struct was generated from the following file:

- inc/elf.h

## 6.11 ELFEnv\_t Struct Reference

```
#include <loader.h>
```

Collaboration diagram for ELFEnv\_t:



## Data Fields

- const [ELFSymbol\\_t](#) \* [exported](#)
- unsigned int [exported\\_size](#)

### 6.11.1 Detailed Description

Environment for execution

### 6.11.2 Field Documentation

#### 6.11.2.1 const [ELFSymbol\\_t](#)\* [ELFEnv\\_t::exported](#)

Pointer to exported symbols array

#### 6.11.2.2 unsigned int [ELFEnv\\_t::exported\\_size](#)

Elements on exported symbol array

The documentation for this struct was generated from the following file:

- inc/loader.h

## 6.12 [ELFSymbol\\_t](#) Struct Reference

```
#include <loader.h>
```

## Data Fields

- const char \* [name](#)
- void \* [ptr](#)

### 6.12.1 Detailed Description

Exported symbol struct

### 6.12.2 Field Documentation

#### 6.12.2.1 const char\* [ELFSymbol\\_t::name](#)

Name of symbol

## 6.12.2.2 void\* ELFSymbol\_t::ptr

Pointer of symbol in memory

The documentation for this struct was generated from the following file:

- inc/loader.h

## 6.13 event\_t Struct Reference

### Data Fields

- event\_type\_e **type**
- int **magic**
- char \* **name**
- unsigned long long **timestamp**

The documentation for this struct was generated from the following file:

- inc/profiler.h

## 6.14 FATFS Struct Reference

File system object structure ([FATFS](#))

```
#include <ff.h>
```

### Data Fields

- BYTE **fs\_type**
- BYTE **drv**
- BYTE **csize**
- BYTE **n\_fats**
- BYTE **wflag**
- BYTE **fsi\_flag**
- WORD **id**
- WORD **n\_rootdir**
- DWORD **last\_clust**
- DWORD **free\_clust**
- DWORD **cdir**
- DWORD **n\_fatent**
- DWORD **fsize**
- DWORD **volbase**
- DWORD **fatbase**
- DWORD **dirbase**
- DWORD **database**
- DWORD **winsect**
- BYTE **win** [\_MAX\_SS]

### 6.14.1 Detailed Description

File system object structure ([FATFS](#))

The documentation for this struct was generated from the following file:

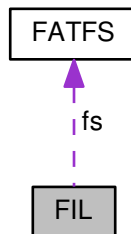
- [inc/ff.h](#)

## 6.15 FIL Struct Reference

File object structure ([FIL](#))

```
#include <ff.h>
```

Collaboration diagram for FIL:



### Data Fields

- [FATFS](#) \* **fs**
- WORD **id**
- BYTE **flag**
- BYTE **err**
- DWORD **fptr**
- DWORD **fsize**
- DWORD **sclust**
- DWORD **clust**
- DWORD **dsect**
- DWORD **dir\_sect**
- BYTE \* **dir\_ptr**
- BYTE **buf** [\_MAX\_SS]

### 6.15.1 Detailed Description

File object structure ([FIL](#))

The documentation for this struct was generated from the following file:

- [inc/ff.h](#)

## 6.16 FILINFO Struct Reference

File status structure ([FILINFO](#))

```
#include <ff.h>
```

### Data Fields

- **DWORD fsize**
- **WORD fdate**
- **WORD ftime**
- **BYTE fattrib**
- **TCHAR fname** [13]

### 6.16.1 Detailed Description

File status structure ([FILINFO](#))

The documentation for this struct was generated from the following file:

- inc/[ff.h](#)

## 6.17 heap\_stats Struct Reference

### Data Fields

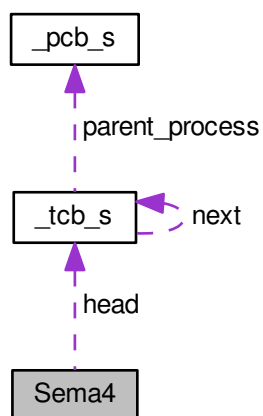
- **int32\_t wordsAllocated**
- **int32\_t wordsAvailable**
- **int32\_t wordsOverhead**
- **int32\_t blocksUsed**
- **int32\_t blocksUnused**

The documentation for this struct was generated from the following file:

- inc/[heap.h](#)

## 6.18 Sema4 Struct Reference

Collaboration diagram for Sema4:



### Data Fields

- long **Value**
- struct `_tcb_s` \* **head**

The documentation for this struct was generated from the following file:

- `inc/OS.h`



## Chapter 7

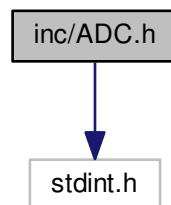
# File Documentation

### 7.1 inc/ADC.h File Reference

ADC driver for the TM4C123G. Provides interfaces for collecting single samples or a series at a given sampling frequency. Does not allow for sampling of more than one channel at any given time. Timer 2 is reserved for this driver.

```
#include <stdint.h>
```

Include dependency graph for ADC.h:



### Functions

- int [ADC\\_Init](#) (uint32\_t channelNum)  
*Configure an ADC channel for continuous sampling. Retrieve measurements from this channel with [ADC\\_In\(\)](#).*
- uint16\_t [ADC\\_In](#) (void)  
*Returns the most recent sample collected by the channel configured in [ADC\\_Init\(...\)](#)*
- int [ADC\\_Collect](#) (uint32\_t channelNum, uint32\_t fs, void(\*handler)(unsigned long))  
*Kick off collection of a sequence of samples to be passed to a user-provided handler. The ADC and Timer will be configured to collect samples at frequency fs.*

### 7.1.1 Detailed Description

ADC driver for the TM4C123G. Provides interfaces for collecting single samples or a series at a given sampling frequency. Does not allow for sampling of more than one channel at any given time. Timer 2 is reserved for this driver.

#### Author

Riley Wood and Jeageun Jung

### 7.1.2 Function Documentation

#### 7.1.2.1 `int ADC_Collect ( uint32_t channelNum, uint32_t fs, void(*) (unsigned long) handler )`

Kick off collection of a sequence of samples to be passed to a user-provided handler. The ADC and Timer will be configured to collect samples at frequency fs.

#### Parameters

<i>channelNum</i>	ADC channel to sample
<i>fs</i>	Sampling frequency
<i>handler</i>	Function which will be passed each sample as it is collected.

#### Returns

int 0 on success, -1 on failure.

#### 7.1.2.2 `uint16_t ADC_In ( void )`

Returns the most recent sample collected by the channel configured in `ADC_Init(...)`

If the channel has not finished collecting its first sample, this function returns 0xFFFF.

If you call this rapidly, faster than the ADC samples, this function may repeat values (since it always returns the most recent).

#### Returns

uint16\_t The conversion result

#### 7.1.2.3 `int ADC_Init ( uint32_t channelNum )`

Configure an ADC channel for continuous sampling. Retrieve measurements from this channel with [ADC\\_In\(\)](#).

#### Parameters

<i>channelNum</i>	The channel to set up
-------------------	-----------------------

## Returns

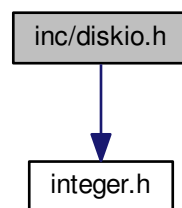
int 0 on success, -1 on failure.

## 7.2 inc/diskio.h File Reference

Low level disk interface module include file (C)ChaN, 2014 converted to TM4C123 Jonathan Valvano, January 13, 2015.

```
#include "integer.h"
```

Include dependency graph for diskio.h:



### Macros

- `#define _USE_WRITE 1` /\* 1: Enable [disk\\_write\(\)](#) function \*/
- `#define _USE_IOCTL 1` /\* 1: Enable [disk\\_ioctl\(\)](#) function \*/
- `#define STA_NOINIT 0x01` /\* Drive not initialized \*/
- `#define STA_NODISK 0x02` /\* No medium in the drive \*/
- `#define STA_PROTECT 0x04` /\* Write protected \*/
- `#define CTRL_SYNC 0` /\* Complete pending write process (needed at \_FS\_READONLY == 0) \*/
- `#define GET_SECTOR_COUNT 1` /\* Get media size (needed at \_USE\_MKFS == 1) \*/
- `#define GET_SECTOR_SIZE 2` /\* Get sector size (needed at \_MAX\_SS != \_MIN\_SS) \*/
- `#define GET_BLOCK_SIZE 3` /\* Get erase block size (needed at \_USE\_MKFS == 1) \*/
- `#define CTRL_TRIM 4` /\* Inform device that the data on the block of sectors is no longer used (needed at \_USE\_TRIM == 1) \*/
- `#define CTRL_FORMAT 5` /\* Create physical format on the media \*/
- `#define CTRL_POWER_IDLE 6` /\* Put the device idle state \*/
- `#define CTRL_POWER_OFF 7` /\* Put the device off state \*/
- `#define CTRL_LOCK 8` /\* Lock media removal \*/
- `#define CTRL_UNLOCK 9` /\* Unlock media removal \*/
- `#define CTRL_EJECT 10` /\* Eject media \*/
- `#define MMC_GET_TYPE 50` /\* Get card type \*/
- `#define MMC_GET_CSD 51` /\* Get CSD \*/
- `#define MMC_GET_CID 52` /\* Get CID \*/
- `#define MMC_GET_OCR 53` /\* Get OCR \*/
- `#define MMC_GET_SDSTAT 54` /\* Get SD status \*/
- `#define ATA_GET_REV 60` /\* Get F/W revision \*/
- `#define ATA_GET_MODEL 61` /\* Get model name \*/

- `#define ATA_GET_SN 62` /\* Get serial number \*/
- `#define CT_MMC 0x01` /\* MMC ver 3 \*/
- `#define CT_SD1 0x02` /\* SD ver 1 \*/
- `#define CT_SD2 0x04` /\* SD ver 2 \*/
- `#define CT_SDC (CT_SD1|CT_SD2)` /\* SD \*/
- `#define CT_BLOCK 0x08` /\* Block addressing \*/

## Typedefs

- typedef BYTE **DSTATUS**

*Status of Disk Functions.*

## Enumerations

- enum **DRESULT** {  
**RES\_OK** = 0, **RES\_ERROR**, **RES\_WRPRT**, **RES\_NOTRDY**,  
**RES\_PARERR** }

*Results of Disk Functions.*

## Functions

- **DSTATUS** **disk\_initialize** (BYTE drv)  
*Initialize disk drive.*
- **DSTATUS** **disk\_status** (BYTE drv)  
*Get disk status.*
- **DRESULT** **disk\_read** (BYTE drv, BYTE \*buff, DWORD sector, UINT count)  
*Read sector(s)*
- **DRESULT** **disk\_write** (BYTE drv, const BYTE \*buff, DWORD sector, UINT count)  
*Write sector(s)*
- **DRESULT** **disk\_ioctl** (BYTE drv, BYTE cmd, void \*buff)  
*Miscellaneous drive controls.*

### 7.2.1 Detailed Description

Low level disk interface module include file (C)ChaN, 2014 converted to TM4C123 Jonathan Valvano, January 13, 2015.

### 7.2.2 Function Documentation

#### 7.2.2.1 **DSTATUS** **disk\_initialize** ( BYTE drv )

Initialize disk drive.

##### Parameters

<i>drv</i>	Physical drive number, which must be 0
------------	--

**Returns**

status (see DSTATUS)

**7.2.2.2 DRESULT disk\_ioctl ( BYTE *drv*, BYTE *cmd*, void \* *buff* )**

Miscellaneous drive controls.

**Parameters**

<i>drv</i>	Physical drive number (0)
<i>cmd</i>	Control command code
<i>buff</i>	Pointer to the control data

**Returns**

status (see DRESULT)

**7.2.2.3 DRESULT disk\_read ( BYTE *drv*, BYTE \* *buff*, DWORD *sector*, UINT *count* )**

Read sector(s)

**Parameters**

<i>drv</i>	Physical drive number (0)
<i>buff</i>	Pointer to the data buffer to store read data
<i>sector</i>	Start sector number (LBA)
<i>count</i>	Number of sectors to read (1..128)

**Returns**

status (see DRESULT)

**7.2.2.4 DSTATUS disk\_status ( BYTE *drv* )**

Get disk status.

**Parameters**

<i>drv</i>	Physical drive number, which must be 0
------------	--

**Returns**

status (see DSTATUS)

#### 7.2.2.5 DRESULT disk\_write ( BYTE *drv*, const BYTE \* *buff*, DWORD *sector*, UINT *count* )

Write sector(s)

##### Parameters

<i>drv</i>	Physical drive number (0)
<i>buff</i>	Pointer to the data buffer to write to disk
<i>sector</i>	Start sector number (LBA)
<i>count</i>	Number of sectors to write (1..128)

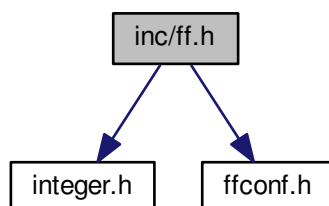
##### Returns

status (see DRESULT)

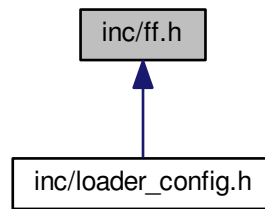
## 7.3 inc/ff.h File Reference

FatFs - FAT file system module include file R0.10c (C)ChaN, 2014 FatFs module is a generic FAT file system module for small embedded systems. This is a free software that opened for education, research and commercial developments under license policy of following terms. Copyright (C) 2014, , all right reserved. The FatFs module is a free software and there is NO WARRANTY. No restriction on use. You can use, modify and redistribute it for personal, non-profit or commercial product UNDER YOUR RESPONSIBILITY. Redistributions of source code must retain the above copyright notice.

```
#include "integer.h"
#include "ffconf.h"
Include dependency graph for ff.h:
```



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [FATFS](#)  
*File system object structure ([FATFS](#))*
- struct [FIL](#)  
*File object structure ([FIL](#))*
- struct [DIR](#)  
*Directory object structure ([DIR](#))*
- struct [FILINFO](#)  
*File status structure ([FILINFO](#))*

## Macros

- `#define _FATFS 80376` /\* Revision ID \*/
- `#define LD2PD(vol) (BYTE)(vol)` /\* Each logical drive is bound to the same physical drive number \*/
- `#define LD2PT(vol) 0` /\* Find first valid partition or in SFD \*/
- `#define _T(x) x`
- `#define _TEXT(x) x`
- `#define f_eof(fp) ((int)((fp)->fptr == (fp)->fsize))`
- `#define f_error(fp) ((fp)->err)`
- `#define f_tell(fp) ((fp)->fptr)`
- `#define f_size(fp) ((fp)->fsize)`
- `#define EOF (-1)`
- `#define FA_READ 0x01`
- `#define FA_OPEN_EXISTING 0x00`
- `#define FA_WRITE 0x02`
- `#define FA_CREATE_NEW 0x04`
- `#define FA_CREATE_ALWAYS 0x08`
- `#define FA_OPEN_ALWAYS 0x10`
- `#define FA__WRITTEN 0x20`
- `#define FA__DIRTY 0x40`
- `#define FS_FAT12 1`
- `#define FS_FAT16 2`
- `#define FS_FAT32 3`
- `#define AM_RDO 0x01` /\* Read only \*/
- `#define AM_HID 0x02` /\* Hidden \*/

- `#define AM_SYS 0x04 /* System */`
- `#define AM_VOL 0x08 /* Volume label */`
- `#define AM_LFN 0x0F /* LFN entry */`
- `#define AM_DIR 0x10 /* Directory */`
- `#define AM_ARC 0x20 /* Archive */`
- `#define AM_MASK 0x3F /* Mask of defined bits */`
- `#define CREATE_LINKMAP 0xFFFFFFFF`
- `#define LD_WORD(ptr) (WORD)(((WORD)*((BYTE*)(ptr)+1)<<8)|((WORD)*((BYTE*)(ptr))`
- `#define LD_DWORD(ptr) (DWORD)(((DWORD)*((BYTE*)(ptr)+3)<<24)|((DWORD)*((BYTE*)(ptr)+2)<<16)|((WORD)*((BYTE*)(ptr)+1)<<8)|((WORD)*((BYTE*)(ptr))`
- `#define ST_WORD(ptr, val) *((BYTE*)(ptr))=(BYTE)(val); *((BYTE*)(ptr)+1)=(BYTE)((WORD)(val)>>8)`
- `#define ST_DWORD(ptr, val) *((BYTE*)(ptr))=(BYTE)(val); *((BYTE*)(ptr)+1)=(BYTE)((WORD)(val)>>8); *((BYTE*)(ptr)+2)=(BYTE)((DWORD)(val)>>16); *((BYTE*)(ptr)+3)=(BYTE)((DWORD)(val)>>24)`

## Typedefs

- typedef char **TCHAR**

## Enumerations

- enum **FRESULT** {  
**FR\_OK** = 0, **FR\_DISK\_ERR**, **FR\_INT\_ERR**, **FR\_NOT\_READY**,  
**FR\_NO\_FILE**, **FR\_NO\_PATH**, **FR\_INVALID\_NAME**, **FR\_DENIED**,  
**FR\_EXIST**, **FR\_INVALID\_OBJECT**, **FR\_WRITE\_PROTECTED**, **FR\_INVALID\_DRIVE**,  
**FR\_NOT\_ENABLED**, **FR\_NO\_FILESYSTEM**, **FR\_MKFS\_ABORTED**, **FR\_TIMEOUT**,  
**FR\_LOCKED**, **FR\_NOT\_ENOUGH\_CORE**, **FR\_TOO\_MANY\_OPEN\_FILES**, **FR\_INVALID\_PARAMETER** }

*File function return code (FRESULT)*

## Functions

- **FRESULT** **f\_open** (**FIL** \*fp, const **TCHAR** \*path, **BYTE** mode)
- **FRESULT** **f\_close** (**FIL** \*fp)
- **FRESULT** **f\_read** (**FIL** \*fp, void \*buff, **UINT** btr, **UINT** \*br)
- **FRESULT** **f\_write** (**FIL** \*fp, const void \*buff, **UINT** btw, **UINT** \*bw)
- **FRESULT** **f\_forward** (**FIL** \*fp, **UINT**(\*func)(const **BYTE** \*, **UINT**), **UINT** btf, **UINT** \*bf)
- **FRESULT** **f\_lseek** (**FIL** \*fp, **DWORD** ofs)
- **FRESULT** **f\_truncate** (**FIL** \*fp)
- **FRESULT** **f\_sync** (**FIL** \*fp)
- **FRESULT** **f\_opendir** (**DIR** \*dp, const **TCHAR** \*path)
- **FRESULT** **f\_closedir** (**DIR** \*dp)
- **FRESULT** **f\_readdir** (**DIR** \*dp, **FILINFO** \*fno)
- **FRESULT** **f\_mkdir** (const **TCHAR** \*path)
- **FRESULT** **f\_unlink** (const **TCHAR** \*path)
- **FRESULT** **f\_rename** (const **TCHAR** \*path\_old, const **TCHAR** \*path\_new)
- **FRESULT** **f\_stat** (const **TCHAR** \*path, **FILINFO** \*fno)
- **FRESULT** **f\_chmod** (const **TCHAR** \*path, **BYTE** value, **BYTE** mask)
- **FRESULT** **f\_utime** (const **TCHAR** \*path, const **FILINFO** \*fno)
- **FRESULT** **f\_chdir** (const **TCHAR** \*path)
- **FRESULT** **f\_chdrive** (const **TCHAR** \*path)
- **FRESULT** **f\_getcwd** (**TCHAR** \*buff, **UINT** len)
- **FRESULT** **f\_getfree** (const **TCHAR** \*path, **DWORD** \*nclst, **FATFS** \*\*fatfs)
- **FRESULT** **f\_getlabel** (const **TCHAR** \*path, **TCHAR** \*label, **DWORD** \*vsn)



- [FRESULT f\\_setlabel](#) (const TCHAR \*label)
- [FRESULT f\\_mount](#) (FATFS \*fs, const TCHAR \*path, BYTE opt)
- [FRESULT f\\_mkfs](#) (const TCHAR \*path, BYTE sdf, UINT au)
- [FRESULT f\\_fdisk](#) (BYTE pdrv, const DWORD szt[], void \*work)
- int [f\\_putc](#) (TCHAR c, [FIL](#) \*fp)
- int [f\\_puts](#) (const TCHAR \*str, [FIL](#) \*cp)
- int [f\\_printf](#) ([FIL](#) \*fp, const TCHAR \*str,...)
- TCHAR \* [f\\_gets](#) (TCHAR \*buff, int len, [FIL](#) \*fp)

### 7.3.1 Detailed Description

FatFs - FAT file system module include file R0.10c (C)ChaN, 2014 FatFs module is a generic FAT file system module for small embedded systems. This is a free software that opened for education, research and commercial developments under license policy of following terms. Copyright (C) 2014, , all right reserved. The FatFs module is a free software and there is NO WARRANTY. No restriction on use. You can use, modify and redistribute it for personal, non-profit or commercial product UNDER YOUR RESPONSIBILITY. Redistributions of source code must retain the above copyright notice.

Author

ChaN

### 7.3.2 Function Documentation

#### 7.3.2.1 FRESULT f\_chdir ( const TCHAR \* *path* )

Change current directory

#### 7.3.2.2 FRESULT f\_chdrive ( const TCHAR \* *path* )

Change current drive

#### 7.3.2.3 FRESULT f\_chmod ( const TCHAR \* *path*, BYTE *value*, BYTE *mask* )

Change attribute of the file/dir

#### 7.3.2.4 FRESULT f\_close ( [FIL](#) \* *fp* )

Close an open file object

#### 7.3.2.5 FRESULT f\_closedir ( [DIR](#) \* *dp* )

Close an open directory

**7.3.2.6 FRESULT f\_fdisk ( BYTE *pdrv*, const DWORD *sztl*[], void \* *work* )**

Divide a physical drive into some partitions

**7.3.2.7 FRESULT f\_forward ( FIL \* *fp*, UINT(\*)(const BYTE \*, UINT) *func*, UINT *btf*, UINT \* *bf* )**

Forward data to the stream

**7.3.2.8 FRESULT f\_getcwd ( TCHAR \* *buff*, UINT *len* )**

Get current directory

**7.3.2.9 FRESULT f\_getfree ( const TCHAR \* *path*, DWORD \* *nclst*, FATFS \*\* *fatfs* )**

Get number of free clusters on the drive

**7.3.2.10 FRESULT f\_getlabel ( const TCHAR \* *path*, TCHAR \* *label*, DWORD \* *vsn* )**

Get volume label

**7.3.2.11 TCHAR\* f\_gets ( TCHAR \* *buff*, int *len*, FIL \* *fp* )**

Get a string from the file

**7.3.2.12 FRESULT f\_lseek ( FIL \* *fp*, DWORD *ofs* )**

Move file pointer of a file object

**7.3.2.13 FRESULT f\_mkdir ( const TCHAR \* *path* )**

Create a sub directory

**7.3.2.14 FRESULT f\_mkfs ( const TCHAR \* *path*, BYTE *sfd*, UINT *au* )**

Create a file system on the volume

**7.3.2.15 FRESULT f\_mount ( FATFS \* *fs*, const TCHAR \* *path*, BYTE *opt* )**

Mount/Unmount a logical drive

**7.3.2.16** **FRESULT** `f_open ( FIL * fp, const TCHAR * path, BYTE mode )`

Open or create a file

**7.3.2.17** **FRESULT** `f_opendir ( DIR * dp, const TCHAR * path )`

Open a directory

**7.3.2.18** `int` `f_printf ( FIL * fp, const TCHAR * str, ... )`

Put a formatted string to the file

**7.3.2.19** `int` `f_putc ( TCHAR c, FIL * fp )`

Put a character to the file

**7.3.2.20** `int` `f_puts ( const TCHAR * str, FIL * cp )`

Put a string to the file

**7.3.2.21** **FRESULT** `f_read ( FIL * fp, void * buff, UINT btr, UINT * br )`

Read data from a file

**7.3.2.22** **FRESULT** `f_readdir ( DIR * dp, FILINFO * fno )`

Read a directory item

**7.3.2.23** **FRESULT** `f_rename ( const TCHAR * path_old, const TCHAR * path_new )`

Rename/Move a file or directory

**7.3.2.24** **FRESULT** `f_setlabel ( const TCHAR * label )`

Set volume label

**7.3.2.25** **FRESULT** `f_stat ( const TCHAR * path, FILINFO * fno )`

Get file status

**7.3.2.26 FRESULT f\_sync ( FIL \* fp )**

Flush cached data of a writing file

**7.3.2.27 FRESULT f\_truncate ( FIL \* fp )**

Truncate file

**7.3.2.28 FRESULT f\_unlink ( const TCHAR \* path )**

Delete an existing file or directory

**7.3.2.29 FRESULT f\_ftime ( const TCHAR \* path, const FILINFO \* fno )**

Change times-tamp of the file/dir

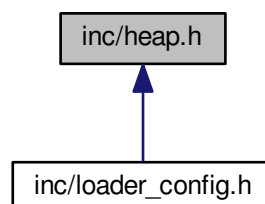
**7.3.2.30 FRESULT f\_write ( FIL \* fp, const void \* buff, UINT btw, UINT \* bw )**

Write data to a file

**7.4 inc/heap.h File Reference**

Implements memory heap for dynamic memory allocation. Follows standard malloc/calloc/realloc/free interface for allocating/unallocating memory. modified 8/31/08 Jonathan Valvano for style modified 12/16/11 Jonathan Valvano for 32-bit machine modified August 10, 2014 for C99 syntax This example accompanies the book "Embedded Systems: Real Time Operating Systems for ARM Cortex M Microcontrollers", ISBN: 978-1466468863, Jonathan Valvano, copyright (c) 2014 Implementation Notes: This is a Knuth Heap. Each block has a header and a trailer, which I shall call the meta-sections. The meta-sections are each a single int32\_t that tells how many int32\_t's/words can be stored between the header and trailer. If the block is used, the meta-sections record the room as a positive number. If the block is unused, the meta-sections record the room as a negative number. Copyright 2014 by Jonathan W. Valvano, [valvano@mail.utexas.edu](mailto:valvano@mail.utexas.edu) You may use, edit, run or distribute this file as long as the above copyright notice remains THIS SOFTWARE IS PROVIDED "AS IS". NO WARRANTIES, WHETHER EXPRESS, IMPLIED OR STATUTORY, INCLUDING, BUT NOT LIMITED TO, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE APPLY TO THIS SOFTWARE. VALVANO SHALL NOT, IN ANY CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, FOR ANY REASON WHATSOEVER. For more information about my classes, my research, and my books, see <http://users.ece.utexas.edu/~valvano/>.

This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [heap\\_stats](#)

## Macros

- `#define HEAP_SIZE_BYTES (2048)`
- `#define HEAP_SIZE_WORDS (HEAP_SIZE_BYTES / sizeof(int32_t))`
- `#define HEAP_OK 0`
- `#define HEAP_ERROR_CORRUPTED_HEAP 1`
- `#define HEAP_ERROR_POINTER_OUT_OF_RANGE 2`

## Typedefs

- typedef struct [heap\\_stats](#) [heap\\_stats\\_t](#)

## Functions

- `int32_t Heap_Init (void)`  
*Initialize the Heap notes: Initializes/resets the heap to a clean state where no memory is allocated.*
- `void * Heap_Malloc (int32_t desiredBytes)`  
*Allocate memory, data not initialized.*
- `void * Heap_Calloc (int32_t desiredBytes)`  
*Allocate memory, data are initialized to 0 notes: the allocated memory block will be zeroed out.*
- `void * Heap_Realloc (void *oldBlock, int32_t desiredBytes)`  
*Reallocate buffer to a new size notes: the given block will be unallocated after its contents are copied to the new block.*
- `int32_t Heap_Free (void *pointer)`  
*return a block to the heap*
- `int32_t Heap_Test (void)`  
*Test the heap.*
- `heap_stats_t Heap_Stats (void)`  
*return the current status of the heap*

### 7.4.1 Detailed Description

Implements memory heap for dynamic memory allocation. Follows standard malloc/calloc/realloc/free interface for allocating/unallocating memory. modified 8/31/08 Jonathan Valvano for style modified 12/16/11 Jonathan Valvano for 32-bit machine modified August 10, 2014 for C99 syntax This example accompanies the book "Embedded Systems: Real Time Operating Systems for ARM Cortex M Microcontrollers", ISBN: 978-1466468863, Jonathan Valvano, copyright (c) 2014 Implementation Notes: This is a Knuth Heap. Each block has a header and a trailer, which I shall call the meta-sections. The meta-sections are each a single int32\_t that tells how many int32\_t's/words can be stored between the header and trailer. If the block is used, the meta-sections record the room as a positive number. If the block is unused, the meta-sections record the room as a negative number. Copyright 2014 by Jonathan W. Valvano, [valvano@mail.utexas.edu](mailto:valvano@mail.utexas.edu) You may use, edit, run or distribute this file as long as the above copyright notice remains THIS SOFTWARE IS PROVIDED "AS IS". NO WARRANTIES, WHETHER EXPRESS, IMPLIED OR STATUTORY, INCLUDING, BUT NOT LIMITED TO, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE APPLY TO THIS SOFTWARE. VALVANO SHALL NOT, IN ANY CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, FOR ANY REASON WHATSOEVER. For more information about my classes, my research, and my books, see <http://users.ece.utexas.edu/~valvano/>.

**Author**

Jacob Egnér

**Date**

2008-07-31

**7.4.2 Function Documentation****7.4.2.1 void\* Heap\_Calloc ( int32\_t *desiredBytes* )**

Allocate memory, data are initialized to 0 notes: the allocated memory block will be zeroed out.

**Parameters**

<i>desiredBytes</i>	desired number of bytes to allocate
---------------------	-------------------------------------

**Returns**

void\* pointing to the allocated memory block or will return NULL if there isn't sufficient space to satisfy allocation request

**7.4.2.2 int32\_t Heap\_Free ( void \* *pointer* )**

return a block to the heap

**Parameters**

<i>pointer</i>	the pointer to memory to unallocate
----------------	-------------------------------------

**Returns**

HEAP\_OK if everything is ok; HEAP\_ERROR\_POINTER\_OUT\_OF\_RANGE if pointer points outside the heap; HEAP\_ERROR\_CORRUPTED\_HEAP if heap has been corrupted or trying to unallocate memory that has already been unallocated;

**7.4.2.3 int32\_t Heap\_Init ( void )**

Initialize the Heap notes: Initializes/resets the heap to a clean state where no memory is allocated.

**Returns**

always HEAP\_OK

**7.4.2.4 void\* Heap\_Malloc ( int32\_t *desiredBytes* )**

Allocate memory, data not initialized.

## Parameters

<i>desiredBytes</i>	desired number of bytes to allocate
---------------------	-------------------------------------

## Returns

void\* pointing to the allocated memory or will return NULL if there isn't sufficient space to satisfy allocation request

7.4.2.5 void\* Heap\_Realloc ( void \* *oldBlock*, int32\_t *desiredBytes* )

Reallocate buffer to a new size notes: the given block will be unallocated after its contents are copied to the new block.

## Parameters

<i>oldBlock</i>	pointer to a block
<i>desiredBytes</i>	a desired number of bytes for a new block where the contents of the old block will be copied to

## Returns

void\* pointing to the new block or will return NULL if there is any reason the reallocation can't be completed

## 7.4.2.6 heap\_stats\_t Heap\_Stats ( void )

return the current status of the heap

## Returns

a heap\_stats\_t that describes the current usage of the heap

## 7.4.2.7 int32\_t Heap\_Test ( void )

Test the heap.

## Returns

validity of the heap - either HEAP\_OK or HEAP\_ERROR\_HEAP\_CORRUPTED

## 7.5 inc/interpreter.h File Reference

## Functions

- void [interpreter\\_task](#) (void)  
*OS Task that sends characters to the interpreter.*
- void [interpreter\\_cmd](#) (char \*cmd\_str)  
*Pass user input to the interpreter and act on their command.*

### 7.5.1 Detailed Description

List of commands

- `adc`
  - Prints 2 consecutive ADC samples of channel 0 to the LCD and UART0
- `lcd`
  - Prints strings on each line of each logical display on the LCD.
- `critical`
  - Prints the percentage of CPU time spent with interrupts disabled.
- `log`
  - Prints profiler events logged
- `clear`
  - Clears the profiler event log and restarts the profiler
- `format`
  - Formats the filesystem on the SD card
- `ls`
  - List all files in the filesystem
- `cat`
  - Print out file in the filesystem.
  - Takes one argument: the name of the file to print
- `rm`
  - Remove file in the filesystem.
  - Takes one argument: the name of the file to remove
- `touch`
  - Create a file in the filesystem.
  - Takes one argument: the name of the file to create
- `echo`
  - Append characters to a file
  - Takes two arguments in this order:
    - \* The name of the file to append to
    - \* Remaining characters are written to the file
- `increase`
  - Artificially increase the time spent in critical sections to test the "critical" command.

### 7.5.2 Function Documentation

#### 7.5.2.1 `void interpreter_cmd ( char * cmd_str )`

Pass user input to the interpreter and act on their command.



## Parameters

<code>cmd_str</code>	String containing the entire user command.
----------------------	--

## 7.6 inc/misc\_macros.h File Reference

Some helper macros.

### Macros

- #define `lengthof`(array) (sizeof(array)/sizeof((array)[0]))  
*Get the number of elements in an array.*
- #define `zeroes`(array) memset(array, 0, sizeof(array))  
*Zeroes out an array.*

### 7.6.1 Detailed Description

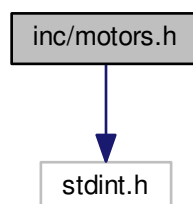
Some helper macros.

## 7.7 inc/motors.h File Reference

Interface to two DC motors controlled by PWM. Allows differential driving.

```
#include <stdint.h>
```

Include dependency graph for motors.h:



## Functions

- void `Motors_Init` (void)  
*Initialize the robot motors.*
- void `Motors_SetTorque` (int16\_t left\_trq, int16\_t right\_trq)  
*Set the torque for each of the two motors atomically.*
- void `Motors_SetTorque_Left` (int16\_t left\_trq)  
*Set the torque of the left motor individually.*
- void `Motors_SetTorque_Right` (int16\_t right\_trq)  
*Set the torque of the right motor individually.*
- void `Motors_Brake` (void)  
*Brake both motors (tie both motors' leads to ground)*
- void `Motors_Brake_Left` (void)  
*Brake left motor (tie motor leads to ground)*
- void `Motors_Brake_Right` (void)  
*Brake right motor (tie motor leads to ground)*

### 7.7.1 Detailed Description

Interface to two DC motors controlled by PWM. Allows differential driving.

#### Author

Riley Wood ([riley.wood@utexas.edu](mailto:riley.wood@utexas.edu))

#### Conventions:

- Definition of "left" versus "right":
  - The "left motor" is the motor on your left when the robot is on the ground with the servo pointed away from you.
  - The "right motor" is the motor on your right when the robot is on the ground with the servo pointed away from you.
- How to connect motors to motor board:
  - The left motor must be connected to motor port A.
  - The right motor must be connected to motor port B.
- Motor pin assignments:
  - The red wire of the left motor will be connected to A- and is controlled by PB6
  - The black wire of the left motor will be connected to A+ and is controlled by PB7
  - The red wire of right motor will be connected to B- and is controlled by PB4
  - The black wire of the right motor will be connected to B+ and is controlled by PB5
- Pin configurations:
  - A- (PB6) and B+ (PB5) will be configured as PWM outputs
  - A+ (PB7) and B- (PB4) will be configured as digital outputs.
  - We alternate + and - so that when both motors are driving forward (i.e. they are rotating OPPOSITE directions) with the same torque, their digital and PWM output configurations will be identical.
- H-Bridge convention:
  - A value of 1 (high) on any of PB4/5/6/7 will connect the corresponding motor terminal (A/B/+/-) to battery power.
  - A value of 0 (low) on any of PB4/5/6/7 will connect the corresponding motor terminal (A/B/+/-) to ground.

## Copyright

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## 7.7.2 Function Documentation

### 7.7.2.1 void Motors\_SetTorque ( int16\_t *left\_trq*, int16\_t *right\_trq* )

Set the torque for each of the two motors atomically.

#### Parameters

<i>left_trq</i>	Torque for left motor. Positive argument indicates forward motion of robot, negative indicates backward. Zero indicates no rotation.
<i>right_trq</i>	Torque for right motor. Positive argument indicates forward motion of robot, negative indicates backward. Zero indicates no rotation.

### 7.7.2.2 void Motors\_SetTorque\_Left ( int16\_t *left\_trq* )

Set the torque of the left motor individually.

#### Parameters

<i>left_trq</i>	Torque for left motor. Positive argument indicates forward motion of robot, negative indicates backward. Zero indicates no rotation.
-----------------	--

### 7.7.2.3 void Motors\_SetTorque\_Right ( int16\_t *right\_trq* )

Set the torque of the left motor individually.

#### Parameters

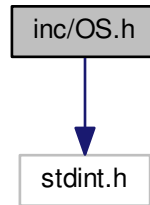
<i>right_trq</i>	Torque for right motor. Positive argument indicates forward motion of robot, negative indicates backward. Zero indicates no rotation.
------------------	---

## 7.8 inc/OS.h File Reference

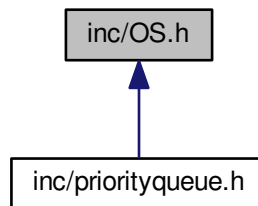
Real Time Operating System for Labs 2 and 3 EE445M/EE380L.12.

```
#include <stdint.h>
```

Include dependency graph for OS.h:



This graph shows which files directly or indirectly include this file:



## Data Structures

- struct [\\_pcb\\_s](#)
- struct [\\_tcb\\_s](#)
- struct [Sema4](#)

## Macros

- `#define TIME_1MS 80000`
- `#define TIME_2MS (2 * TIME_1MS)`
- `#define TIME_500US (TIME_1MS / 2)`
- `#define TIME_250US (TIME_1MS / 4)`
- `#define TASK_STACK_SIZE 128`
- `#define TCB_MAGIC (0x900d900d)`
- `#define OS\_AddThread(task, stackSize, priority)`
- `#define OS\_AddPeriodicThread(task, period, priority) OS_AddPeriodicThread_priv(task, period, priority, #task)`

## Typedefs

- typedef struct [\\_pcb\\_s](#) **pcb\_t**
- typedef struct [\\_tcb\\_s](#) **tcb\_t**
- typedef struct [Sema4](#) **Sema4Type**

## Functions

- void [OS\\_Init](#) (void)
- void [OS\\_InitSemaphore](#) ([Sema4Type](#) \*semaPt, long value)
- void [OS\\_Wait](#) ([Sema4Type](#) \*semaPt)
- void [OS\\_Signal](#) ([Sema4Type](#) \*semaPt)
- void [OS\\_bWait](#) ([Sema4Type](#) \*semaPt)
- void [OS\\_bSignal](#) ([Sema4Type](#) \*semaPt)
- void [Jitter](#) (void)  
*Print the max periodic task jitter measured thus far to the ST7735 display.*
- int [\\_\\_OS\\_AddThread](#) (void(\*task)(void), unsigned long stackSize, unsigned long priority, char \*task\_name, [pcb\\_t](#) \*parent\_process)
- unsigned long [OS\\_Id](#) (void)
- int [OS\\_AddPeriodicThread\\_priv](#) (void(\*task)(void), unsigned long period, unsigned long priority, char \*task\_name)
- int [OS\\_AddSW1Task](#) (void(\*task)(void), unsigned long priority)
- int [OS\\_AddSW2Task](#) (void(\*task)(void), unsigned long priority)
- void [OS\\_Sleep](#) (unsigned long sleepTime)
- void [OS\\_Kill](#) (void)
- void [OS\\_Suspend](#) (void)
- void [OS\\_Fifo\\_Init](#) (unsigned long size)
- int [OS\\_Fifo\\_Put](#) (unsigned long data)
- unsigned long [OS\\_Fifo\\_Get](#) (void)
- long [OS\\_Fifo\\_Size](#) (void)
- void [OS\\_MailBox\\_Init](#) (void)
- void [OS\\_MailBox\\_Send](#) (unsigned long data)
- unsigned long [OS\\_MailBox\\_Recv](#) (void)
- unsigned long long [OS\\_Time](#) (void)
- unsigned long long [OS\\_TimeDifference](#) (unsigned long long start, unsigned long long stop)
- void [OS\\_ClearMsTime](#) (void)
- unsigned long [OS\\_MsTime](#) (void)
- void [OS\\_Launch](#) (unsigned long theTimeSlice)
- int [OS\\_AddProcess](#) (void(\*entry)(void), void \*text, void \*data, unsigned long stackSize, unsigned long priority)  
*Launch a process in the OS.*
- long [StartCritical](#) (void)
- void [EndCritical](#) (long sr)
- void [DisableInterrupts](#) (void)
- void [EnableInterrupts](#) (void)

## Variables

- [tcb\\_t](#) \* **cur\_tcb**



## Parameters

<i>task</i>	Task function
<i>stackSize</i>	Size of the stack in bytes. Should be divisible by 8
<i>priority</i>	Priority of the task. 0 is highest, 5 is lowest.

## Returns

1 if successful, 0 if this thread can not be added

## 7.8.3 Function Documentation

7.8.3.1 `int OS_AddProcess ( void(*) (void) entry, void * text, void * data, unsigned long stackSize, unsigned long priority )`

Launch a process in the OS.

## Parameters

<i>entry</i>	Entry point, usually main() of the process
<i>text</i>	Text (code) section start address
<i>data</i>	Data section start address
<i>stackSize</i>	Size of the stack for the first thread
<i>priority</i>	Priority for the first thread

## Returns

int 0 on success, -1 on failure.

7.8.3.2 `int OS_AddSW1Task ( void(*) (void) task, unsigned long priority )`

add a background task to run whenever the SW1 (PF4) button is pushed

## Parameters

<i>pointer</i>	to a void/void background function
<i>priority</i>	0 is the highest, 5 is the lowest

## Returns

1 if successful, 0 if this thread can not be added It is assumed that the user task will run to completion and return This task can not spin, block, loop, sleep, or kill This task can call OS\_Signal OS\_bSignal OS\_AddThread This task does not have a Thread ID In labs 2 and 3, this command will be called 0 or 1 times In lab 2, the priority field can be ignored In lab 3, there will be up to four background threads, and this priority field determines the relative priority of these four threads

7.8.3.3 int OS\_AddSW2Task ( void(\*) (void) *task*, unsigned long *priority* )

add a background task to run whenever the SW2 (PF0) button is pushed



## Parameters

<i>pointer</i>	to a void/void background function
<i>priority</i>	0 is highest, 5 is lowest

## Returns

1 if successful, 0 if this thread can not be added It is assumed user task will run to completion and return This task can not spin block loop sleep or kill This task can call issue OS\_Signal, it can call OS\_AddThread This task does not have a Thread ID In lab 2, this function can be ignored In lab 3, this command will be called will be called 0 or 1 times In lab 3, there will be up to four background threads, and this priority field determines the relative priority of these four threads

7.8.3.4 void OS\_bSignal ( Sema4Type \* *semaPt* )

Lab2 spinlock, set to 1 Lab3 wakeup blocked thread if appropriate

## Parameters

<i>semaPt</i>	pointer to a binary semaphore
---------------	-------------------------------

7.8.3.5 void OS\_bWait ( Sema4Type \* *semaPt* )

Lab2 spinlock, set to 0 Lab3 block if less than zero

## Parameters

<i>semaPt</i>	pointer to a binary semaphore
---------------	-------------------------------

## 7.8.3.6 void OS\_ClearMsTime ( void )

Sets the system time to zero (from Lab 1). You are free to change how this works.

## Returns

none

## 7.8.3.7 unsigned long OS\_Fifo\_Get ( void )

Remove one data sample from the Fifo. Called in foreground, will spin/block if empty

## Returns

data

#### 7.8.3.8 void OS\_Fifo\_Init ( unsigned long *size* )

Initialize the Fifo to be empty. In Lab 2, you can ignore the size field. In Lab 3, you should implement the user-defined fifo size. In Lab 3, you can put whatever restrictions you want on size e.g., 4 to 64 elements e.g., must be a power of 2,4,8,16,32,64,128

##### Parameters

<i>size</i>	Size of the fifo
-------------	------------------

##### Returns

none

#### 7.8.3.9 int OS\_Fifo\_Put ( unsigned long *data* )

Enter one data sample into the Fifo. Called from the background, so no waiting. Since this is called by interrupt handlers this function can not disable or enable interrupts.

##### Parameters

<i>data</i>	Data to put in the FIFO
-------------	-------------------------

##### Returns

true if data is properly saved, false if data not saved, because it was full

#### 7.8.3.10 long OS\_Fifo\_Size ( void )

Check the status of the Fifo.

##### Returns

returns the number of elements in the Fifo. Greater than zero if a call to OS\_Fifo\_Get will return right away, zero or less than zero if the Fifo is empty, zero or less than zero if a call to OS\_Fifo\_Get will spin or block

#### 7.8.3.11 unsigned long OS\_Id ( void )

returns the thread ID for the currently running thread

##### Returns

Thread ID, number greater than zero

#### 7.8.3.12 void OS\_Init ( void )

initialize operating system, disable interrupts until OS\_Launch initialize OS controlled I/O: serial, ADC, systick, LaunchPad I/O and timers

#### 7.8.3.13 void OS\_InitSemaphore ( Sema4Type \* *semaPt*, long *value* )

initialize semaphore

## Parameters

<i>sema</i> ↔ <i>Pt</i>	pointer to a semaphore
----------------------------	------------------------

## 7.8.3.14 void OS\_Kill ( void )

kill the currently running thread, release its TCB and stack

7.8.3.15 void OS\_Launch ( unsigned long *theTimeSlice* )

Start the scheduler, enable interrupts. In Lab 2, you can ignore the *theTimeSlice* field. In Lab 3, you should implement the user-defined TimeSlice field. It is ok to limit the range of *theTimeSlice* to match the 24-bit SysTick.

## Parameters

<i>theTimeSlice</i>	number of 12.5ns clock cycles for each time slice
---------------------	---

## Returns

none (does not return)

## 7.8.3.16 void OS\_MailBox\_Init ( void )

Initialize communication channel

## Returns

none

## 7.8.3.17 unsigned long OS\_MailBox\_Recv ( void )

Remove mail from the MailBox. This function will be called from a foreground thread. It will spin/block if the MailBox is empty.

## Returns

data received

7.8.3.18 void OS\_MailBox\_Send ( unsigned long *data* )

Enter mail into the MailBox. This function will be called from a foreground thread. It will spin/block if the MailBox contains data not yet received

## Parameters

<i>data</i>	to be sent
-------------	------------

## Returns

none

## 7.8.3.19 unsigned long OS\_MsTime ( void )

Reads the current time in msec (from Lab 1). You are free to select the time resolution for this function. It is ok to make the resolution to match the first call to OS\_AddPeriodicThread.

## Returns

time in ms units

7.8.3.20 void OS\_Signal ( Sema4Type \* *semaPt* )

increment semaphore Lab2 spinlock Lab3 wakeup blocked thread if appropriate

## Parameters

<i>semaPt</i>	pointer to a counting semaphore
---------------	---------------------------------

7.8.3.21 void OS\_Sleep ( unsigned long *sleepTime* )

Place this thread into a dormant state. You are free to select the time resolution for this function. OS\_Sleep(0) implements cooperative multitasking.

## Parameters

<i>sleepTime</i>	number of msec to sleep
------------------	-------------------------

## 7.8.3.22 void OS\_Suspend ( void )

suspend execution of currently running thread. scheduler will choose another thread to execute. Can be used to implement cooperative multitasking. Same function as OS\_Sleep(0).

## 7.8.3.23 unsigned long long OS\_Time ( void )

Return the system time in system time units (12.5ns)

**Returns**

time in 12.5ns units, 0 to 4294967295

**7.8.3.24 unsigned long long OS\_TimeDifference ( unsigned long long *start*, unsigned long long *stop* )**

Calculates difference between two times. The time resolution should be less than or equal to 1us, and the precision at least 12 bits. It is ok to change the resolution and precision of this function as long as this function and OS\_Time have the same resolution and precision.

**Parameters**

<i>start</i>	Start time measured with OS_Time
<i>stop</i>	Stop time measured with OS_Time

**Returns**

time difference in 12.5ns units

**7.8.3.25 void OS\_Wait ( Sema4Type \* *semaPt* )**

decrement semaphore Lab2 spinlock Lab3 block if less than zero

**Parameters**

<i>semaPt</i>	pointer to a counting semaphore
---------------	---------------------------------

**7.9 inc/PLL.h File Reference**

Runs on LM4F120/TM4C123 A software function to change the bus frequency using the PLL.

**Macros**

- #define **Bus80MHz** 4
- #define **Bus80\_000MHz** 4
- #define **Bus66\_667MHz** 5
- #define **Bus50\_000MHz** 7
- #define **Bus50MHz** 7
- #define **Bus44\_444MHz** 8
- #define **Bus40\_000MHz** 9
- #define **Bus40MHz** 9
- #define **Bus36\_364MHz** 10
- #define **Bus33\_333MHz** 11
- #define **Bus30\_769MHz** 12
- #define **Bus28\_571MHz** 13

- #define **Bus26\_667MHz** 14
- #define **Bus25\_000MHz** 15
- #define **Bus25MHz** 15
- #define **Bus23\_529MHz** 16
- #define **Bus22\_222MHz** 17
- #define **Bus21\_053MHz** 18
- #define **Bus20\_000MHz** 19
- #define **Bus20MHz** 19
- #define **Bus19\_048MHz** 20
- #define **Bus18\_182MHz** 21
- #define **Bus17\_391MHz** 22
- #define **Bus16\_667MHz** 23
- #define **Bus16\_000MHz** 24
- #define **Bus16MHz** 24
- #define **Bus15\_385MHz** 25
- #define **Bus14\_815MHz** 26
- #define **Bus14\_286MHz** 27
- #define **Bus13\_793MHz** 28
- #define **Bus13\_333MHz** 29
- #define **Bus12\_903MHz** 30
- #define **Bus12\_500MHz** 31
- #define **Bus12\_121MHz** 32
- #define **Bus11\_765MHz** 33
- #define **Bus11\_429MHz** 34
- #define **Bus11\_111MHz** 35
- #define **Bus10\_811MHz** 36
- #define **Bus10\_526MHz** 37
- #define **Bus10\_256MHz** 38
- #define **Bus10\_000MHz** 39
- #define **Bus10MHz** 39
- #define **Bus9\_756MHz** 40
- #define **Bus9\_524MHz** 41
- #define **Bus9\_302MHz** 42
- #define **Bus9\_091MHz** 43
- #define **Bus8\_889MHz** 44
- #define **Bus8\_696MHz** 45
- #define **Bus8\_511MHz** 46
- #define **Bus8\_333MHz** 47
- #define **Bus8\_163MHz** 48
- #define **Bus8\_000MHz** 49
- #define **Bus8MHz** 49
- #define **Bus7\_843MHz** 50
- #define **Bus7\_692MHz** 51
- #define **Bus7\_547MHz** 52
- #define **Bus7\_407MHz** 53
- #define **Bus7\_273MHz** 54
- #define **Bus7\_143MHz** 55
- #define **Bus7\_018MHz** 56
- #define **Bus6\_897MHz** 57
- #define **Bus6\_780MHz** 58
- #define **Bus6\_667MHz** 59
- #define **Bus6\_557MHz** 60
- #define **Bus6\_452MHz** 61
- #define **Bus6\_349MHz** 62
- #define **Bus6\_250MHz** 63

- #define **Bus6\_154MHz** 64
- #define **Bus6\_061MHz** 65
- #define **Bus5\_970MHz** 66
- #define **Bus5\_882MHz** 67
- #define **Bus5\_797MHz** 68
- #define **Bus5\_714MHz** 69
- #define **Bus5\_634MHz** 70
- #define **Bus5\_556MHz** 71
- #define **Bus5\_479MHz** 72
- #define **Bus5\_405MHz** 73
- #define **Bus5\_333MHz** 74
- #define **Bus5\_263MHz** 75
- #define **Bus5\_195MHz** 76
- #define **Bus5\_128MHz** 77
- #define **Bus5\_063MHz** 78
- #define **Bus5\_000MHz** 79
- #define **Bus4\_938MHz** 80
- #define **Bus4\_878MHz** 81
- #define **Bus4\_819MHz** 82
- #define **Bus4\_762MHz** 83
- #define **Bus4\_706MHz** 84
- #define **Bus4\_651MHz** 85
- #define **Bus4\_598MHz** 86
- #define **Bus4\_545MHz** 87
- #define **Bus4\_494MHz** 88
- #define **Bus4\_444MHz** 89
- #define **Bus4\_396MHz** 90
- #define **Bus4\_348MHz** 91
- #define **Bus4\_301MHz** 92
- #define **Bus4\_255MHz** 93
- #define **Bus4\_211MHz** 94
- #define **Bus4\_167MHz** 95
- #define **Bus4\_124MHz** 96
- #define **Bus4\_082MHz** 97
- #define **Bus4\_040MHz** 98
- #define **Bus4\_000MHz** 99
- #define **Bus4MHz** 99
- #define **Bus3\_960MHz** 100
- #define **Bus3\_922MHz** 101
- #define **Bus3\_883MHz** 102
- #define **Bus3\_846MHz** 103
- #define **Bus3\_810MHz** 104
- #define **Bus3\_774MHz** 105
- #define **Bus3\_738MHz** 106
- #define **Bus3\_704MHz** 107
- #define **Bus3\_670MHz** 108
- #define **Bus3\_636MHz** 109
- #define **Bus3\_604MHz** 110
- #define **Bus3\_571MHz** 111
- #define **Bus3\_540MHz** 112
- #define **Bus3\_509MHz** 113
- #define **Bus3\_478MHz** 114
- #define **Bus3\_448MHz** 115
- #define **Bus3\_419MHz** 116
- #define **Bus3\_390MHz** 117



- `#define Bus3_361MHz` 118
- `#define Bus3_333MHz` 119
- `#define Bus3_306MHz` 120
- `#define Bus3_279MHz` 121
- `#define Bus3_252MHz` 122
- `#define Bus3_226MHz` 123
- `#define Bus3_200MHz` 124
- `#define Bus3_175MHz` 125
- `#define Bus3_150MHz` 126
- `#define Bus3_125MHz` 127

## Functions

- void [PLL\\_Init](#) (uint32\_t freq)  
*configure the system to get its clock from the PLL*

### 7.9.1 Detailed Description

Runs on LM4F120/TM4C123 A software function to change the bus frequency using the PLL.

#### Author

Daniel Valvano

### 7.9.2 Function Documentation

#### 7.9.2.1 void PLL\_Init ( uint32\_t freq )

configure the system to get its clock from the PLL

#### Parameters

<i>freq</i>	Macro defined in <a href="#">PLL.h</a> to choose frequency
-------------	--

## 7.10 inc/profiler.h File Reference

Thread profiler utility.

## Data Structures

- struct [event\\_t](#)

## Macros

- `#define EVENT_MAGIC` (0x02344629)
- `#define MAX_EVENTS` (100)

## Enumerations

- enum **event\_type\_e** { **EVENT\_FGTH\_START**, **EVENT\_PTH\_START**, **EVENT\_PTH\_END**, **EVENT\_NUM↵\_TYPES** }

## Functions

- void **Profiler\_Init** (void)  
*Initialize the thread profiler. Call before use.*
- int **Profiler\_Event** (event\_type\_e event\_type, char \*event\_name)  
*Register an event has occurred in the profiler.*
- void **Profiler\_Clear** (void)  
*Clear profiler history.*
- void **Profiler\_Foreach** (void(\*)(const event\_t \*))  
*Executes a function f on each event in the log in the order they occurred in the system.*

### 7.10.1 Detailed Description

Thread profiler utility.

#### Author

Riley Wood ([riley.wood@utexas.edu](mailto:riley.wood@utexas.edu))

### 7.10.2 Function Documentation

#### 7.10.2.1 int Profiler\_Event ( event\_type\_e event\_type, char \* event\_name )

Register an event has occurred in the profiler.

##### Parameters

<i>event↵_id</i>	ID of the event that occurred
------------------	-------------------------------

##### Returns

-1 on error, 0 on success

#### 7.10.2.2 void Profiler\_Foreach ( void(\*)(const event\_t \*) f )

Executes a function f on each event in the log in the order they occurred in the system.

##### Parameters

<i>f</i>	Function to execute on each event in the log.
----------	---

## 7.11 inc/ST7735.h File Reference

Low level drivers for the ST7735.

### Macros

- `#define ST7735_BLACK 0x0000`
- `#define ST7735_BLUE 0xF800`
- `#define ST7735_RED 0x001F`
- `#define ST7735_GREEN 0x07E0`
- `#define ST7735_CYAN 0xFFE0`
- `#define ST7735_MAGENTA 0xF81F`
- `#define ST7735_YELLOW 0x07FF`
- `#define ST7735_WHITE 0xFFFF`

### Enumerations

- `enum initRFlags {  
    none, INTR_GREENTAB, INTR_REDTAB, INTR_BLACKTAB,  
    none, INTR_GREENTAB, INTR_REDTAB, INTR_BLACKTAB }`

### Functions

- `void ST7735_InitB (void)`
- `void ST7735_InitR (enum initRFlags option)`
- `void ST7735_DrawPixel (short x, short y, unsigned short color)`
- `void ST7735_DrawFastVLine (short x, short y, short h, unsigned short color)`
- `void ST7735_DrawFastHLine (short x, short y, short w, unsigned short color)`
- `void ST7735_FillScreen (unsigned short color)`
- `void ST7735_FillRect (short x, short y, short w, short h, unsigned short color)`
- `unsigned short ST7735_Color565 (unsigned char r, unsigned char g, unsigned char b)`
- `unsigned short ST7735_SwapColor (unsigned short x)`
- `void ST7735_DrawBitmap (short x, short y, const unsigned short *image, short w, short h)`
- `void ST7735_DrawCharS (short x, short y, char c, short textColor, short bgColor, unsigned char size)`
- `void ST7735_DrawChar (short x, short y, char c, short textColor, short bgColor, unsigned char size)`
- `unsigned long ST7735_OutString (unsigned short x, unsigned short y, char *pt, short textColor)`
- `void ST7735_Message (unsigned long d, unsigned long l, char *pt, long value)`
- `void ST7735_SetRotation (unsigned char m)`
- `void ST7735_InvertDisplay (int i)`

### 7.11.1 Detailed Description

Low level drivers for the ST7735.

Runs on LM4F120/TM4C123. Low level drivers for the ST7735 160x128 LCD based off of the file described above. This version coexists with the SDC

#### Version

V1.0

#### Author

Valvano

#### Copyright

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#### Warning

AS-IS

#### Note

For more information see <http://users.ece.utexas.edu/~valvano/>

#### Date

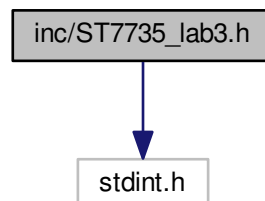
March 9, 2017

## 7.12 inc/ST7735\_lab3.h File Reference

This is a library for the Adafruit 1.8" SPI display.

```
#include <stdint.h>
```

Include dependency graph for ST7735\_lab3.h:



## Macros

- `#define ST7735_TFTWIDTH 128`
- `#define ST7735_TFTHEIGHT 160`
- `#define ST7735_BLACK 0x0000`
- `#define ST7735_BLUE 0xF800`
- `#define ST7735_RED 0x001F`
- `#define ST7735_GREEN 0x07E0`
- `#define ST7735_CYAN 0xFFE0`
- `#define ST7735_MAGENTA 0xF81F`
- `#define ST7735_YELLOW 0x07FF`
- `#define ST7735_WHITE 0xFFFF`

## Enumerations

- enum `initRFlags` {  
**none**, **INTR\_GREENTAB**, **INTR\_REDTAB**, **INTR\_BLACKTAB**,  
**none**, **INTR\_GREENTAB**, **INTR\_REDTAB**, **INTR\_BLACKTAB** }  
*some flags for `ST7735_InitR()`*

## Functions

- void `ST7735_InitB` (void)  
*Initialization for ST7735B screens.*
- void `ST7735_InitR` (enum `initRFlags` option)  
*Initialization for ST7735R screens (green or red tabs).*
- void `ST7735_DrawPixel` (int16\_t x, int16\_t y, uint16\_t color)  
*Color the pixel at the given coordinates with the given color. Requires 13 bytes of transmission.*
- void `ST7735_DrawFastVLine` (int16\_t x, int16\_t y, int16\_t h, uint16\_t color)  
*Draw a vertical line at the given coordinates with the given height and color. A vertical line is parallel to the longer side of the rectangular display Requires (11 + 2\*h) bytes of transmission (assuming image fully on screen)*
- void `ST7735_DrawFastHLine` (int16\_t x, int16\_t y, int16\_t w, uint16\_t color)  
*Draw a horizontal line at the given coordinates with the given width and color. A horizontal line is parallel to the shorter side of the rectangular display Requires (11 + 2\*w) bytes of transmission (assuming image fully on screen)*
- void `ST7735_FillScreen` (uint16\_t color)  
*Fill the screen with the given color. Requires 40,971 bytes of transmission.*
- void `ST7735_FillRect` (int16\_t x, int16\_t y, int16\_t w, int16\_t h, uint16\_t color)  
*Draw a filled rectangle at the given coordinates with the given width, height, and color. Requires (11 + 2\*w\*h) bytes of transmission (assuming image fully on screen)*
- uint16\_t `ST7735_Color565` (uint8\_t r, uint8\_t g, uint8\_t b)  
*Pass 8-bit (each) R,G,B and get back 16-bit packed color.*
- uint16\_t `ST7735_SwapColor` (uint16\_t x)  
*Swaps the red and blue values of the given 16-bit packed color; green is unchanged.*
- void `ST7735_DrawBitmap` (int16\_t x, int16\_t y, const uint16\_t \*image, int16\_t w, int16\_t h)  
*Displays a 16-bit color BMP image. A bitmap file that is created by a PC image processing program has a header and may be padded with dummy columns so the data have four byte alignment. This function assumes that all of that has been stripped out, and the array image[] has one 16-bit halfword for each pixel to be displayed on the screen (encoded in reverse order, which is standard for bitmap files). An array can be created in this format from a 24-bit-per-pixel .bmp file using the associated converter program. (x,y) is the screen location of the lower left corner of BMP image Requires (11 + 2\*w\*h) bytes of transmission (assuming image fully on screen) Must be less than or equal to 128 pixels wide by 160 pixels high.*
- void `ST7735_DrawCharS` (int16\_t x, int16\_t y, char c, int16\_t textColor, int16\_t bgColor, uint8\_t size)

Simple character draw function. This is the same function from Adafruit\_GFX.c but adapted for this processor. However, each call to `ST7735_DrawPixel()` calls `setAddrWindow()`, which needs to send many extra data and commands. If the background color is the same as the text color, no background will be printed, and text can be drawn right over existing images without covering them with a box. Requires  $(11 + 2 \times \text{size} \times \text{size}) \times 6 \times 8$  (image fully on screen; `textcolor != bgColor`)

- void `ST7735_DrawChar` (int16\_t x, int16\_t y, char c, int16\_t textColor, int16\_t bgColor, uint8\_t size)  
Advanced character draw function. This is similar to the function from Adafruit\_GFX.c but adapted for this processor. However, this function only uses one call to `setAddrWindow()`, which allows it to run at least twice as fast. Requires  $(11 + \text{size} \times \text{size} \times 6 \times 8)$  bytes of transmission (assuming image fully on screen)
- uint32\_t `ST7735_DrawString` (uint16\_t x, uint16\_t y, char \*pt, int16\_t textColor, int16\_t bgColor)  
String draw function. 16 rows (0 to 15) and 21 characters (0 to 20) Requires  $(11 + \text{size} \times \text{size} \times 6 \times 8)$  bytes of transmission for each character If `bgColor` is same as `textColor`, no background will be filled in for chars.
- void `ST7735_SetCursor` (uint32\_t newX, uint32\_t newY)  
Move the cursor to the desired X- and Y-position. The next character will be printed here. X=0 is the leftmost column. Y=0 is the top row.
- void `ST7735_OutUDec` (uint32\_t n)  
Output a 32-bit number in unsigned decimal format Position determined by `ST7735_SetCursor` command Color set by `ST7735_SetTextColor`.
- void `ST7735_SetRotation` (uint8\_t m)  
Change the image rotation. Requires 2 bytes of transmission.
- void `ST7735_InvertDisplay` (int i)  
Send the command to invert all of the colors. Requires 1 byte of transmission.
- void `ST7735_PlotClear` (int32\_t ymin, int32\_t ymax)  
Clear the graphics buffer, set X coordinate to 0 This routine clears the display.
- void `ST7735_PlotPoint` (int32\_t y)  
Used in the voltage versus time plot, plot one point at y It does output to display.
- void `ST7735_PlotLine` (int32\_t y)  
Used in the voltage versus time plot, plot line to new point It does output to display.
- void `ST7735_PlotPoints` (int32\_t y1, int32\_t y2)  
Used in the voltage versus time plot, plot two points at y1, y2 It does output to display.
- void `ST7735_PlotBar` (int32\_t y)  
Used in the voltage versus time bar, plot one bar at y It does not output to display until `RIT128x96x4ShowPlot` called.
- void `ST7735_PlotdBfs` (int32\_t y)  
Used in the amplitude versus frequency plot, plot bar point at y 0 to 0.625V scaled on a log plot from min to max It does output to display.
- void `ST7735_PlotNext` (void)  
Used in all the plots to step the X coordinate one pixel X steps from 0 to 127, then back to 0 again It does not output to display.
- void `ST7735_PlotNextErase` (void)  
Used in all the plots to step the X coordinate one pixel X steps from 0 to 127, then back to 0 again It clears the vertical space into which the next pixel will be drawn.
- void `ST7735_OutChar` (char ch)  
Output one character to the LCD Position determined by `ST7735_SetCursor` command Color set by `ST7735_SetTextColor`.
- void `ST7735_OutString` (char \*ptr)  
Print a string of characters to the ST7735 LCD. Position determined by `ST7735_SetCursor` command Color set by `ST7735_SetTextColor` The string will not automatically wrap.
- void `ST7735_SetTextColor` (uint16\_t color)  
Sets the color in which the characters will be printed Background color is fixed at black.
- void `Output_Init` (void)  
Standard device driver initialization function for printf Initialize ST7735 LCD.
- void `Output_Clear` (void)  
Clear display.
- void `Output_Off` (void)

*Turn off display (low power)*

- void `Output_On` (void)

*Turn on display.*

- void `Output_Color` (uint32\_t newColor)

*set the color for future output Background color is fixed at black*

- void `ST7735_Message` (int device, int line, char \*string, int32\_t value)

*Display a string and number on one of two logical displays at a given line number relative to that display. The LCD display is logically divided into two displays: top and bottom. These logical displays are identified with a device ID. Device 0 is the top display, device 1 is the bottom display. Each logical device has 4 lines, numbered 0 to 3. Prints in black text on a white background. This function is not (yet) reentrant.*

## 7.12.1 Detailed Description

This is a library for the Adafruit 1.8" SPI display.

## 7.12.2 Function Documentation

### 7.12.2.1 void `Output_Color` ( uint32\_t newColor )

set the color for future output Background color is fixed at black

#### Parameters

<i>newColor</i>	16-bit packed color
-----------------	---------------------

### 7.12.2.2 uint16\_t `ST7735_Color565` ( uint8\_t r, uint8\_t g, uint8\_t b )

Pass 8-bit (each) R,G,B and get back 16-bit packed color.

#### Parameters

<i>r</i>	red value
<i>g</i>	green value
<i>b</i>	blue value

#### Returns

uint16\_t 16-bit color

### 7.12.2.3 void `ST7735_DrawBitmap` ( int16\_t x, int16\_t y, const uint16\_t \* image, int16\_t w, int16\_t h )

Displays a 16-bit color BMP image. A bitmap file that is created by a PC image processing program has a header and may be padded with dummy columns so the data have four byte alignment. This function assumes that all of that has been stripped out, and the array `image[]` has one 16-bit halfword for each pixel to be displayed on the screen (encoded in reverse order, which is standard for bitmap files). An array can be created in this format from a

24-bit-per-pixel .bmp file using the associated converter program. (x,y) is the screen location of the lower left corner of BMP image Requires  $(11 + 2*w*h)$  bytes of transmission (assuming image fully on screen) Must be less than or equal to 128 pixels wide by 160 pixels high.



## Parameters

<i>x</i>	horizontal position of the bottom left corner of the image, columns from the left edge
<i>y</i>	vertical position of the bottom left corner of the image, rows from the top edge
<i>image</i>	pointer to a 16-bit color BMP image
<i>w</i>	number of pixels wide
<i>h</i>	number of pixels tall

## 7.12.2.4 void ST7735\_DrawChar ( int16\_t x, int16\_t y, char c, int16\_t textColor, int16\_t bgColor, uint8\_t size )

Advanced character draw function. This is similar to the function from Adafruit\_GFX.c but adapted for this processor. However, this function only uses one call to setAddrWindow(), which allows it to run at least twice as fast. Requires (11 + size\*size\*6\*8) bytes of transmission (assuming image fully on screen)

## Parameters

<i>x</i>	horizontal position of the top left corner of the character, columns from the left edge
<i>y</i>	vertical position of the top left corner of the character, rows from the top edge
<i>c</i>	character to be printed
<i>textColor</i>	16-bit color of the character
<i>bgColor</i>	16-bit color of the background
<i>size</i>	number of pixels per character pixel (e.g. size==2 prints each pixel of font as 2x2 square)

## 7.12.2.5 void ST7735\_DrawCharS ( int16\_t x, int16\_t y, char c, int16\_t textColor, int16\_t bgColor, uint8\_t size )

Simple character draw function. This is the same function from Adafruit\_GFX.c but adapted for this processor. However, each call to [ST7735\\_DrawPixel\(\)](#) calls setAddrWindow(), which needs to send many extra data and commands. If the background color is the same as the text color, no background will be printed, and text can be drawn right over existing images without covering them with a box. Requires (11 + 2\*size\*size)\*6\*8 (image fully on screen; textcolor != bgColor)

## Parameters

<i>x</i>	horizontal position of the top left corner of the character, columns from the left edge
<i>y</i>	vertical position of the top left corner of the character, rows from the top edge
<i>c</i>	character to be printed
<i>textColor</i>	16-bit color of the character
<i>bgColor</i>	16-bit color of the background
<i>size</i>	number of pixels per character pixel (e.g. size==2 prints each pixel of font as 2x2 square)

## 7.12.2.6 void ST7735\_DrawFastHLine ( int16\_t x, int16\_t y, int16\_t w, uint16\_t color )

Draw a horizontal line at the given coordinates with the given width and color. A horizontal line is parallel to the shorter side of the rectangular display Requires (11 + 2\*w) bytes of transmission (assuming image fully on screen)

## Parameters

<i>x</i>	horizontal position of the start of the line, columns from the left edge
<i>y</i>	vertical position of the start of the line, rows from the top edge
<i>w</i>	horizontal width of the line
<i>color</i>	16-bit color, which can be produced by <a href="#">ST7735_Color565()</a>

7.12.2.7 void ST7735\_DrawFastVLine ( int16\_t x, int16\_t y, int16\_t h, uint16\_t color )

Draw a vertical line at the given coordinates with the given height and color. A vertical line is parallel to the longer side of the rectangular display Requires (11 + 2\*h) bytes of transmission (assuming image fully on screen)

## Parameters

<i>x</i>	horizontal position of the start of the line, columns from the left edge
<i>y</i>	vertical position of the start of the line, rows from the top edge
<i>h</i>	vertical height of the line
<i>color</i>	16-bit color, which can be produced by <a href="#">ST7735_Color565()</a>

7.12.2.8 void ST7735\_DrawPixel ( int16\_t x, int16\_t y, uint16\_t color )

Color the pixel at the given coordinates with the given color. Requires 13 bytes of transmission.

## Parameters

<i>x</i>	horizontal position of the pixel, columns from the left edge must be less than 128 0 is on the left, 126 is near the right
<i>y</i>	vertical position of the pixel, rows from the top edge must be less than 160 159 is near the wires, 0 is the side opposite the wires
<i>color</i>	16-bit color, which can be produced by <a href="#">ST7735_Color565()</a>

7.12.2.9 uint32\_t ST7735\_DrawString ( uint16\_t x, uint16\_t y, char \* pt, int16\_t textColor, int16\_t bgColor )

String draw function. 16 rows (0 to 15) and 21 characters (0 to 20) Requires (11 + size\*size\*6\*8) bytes of transmission for each character If bgColor is same as textColor, no background will be filled in for chars.

## Parameters

<i>x</i>	columns from the left edge (0 to 20)
<i>y</i>	rows from the top edge (0 to 15)
<i>pt</i>	pointer to a null terminated string to be printed
<i>textColor</i>	16-bit color of the characters
<i>bgColor</i>	16-bit color of the background

**Returns**

uint32\_t number of characters printed

**7.12.2.10 void ST7735\_FillRect ( int16\_t x, int16\_t y, int16\_t w, int16\_t h, uint16\_t color )**

Draw a filled rectangle at the given coordinates with the given width, height, and color. Requires (11 + 2\*w\*h) bytes of transmission (assuming image fully on screen)

**Parameters**

<i>x</i>	horizontal position of the top left corner of the rectangle, columns from the left edge
<i>y</i>	vertical position of the top left corner of the rectangle, rows from the top edge
<i>w</i>	horizontal width of the rectangle
<i>h</i>	vertical height of the rectangle
<i>color</i>	16-bit color, which can be produced by <a href="#">ST7735_Color565()</a>

**7.12.2.11 void ST7735\_FillScreen ( uint16\_t color )**

Fill the screen with the given color. Requires 40,971 bytes of transmission.

**Parameters**

<i>color</i>	16-bit color, which can be produced by <a href="#">ST7735_Color565()</a>
--------------	--

**7.12.2.12 void ST7735\_InitR ( enum initRFlags option )**

Initialization for ST7735R screens (green or red tabs).

**Parameters**

<i>initRFlags</i>	one of the enumerated options depending on tabs
-------------------	---

**7.12.2.13 void ST7735\_InvertDisplay ( int i )**

Send the command to invert all of the colors. Requires 1 byte of transmission.

**Parameters**

<i>i</i>	0 to disable inversion; non-zero to enable inversion
----------	--

#### 7.12.2.14 void ST7735\_Message ( int device, int line, char \* string, int32\_t value )

Display a string and number on one of two logical displays at a given line number relative to that display. The LCD display is logically divided into two displays: top and bottom. These logical displays are identified with a device ID. Device 0 is the top display, device 1 is the bottom display. Each logical device has 4 lines, numbered 0 to 3. Prints in black text on a white background. This function is not (yet) reentrant.

##### Parameters

<i>device</i>	Device ID, 0 or 1
<i>line</i>	Line number, 0 to 3, relative to the logical display.
<i>string</i>	Null-terminated string to print on the select logical display and line.
<i>value</i>	Integer value printed after the string.

#### 7.12.2.15 void ST7735\_OutChar ( char ch )

Output one character to the LCD Position determined by ST7735\_SetCursor command Color set by ST7735\_SetTextColor.

##### Parameters

<i>ch</i>	8-bit ASCII character
-----------	-----------------------

#### 7.12.2.16 void ST7735\_OutString ( char \* ptr )

Print a string of characters to the ST7735 LCD. Position determined by ST7735\_SetCursor command Color set by ST7735\_SetTextColor The string will not automatically wrap.

##### Parameters

<i>ptr</i>	pointer to NULL-terminated ASCII string
------------	---

#### 7.12.2.17 void ST7735\_OutUDec ( uint32\_t n )

Output a 32-bit number in unsigned decimal format Position determined by ST7735\_SetCursor command Color set by ST7735\_SetTextColor.

##### Parameters

<i>n</i>	32-bit number to be transferred
----------	---------------------------------

#### 7.12.2.18 void ST7735\_PlotBar ( int32\_t y )

Used in the voltage versus time bar, plot one bar at y It does not output to display until RIT128x96x4ShowPlot called.

## Parameters

<i>y</i>	the y coordinate of the bar plotted
----------	-------------------------------------

7.12.2.19 void ST7735\_PlotClear ( int32\_t *ymin*, int32\_t *ymax* )

Clear the graphics buffer, set X coordinate to 0 This routine clears the display.

## Parameters

<i>ymin</i>	Lower bound of plot
<i>ymax</i>	Upper bound of plot

7.12.2.20 void ST7735\_PlotdBfs ( int32\_t *y* )

Used in the amplitude versus frequency plot, plot bar point at y 0 to 0.625V scaled on a log plot from min to max It does output to display.

## Parameters

<i>y</i>	the y ADC value of the bar plotted
----------	------------------------------------

7.12.2.21 void ST7735\_PlotLine ( int32\_t *y* )

Used in the voltage versus time plot, plot line to new point It does output to display.

## Parameters

<i>y</i>	the y coordinate of the point plotted
----------	---------------------------------------

7.12.2.22 void ST7735\_PlotPoint ( int32\_t *y* )

Used in the voltage versus time plot, plot one point at y It does output to display.

## Parameters

<i>y</i>	the y coordinate of the point plotted
----------	---------------------------------------

7.12.2.23 void ST7735\_PlotPoints ( int32\_t *y1*, int32\_t *y2* )

Used in the voltage versus time plot, plot two points at y1, y2 It does output to display.

**Parameters**

<i>y1</i>	the y coordinate of the first point plotted
<i>y2</i>	the y coordinate of the second point plotted

**7.12.2.24 void ST7735\_SetCursor ( uint32\_t *newX*, uint32\_t *newY* )**

Move the cursor to the desired X- and Y-position. The next character will be printed here. X=0 is the leftmost column. Y=0 is the top row.

**Parameters**

<i>newX</i>	new X-position of the cursor (0<= <i>newX</i> <=20)
<i>newY</i>	new Y-position of the cursor (0<= <i>newY</i> <=15)

**7.12.2.25 void ST7735\_SetRotation ( uint8\_t *m* )**

Change the image rotation. Requires 2 bytes of transmission.

**Parameters**

<i>m</i>	new rotation value (0 to 3)
----------	-----------------------------

**7.12.2.26 void ST7735\_SetTextColor ( uint16\_t *color* )**

Sets the color in which the characters will be printed Background color is fixed at black.

**Parameters**

<i>color</i>	16-bit packed color
--------------	---------------------

**7.12.2.27 uint16\_t ST7735\_SwapColor ( uint16\_t *x* )**

Swaps the red and blue values of the given 16-bit packed color; green is unchanged.

**Parameters**

<i>x</i>	16-bit color in format B, G, R
----------	--------------------------------

**Returns**

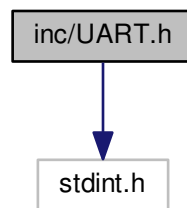
uint16\_t 16-bit color in format R, G, B

## 7.13 inc/UART.h File Reference

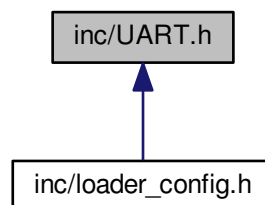
Runs on LM4F120/TM4C123 Use UART0 to implement bidirectional data transfer to and from a computer running HyperTerminal. This time, interrupts and FIFOs are used.

```
#include <stdint.h>
```

Include dependency graph for UART.h:



This graph shows which files directly or indirectly include this file:



### Macros

- `#define CR 0x0D`
- `#define LF 0x0A`
- `#define BS 0x08`
- `#define ESC 0x1B`
- `#define SP 0x20`
- `#define DEL 0x7F`

## Functions

- void **UART\_Init** (void)  
*Initialize the UART for 115,200 baud rate (assuming 50 MHz clock), 8 bit word length, no parity bits, one stop bit, FIFOs enabled.*
- char **UART\_InChar** (void)  
*Wait for new serial port input.*
- void **UART\_OutChar** (char data)  
*8-bit to serial port*
- void **UART\_OutString** (char \*pt)  
*Output String (NULL termination)*
- uint32\_t **UART\_InUDec** (void)  
*InUDec accepts ASCII input in unsigned decimal format and converts to a 32-bit unsigned number valid range is 0 to 4294967295 ( $2^{32}-1$ ) If you enter a number above 4294967295, it will return an incorrect value Backspace will remove last digit typed.*
- void **UART\_OutUDec** (uint32\_t n)  
*Output a 32-bit number in unsigned decimal format.*
- uint32\_t **UART\_InUHex** (void)  
*Accepts ASCII input in unsigned hexadecimal (base 16) format No '\$' or '0x' need be entered, just the 1 to 8 hex digits It will convert lower case a-f to uppercase A-F and converts to a 16 bit unsigned number value range is 0 to FFFFFFFF If you enter a number above FFFFFFFF, it will return an incorrect value Backspace will remove last digit typed.*
- void **UART\_OutUHex** (uint32\_t number)  
*Output a 32-bit number in unsigned hexadecimal format Variable format 1 to 8 digits with no space before or after.*
- void **UART\_InString** (char \*bufPt, uint16\_t max)  
*Accepts ASCII characters from the serial port and adds them to a string until <enter> is typed or until max length of the string is reached. It echoes each character as it is inputted. If a backspace is inputted, the string is modified and the backspace is echoed terminates the string with a null character uses busy-waiting synchronization on RDRF Modified by Agustinus Darmawan + Mingjie Qiu.*

### 7.13.1 Detailed Description

Runs on LM4F120/TM4C123 Use UART0 to implement bidirectional data transfer to and from a computer running HyperTerminal. This time, interrupts and FIFOs are used.

#### Author

Daniel Valvano

### 7.13.2 Function Documentation

#### 7.13.2.1 char UART\_InChar ( void )

Wait for new serial port input.

#### Returns

char ASCII code for key typed

#### 7.13.2.2 void UART\_InString ( char \* bufPt, uint16\_t max )

Accepts ASCII characters from the serial port and adds them to a string until <enter> is typed or until max length of the string is reached. It echoes each character as it is inputted. If a backspace is inputted, the string is modified and the backspace is echoed terminates the string with a null character uses busy-waiting synchronization on RDRF Modified by Agustinus Darmawan + Mingjie Qiu.



## Parameters

<i>buf</i> ↔ <i>Pt</i>	pointer to empty buffer
<i>max</i>	size of buffer

## 7.13.2.3 uint32\_t UART\_InUDec ( void )

InUDec accepts ASCII input in unsigned decimal format and converts to a 32-bit unsigned number valid range is 0 to 4294967295 ( $2^{32}-1$ ) If you enter a number above 4294967295, it will return an incorrect value Backspace will remove last digit typed.

## Returns

uint32\_t 32-bit unsigned number

## 7.13.2.4 uint32\_t UART\_InUHex ( void )

Accepts ASCII input in unsigned hexadecimal (base 16) format No '\$' or '0x' need be entered, just the 1 to 8 hex digits It will convert lower case a-f to uppercase A-F and converts to a 16 bit unsigned number value range is 0 to FFFFFFFF If you enter a number above FFFFFFFF, it will return an incorrect value Backspace will remove last digit typed.

## Returns

uint32\_t 32-bit unsigned number

7.13.2.5 void UART\_OutChar ( char *data* )

8-bit to serial port

## Parameters

<i>data</i>	letter is an 8-bit ASCII character to be transferred
-------------	--

7.13.2.6 void UART\_OutString ( char \* *pt* )

Output String (NULL termination)

## Parameters

<i>pt</i>	pointer to a NULL-terminated string to be transferred
-----------	---

#### 7.13.2.7 void UART\_OutUDec ( uint32\_t *n* )

Output a 32-bit number in unsigned decimal format.

##### Parameters

<i>n</i>	32-bit number to be transferred
----------	---------------------------------

#### 7.13.2.8 void UART\_OutUHex ( uint32\_t *number* )

Output a 32-bit number in unsigned hexadecimal format Variable format 1 to 8 digits with no space before or after.

##### Parameters

<i>number</i>	32-bit number to be transferred
---------------	---------------------------------

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