Note: Inside a makefile, we can call out to the shell and execute a shell command. For example, `$(shell echo src/*.c)` will return a list of all of the files in the src directory that end in "`.c`".

```
# Example Makefile

SHELL = /bin/sh
CC = gcc
CFLAGS = -Wall
RELEASEFLAGS = -D NDEBUG

TARGET = foomatic-widget
COMMON = definitions.h debug.h
SOURCES = $(shell echo *.c)
HEADERS = $(shell echo *.h)
OBJECTS = $(SOURCES:.c=.0)

PREFIX = .
BINDIR = $(PREFIX)/bin

all: $(TARGET)

$(TARGET): $(OBJECTS) $(COMMON)
      $(CC) $(CFLAGS) -o $(TARGET) $(OBJECTS)

release: $(SOURCES) $(HEADERS) $(COMMON)
      $(CC) $(CFLAGS) $(RELEASEFLAGS) -o $(TARGET) $(SOURCES)

install: release
      cp $(TARGET) $(BINDIR)/$(TARGET)

uninstall:
      -rm $(BINDIR)/$(TARGET)

clean:
      -rm -f $(OBJECTS)
      -rm -f $(TARGET)

%.o: %.c $(HEADERS) $(COMMON)
      $(CC) $(CFLAGS) -c -o $@ $<
```