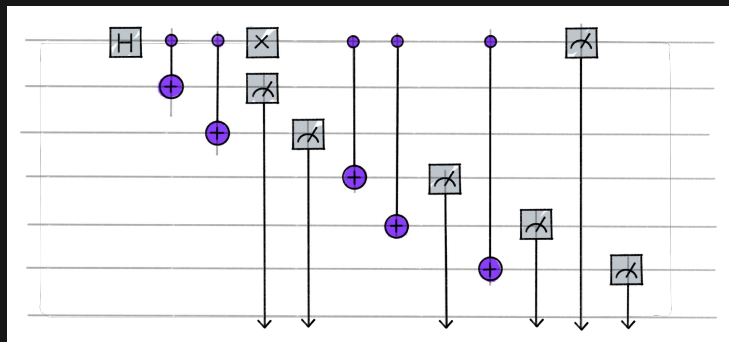


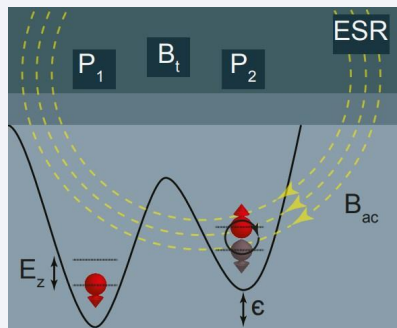
# #18 Circuit/Pulse Optimization Across Different Quantum Hardware Modalities

Mentees: Sebastian Brandhofer, Philip Kim, Jakub Mrozek, Siyuan Niu

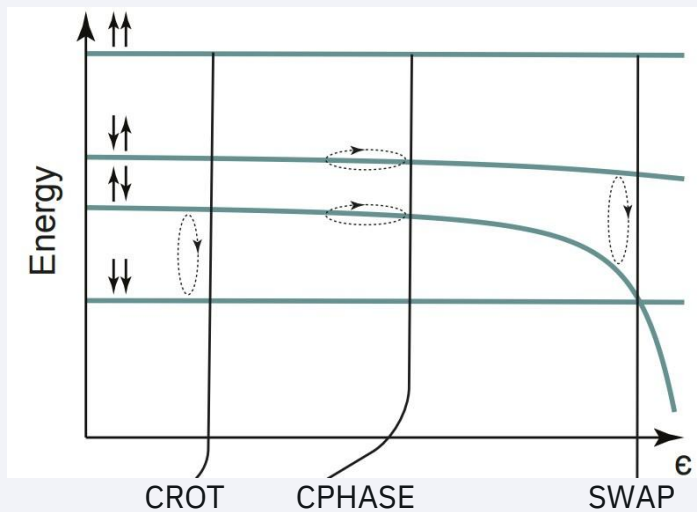
Mentor: Nicholas T. Bronn



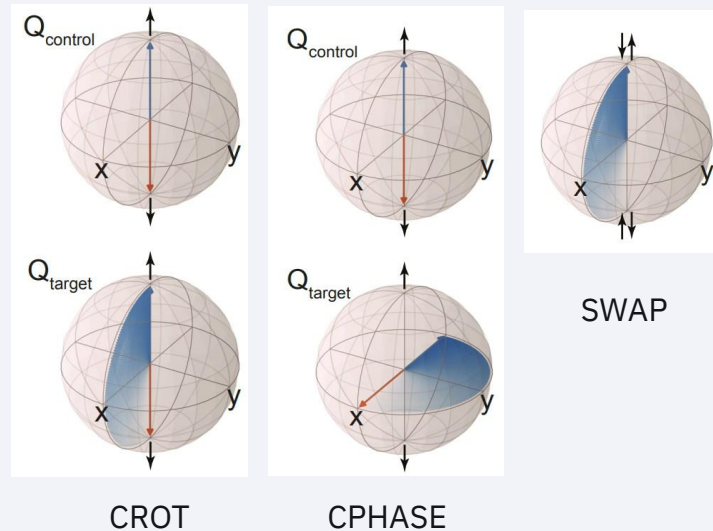
# Two-Qubit Gate Implementation on Spin Qubits



Device geometry

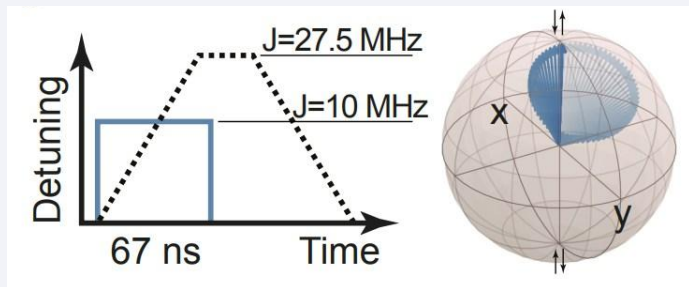


Energy-Detuning Graph



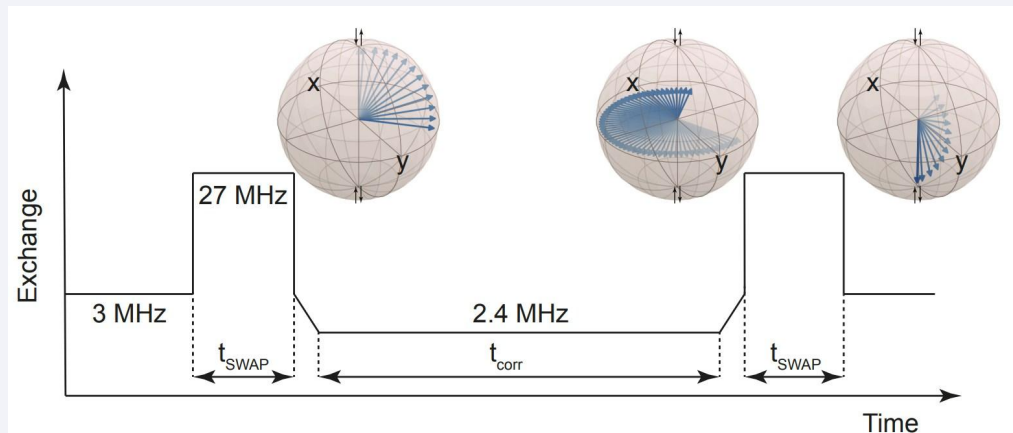
$$\hat{H} = \frac{1}{2} \Delta E_z(\epsilon) (\hat{S}_z^1 - \hat{S}_z^2) + J(\epsilon) (\hat{S}^1 \cdot \hat{S}^2 - \frac{1}{4})$$

# Adiabatic and composite gates



Diabatic and adiabatic CPHASE

$$\hat{H} = \frac{1}{2} \Delta E_z(\epsilon) (\hat{S}_z^1 - \hat{S}_z^2) + J(\epsilon) \hat{S}_z^1 \hat{S}_z^2$$

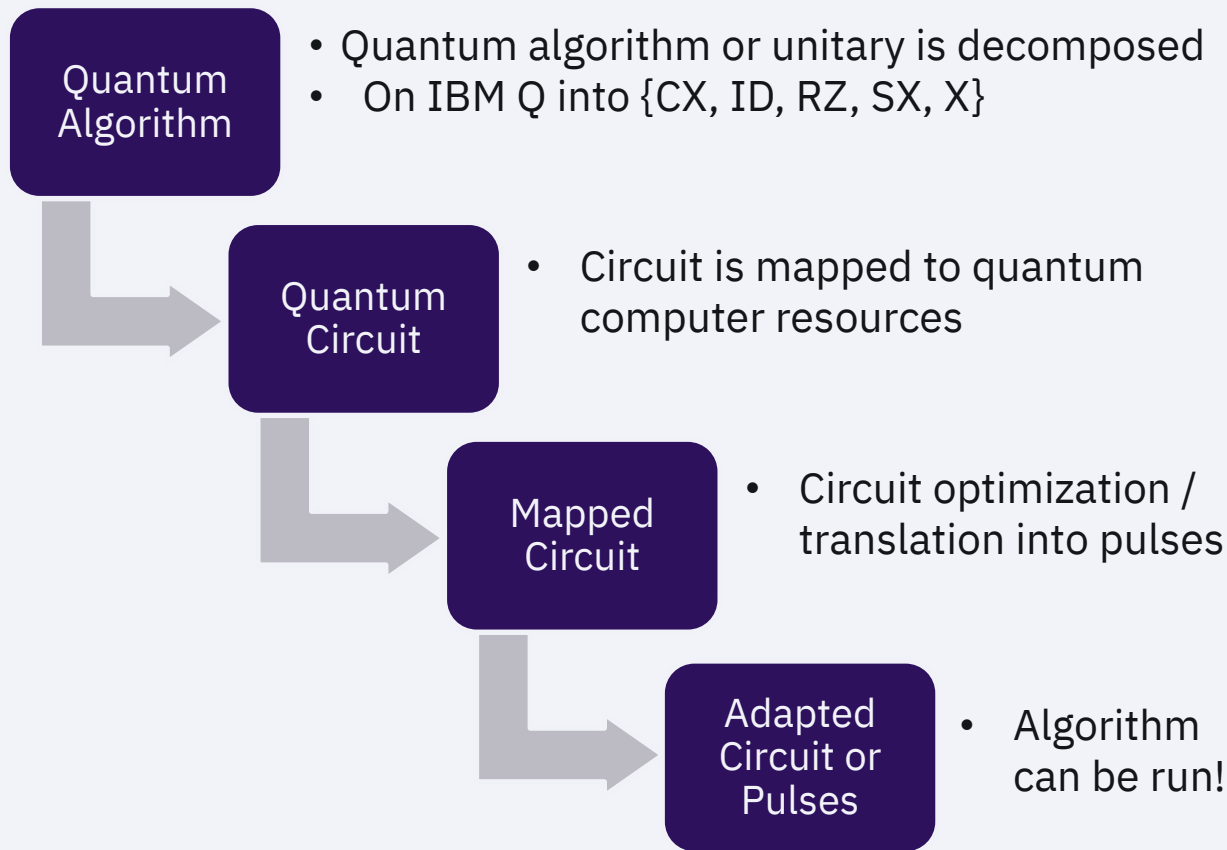


Composite SWAP

$$\hat{H} = J(\epsilon) (\hat{S}^1 \cdot \hat{S}^2 - \frac{1}{4})$$

$$\hat{H} = \frac{1}{2} \Delta E_z(\epsilon) (\hat{S}_z^1 - \hat{S}_z^2) + J(\epsilon) (\hat{S}^1 \cdot \hat{S}^2 - \frac{1}{4})$$

# Quantum Algorithm Computation



## Spin qubits

- Consider native gates
- Gate characteristics

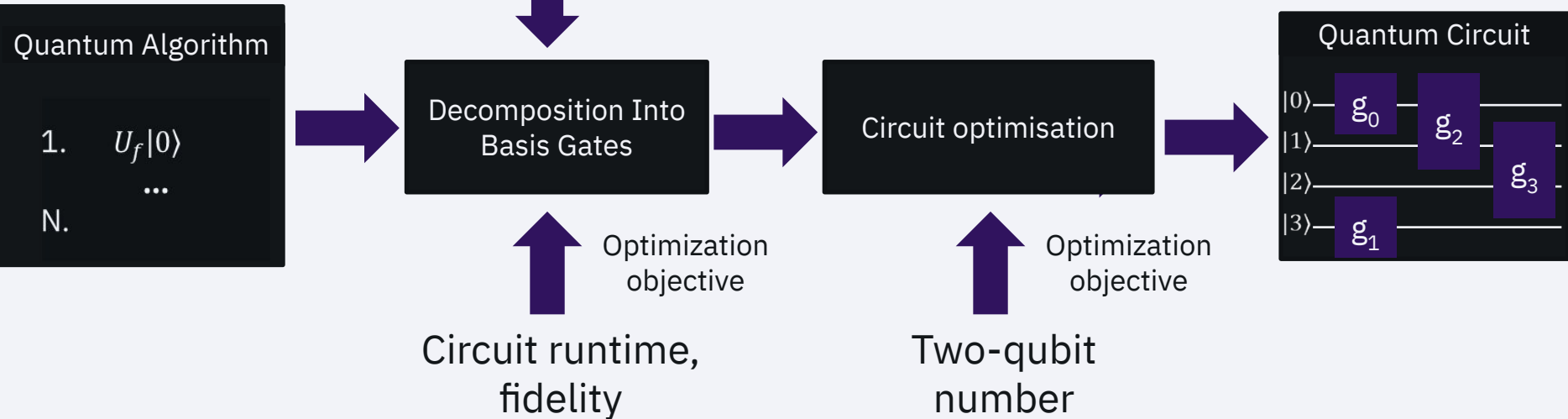
- Mitigate crosstalk
- Consider calibration

- Replace non-native gates
- Translate into spin qubit pulses

# Computing Quantum Algorithms on Spin Qubits

	RZ	RX, RY	CROT	CPHASE	CPHASE-Diabatic	SWAP	SWAP-C
Gate time [ns]	0	~40	660	152	67	19	89
Fidelity	1	0.999	0.994	0.978	0.994	0.842	0.994

Target basis gates



# The Team

## Mentees



**Sebastian Brandhofer**



**Philip Kim**



**Jakub Mrozek**



**Siyuan Niu**



**Mentor Nicholas T. Bronn**