ZNE error mitigation with mid-transpilation noise amplification #4

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Outline



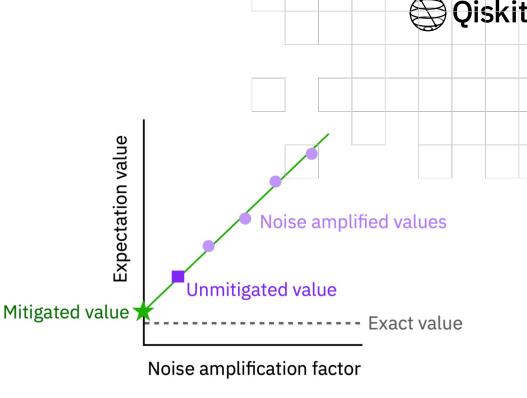
- Zero Noise Extrapolation in a nutshell
- Work Plan
- Short **Demo**

ZNE: Brief introduction:

Goal: Mitigate the effects of **gate errors** in expectation value computations

Steps:

- Amplify the noise introduced by the gates of the circuit
- 2. Extrapolate the noisy expectation values to the zero-noise limit



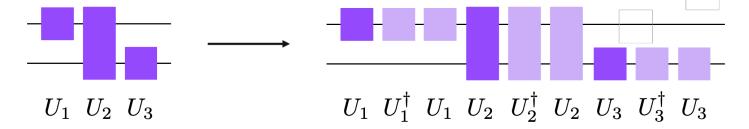
Incrementally make things worse to fit a curve back to the best

- Increment = Noise factor

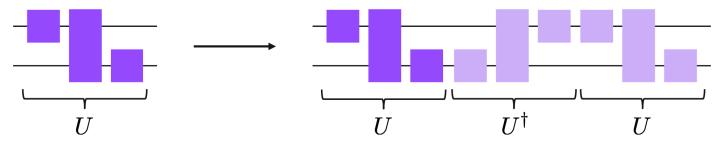
Noise amplification in Digital ZNE



Local folding – repeatedly folding the gates inside the circuit



Global folding – repeatedly folding the whole **circuit**



Work Plan

Current Structure:

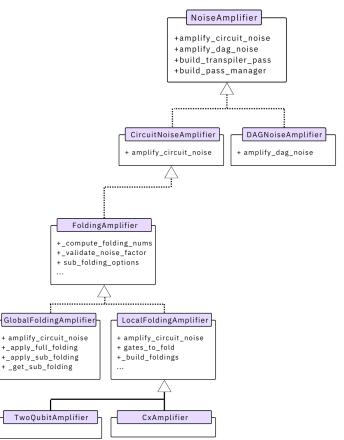


Goal:

Make noise amplification more efficient and improve overall error mitigation

Prototype ZNE: https://github.com/qiskit-community/prototype-zne

- Extends
 CircuitNoiseAmplifier
- Folding is done on the QuantumCircuit API
- Noise Amplification happens pretranspilation
- Error mitigation is less precise in general



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Work Plan

Proposed Structure:



Goal:

Make noise amplification more efficient and improve overall error mitigation

Prototype ZNE: https://github.com/qiskit -community/prototypezne

PR: https://github.com/qiskit-community/prototype-zne/pull/36

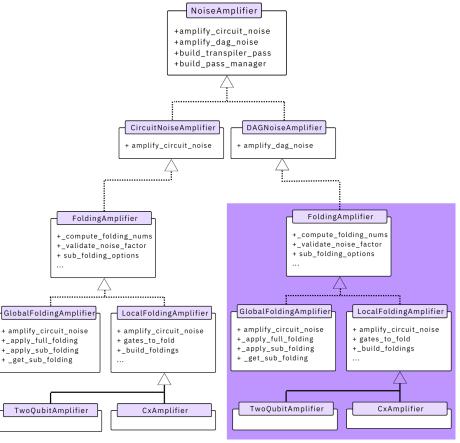
Extends
DAGNoiseAmplifier

 Folding is done on the DAGCircuit API

 Noise Amplification happens midtranspilation

 Error mitigation is more precise

More efficient computationally





Where are we right now?

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Thank you for your time.

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