



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

LABORATORY WORK SHEET

Date: 14.06.2021

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Exp No: 10 Experiment Name: Transistor as a switch

DAY TO DAY EVALUATION:

| | Preparation | Algorithm | Source Code | Program Execution | Viva | Total |
|------------|-------------|------------------------|-------------------------|----------------------------|------|-------|
| | | Performance in the Lab | Calculations and Graphs | Results and Error Analysis | | |
| Max. Marks | 4 | 4 | 4 | 4 | 4 | 20 |
| Obtained | 4 | 3 | 4 | 4 | 4 | 19 |

Signature of Lab I/C

START WRITING FROM HERE:

Aim:- To study and observe the switching characteristics of a transistor.

Apparatus:-

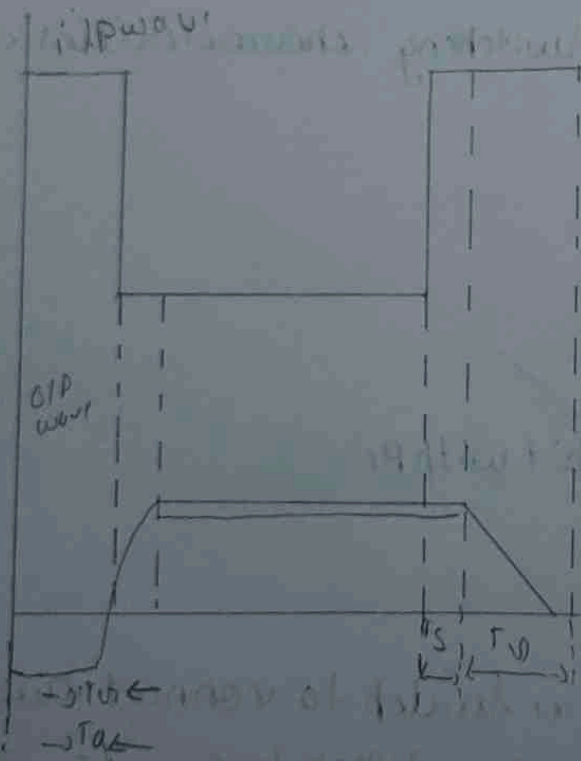
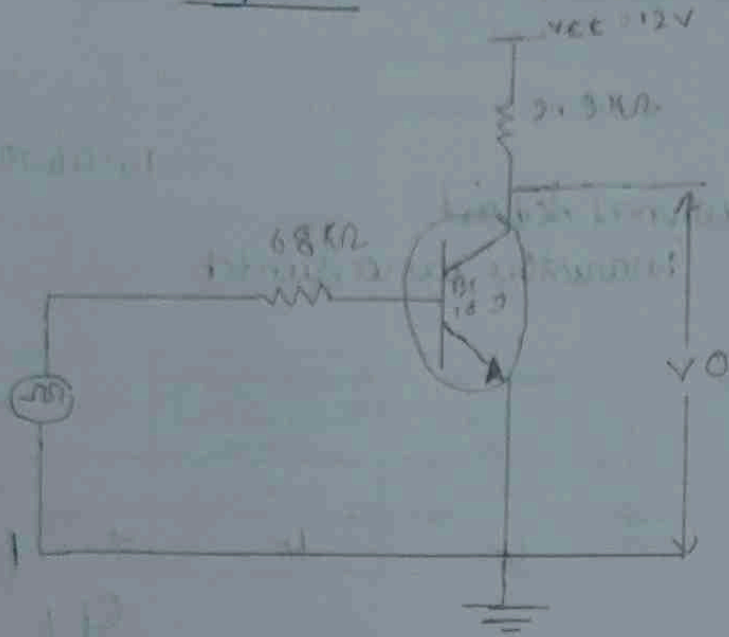
1. Resistor
2. Transistor
3. Breadboard
4. Digital analog discovery kit with P1
5. connecting wires

Theory:-

The transistor can be used as a switch to connect and disconnect load R_L from source V_{CC} . When transistor is saturated, it is like a closed switch from collector to the emitter. When a transistor is cutoff it is like an open switch $V_{CE} = V_{CC}$

Procedure:-

circuit diagram



1. connect the circuit as shown in figure
2. Switch on the power supply and observe the output of the function generator on CRO.
3. Adjust input signal amplitude such that output signal peak to peak value is less than the saturation level.
4. observe output wave form on CRO and note down the readings.
5. Plot the graph between input and output wave forms at given input frequency.

Calculations

rising edge constant = 226.9 ns

rising edge = $1.024 \mu\text{s}$

constant = $8.017 \mu\text{s}$

Falling edge constant = 849 ns

Falling edge = $2.473 \mu\text{s}$

● Result:- Study and observe the switching characteristics of a inverter.

Scale:-

X-axis:- Unit
Time

Y-axis:- Unit

Input Signal

Output Signal

