



图中 state 为状态机的当前状态，图中所有信号均由状态机状态控制。每个状态的时间长度可配。对应寄存器如下：

state 0 ; I2C_MST_ANA_CONF8 ; [23:16]
state 1 ; I2C_MST_ANA_CONF5 ; [7:0]
state 2 ; I2C_MST_ANA_CONF5 ; [15:8]
state 3 ; I2C_MST_ANA_CONF5 ; [23:16]
state 4 ; I2C_MST_ANA_CONF6 ; [7:0]
state 5 ; I2C_MST_ANA_CONF6 ; [15:8]
state 6 ; I2C_MST_ANA_CONF6 ; [23:16]

修改寄存器如下：

sar_data_inv → I2C_MST_ANA_CONF8[5]
use_entest → I2C_MST_ANA_CONF7[19]
sar_start_before_meas → I2C_MST_ANA_CONF7[20]
sar_start_delay → I2C_MST_ANA_CONF8[15:8]

SARADC_XPD = I2C_MST_ANA_CONF8[7] ? I2C_MST_ANA_CONF8[6] : sar_xpd;

(chip7.2.2 如下：

SARADC_XPD = I2C_MST_ANA_CONF8[8] ? I2C_MST_ANA_CONF8[7] : sar_xpd;)