

# Chip722 Current Test Report



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# **Current Test**

#### 1.1. Test Condition

Information	Condition
Chip Name	Chip722B
Temperature	25°C
DC	3.3V
RF Test Bin	

Test block diagram was shown in below picture.

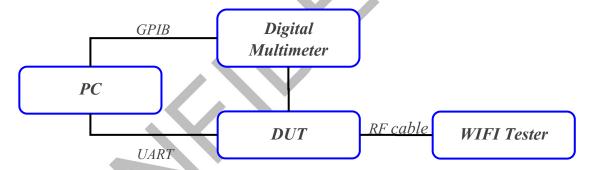


Figure 1-1. Test Block Diagram



### 1.2. Transmitter ( Tx ) Current Test Result

Rate	Power(dBm)	Max_Curr(mA)	Avg_Curr(mA)
1m	19.2	380.35	228.97
2m	19.15	379.52	228.63
5.5m	19.35	393.72	235.79
11m	19.05	373.49	225.62
6m	17.76	303.41	188.86
9m	17.74	293.67	185.06
12m	17.72	302.8	188.58
18m	17.7	296.72	185.6
24m	16.75	285.06	179.76
36m	16.74	286.57	180.44
48m	15.75	269.45	171.96
54m	13.72	241.93	158.22
HT20 MCS0	17.52	299.43	187.01
HT20 MCS1	17.5	297.84	186.16
HT20 MCS2	17.48	301.82	188.22
HT20 MCS3	16.55	282.4	178.46
HT20 MCS4	16.54	290.43	182.64
HT20 MCS5	15.57	280.01	177.46
HT20 MCS6	13.55	235.53	155.27
HT20 MCS7	12.56	239.95	157.43
HT40 MCS0	17.17	275.05	179.67
HT40 MCS1	17.14	293.46	188.88

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HT40 MCS2	17.12	295.13	189.69
HT40 MCS3	16.27	264.56	174.17
HT40 MCS4	16.24	277.35	180.83
HT40 MCS5	15.33	252.73	168.52
HT40 MCS6	13.38	230.94	157.3
HT40 MCS7	12.4	218.47	151.41

## 1.3. Receiver (Rx) Current Test Result

Rate	Max_Curr(mA)	Avg_Curr(mA)
1m	78.61	78.3
2m	78.82	78.41
5.5m	78.82	78.39
11m	78.77	78.26
6m	78.71	78.23
9m	78.77	78.29
12m	78.66	78.28
18m	78.77	78.26
24m	78.61	78.17
36m	78.71	78.33
48m	78.71	78.29
54m	78.82	78.26
HT20 MCS0	78.66	78.28
HT20 MCS1	78.66	78.26
HT20 MCS2	78.71	78.37



HT20 MCS3	78.66	78.28
HT20 MCS4	78.66	78.3
HT20 MCS5	78.66	78.34
HT20 MCS6	78.55	78.32
HT20 MCS7	78.71	78.3
HT40 MCS0	85.05	83.75
HT40 MCS1	85.16	83.77
HT40 MCS2	85.16	83.77
HT40 MCS3	85.16	83.77
HT40 MCS4	85.11	83.73
HT40 MCS5	85	83.71
HT40 MCS6	84.95	83.66
HT40 MCS7	85.05	83.77