

# Chip722 Current Test Report



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## **Current Test**

#### 1.1. Test Condition

Information	Condition
Chip Name	Chip722A
Temperature	25℃
DC	3.3V
RF Test Bin	

Test block diagram was shown in below picture.

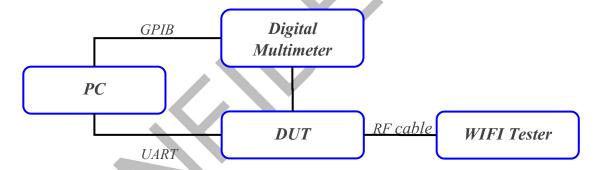


Figure 1-1. Test Block Diagram



### 1.2. Transmitter ( Tx ) Current Test Result

Rate	Power(dBm)	Max_Curr(mA)	Avg_Curr(mA)
1m	19.45	324.84	201.86
2m	19.39	337.82	206.76
5.5m	19.5	347.67	211.77
11m	19.36	340.36	208.06
6m	17.86	271.28	173.52
9m	17.87	266.76	171.21
12m	17.87	266.86	171.28
18m	17.87	256.64	167.62
24m	16.87	245.74	162.26
36m	16.86	257.13	166.42
48m	15.85	242.51	159.05
54m	13.8	225.34	150.5
HT20 MCS0	17.78	263.82	169.79
HT20 MCS1	17.77	280.38	178.05
HT20 MCS2	17.77	265.85	170.81
HT20 MCS3	16.76	261.37	168.51
HT20 MCS4	16.76	264.62	170.13
HT20 MCS5	15.75	240.82	158.27
HT20 MCS6	13.71	221.26	148.46
HT20 MCS7	12.72	198.63	138.59
HT40 MCS0	17.7	263.25	174.45
HT40 MCS1	17.65	263.33	174.28

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HT40 MCS2	17.63	263.22	174.19
HT40 MCS3	16.64	242.67	163.92
HT40 MCS4	16.64	243.33	164.41
HT40 MCS5	15.63	224.21	155.31
HT40 MCS6	13.59	204.95	145.73
HT40 MCS7	12.58	203.01	143.95

## 1.3. Receiver (Rx) Current Test Result

Rate	Max_Curr(mA)	Avg_Curr(mA)
1m	80.06	79.25
2m	80.27	79.12
5.5m	81.29	79.33
11m	79.84	79.07
6m	79.79	79.19
9m	80	79.25
12m	79.84	79.21
18m	81.83	79.21
24m	79.95	79.22
36m	79.9	79.01
48m	80.22	79.31
54m	80.06	79.23
HT20 MCS0	82.26	79.36
HT20 MCS1	81.13	79.4
HT20 MCS2	79.9	79.22



HT20 MCS3	80.27	79.15
HT20 MCS4	81.94	79.28
HT20 MCS5	79.95	79.23
HT20 MCS6	85.05	79.39
HT20 MCS7	80.65	79.24
HT40 MCS0	87.63	85.32
HT40 MCS1	87.37	85.46
HT40 MCS2	87.26	85.5
HT40 MCS3	87.15	85.34
HT40 MCS4	86.67	85.37
HT40 MCS5	87.2	85.43
HT40 MCS6	87.63	85.3
HT40 MCS7	86.88	85.42