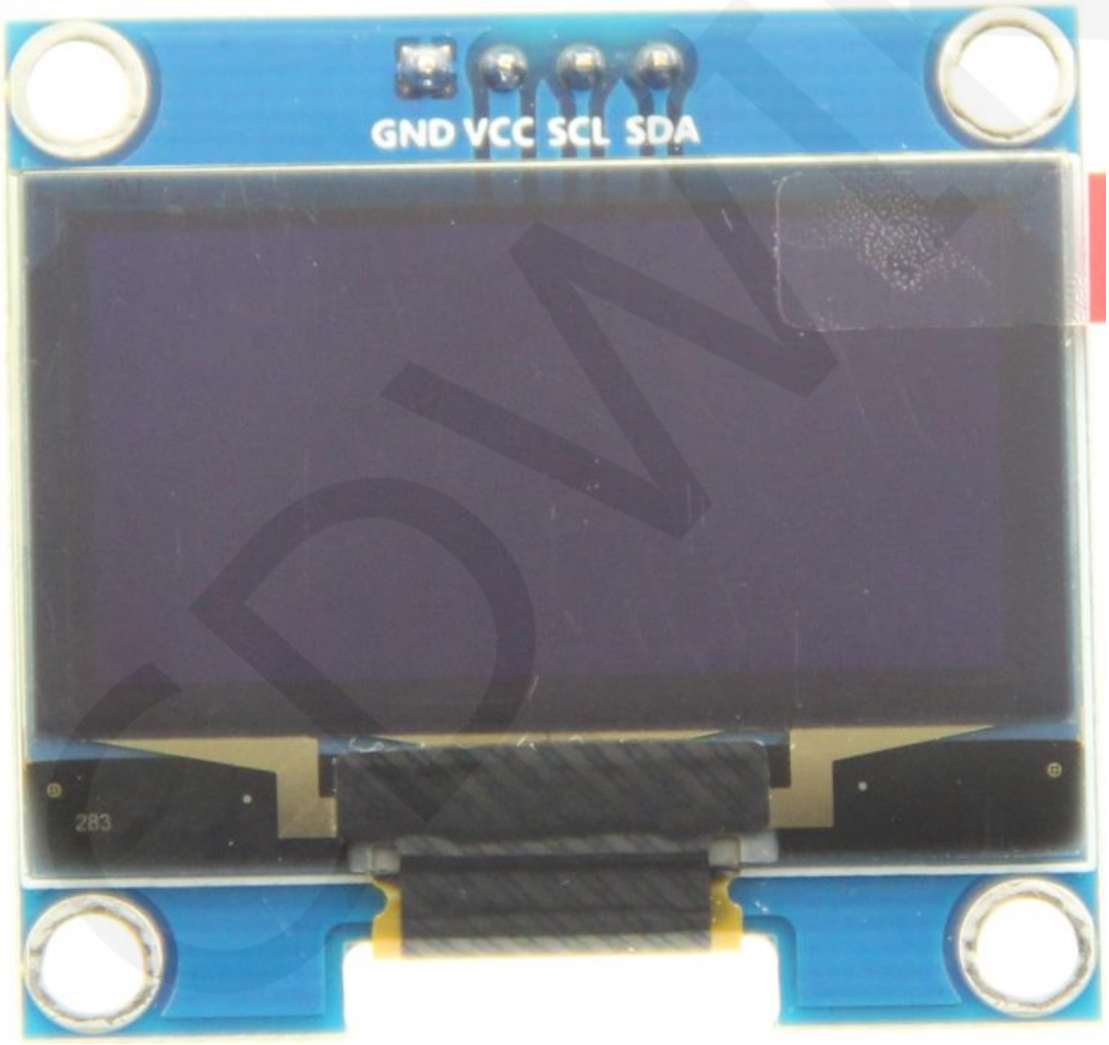
Test platform introduction:

Development board: STC89/STC12 development board

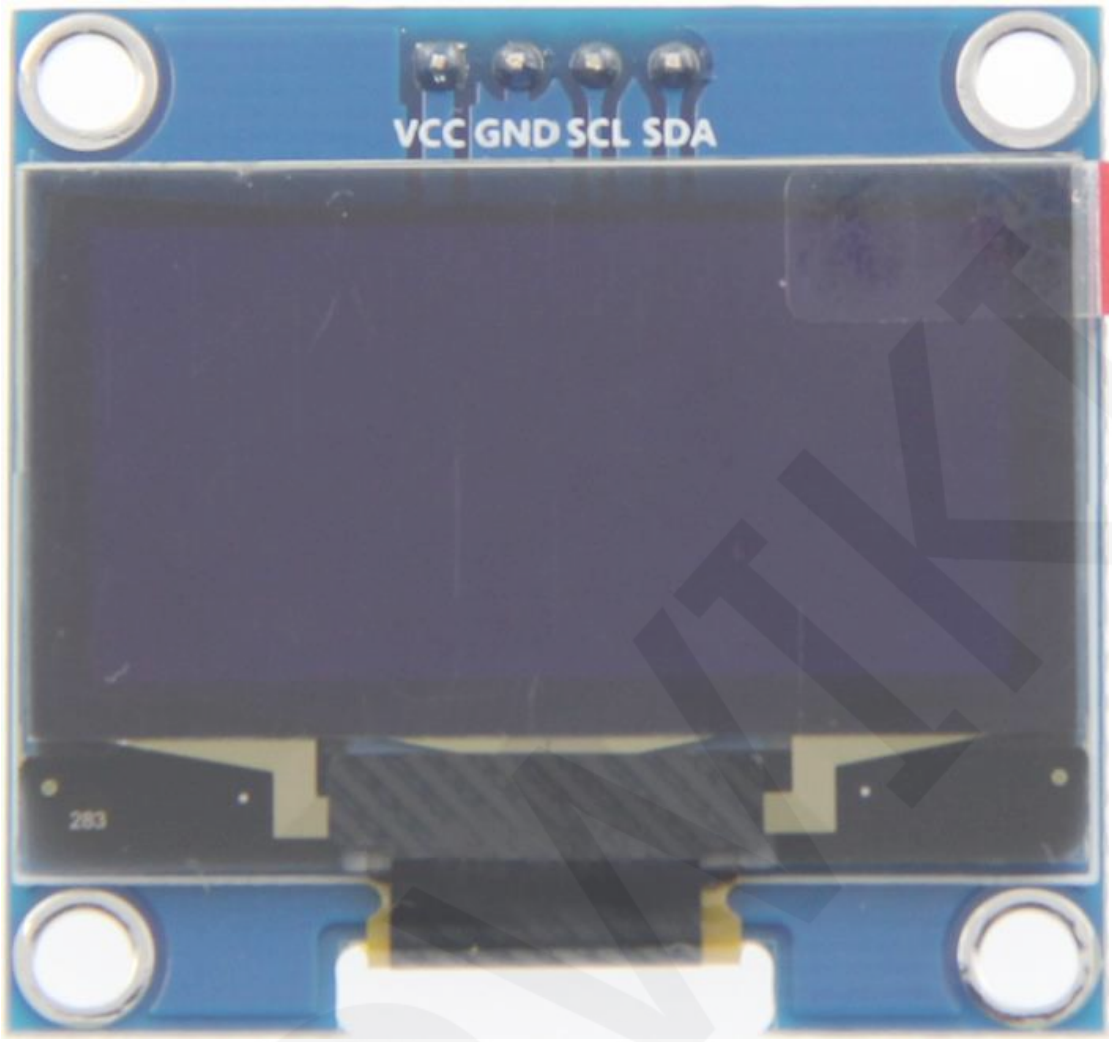
MCU : STC89C52RC /STC12C5A60S2

Crystal frequency : 12MHZ

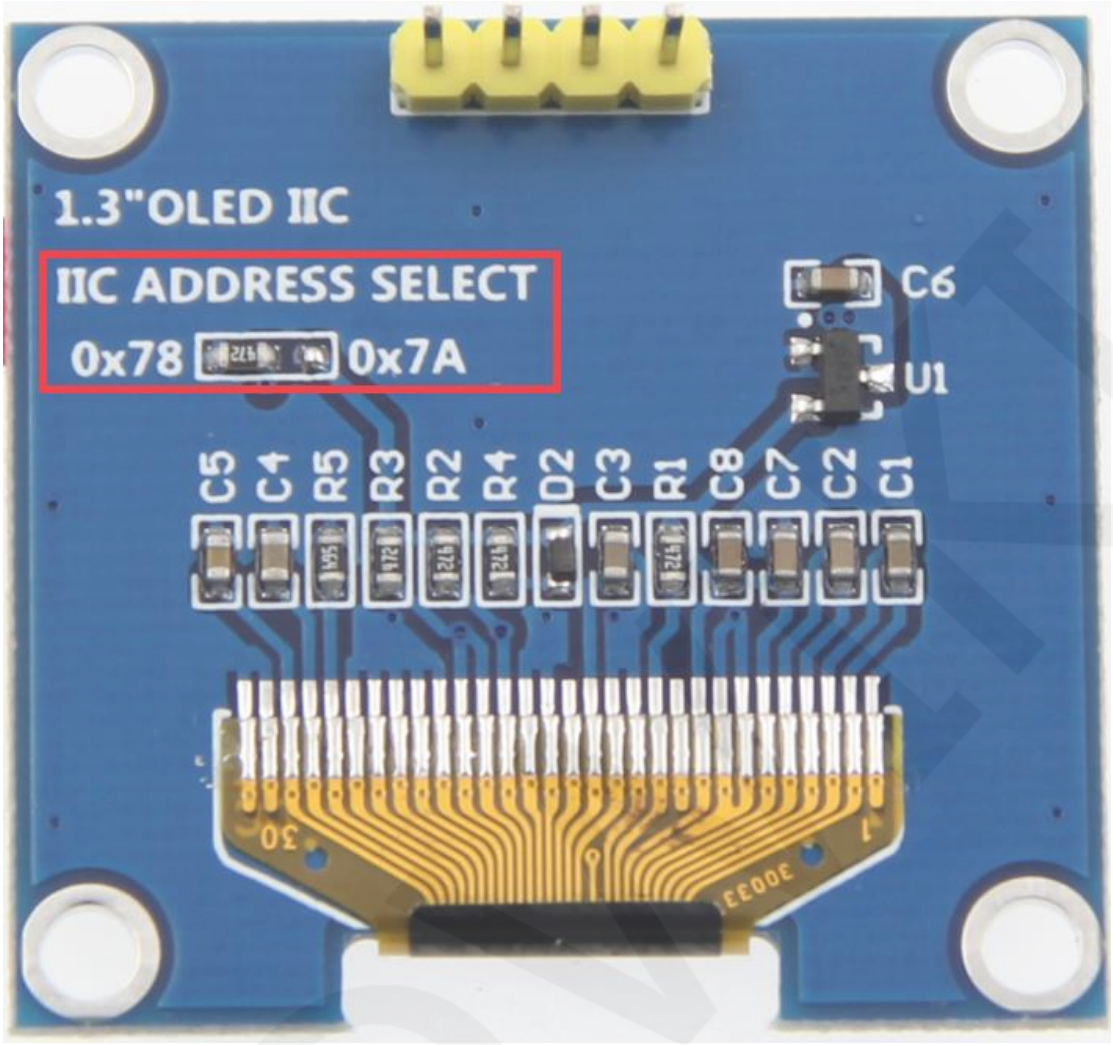
Wiring instructions:



Picture 1. Module pin silk screen (1 pin is GND)



Picture 2. Module pin silk screen (1 pin is VCC)



Picture 3. Rear view of the module

NOTE:

1. This module supports IIC slave device address switching (shown in red box in Picture 3), as follows:
   A. Solder the 0x78 side resistance, disconnect the 0x7A side, then select the 0x78 slave address (default);
   B. Solder the 0x7A side resistance, disconnect the 0x78 side, then select the 0x7A slave address;
2. The hardware switches the IIC from the set address, and the software also needs to be modified accordingly. For the specific modification method, see the following IIC slave device address modification instructions.

STC89C52RC and STC12C5A60S2 microcontroller test program wiring instructions			
Number	Module Pin	Corresponding to STC89/STC12 development board wiring pin	Remarks
1	GND	GND	OLED power ground
2	VCC	5V/3.3V	OLED power positive (3.3V~5V)
3	SCL	P17	OLED IIC bus clock signal
4	SDA	P15	OLED IIC bus data signal

Demo function description:

1. This set of test program procedures is applicable to the STC89C52RC and STC12C5A60S2 platforms;
2. This set of test programs uses the analog IIC bus to transfer data;
3. Please select the corresponding test program and development board to wire according to the above wiring instructions;
4. STC89C52RC microcontroller RAM is only 25KB, can only burn less than 25KB program, so the test content is simple;
5. This set of test procedures contains the following test items:
   A. the main interface displays the test;
   B. simple black and white color brush test;
   C. English display test;
   D. symbol and digital display test
   E. Chinese display test;
   F. BMP monochrome picture display test;

IIC slave device address modification instructions:

i. Open the iic.h file in the program and find the following:

```
//定义IIC从设备地址
#define IIC_SLAVE_ADDR 0x78
```

ii. Modify the IIC_SLAVE_ADDR macro definition (default is 0x78):

For example, change to 0x7A, then the IIC slave address is 0x7A;