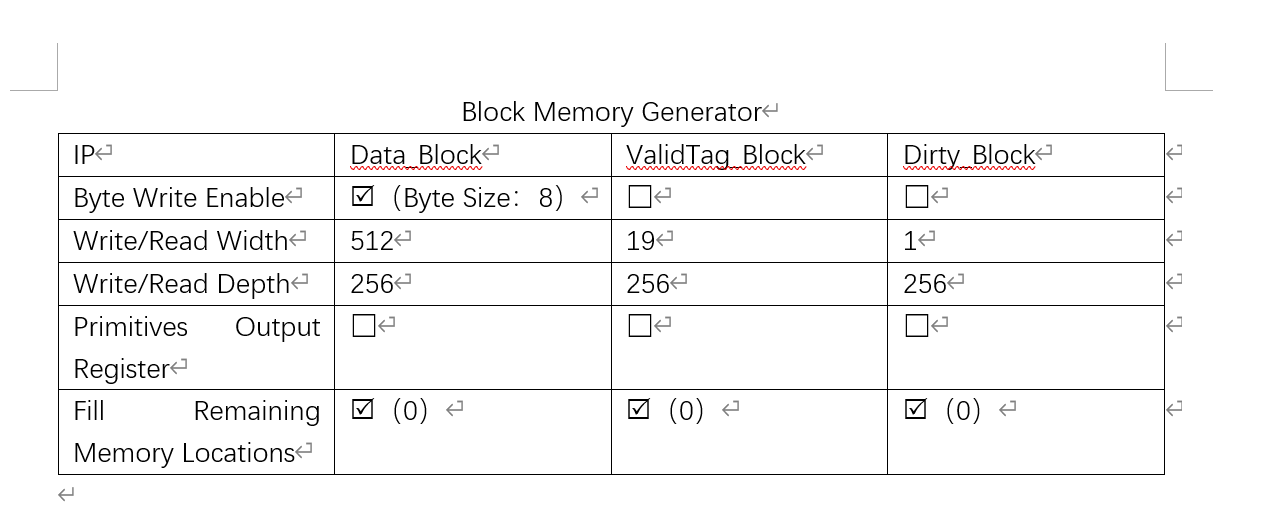
1. Block ram



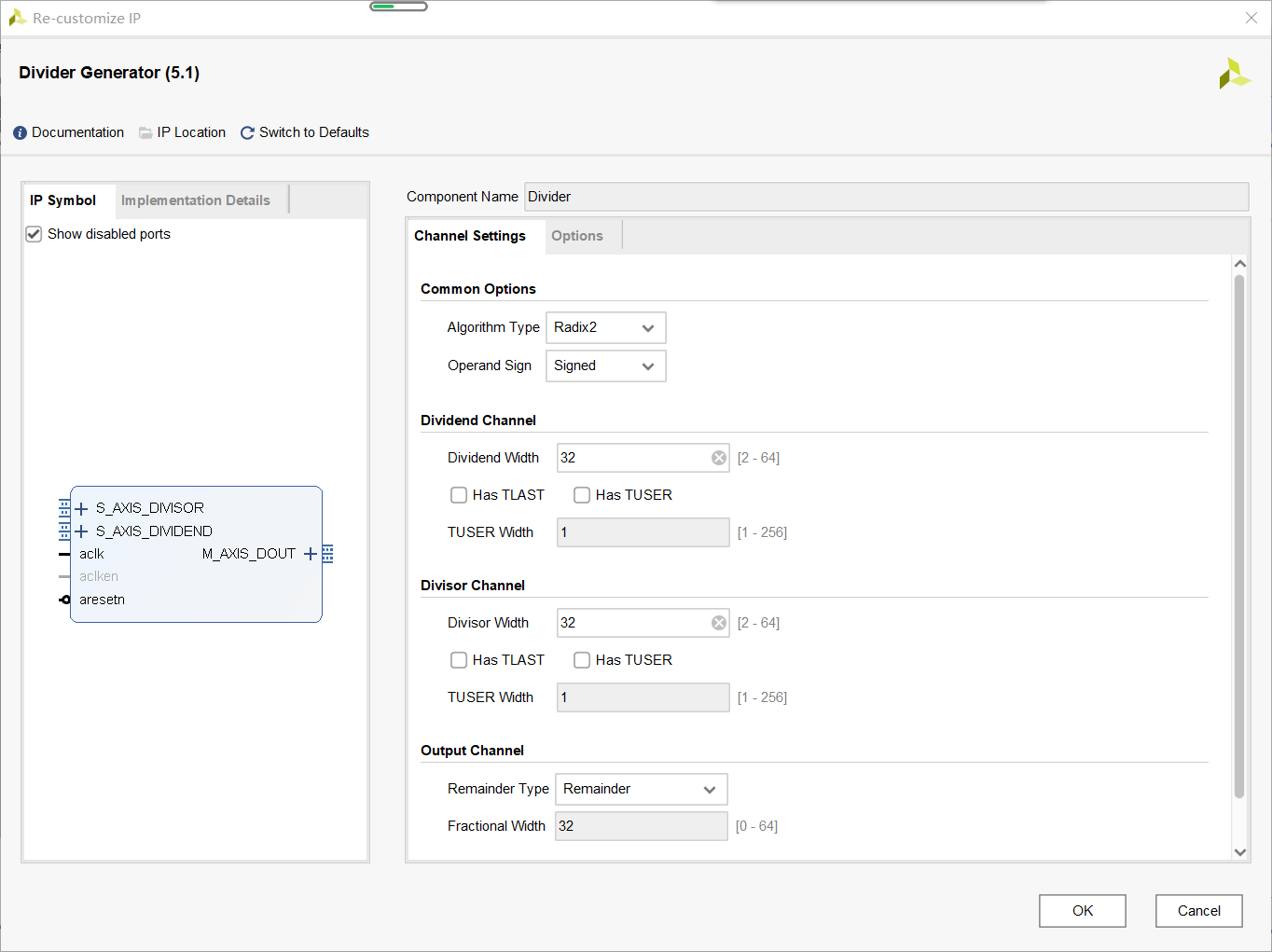
|  |  |  |  |
| --- | --- | --- | --- |
| IP | Data\_Block | ValidTag\_Block | Dirty\_Block |
| Byte Write Enable | √（byte size:8） | □ | □ |
| Write /Read width | 512 | 21 | 1 |
| Write/read depth | 64 | 64 | 64 |
| Primitives output register | □ | □ | □ |
| Fill remaining memory locations | √(0) | √(0) | √(0) |

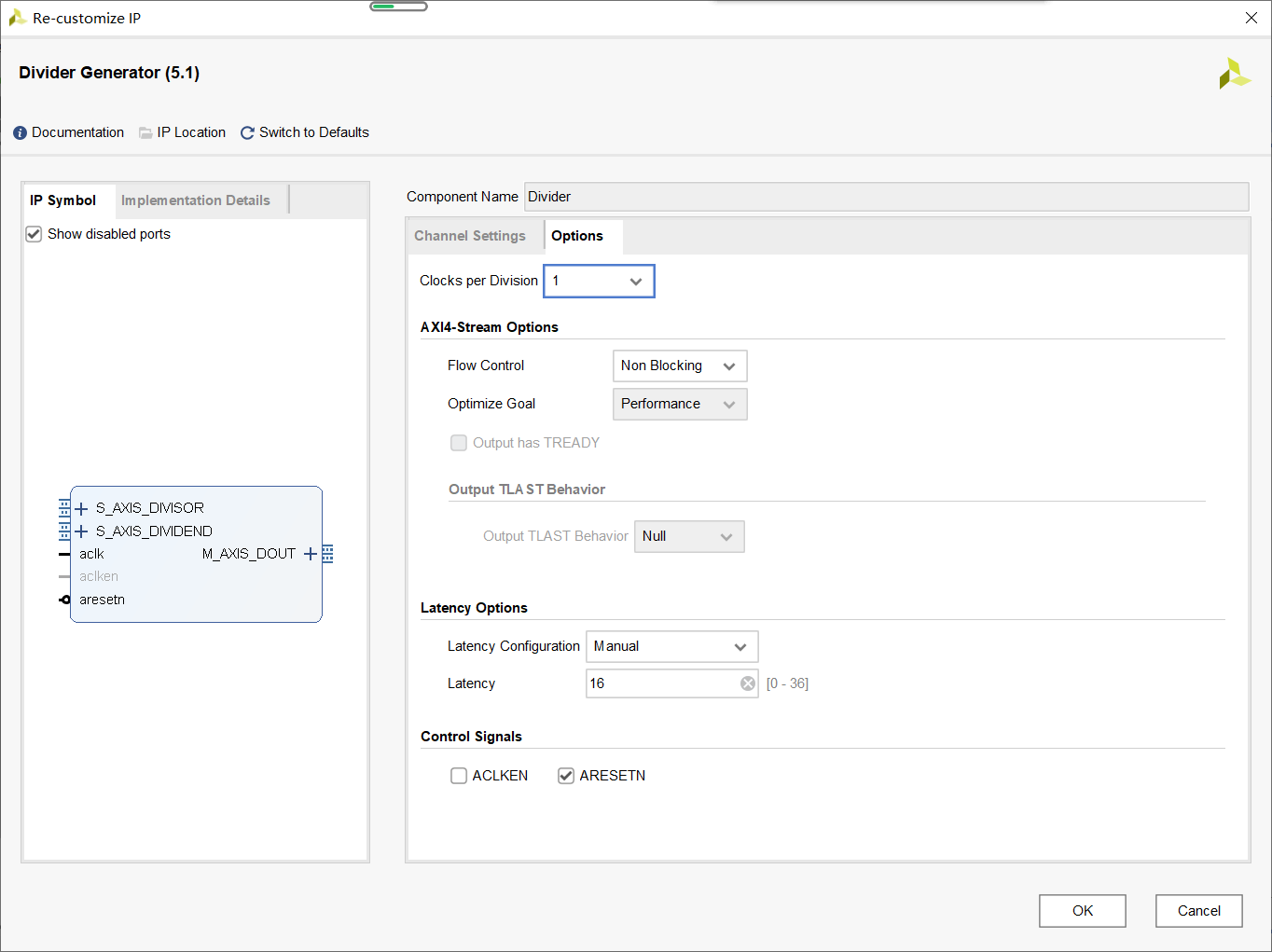
Distributed RAM

Data\_Distributed

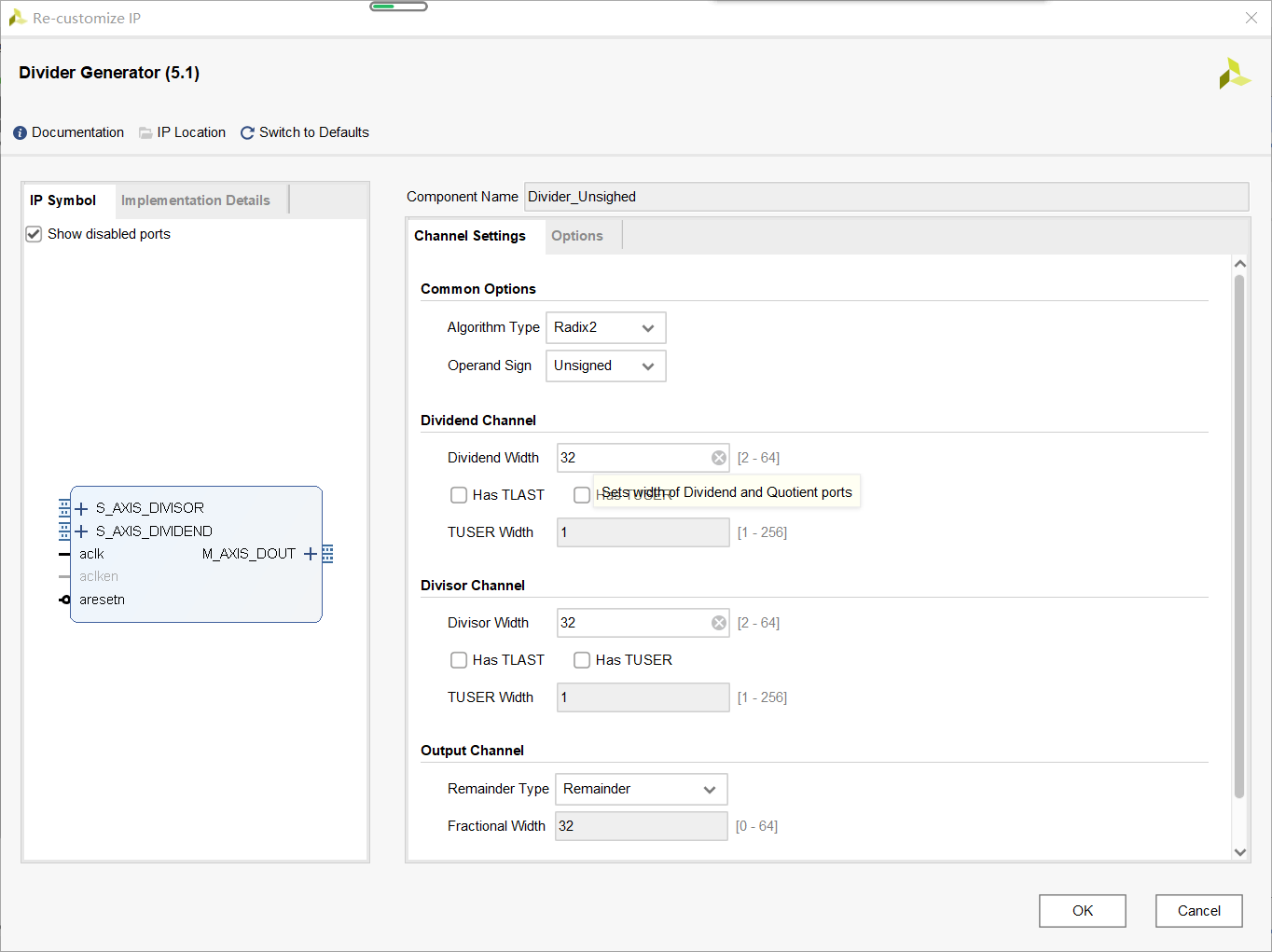
Depth:64 Data Width:32 Simple Dual Port RAM

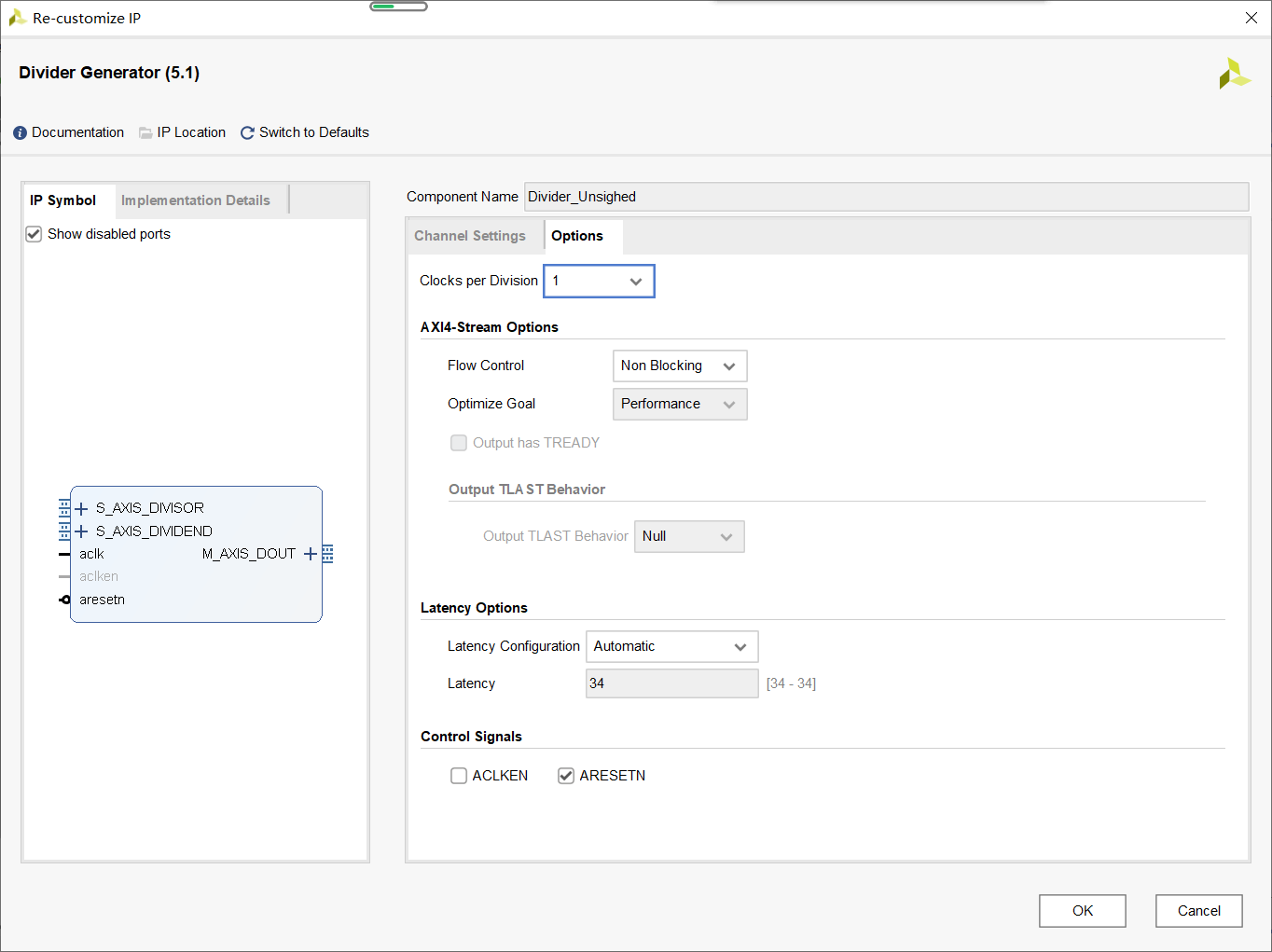
1. divider



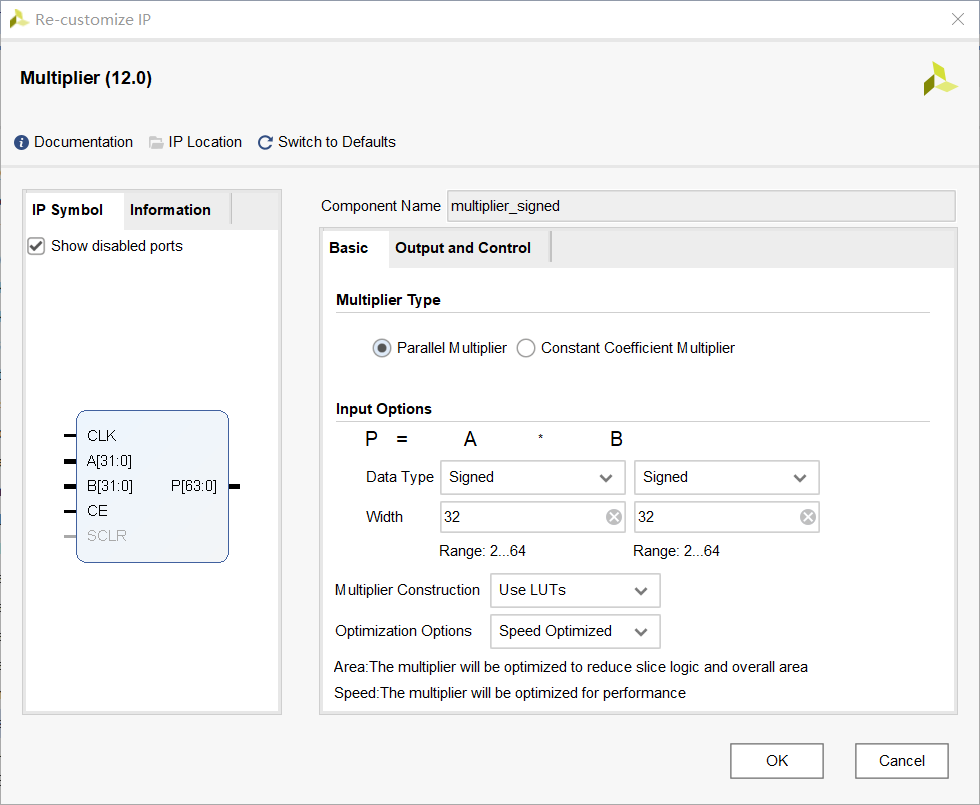


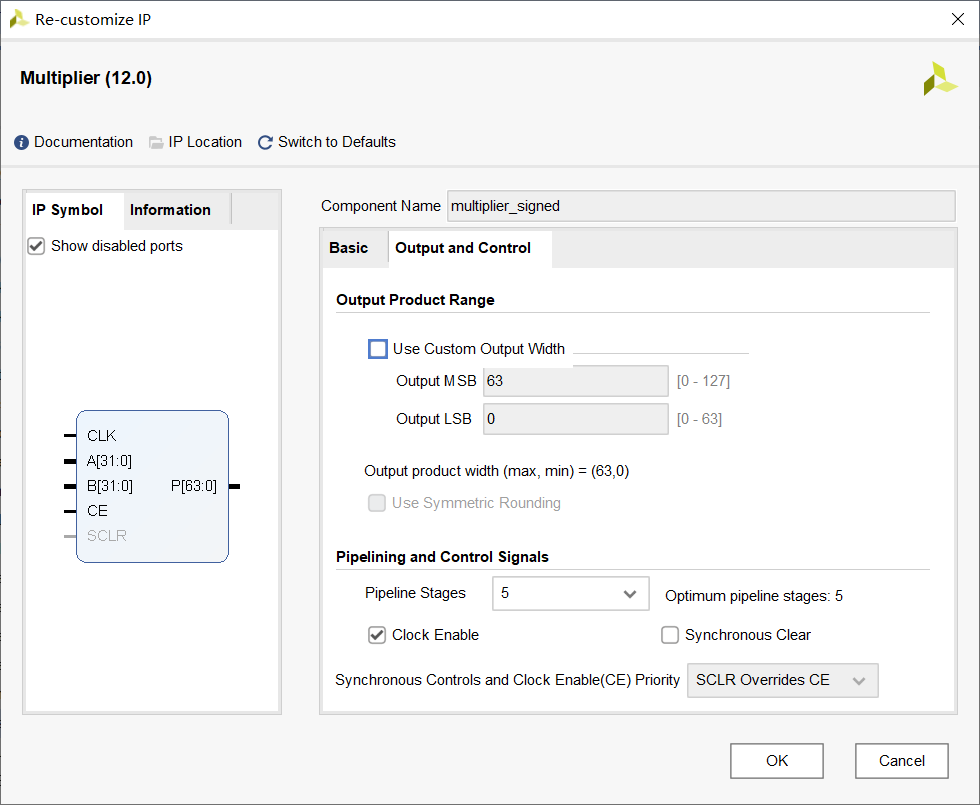
1. divider unsigned





1. multiplier signed





1. multiplier unsigned

