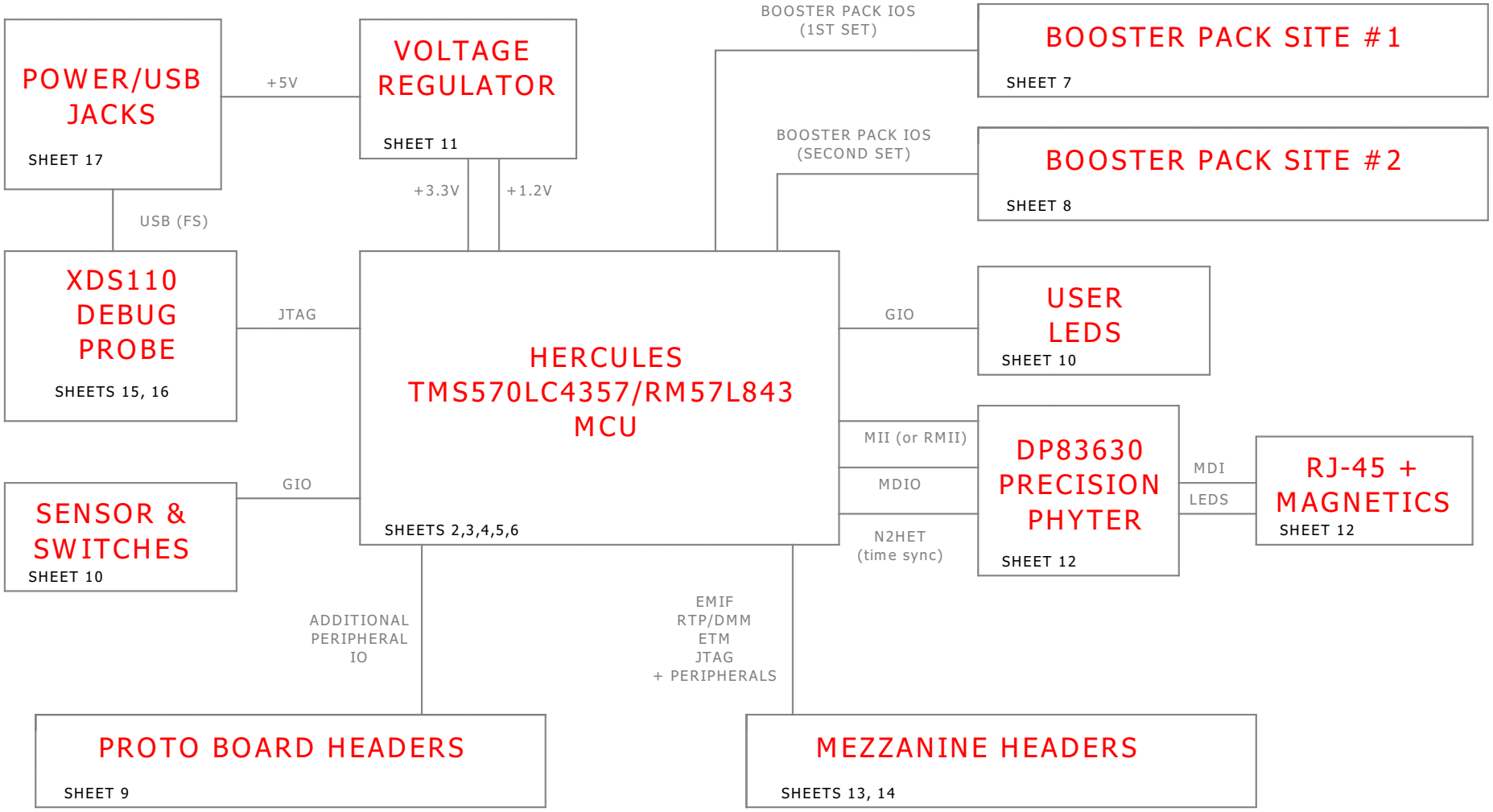
LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L
337 ZWT LAUNCHPAD XL2



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Document Number: LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L	REV: A1
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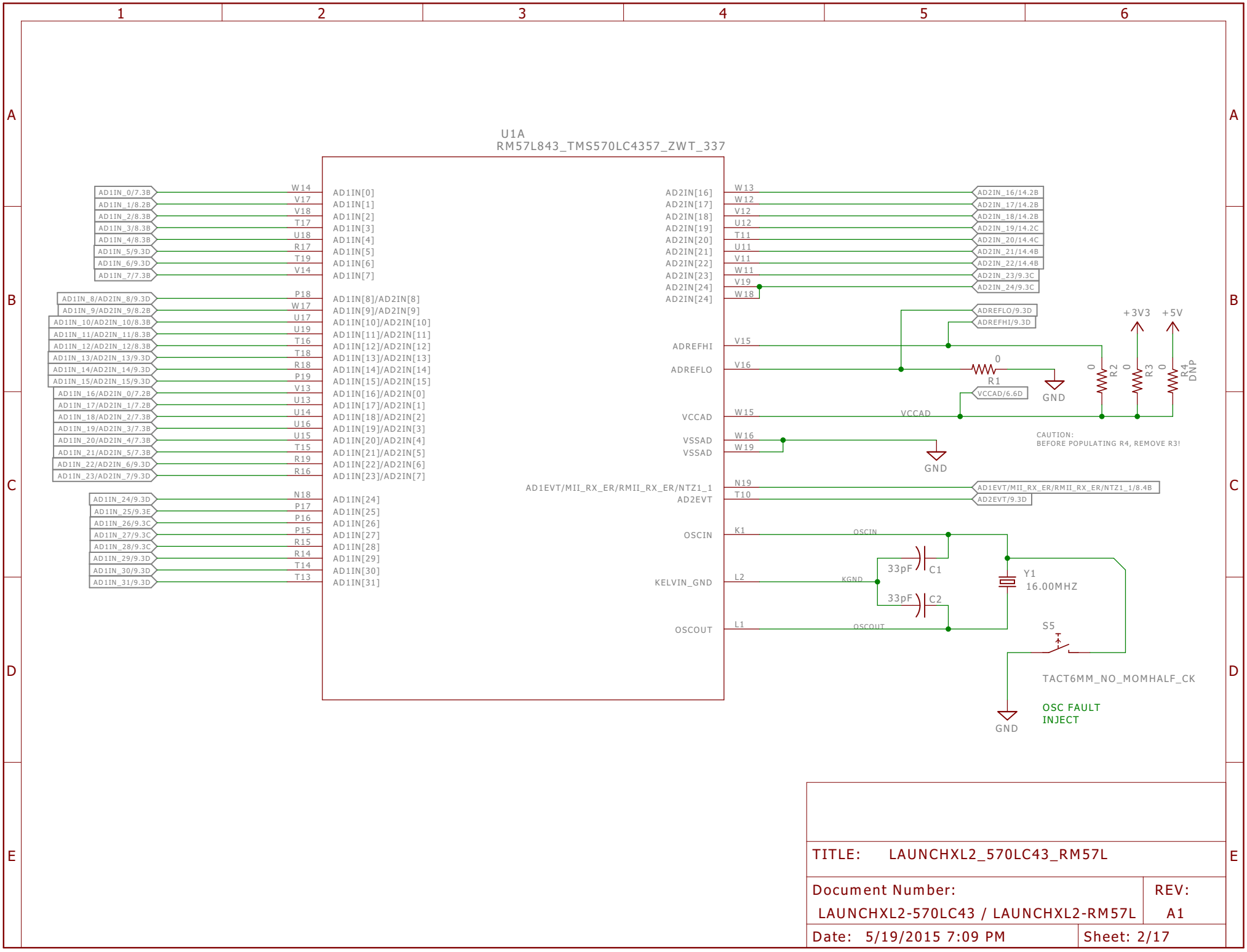
U1A
RM57L843_TMS570LC4357_ZWT_337
AD1IN_0/7.3B
AD1IN_1/8.2B
AD1IN_2/8.3B
AD1IN_3/8.3B
AD1IN_4/8.3B
AD1IN_5/9.3D
AD1IN_6/9.3D
AD1IN_7/7.3B
AD1IN_8/AD2IN_8/9.3D
AD1IN_9/AD2IN_9/8.2B
AD1IN_10/AD2IN_10/8.3B
AD1IN_11/AD2IN_11/8.3B
AD1IN_12/AD2IN_12/8.3B
AD1IN_13/AD2IN_13/9.3D
AD1IN_14/AD2IN_14/9.3D
AD1IN_15/AD2IN_15/9.3D
AD1IN_16/AD2IN_0/7.2B
AD1IN_17/AD2IN_1/7.2B
AD1IN_18/AD2IN_2/7.3B
AD1IN_19/AD2IN_3/7.3B
AD1IN_20/AD2IN_4/7.3B
AD1IN_21/AD2IN_5/7.3B
AD1IN_22/AD2IN_6/9.3D
AD1IN_23/AD2IN_7/9.3D
AD1IN_24/9.3D
AD1IN_25/9.3E
AD1IN_26/9.3C
AD1IN_27/9.3C
AD1IN_28/9.3C
AD1IN_29/9.3D
AD1IN_30/9.3D
AD1IN_31/9.3D
W14 V17 V18 T17 U18 R17 T19 V14
P18 W17 U17 U19 T16 T18 R18 P19 V13 U13 U14 U16 U15 T15 R19 R16
N18 P17 P16 P15 R15 R14 T14 T13
AD1IN[0] AD1IN[1] AD1IN[2] AD1IN[3] AD1IN[4] AD1IN[5] AD1IN[6] AD1IN[7]
AD1IN[8]/AD2IN[8] AD1IN[9]/AD2IN[9] AD1IN[10]/AD2IN[10] AD1IN[11]/AD2IN[11] AD1IN[12]/AD2IN[12] AD1IN[13]/AD2IN[13] AD1IN[14]/AD2IN[14] AD1IN[15]/AD2IN[15] AD1IN[16]/AD2IN[0] AD1IN[17]/AD2IN[1] AD1IN[18]/AD2IN[2] AD1IN[19]/AD2IN[3] AD1IN[20]/AD2IN[4] AD1IN[21]/AD2IN[5] AD1IN[22]/AD2IN[6] AD1IN[23]/AD2IN[7]
AD1IN[24] AD1IN[25] AD1IN[26] AD1IN[27] AD1IN[28] AD1IN[29] AD1IN[30] AD1IN[31]
AD2IN[16] AD2IN[17] AD2IN[18] AD2IN[19] AD2IN[20] AD2IN[21] AD2IN[22] AD2IN[23] AD2IN[24] AD2IN[24]
W13 W12 V12 U12 T11 U11 V11 W11 V19 W18
AD2IN_16/14.2B AD2IN_17/14.2B AD2IN_18/14.2B AD2IN_19/14.2C AD2IN_20/14.4C AD2IN_21/14.4B AD2IN_22/14.4B AD2IN_23/9.3C AD2IN_24/9.3C
ADREFHI ADREFLO
V15 V16
ADREFLO/9.3D ADREFHI/9.3D
0 R1
VCCAD/6.6D
GND
+3V3 +5V
0 R2 0 R3 0 R4 DNP
VCCAD
W15
VSSAD VSSAD
W16 W19
GND
CAUTION:
BEFORE POPULATING R4, REMOVE R3!
AD1EVT/MII_RX_ER/RMII_RX_ER/NTZ1_1 AD2EVT
N19 T10
AD1EVT/MII_RX_ER/RMII_RX_ER/NTZ1_1/8.4B AD2EVT/9.3D
OSCIN KELVIN_GND OSCOUT
K1 L2 L1
OSCIN KGND OSCOUT
33pF C1 33pF C2
Y1 16.00MHZ
S5
TACT6MM_NO_MOMHALF_CK
GND
OSC FAULT INJECT
TITLE: LAUNCHXL2_570LC43_RM57L
Document Number: LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L
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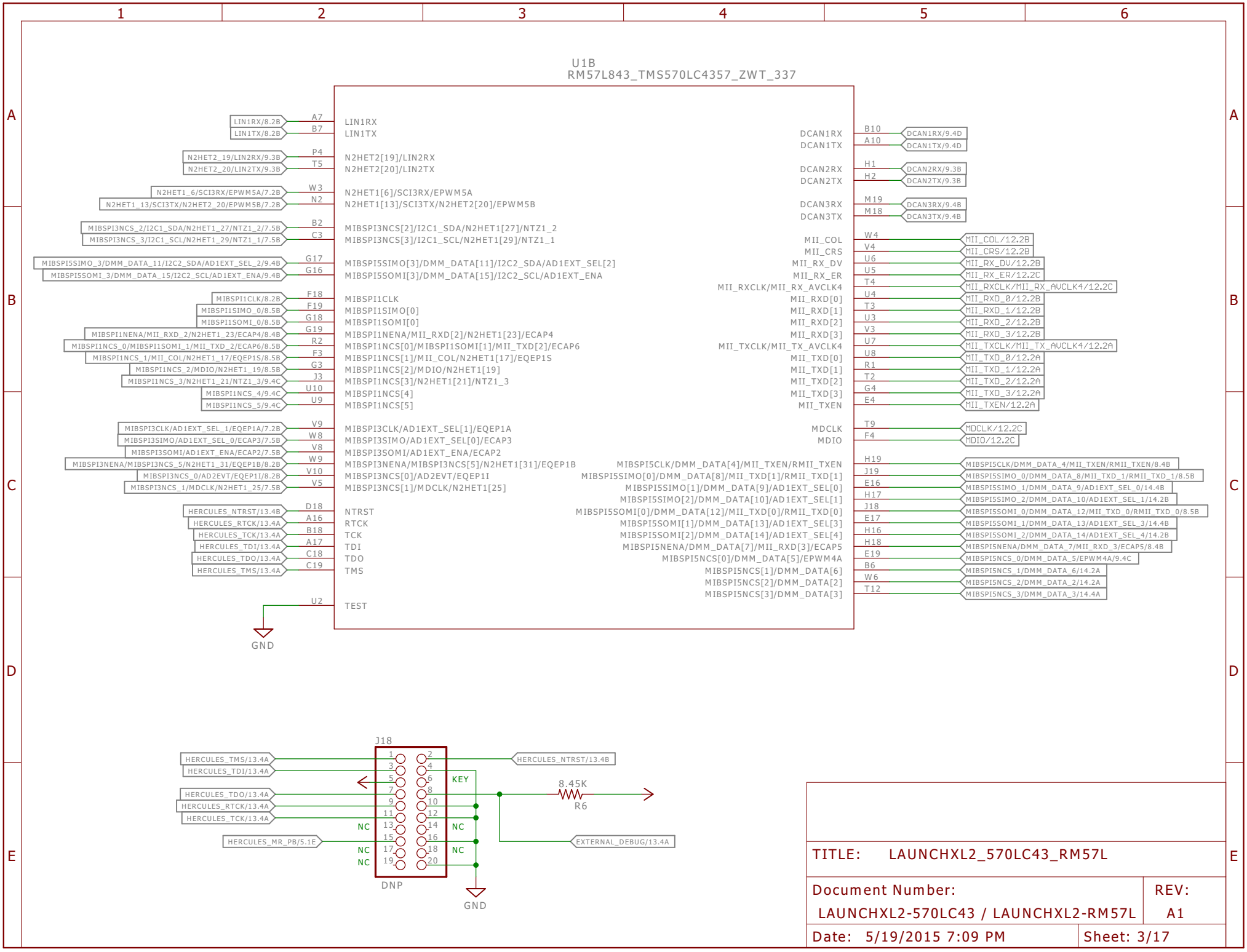
1
2
3
4
5
6
A
B
C
D
E
U1B
RM57L843_TMS570LC4357_ZWT_337
LIN1RX/8.2B
LIN1TX/8.2B
A7
B7
LIN1RX
LIN1TX
N2HET2_19/LIN2RX/9.3B
N2HET2_20/LIN2TX/9.3B
P4
T5
N2HET2[19]/LIN2RX
N2HET2[20]/LIN2TX
N2HET1_6/SCI3RX/EPWM5A/7.2B
N2HET1_13/SCI3TX/N2HET2_20/EPWM5B/7.2B
W3
N2
N2HET1[6]/SCI3RX/EPWM5A
N2HET1[13]/SCI3TX/N2HET2[20]/EPWM5B
MIBSPI3NCS_2/I2C1_SDA/N2HET1_27/NTZ1_2/7.5B
MIBSPI3NCS_3/I2C1_SCL/N2HET1_29/NTZ1_1/7.5B
B2
C3
MIBSPI3NCS[2]/I2C1_SDA/N2HET1[27]/NTZ1_2
MIBSPI3NCS[3]/I2C1_SCL/N2HET1[29]/NTZ1_1
MIBSPI5SIMO_3/DMM_DATA_11/I2C2_SDA/AD1EXT_SEL_2/9.4B
MIBSPI5SOMI_3/DMM_DATA_15/I2C2_SCL/AD1EXT_ENA/9.4B
G17
G16
MIBSPI5SIMO[3]/DMM_DATA[11]/I2C2_SDA/AD1EXT_SEL[2]
MIBSPI5SOMI[3]/DMM_DATA[15]/I2C2_SCL/AD1EXT_ENA
MIBSPI1CLK/8.2B
MIBSPI1SIMO_0/8.5B
MIBSPI1SOMI_0/8.5B
MIBSPI1NENA/MII_RXD_2/N2HET1_23/ECAP4/8.4B
MIBSPI1NCS_0/MIBSPI1SOMI_1/MII_TXD_2/ECAP6/8.5B
MIBSPI1NCS_1/MII_COL/N2HET1_17/EQEP1S/8.5B
MIBSPI1NCS_2/MDIO/N2HET1_19/8.5B
MIBSPI1NCS_3/N2HET1_21/NTZ1_3/9.4C
MIBSPI1NCS_4/9.4C
MIBSPI1NCS_5/9.4C
F18
F19
G18
G19
R2
F3
G3
J3
U10
U9
MIBSPI1CLK
MIBSPI1SIMO[0]
MIBSPI1SOMI[0]
MIBSPI1NENA/MII_RXD[2]/N2HET1[23]/ECAP4
MIBSPI1NCS[0]/MIBSPI1SOMI[1]/MII_TXD[2]/ECAP6
MIBSPI1NCS[1]/MII_COL/N2HET1[17]/EQEP1S
MIBSPI1NCS[2]/MDIO/N2HET1[19]
MIBSPI1NCS[3]/N2HET1[21]/NTZ1_3
MIBSPI1NCS[4]
MIBSPI1NCS[5]
MIBSPI3CLK/AD1EXT_SEL_1/EQEP1A/7.2B
MIBSPI3SIMO/AD1EXT_SEL_0/ECAP3/7.5B
MIBSPI3SOMI/AD1EXT_ENA/ECAP2/7.5B
MIBSPI3NENA/MIBSPI3NCS_5/N2HET1_31/EQEP1B/8.2B
MIBSPI3NCS_0/AD2EVT/EQEP1I/8.2B
MIBSPI3NCS_1/MDCLK/N2HET1_25/7.5B
V9
W8
V8
W9
V10
V5
MIBSPI3CLK/AD1EXT_SEL[1]/EQEP1A
MIBSPI3SIMO/AD1EXT_SEL[0]/ECAP3
MIBSPI3SOMI/AD1EXT_ENA/ECAP2
MIBSPI3NENA/MIBSPI3NCS[5]/N2HET1[31]/EQEP1B
MIBSPI3NCS[0]/AD2EVT/EQEP1I
MIBSPI3NCS[1]/MDCLK/N2HET1[25]
HERCULES_NTRST/13.4B
HERCULES_RTCK/13.4A
HERCULES_TCK/13.4A
HERCULES_TDI/13.4A
HERCULES_TDO/13.4A
HERCULES_TMS/13.4A
D18
A16
B18
A17
C18
C19
NTRST
RTCK
TCK
TDI
TDO
TMS
U2
TEST
GND
DCAN1RX
DCAN1TX
B10
A10
DCAN1RX/9.4D
DCAN1TX/9.4D
DCAN2RX
DCAN2TX
H1
H2
DCAN2RX/9.3B
DCAN2TX/9.3B
DCAN3RX
DCAN3TX
M19
M18
DCAN3RX/9.4B
DCAN3TX/9.4B
MII_COL
MII_CRS
MII_RX_DV
MII_RX_ER
MII_RXCLK/MII_RX_AVCLK4
MII_RXD[0]
MII_RXD[1]
MII_RXD[2]
MII_RXD[3]
MII_TXCLK/MII_TX_AVCLK4
MII_TXD[0]
MII_TXD[1]
MII_TXD[2]
MII_TXD[3]
MII_TXEN
W4
V4
U6
U5
T4
U4
T3
U3
V3
U7
U8
R1
T2
G4
E4
MII_COL/12.2B
MII_CRS/12.2B
MII_RX_DV/12.2B
MII_RX_ER/12.2C
MII_RXCLK/MII_RX_AVCLK4/12.2C
MII_RXD_0/12.2B
MII_RXD_1/12.2B
MII_RXD_2/12.2B
MII_RXD_3/12.2B
MII_TXCLK/MII_TX_AVCLK4/12.2A
MII_TXD_0/12.2A
MII_TXD_1/12.2A
MII_TXD_2/12.2A
MII_TXD_3/12.2A
MII_TXEN/12.2A
MDCLK
MDIO
T9
F4
MDCLK/12.2C
MDIO/12.2C
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MIBSPI5SIMO[0]/DMM_DATA[8]/MII_TXD[1]/RMII_TXD[1]
MIBSPI5SIMO[1]/DMM_DATA[9]/AD1EXT_SEL[0]
MIBSPI5SIMO[2]/DMM_DATA[10]/AD1EXT_SEL[1]
MIBSPI5SOMI[0]/DMM_DATA[12]/MII_TXD[0]/RMII_TXD[0]
MIBSPI5SOMI[1]/DMM_DATA[13]/AD1EXT_SEL[3]
MIBSPI5SOMI[2]/DMM_DATA[14]/AD1EXT_SEL[4]
MIBSPI5NENA/DMM_DATA[7]/MII_RXD[3]/ECAP5
MIBSPI5NCS[0]/DMM_DATA[5]/EPWM4A
MIBSPI5NCS[1]/DMM_DATA[6]
MIBSPI5NCS[2]/DMM_DATA[2]
MIBSPI5NCS[3]/DMM_DATA[3]
H19
J19
E16
H17
J18
E17
H16
H18
E19
B6
W6
T12
MIBSPI5CLK/DMM_DATA_4/MII_TXEN/RMII_TXEN/8.4B
MIBSPI5SIMO_0/DMM_DATA_8/MII_TXD_1/RMII_TXD_1/8.5B
MIBSPI5SIMO_1/DMM_DATA_9/AD1EXT_SEL_0/14.4B
MIBSPI5SIMO_2/DMM_DATA_10/AD1EXT_SEL_1/14.2B
MIBSPI5SOMI_0/DMM_DATA_12/MII_TXD_0/RMII_TXD_0/8.5B
MIBSPI5SOMI_1/DMM_DATA_13/AD1EXT_SEL_3/14.4B
MIBSPI5SOMI_2/DMM_DATA_14/AD1EXT_SEL_4/14.2B
MIBSPI5NENA/DMM_DATA_7/MII_RXD_3/ECAP5/8.4B
MIBSPI5NCS_0/DMM_DATA_5/EPWM4A/9.4C
MIBSPI5NCS_1/DMM_DATA_6/14.2A
MIBSPI5NCS_2/DMM_DATA_2/14.2A
MIBSPI5NCS_3/DMM_DATA_3/14.4A
J18
HERCULES_TMS/13.4A
HERCULES_TDI/13.4A
HERCULES_TDO/13.4A
HERCULES_RTCK/13.4A
HERCULES_TCK/13.4A
HERCULES_MR_PB/5.1E
HERCULES_NTRST/13.4B
EXTERNAL_DEBUG/13.4A
KEY
NC
8.45K
R6
DNP
GND
TITLE: LAUNCHXL2_570LC43_RM57L
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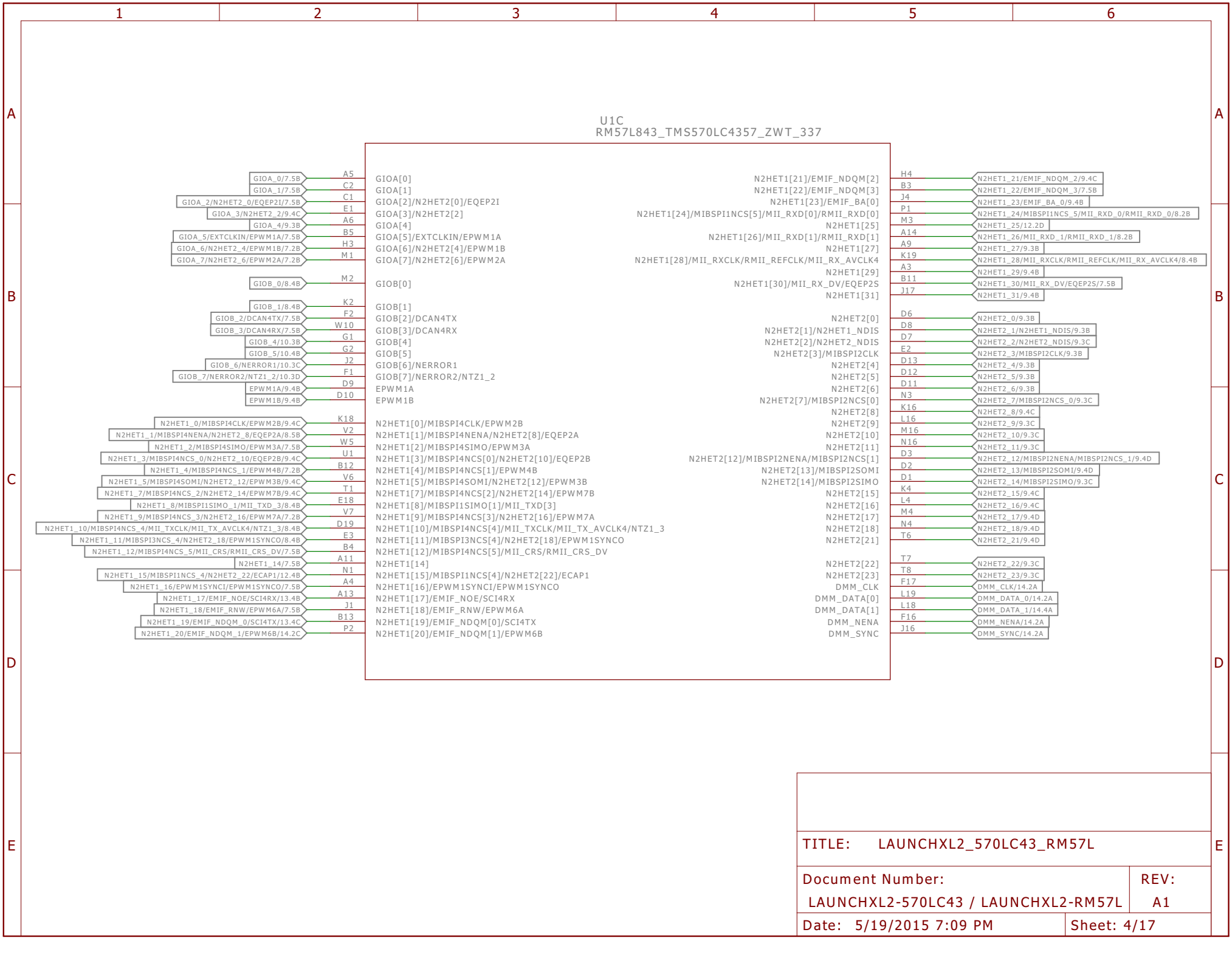
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RM57L843_TMS570LC4357_ZWT_337
GIOA_0/7.5B
GIOA_1/7.5B
GIOA_2/N2HET2_0/EQEP2I/7.5B
GIOA_3/N2HET2_2/9.4C
GIOA_4/9.3B
GIOA_5/EXTCLKIN/EPWM1A/7.5B
GIOA_6/N2HET2_4/EPWM1B/7.2B
GIOA_7/N2HET2_6/EPWM2A/7.2B
GIOB_0/8.4B
GIOB_1/8.4B
GIOB_2/DCAN4TX/7.5B
GIOB_3/DCAN4RX/7.5B
GIOB_4/10.3B
GIOB_5/10.4B
GIOB_6/NERROR1/10.3C
GIOB_7/NERROR2/NTZ1_2/10.3D
EPWM1A/9.4B
EPWM1B/9.4B
N2HET1_0/MIBSPI4CLK/EPWM2B/9.4C
N2HET1_1/MIBSPI4NENA/N2HET2_8/EQEP2A/8.5B
N2HET1_2/MIBSPI4SIMO/EPWM3A/7.5B
N2HET1_3/MIBSPI4NCS_0/N2HET2_10/EQEP2B/9.4C
N2HET1_4/MIBSPI4NCS_1/EPWM4B/7.2B
N2HET1_5/MIBSPI4SOMI/N2HET2_12/EPWM3B/9.4C
N2HET1_7/MIBSPI4NCS_2/N2HET2_14/EPWM7B/9.4C
N2HET1_8/MIBSPI1SIMO_1/MII_TXD_3/8.4B
N2HET1_9/MIBSPI4NCS_3/N2HET2_16/EPWM7A/7.2B
N2HET1_10/MIBSPI4NCS_4/MII_TXCLK/MII_TX_AVCLK4/NTZ1_3/8.4B
N2HET1_11/MIBSPI3NCS_4/N2HET2_18/EPWM1SYNCO/8.4B
N2HET1_12/MIBSPI4NCS_5/MII_CRS/RMII_CRS_DV/7.5B
N2HET1_14/7.5B
N2HET1_15/MIBSPI1NCS_4/N2HET2_22/ECAP1/12.4B
N2HET1_16/EPWM1SYNCI/EPWM1SYNCO/7.5B
N2HET1_17/EMIF_NOE/SCI4RX/13.4B
N2HET1_18/EMIF_RNW/EPWM6A/7.5B
N2HET1_19/EMIF_NDQM_0/SCI4TX/13.4C
N2HET1_20/EMIF_NDQM_1/EPWM6B/14.2C
A5 C2 C1 E1 A6 B5 H3 M1 M2 K2 F2 W10 G1 G2 J2 F1 D9 D10 K18 V2 W5 U1 B12 V6 T1 E18 V7 D19 E3 B4 A11 N1 A4 A13 J1 B13 P2
GIOA[0]
GIOA[1]
GIOA[2]/N2HET2[0]/EQEP2I
GIOA[3]/N2HET2[2]
GIOA[4]
GIOA[5]/EXTCLKIN/EPWM1A
GIOA[6]/N2HET2[4]/EPWM1B
GIOA[7]/N2HET2[6]/EPWM2A
GIOB[0]
GIOB[1]
GIOB[2]/DCAN4TX
GIOB[3]/DCAN4RX
GIOB[4]
GIOB[5]
GIOB[6]/NERROR1
GIOB[7]/NERROR2/NTZ1_2
EPWM1A
EPWM1B
N2HET1[0]/MIBSPI4CLK/EPWM2B
N2HET1[1]/MIBSPI4NENA/N2HET2[8]/EQEP2A
N2HET1[2]/MIBSPI4SIMO/EPWM3A
N2HET1[3]/MIBSPI4NCS[0]/N2HET2[10]/EQEP2B
N2HET1[4]/MIBSPI4NCS[1]/EPWM4B
N2HET1[5]/MIBSPI4SOMI/N2HET2[12]/EPWM3B
N2HET1[7]/MIBSPI4NCS[2]/N2HET2[14]/EPWM7B
N2HET1[8]/MIBSPI1SIMO[1]/MII_TXD[3]
N2HET1[9]/MIBSPI4NCS[3]/N2HET2[16]/EPWM7A
N2HET1[10]/MIBSPI4NCS[4]/MII_TXCLK/MII_TX_AVCLK4/NTZ1_3
N2HET1[11]/MIBSPI3NCS[4]/N2HET2[18]/EPWM1SYNCO
N2HET1[12]/MIBSPI4NCS[5]/MII_CRS/RMII_CRS_DV
N2HET1[14]
N2HET1[15]/MIBSPI1NCS[4]/N2HET2[22]/ECAP1
N2HET1[16]/EPWM1SYNCI/EPWM1SYNCO
N2HET1[17]/EMIF_NOE/SCI4RX
N2HET1[18]/EMIF_RNW/EPWM6A
N2HET1[19]/EMIF_NDQM[0]/SCI4TX
N2HET1[20]/EMIF_NDQM[1]/EPWM6B
N2HET1[21]/EMIF_NDQM[2]
N2HET1[22]/EMIF_NDQM[3]
N2HET1[23]/EMIF_BA[0]
N2HET1[24]/MIBSPI1NCS[5]/MII_RXD[0]/RMII_RXD[0]
N2HET1[25]
N2HET1[26]/MII_RXD[1]/RMII_RXD[1]
N2HET1[27]
N2HET1[28]/MII_RXCLK/RMII_REFCLK/MII_RX_AVCLK4
N2HET1[29]
N2HET1[30]/MII_RX_DV/EQEP2S
N2HET1[31]
N2HET2[0]
N2HET2[1]/N2HET1_NDIS
N2HET2[2]/N2HET2_NDIS
N2HET2[3]/MIBSPI2CLK
N2HET2[4]
N2HET2[5]
N2HET2[6]
N2HET2[7]/MIBSPI2NCS[0]
N2HET2[8]
N2HET2[9]
N2HET2[10]
N2HET2[11]
N2HET2[12]/MIBSPI2NENA/MIBSPI2NCS[1]
N2HET2[13]/MIBSPI2SOMI
N2HET2[14]/MIBSPI2SIMO
N2HET2[15]
N2HET2[16]
N2HET2[17]
N2HET2[18]
N2HET2[21]
N2HET2[22]
N2HET2[23]
DMM_CLK
DMM_DATA[0]
DMM_DATA[1]
DMM_NENA
DMM_SYNC
H4 B3 J4 P1 M3 A14 A9 K19 A3 B11 J17 D6 D8 D7 E2 D13 D12 D11 N3 K16 L16 M16 N16 D3 D2 D1 K4 L4 M4 N4 T6 T7 T8 F17 L19 L18 F16 J16
N2HET1_21/EMIF_NDQM_2/9.4C
N2HET1_22/EMIF_NDQM_3/7.5B
N2HET1_23/EMIF_BA_0/9.4B
N2HET1_24/MIBSPI1NCS_5/MII_RXD_0/RMII_RXD_0/8.2B
N2HET1_25/12.2D
N2HET1_26/MII_RXD_1/RMII_RXD_1/8.2B
N2HET1_27/9.3B
N2HET1_28/MII_RXCLK/RMII_REFCLK/MII_RX_AVCLK4/8.4B
N2HET1_29/9.4B
N2HET1_30/MII_RX_DV/EQEP2S/7.5B
N2HET1_31/9.4B
N2HET2_0/9.3B
N2HET2_1/N2HET1_NDIS/9.3B
N2HET2_2/N2HET2_NDIS/9.3C
N2HET2_3/MIBSPI2CLK/9.3B
N2HET2_4/9.3B
N2HET2_5/9.3B
N2HET2_6/9.3B
N2HET2_7/MIBSPI2NCS_0/9.3C
N2HET2_8/9.4C
N2HET2_9/9.3C
N2HET2_10/9.3C
N2HET2_11/9.3C
N2HET2_12/MIBSPI2NENA/MIBSPI2NCS_1/9.4D
N2HET2_13/MIBSPI2SOMI/9.4D
N2HET2_14/MIBSPI2SIMO/9.3C
N2HET2_15/9.4C
N2HET2_16/9.4C
N2HET2_17/9.4D
N2HET2_18/9.4D
N2HET2_21/9.4D
N2HET2_22/9.3C
N2HET2_23/9.3C
DMM_CLK/14.2A
DMM_DATA_0/14.2A
DMM_DATA_1/14.4A
DMM_NENA/14.2A
DMM_SYNC/14.2A
TITLE: LAUNCHXL2_570LC43_RM57L
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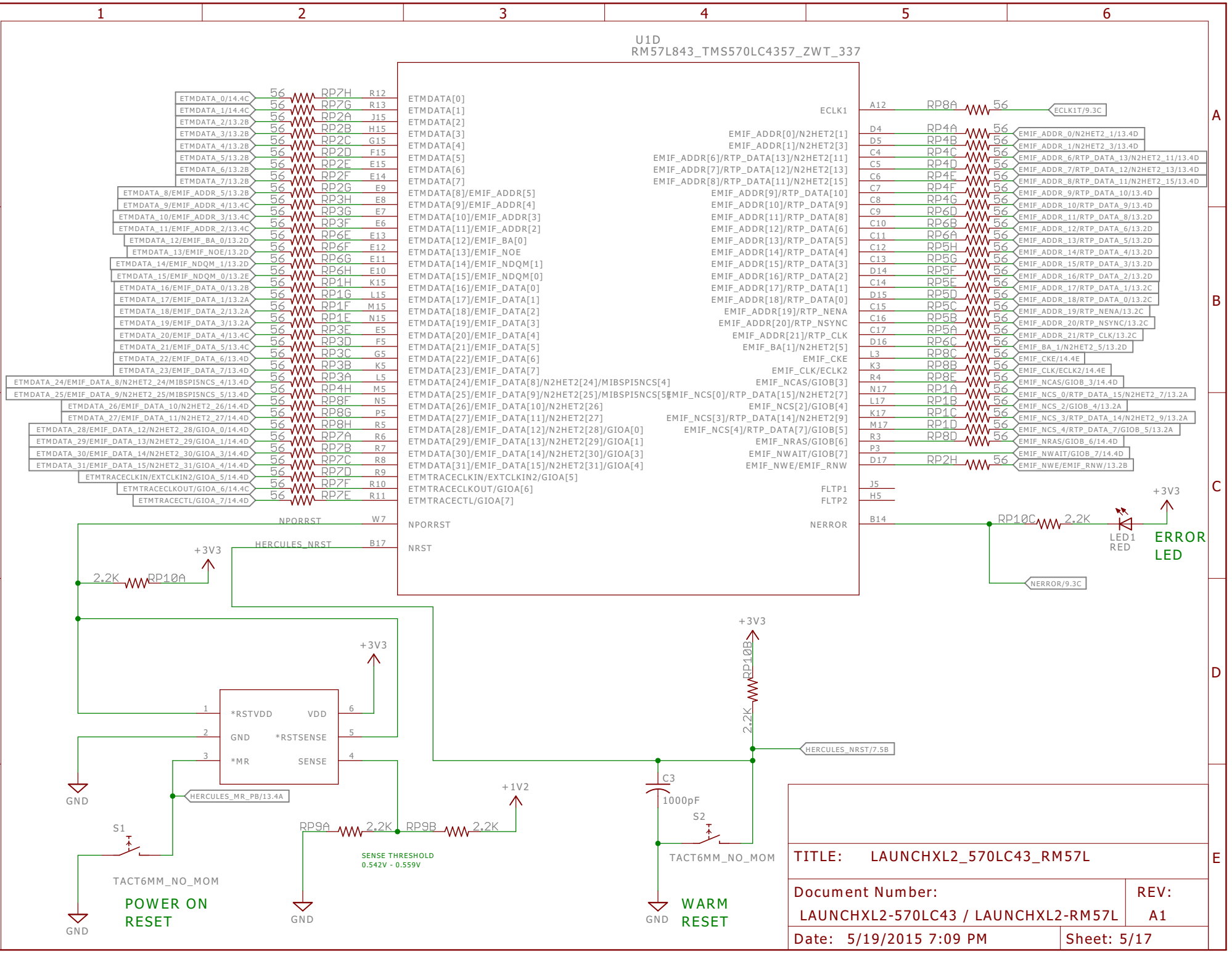
U1D
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ETMDATA_0/14.4C
ETMDATA_1/14.4C
ETMDATA_2/13.2B
ETMDATA_3/13.2B
ETMDATA_4/13.2B
ETMDATA_5/13.2B
ETMDATA_6/13.2B
ETMDATA_7/13.2B
ETMDATA_8/EMIF_ADDR_5/13.2B
ETMDATA_9/EMIF_ADDR_4/13.4C
ETMDATA_10/EMIF_ADDR_3/13.4C
ETMDATA_11/EMIF_ADDR_2/13.4C
ETMDATA_12/EMIF_BA_0/13.2D
ETMDATA_13/EMIF_NOE/13.2D
ETMDATA_14/EMIF_NDQM_1/13.2D
ETMDATA_15/EMIF_NDQM_0/13.2E
ETMDATA_16/EMIF_DATA_0/13.2B
ETMDATA_17/EMIF_DATA_1/13.2A
ETMDATA_18/EMIF_DATA_2/13.2A
ETMDATA_19/EMIF_DATA_3/13.2A
ETMDATA_20/EMIF_DATA_4/13.4C
ETMDATA_21/EMIF_DATA_5/13.4C
ETMDATA_22/EMIF_DATA_6/13.4D
ETMDATA_23/EMIF_DATA_7/13.4D
ETMDATA_24/EMIF_DATA_8/N2HET2_24/MIBSPI5NCS_4/13.4D
ETMDATA_25/EMIF_DATA_9/N2HET2_25/MIBSPI5NCS_5/13.4D
ETMDATA_26/EMIF_DATA_10/N2HET2_26/14.4D
ETMDATA_27/EMIF_DATA_11/N2HET2_27/14.4D
ETMDATA_28/EMIF_DATA_12/N2HET2_28/GIOA_0/14.4D
ETMDATA_29/EMIF_DATA_13/N2HET2_29/GIOA_1/14.4D
ETMDATA_30/EMIF_DATA_14/N2HET2_30/GIOA_3/14.4D
ETMDATA_31/EMIF_DATA_15/N2HET2_31/GIOA_4/14.4D
ETMTRACECLKIN/EXTCLKIN2/GIOA_5/14.4D
ETMTRACECLKOUT/GIOA_6/14.4C
ETMTRACECTL/GIOA_7/14.4D
56 RP7H R12
56 RP7G R13
56 RP2A J15
56 RP2B H15
56 RP2C G15
56 RP2D F15
56 RP2E E15
56 RP2F E14
56 RP2G E9
56 RP3H E8
56 RP3G E7
56 RP3F E6
56 RP6E E13
56 RP6F E12
56 RP6G E11
56 RP6H E10
56 RP1H K15
56 RP1G L15
56 RP1F M15
56 RP1E N15
56 RP3E E5
56 RP3D F5
56 RP3C G5
56 RP3B K5
56 RP3A L5
56 RP4H M5
56 RP8F N5
56 RP8G P5
56 RP8H R5
56 RP7A R6
56 RP7B R7
56 RP7C R8
56 RP7D R9
56 RP7F R10
56 RP7E R11
ETMDATA[0]
ETMDATA[1]
ETMDATA[2]
ETMDATA[3]
ETMDATA[4]
ETMDATA[5]
ETMDATA[6]
ETMDATA[7]
ETMDATA[8]/EMIF_ADDR[5]
ETMDATA[9]/EMIF_ADDR[4]
ETMDATA[10]/EMIF_ADDR[3]
ETMDATA[11]/EMIF_ADDR[2]
ETMDATA[12]/EMIF_BA[0]
ETMDATA[13]/EMIF_NOE
ETMDATA[14]/EMIF_NDQM[1]
ETMDATA[15]/EMIF_NDQM[0]
ETMDATA[16]/EMIF_DATA[0]
ETMDATA[17]/EMIF_DATA[1]
ETMDATA[18]/EMIF_DATA[2]
ETMDATA[19]/EMIF_DATA[3]
ETMDATA[20]/EMIF_DATA[4]
ETMDATA[21]/EMIF_DATA[5]
ETMDATA[22]/EMIF_DATA[6]
ETMDATA[23]/EMIF_DATA[7]
ETMDATA[24]/EMIF_DATA[8]/N2HET2[24]/MIBSPI5NCS[4]
ETMDATA[25]/EMIF_DATA[9]/N2HET2[25]/MIBSPI5NCS[5]
ETMDATA[26]/EMIF_DATA[10]/N2HET2[26]
ETMDATA[27]/EMIF_DATA[11]/N2HET2[27]
ETMDATA[28]/EMIF_DATA[12]/N2HET2[28]/GIOA[0]
ETMDATA[29]/EMIF_DATA[13]/N2HET2[29]/GIOA[1]
ETMDATA[30]/EMIF_DATA[14]/N2HET2[30]/GIOA[3]
ETMDATA[31]/EMIF_DATA[15]/N2HET2[31]/GIOA[4]
ETMTRACECLKIN/EXTCLKIN2/GIOA[5]
ETMTRACECLKOUT/GIOA[6]
ETMTRACECTL/GIOA[7]
NPORRST W7
HERCULES_NRST B17
NPORRST
NRST
ECLK1
EMIF_ADDR[0]/N2HET2[1]
EMIF_ADDR[1]/N2HET2[3]
EMIF_ADDR[6]/RTP_DATA[13]/N2HET2[11]
EMIF_ADDR[7]/RTP_DATA[12]/N2HET2[13]
EMIF_ADDR[8]/RTP_DATA[11]/N2HET2[15]
EMIF_ADDR[9]/RTP_DATA[10]
EMIF_ADDR[10]/RTP_DATA[9]
EMIF_ADDR[11]/RTP_DATA[8]
EMIF_ADDR[12]/RTP_DATA[6]
EMIF_ADDR[13]/RTP_DATA[5]
EMIF_ADDR[14]/RTP_DATA[4]
EMIF_ADDR[15]/RTP_DATA[3]
EMIF_ADDR[16]/RTP_DATA[2]
EMIF_ADDR[17]/RTP_DATA[1]
EMIF_ADDR[18]/RTP_DATA[0]
EMIF_ADDR[19]/RTP_NENA
EMIF_ADDR[20]/RTP_NSYNC
EMIF_ADDR[21]/RTP_CLK
EMIF_BA[1]/N2HET2[5]
EMIF_CKE
EMIF_CLK/ECLK2
EMIF_NCAS/GIOB[3]
EMIF_NCS[0]/RTP_DATA[15]/N2HET2[7]
EMIF_NCS[2]/GIOB[4]
EMIF_NCS[3]/RTP_DATA[14]/N2HET2[9]
EMIF_NCS[4]/RTP_DATA[7]/GIOB[5]
EMIF_NRAS/GIOB[6]
EMIF_NWAIT/GIOB[7]
EMIF_NWE/EMIF_RNW
FLTP1
FLTP2
NERROR
A12 RP8A 56 ECLK1T/9.3C
D4 RP4A 56 EMIF_ADDR_0/N2HET2_1/13.4D
D5 RP4B 56 EMIF_ADDR_1/N2HET2_3/13.4D
C4 RP4C 56 EMIF_ADDR_6/RTP_DATA_13/N2HET2_11/13.4D
C5 RP4D 56 EMIF_ADDR_7/RTP_DATA_12/N2HET2_13/13.4D
C6 RP4E 56 EMIF_ADDR_8/RTP_DATA_11/N2HET2_15/13.4D
C7 RP4F 56 EMIF_ADDR_9/RTP_DATA_10/13.4D
C8 RP4G 56 EMIF_ADDR_10/RTP_DATA_9/13.4D
C9 RP6D 56 EMIF_ADDR_11/RTP_DATA_8/13.2D
C10 RP6B 56 EMIF_ADDR_12/RTP_DATA_6/13.2D
C11 RP6A 56 EMIF_ADDR_13/RTP_DATA_5/13.2D
C12 RP5H 56 EMIF_ADDR_14/RTP_DATA_4/13.2D
C13 RP5G 56 EMIF_ADDR_15/RTP_DATA_3/13.2D
D14 RP5F 56 EMIF_ADDR_16/RTP_DATA_2/13.2D
C14 RP5E 56 EMIF_ADDR_17/RTP_DATA_1/13.2C
D15 RP5D 56 EMIF_ADDR_18/RTP_DATA_0/13.2C
C15 RP5C 56 EMIF_ADDR_19/RTP_NENA/13.2C
C16 RP5B 56 EMIF_ADDR_20/RTP_NSYNC/13.2C
C17 RP5A 56 EMIF_ADDR_21/RTP_CLK/13.2C
D16 RP6C 56 EMIF_BA_1/N2HET2_5/13.2D
L3 RP8C 56 EMIF_CKE/14.4E
K3 RP8B 56 EMIF_CLK/ECLK2/14.4E
R4 RP8E 56 EMIF_NCAS/GIOB_3/14.4D
N17 RP1A 56 EMIF_NCS_0/RTP_DATA_15/N2HET2_7/13.2A
L17 RP1B 56 EMIF_NCS_2/GIOB_4/13.2A
K17 RP1C 56 EMIF_NCS_3/RTP_DATA_14/N2HET2_9/13.2A
M17 RP1D 56 EMIF_NCS_4/RTP_DATA_7/GIOB_5/13.2A
R3 RP8D 56 EMIF_NRAS/GIOB_6/14.4D
P3 EMIF_NWAIT/GIOB_7/14.4D
D17 RP2H 56 EMIF_NWE/EMIF_RNW/13.2B
J5
H5
B14
+3V3
RP10C 2.2K
LED1
RED
ERROR LED
NERROR/9.3C
+3V3
2.2K RP10A
HERCULES_NRST
+3V3
*RSTVDD
VDD
GND
*RSTSENSE
*MR
SENSE
GND
HERCULES_MR_PB/13.4A
S1
TACT6MM_NO_MOM
POWER ON RESET
GND
RP9A 2.2K RP9B 2.2K
+1V2
SENSE THRESHOLD
0.542V - 0.559V
GND
+3V3
RP10B
2.2K
HERCULES_NRST/7.5B
C3
1000pF
S2
TACT6MM_NO_MOM
GND
WARM RESET
TITLE: LAUNCHXL2_570LC43_RM57L
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LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L
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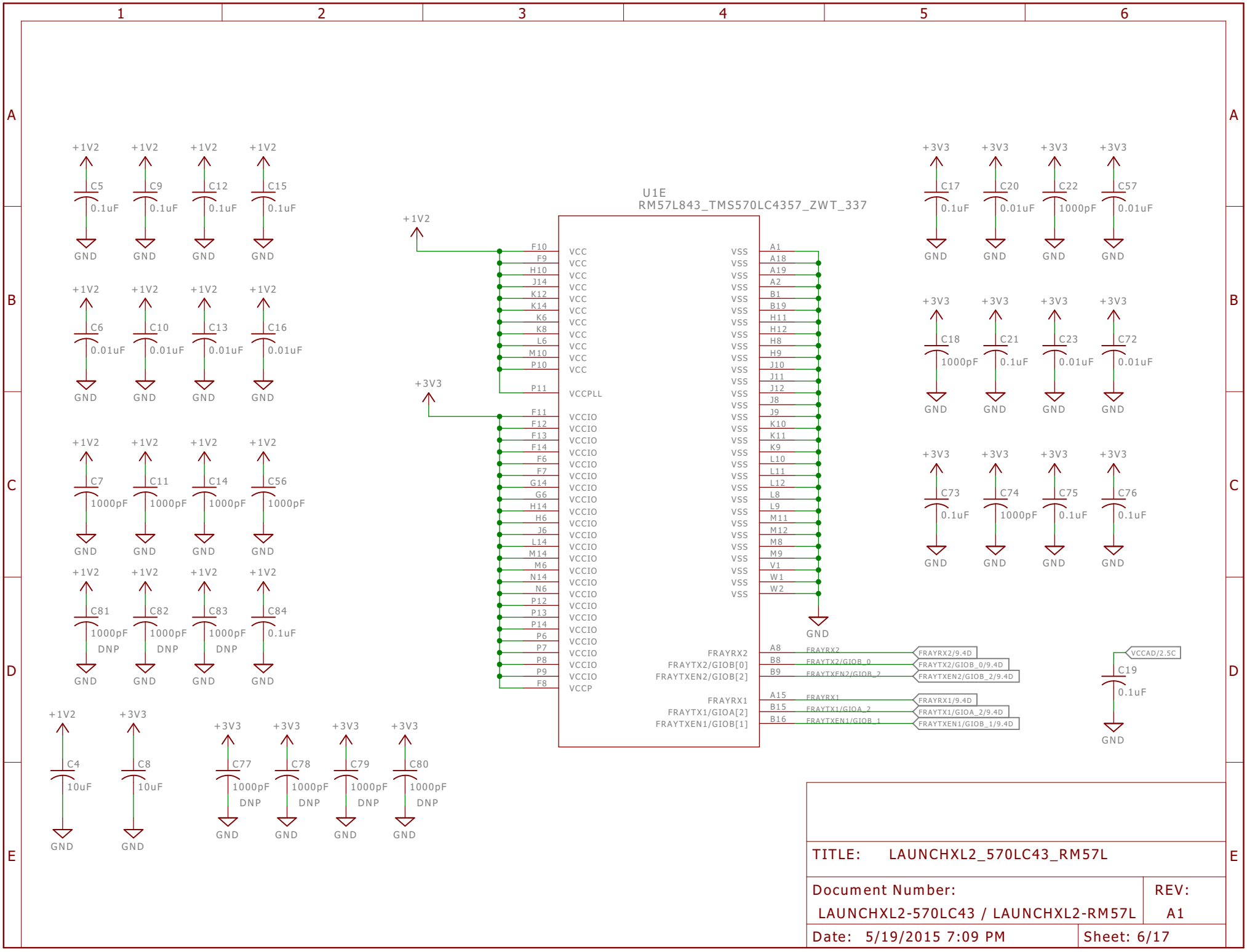
1
2
3
4
5
6
A
B
C
D
E
U1E
RM57L843_TMS570LC4357_ZWT_337
+1V2
+3V3
GND
C5 0.1uF
C9 0.1uF
C12 0.1uF
C15 0.1uF
C6 0.01uF
C10 0.01uF
C13 0.01uF
C16 0.01uF
C7 1000pF
C11 1000pF
C14 1000pF
C56 1000pF
C81 1000pF DNP
C82 1000pF DNP
C83 1000pF DNP
C84 0.1uF
C4 10uF
C8 10uF
C77 1000pF DNP
C78 1000pF DNP
C79 1000pF DNP
C80 1000pF DNP
C17 0.1uF
C20 0.01uF
C22 1000pF
C57 0.01uF
C18 1000pF
C21 0.1uF
C23 0.01uF
C72 0.01uF
C73 0.1uF
C74 1000pF
C75 0.1uF
C76 0.1uF
C19 0.1uF
VCCAD/2.5C
VCC
VCCPLL
VCCIO
VCCP
VSS
FRAYRX2
FRAYTX2/GIOB[0]
FRAYTXEN2/GIOB[2]
FRAYRX1
FRAYTX1/GIOA[2]
FRAYTXEN1/GIOB[1]
FRAYRX2/9.4D
FRAYTX2/GIOB_0/9.4D
FRAYTXEN2/GIOB_2/9.4D
FRAYRX1/9.4D
FRAYTX1/GIOA_2/9.4D
FRAYTXEN1/GIOB_1/9.4D
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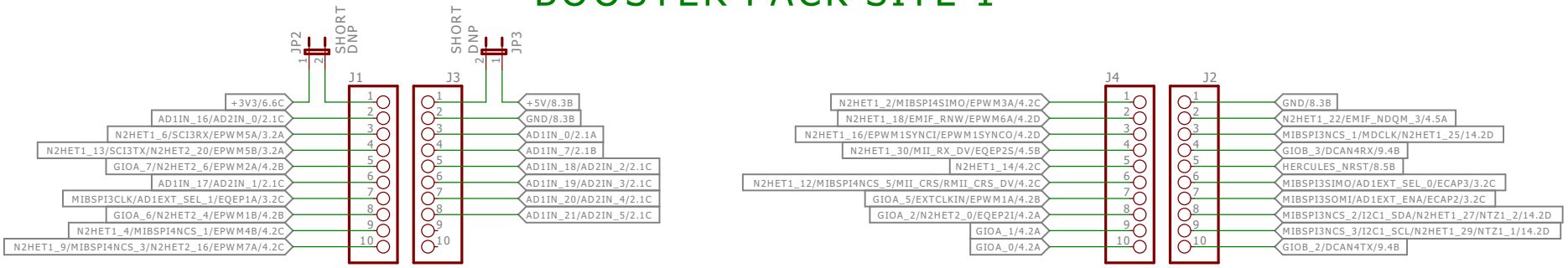
BOOSTER PACK SITE 1



Headers/Receptacles may be ordered from http://launchpad.mlelectronics.com/

NOTES:

JP2 & JP3 are normally shorted on the PCB - the footprint consists of an (unpopulated) 2 pin SMT header and a solder-bridging structure.
Most users can leave these jumpers as-is.

Certain booster packs may require that the position is opened - these are booster packs that would otherwise supply power to the launchpad through these pins.
Providing +3V3 to the launchpad is a problem because there would be a conflict with the launchpad's on-board LM26420 regulator.
Providing +5V to the launchpad through the booster pack could be ok, but the barrel jack is preferred as it is protected with a PTC.
Also be careful to avoid back powering the USB connection if you do this.

In some cases you may find the need to make/break the connections JP2,JP3 frequently.
If you do, then you can remove the solder bridge and mount a 2 pin SMT header on the footprint location which can then be opened/closed by using a Jumper or Shunt.

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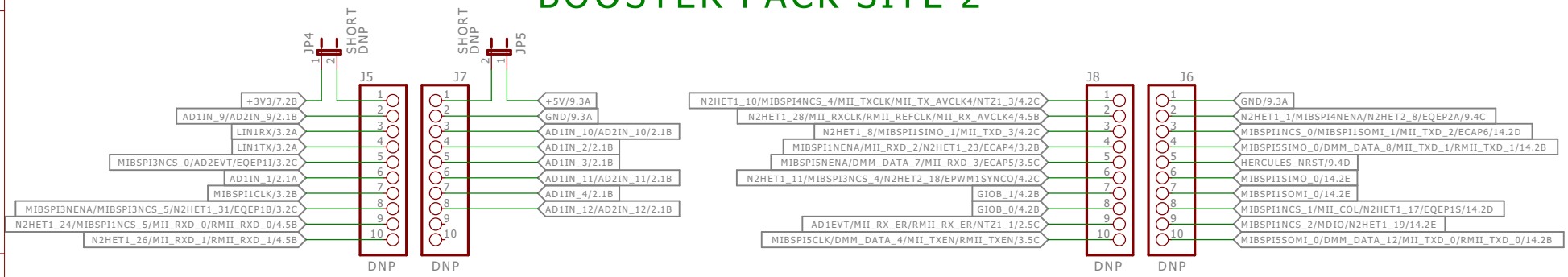
BOOSTER PACK SITE 2



Headers/Receptacles may be ordered from http://launchpad.mlelectronics.com/

NOTES:

JP4 & JP5 are normally shorted on the PCB - the footprint consists of an (unpopulated) 2 pin SMT header and a solder-bridging structure.
Most users can leave these jumpers as-is.

Certain booster packs may require that the position is opened - these are booster packs that would otherwise supply power to the launchpad through these pins.
Providing +3V3 to the launchpad is a problem because there would be a conflict with the launchpad's on-board LM26420 regulator.
Providing +5V to the launchpad through the booster pack could be ok, but the barrel jack is preferred as it is protected with a PTC.
Also be careful to avoid back powering the USB connection if you do this.

In some cases you may find the need to make/break the connections JP4,JP5 frequently.
If you do, then you can remove the solder bridge and mount a 2 pin SMT header on the footprint location which can then be opened/closed by using a Jumper or Shunt.

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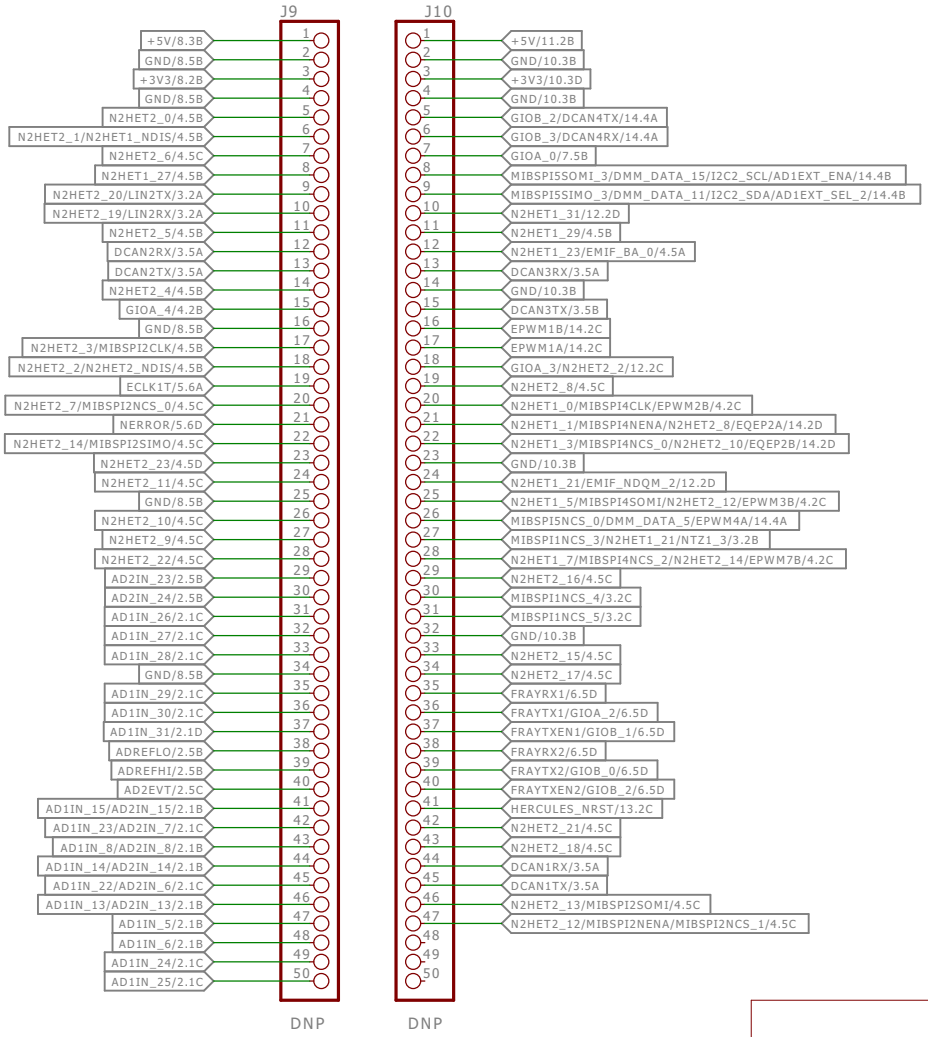
PROTO BOARD HEADERS



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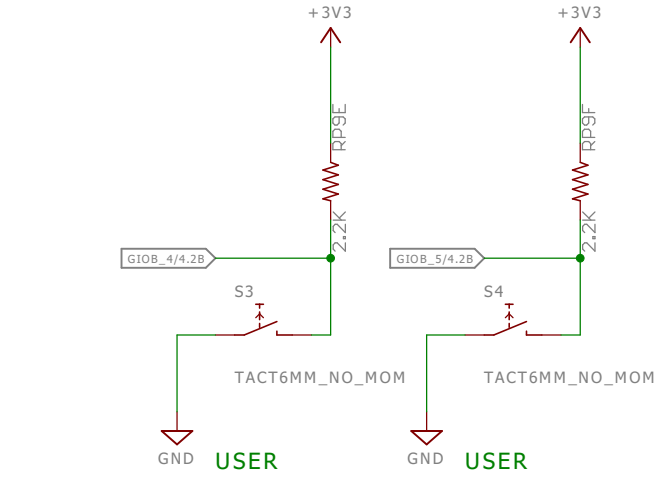
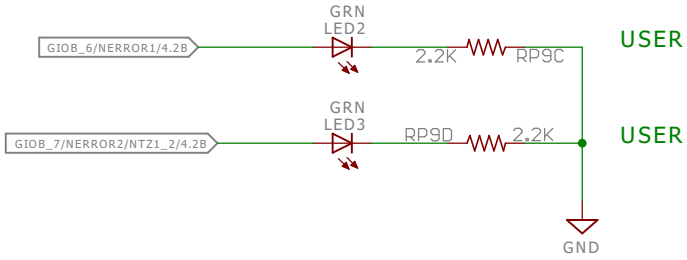
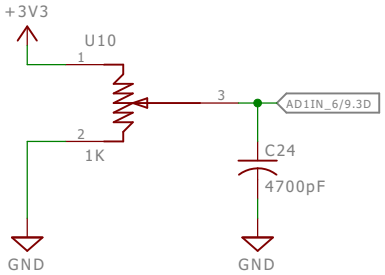




TRIM POT. FOR ANALOG INPUT
(REPLACES TEMP SENSOR)



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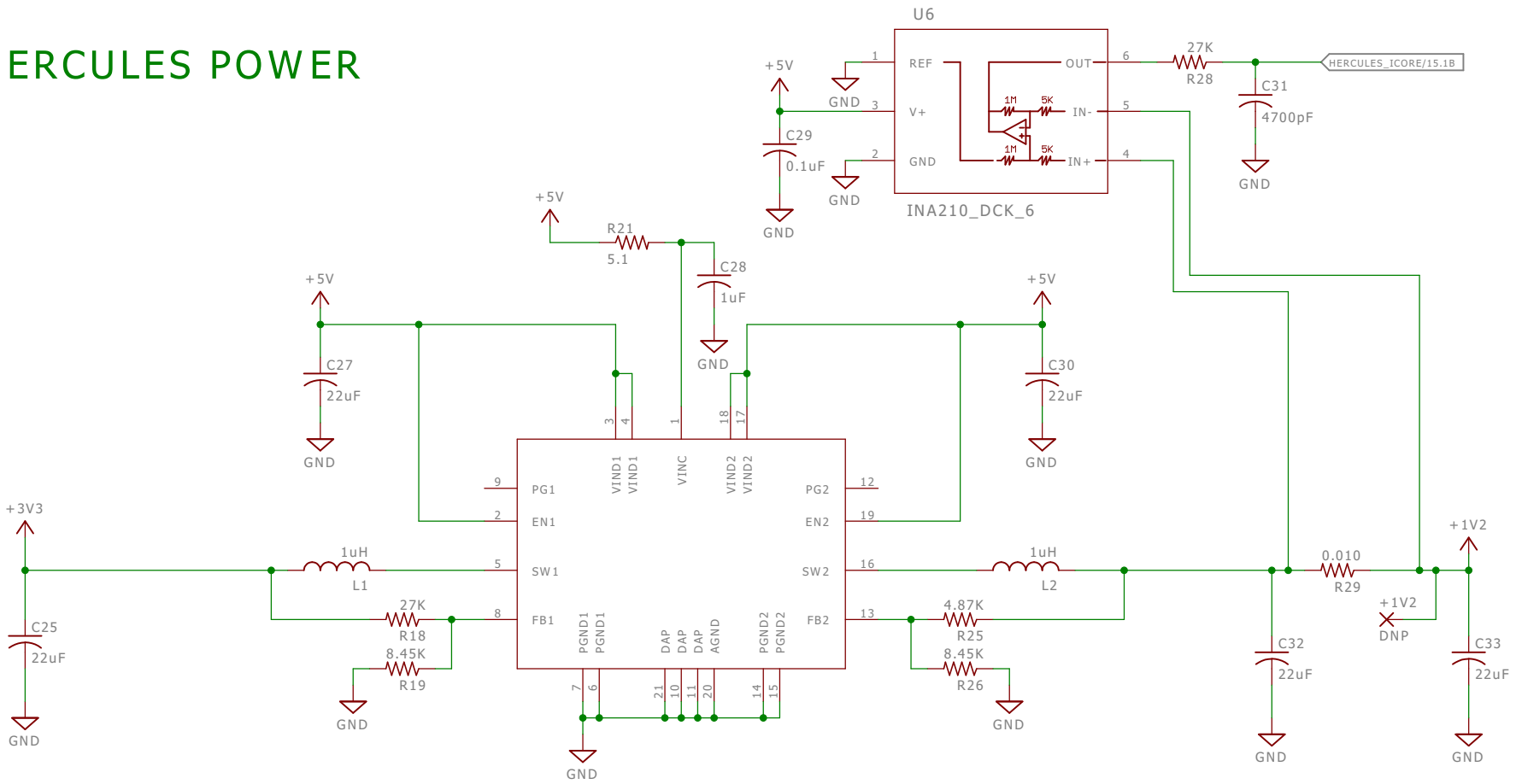
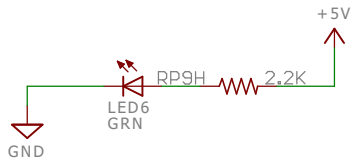
HERCULES POWER



POWER INDICATOR LED



+5V

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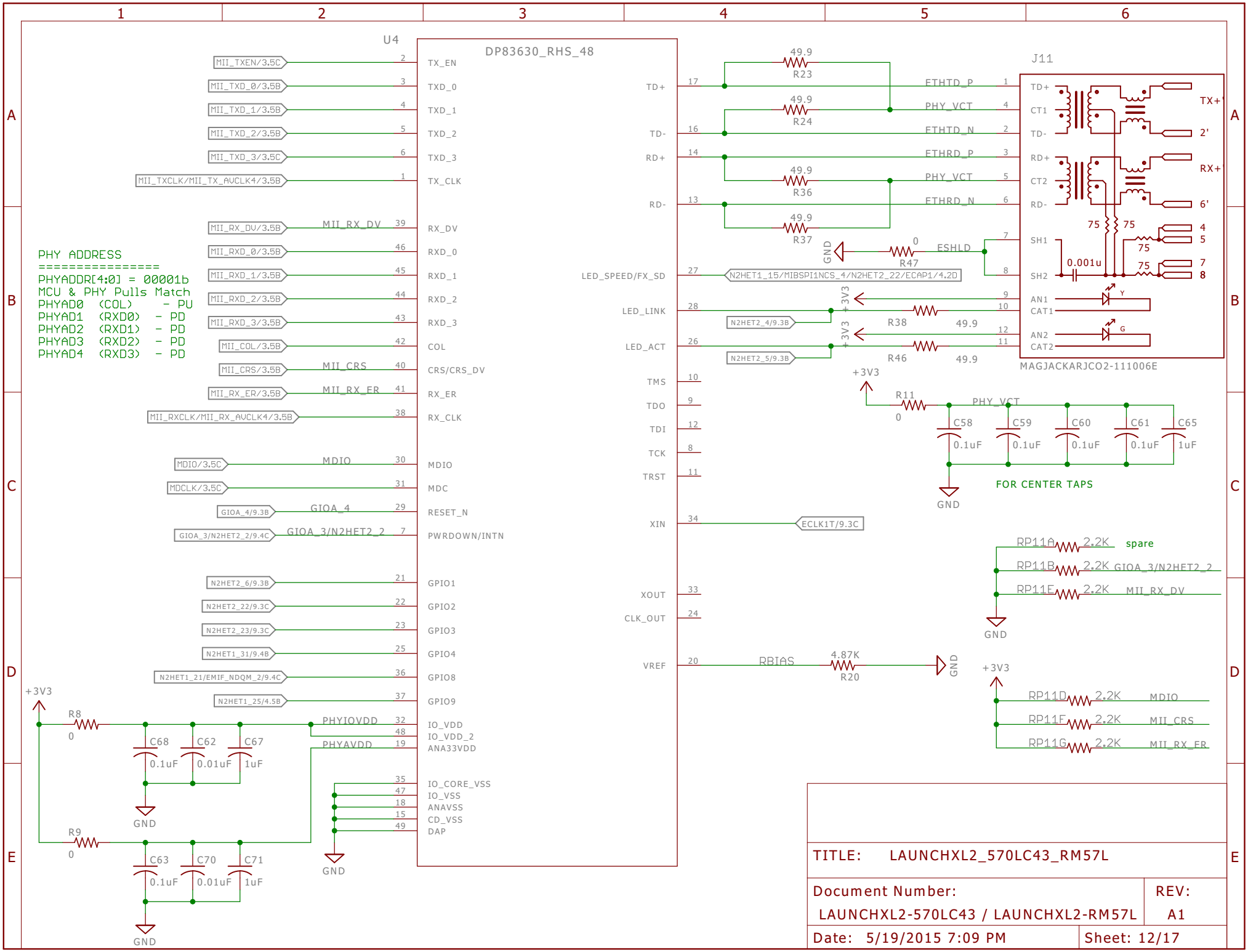
U4
DP83630_RHS_48
MII_TXEN/3.5C
MII_TXD_0/3.5B
MII_TXD_1/3.5B
MII_TXD_2/3.5B
MII_TXD_3/3.5B
MII_TXCLK/MII_TX_AVCLK4/3.5B
MII_RX_DV/3.5B
MII_RXD_0/3.5B
MII_RXD_1/3.5B
MII_RXD_2/3.5B
MII_RXD_3/3.5B
MII_COL/3.5B
MII_CRS/3.5B
MII_RX_ER/3.5B
MII_RXCLK/MII_RX_AVCLK4/3.5B
MDIO/3.5C
MDCLK/3.5C
GIOA_4/9.3B
GIOA_3/N2HET2_2/9.4C
N2HET2_6/9.3B
N2HET2_22/9.3C
N2HET2_23/9.3C
N2HET1_31/9.4B
N2HET1_21/EMIF_NDQM_2/9.4C
N2HET1_25/4.5B
TX_EN
TXD_0
TXD_1
TXD_2
TXD_3
TX_CLK
RX_DV
RXD_0
RXD_1
RXD_2
RXD_3
COL
CRS/CRS_DV
RX_ER
RX_CLK
MDIO
MDC
RESET_N
PWRDOWN/INTN
GPIO1
GPIO2
GPIO3
GPIO4
GPIO8
GPIO9
IO_VDD
IO_VDD_2
ANA33VDD
IO_CORE_VSS
IO_VSS
ANAVSS
CD_VSS
DAP
TD+
TD-
RD+
RD-
LED_SPEED/FX_SD
LED_LINK
LED_ACT
TMS
TDO
TDI
TCK
TRST
XIN
XOUT
CLK_OUT
VREF
PHY ADDRESS
PHYADDR[4:0] = 00001b
MCU & PHY Pulls Match
PHYAD0 (COL) - PU
PHYAD1 (RXD0) - PD
PHYAD2 (RXD1) - PD
PHYAD3 (RXD2) - PD
PHYAD4 (RXD3) - PD
+3V3
R8
R9
C68
C62
C67
C63
C70
C71
PHYIOVDD
PHYAVDD
GND
49.9
R23
R24
R36
R37
ETHTD_P
ETHTD_N
ETHRD_P
ETHRD_N
PHY_VCT
R47
ESHLD
N2HET1_15/MIBSPI1NCS_4/N2HET2_22/ECAP1/4.2D
N2HET2_4/9.3B
N2HET2_5/9.3B
R38
R46
J11
TD+
CT1
TD-
RD+
CT2
RD-
SH1
SH2
AN1
CAT1
AN2
CAT2
TX+
2'
RX+
6'
75
0.001u
MAGJACKARJCO2-111006E
R11
C58
C59
C60
C61
C65
0.1uF
1uF
FOR CENTER TAPS
ECLK1T/9.3C
RP11A
RP11B
RP11E
2.2K
spare
GIOA_3/N2HET2_2
MII_RX_DV
RBIAS
4.87K
R20
RP11D
RP11F
RP11G
MDIO
MII_CRS
MII_RX_FR
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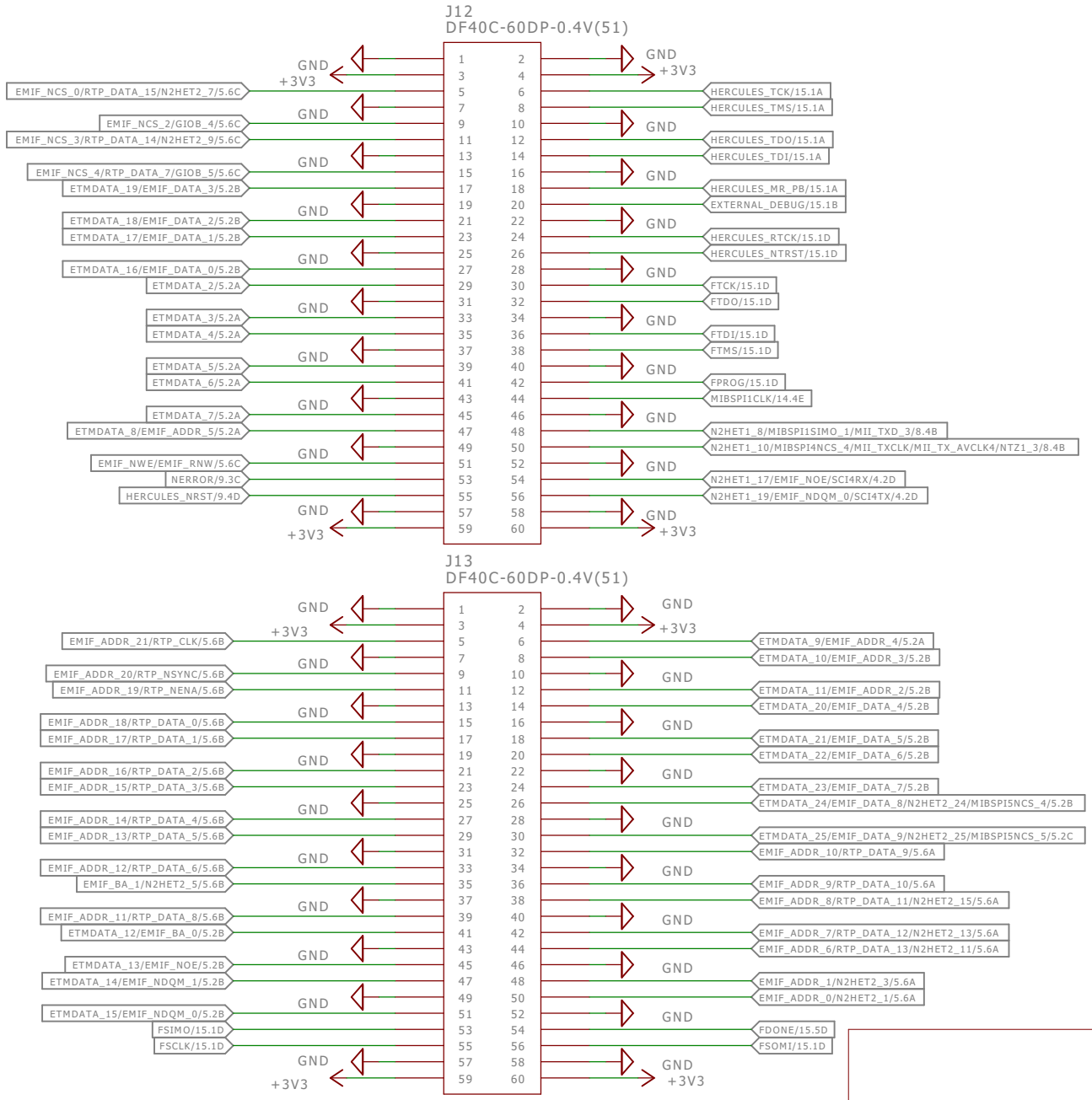

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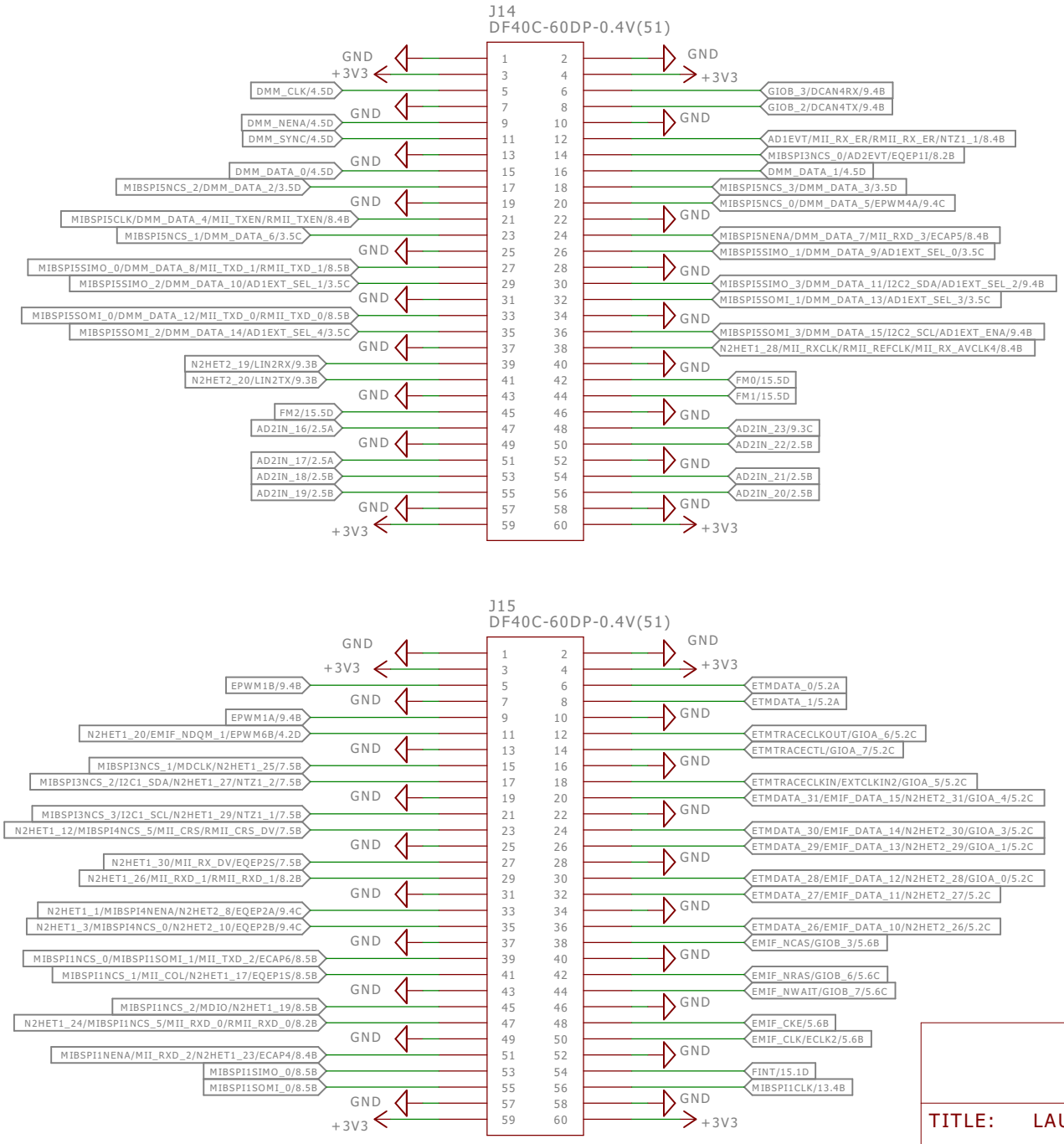

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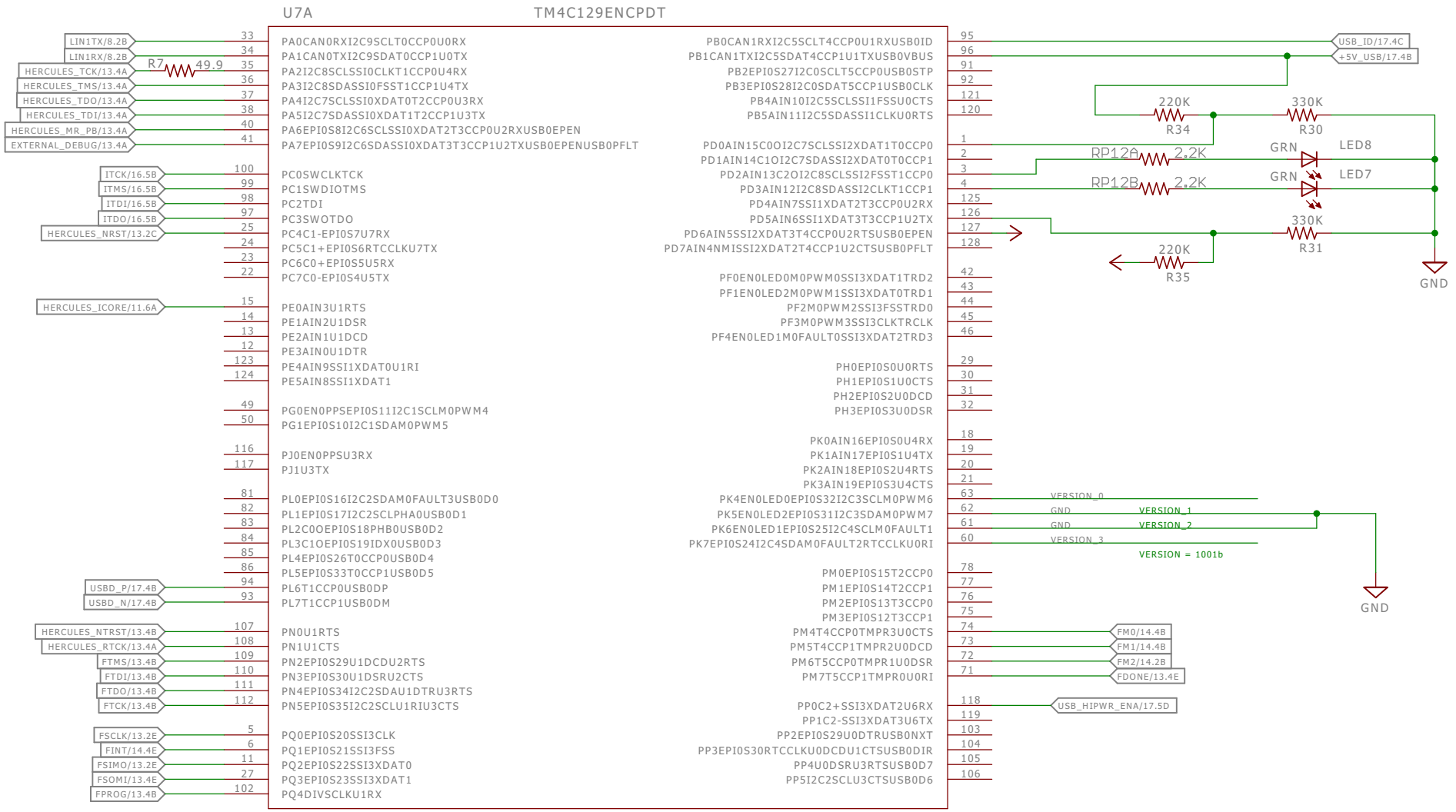
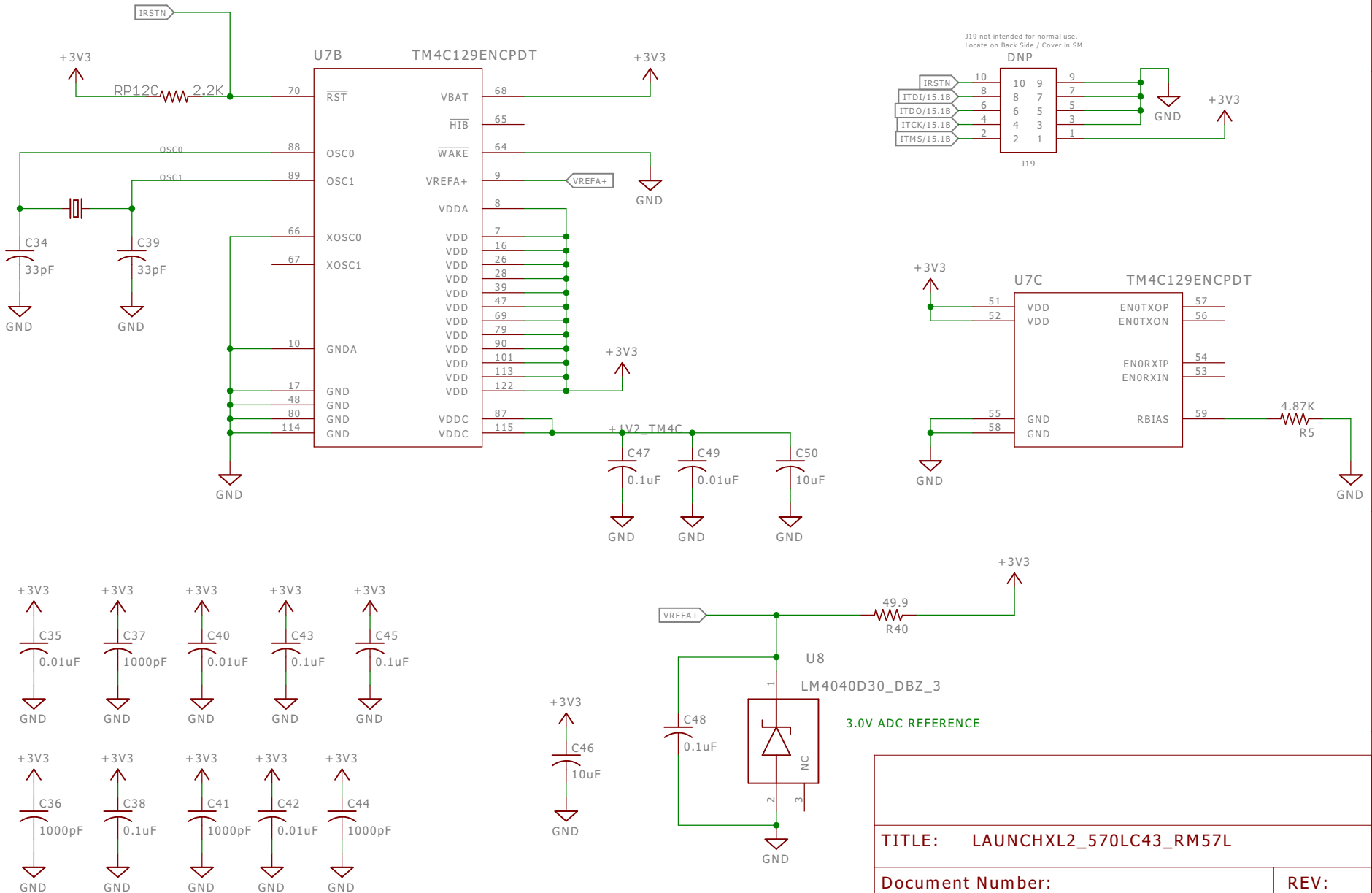
Date: 5/19/2015 7:09 PM	Sheet: 14/17

XDS110 DEBUG PROBE



TITLE: LAUNCHXL2_570LC43_RM57L	
Document Number: LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L	REV: A1
Date: 5/19/2015 7:09 PM	Sheet: 15/17

XDS110 DEBUG PROBE



TITLE: LAUNCHXL2_570LC43_RM57L	
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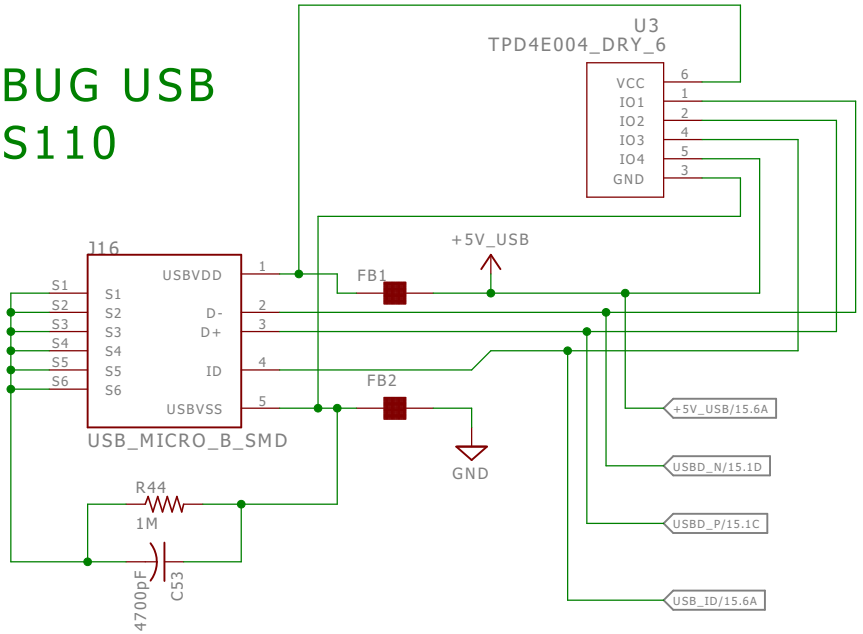
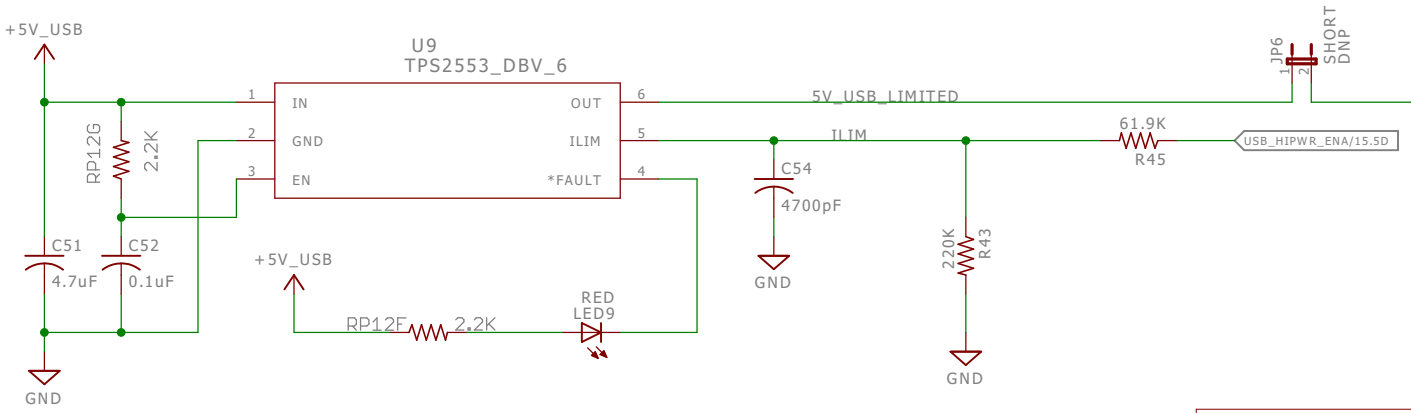
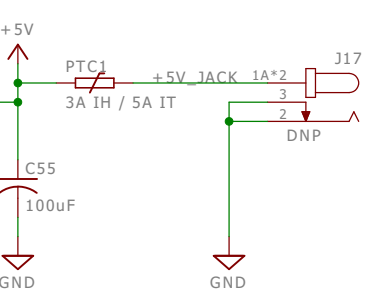
DEBUG USB XDS110



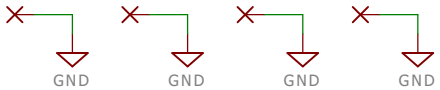
CURRENT LIMITING FOR BUS POWERED OPERATION



REMOVE JUMPER TO POWER THROUGH BARREL JACK



GND POINTS



FIDUCIALS

BOARD QFP

MNT HOLES

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