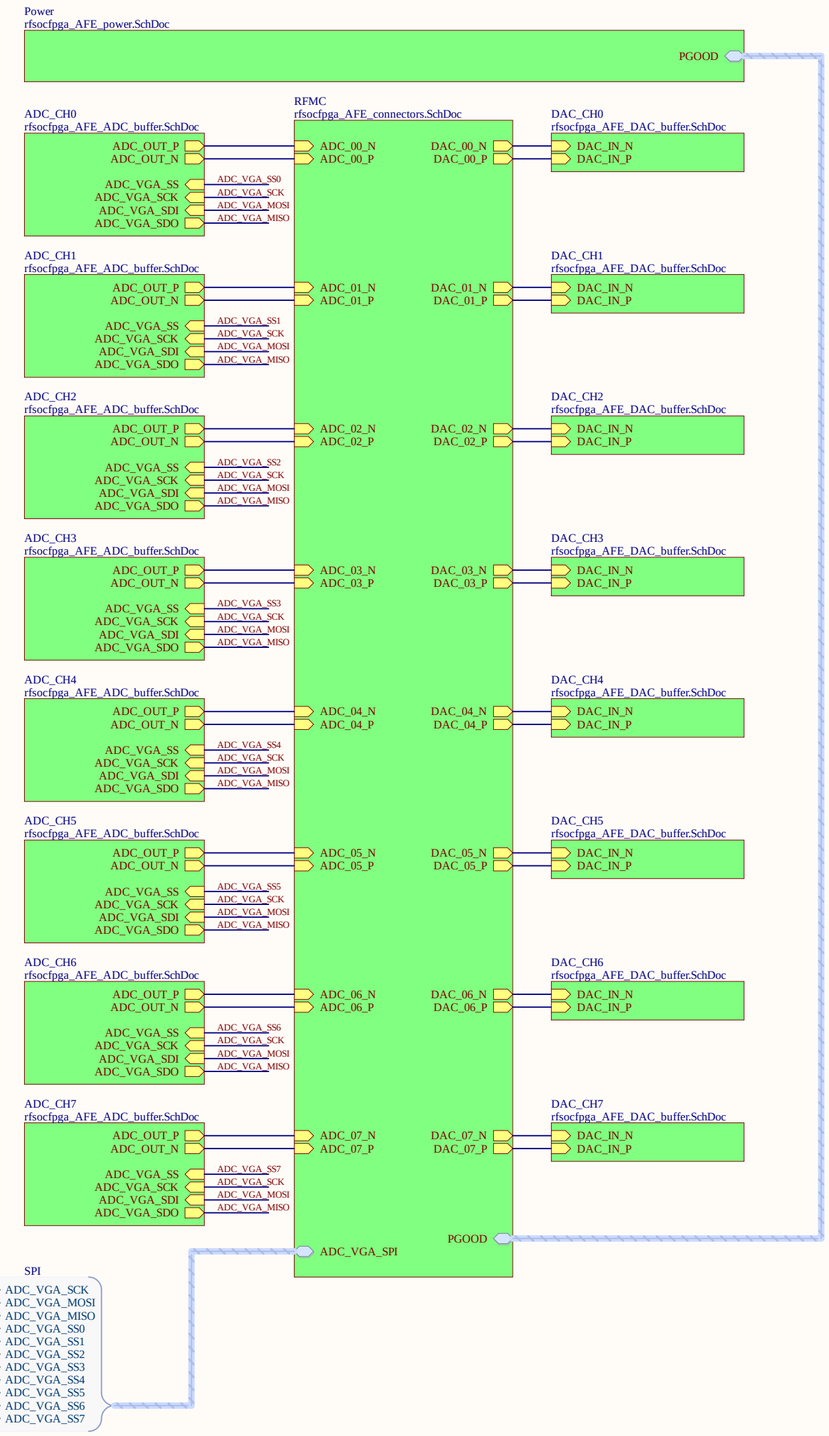

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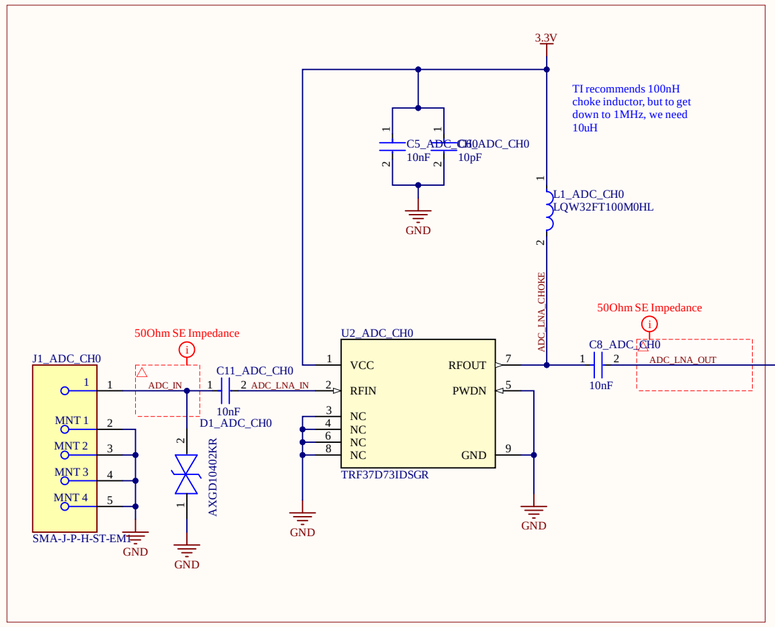

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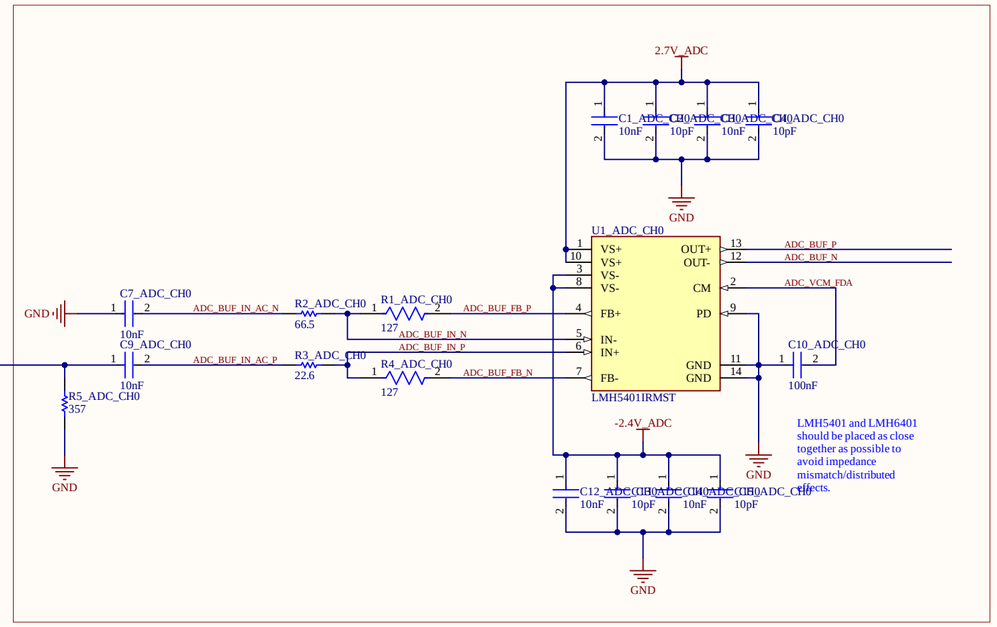
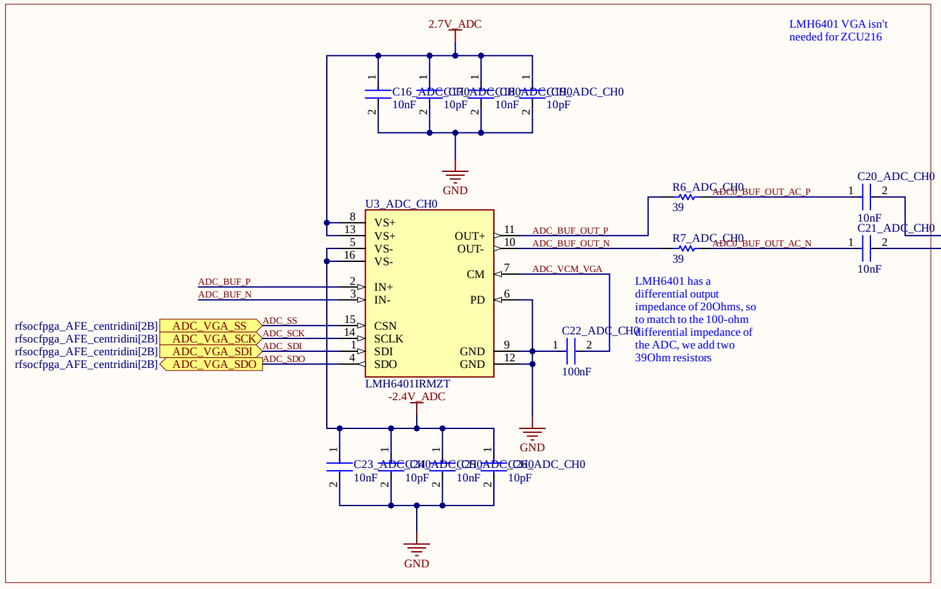


VGA to set signal level



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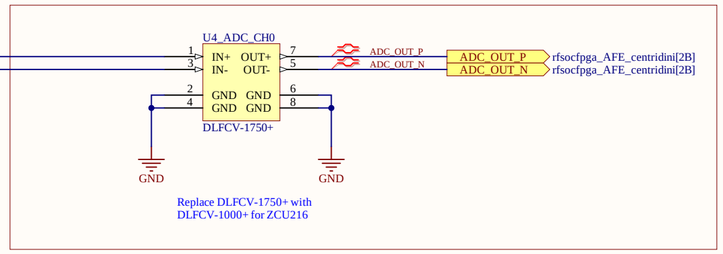

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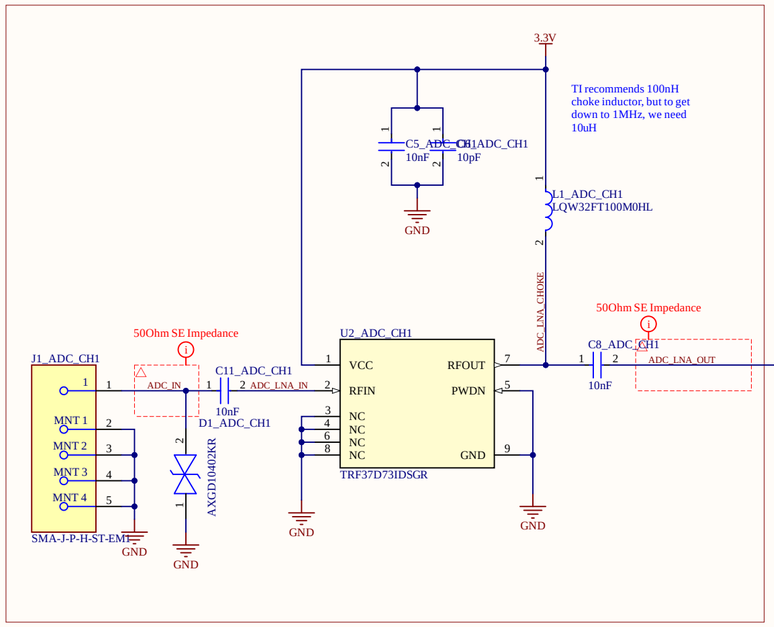

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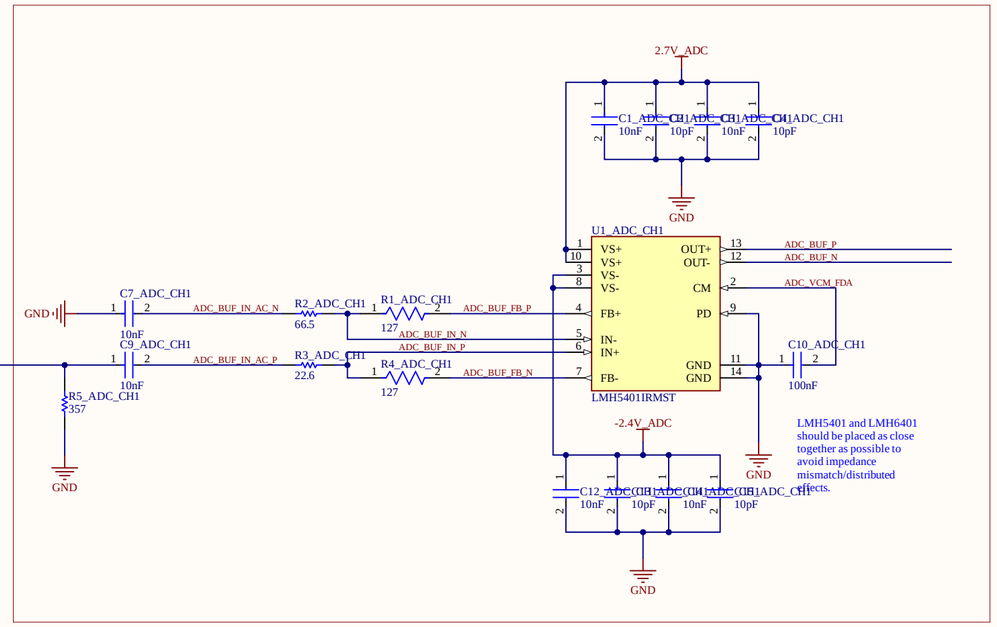

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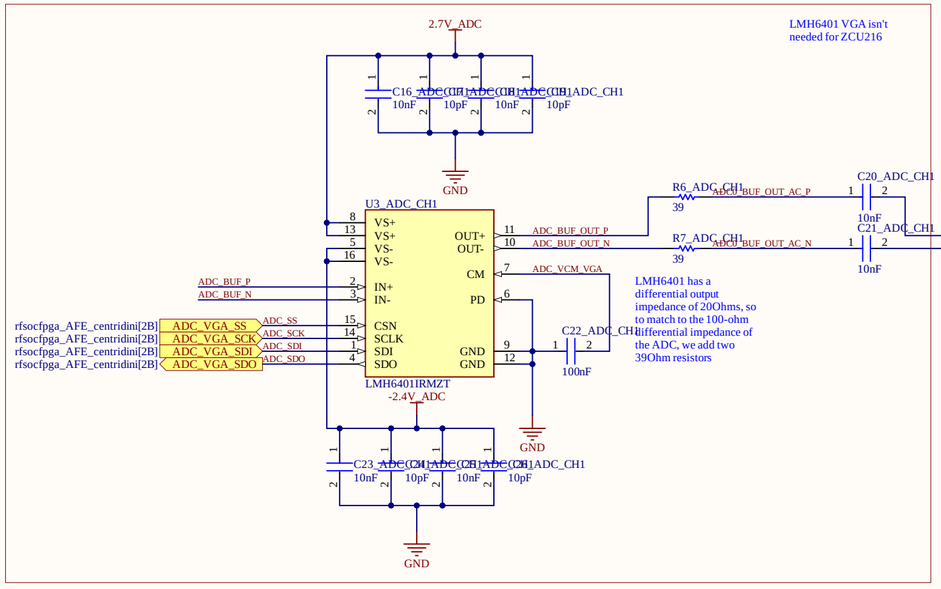

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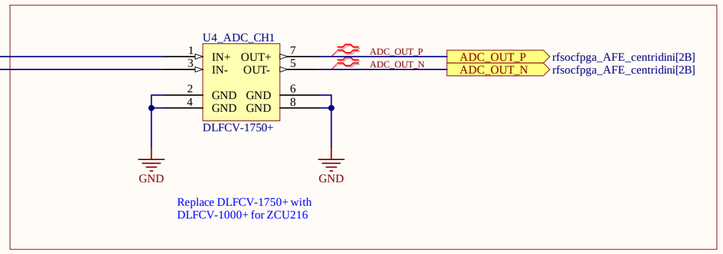

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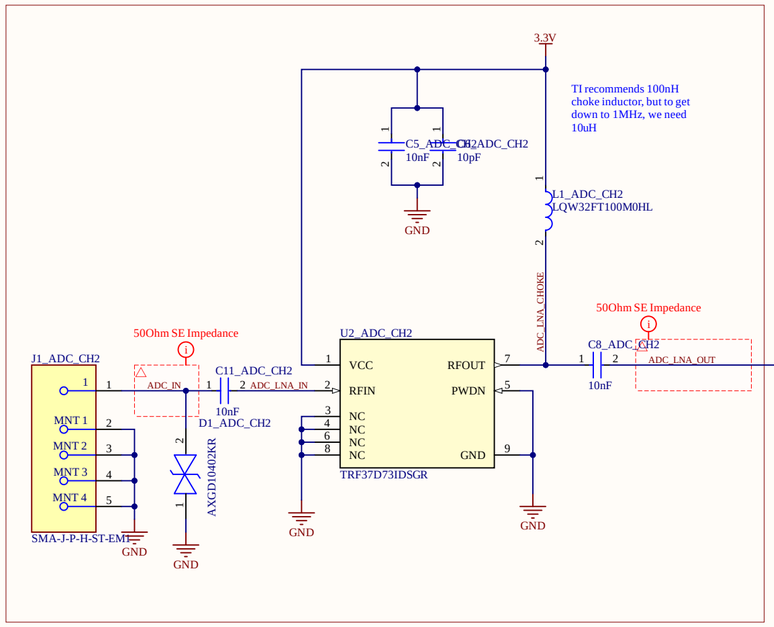

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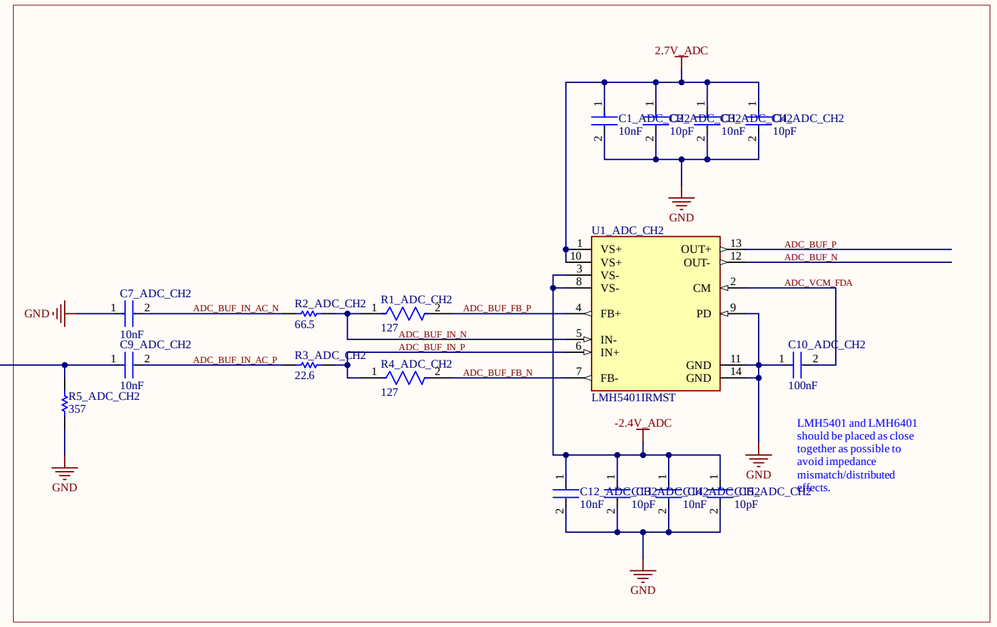

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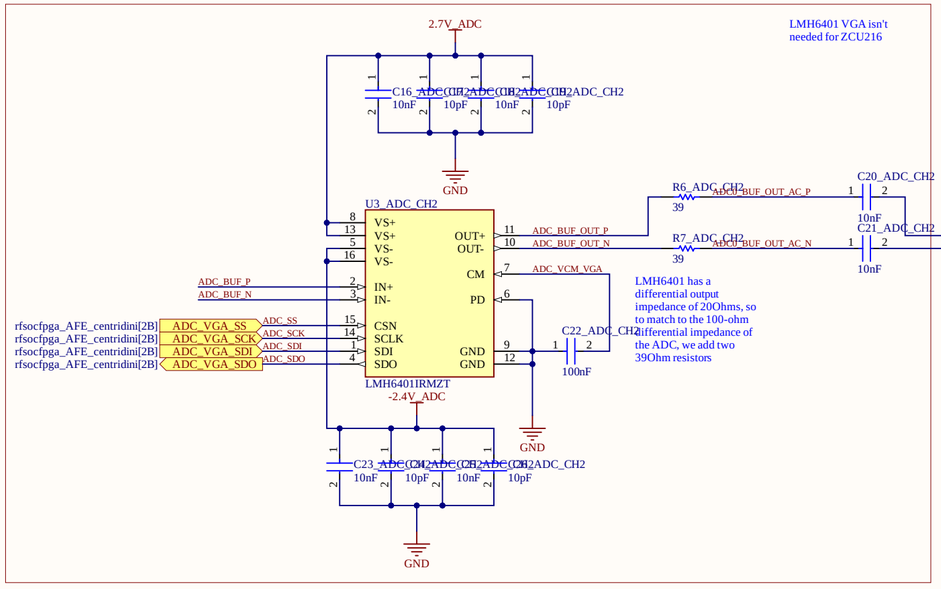

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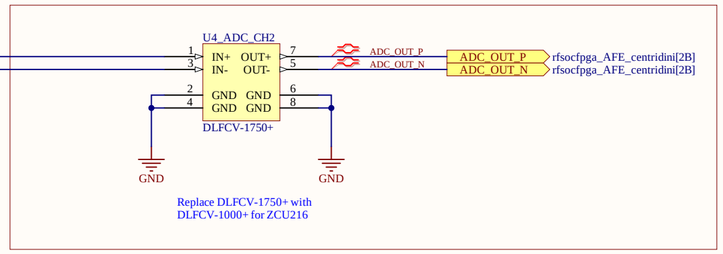

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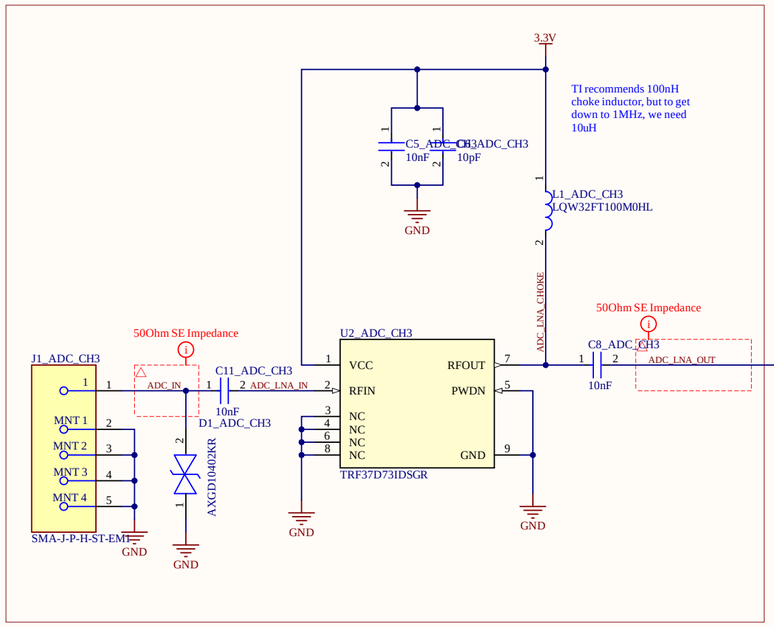

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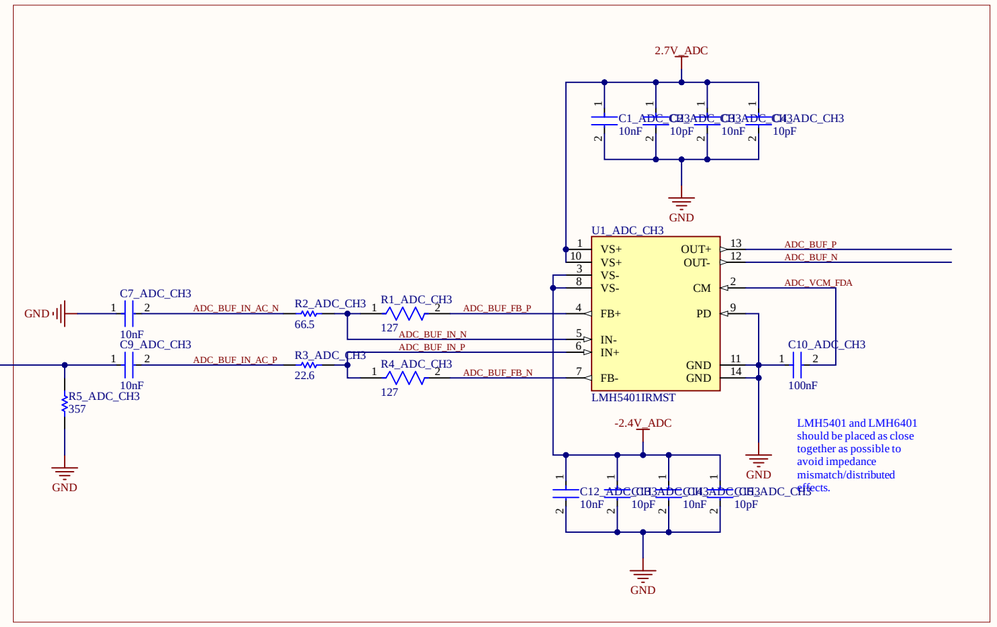

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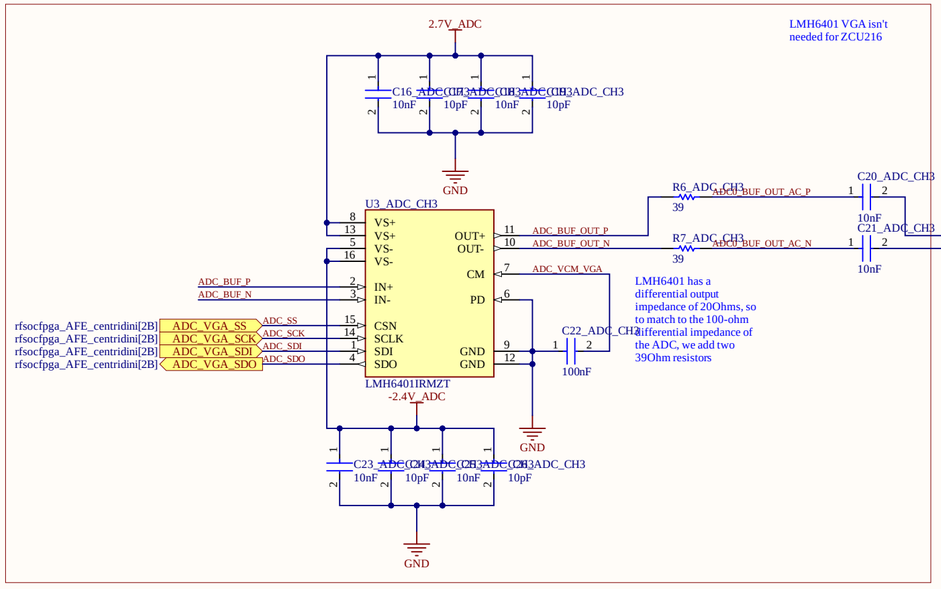

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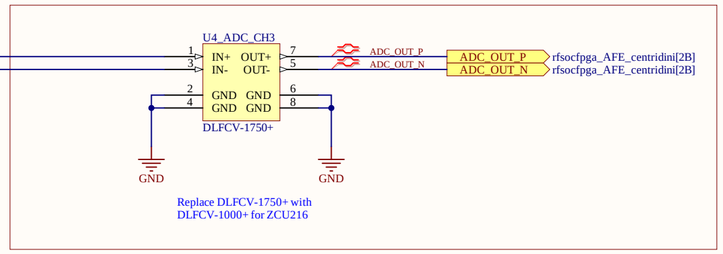

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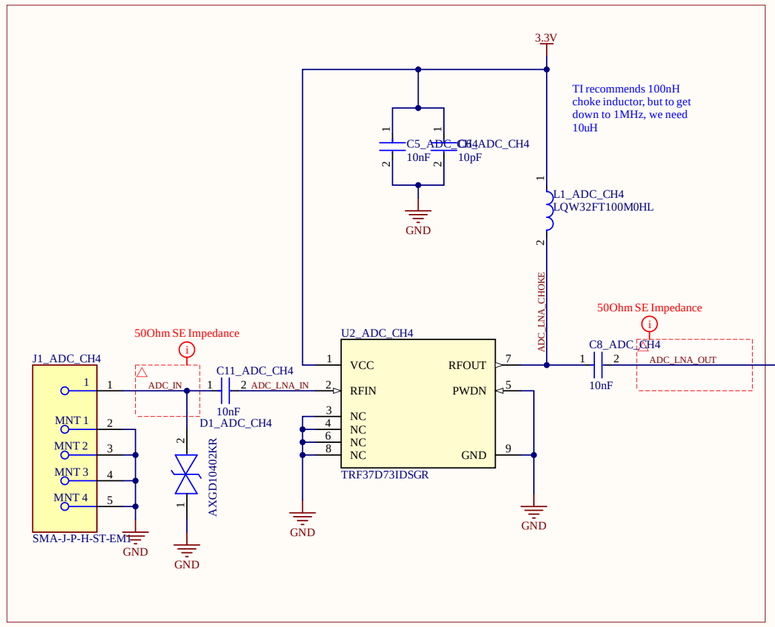

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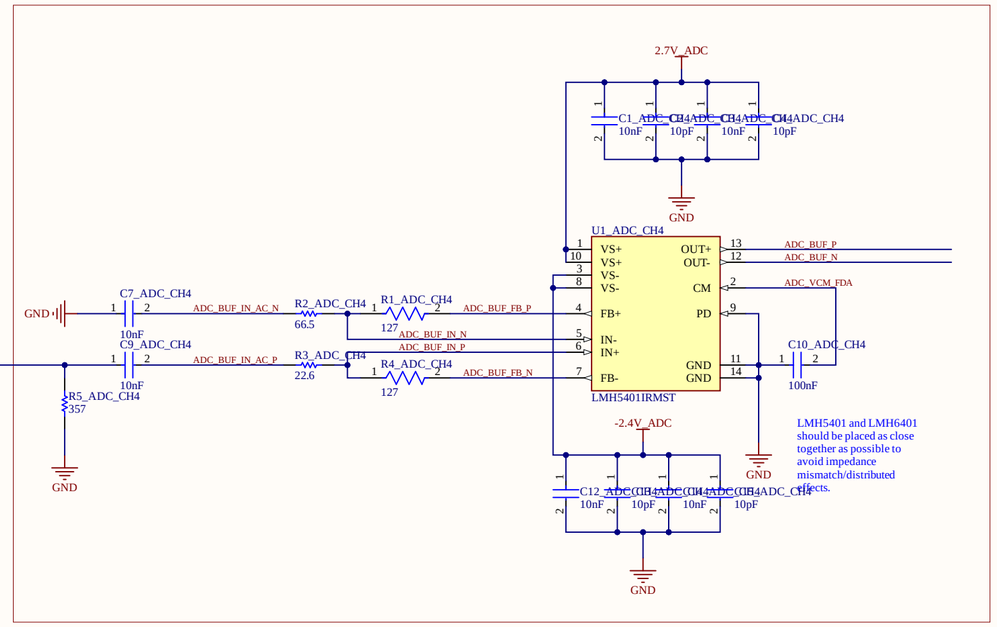

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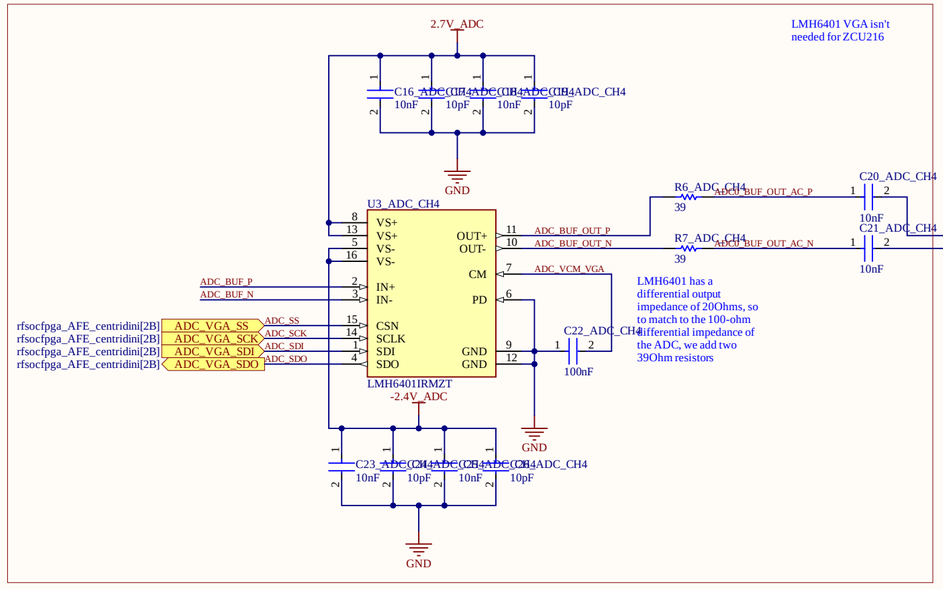

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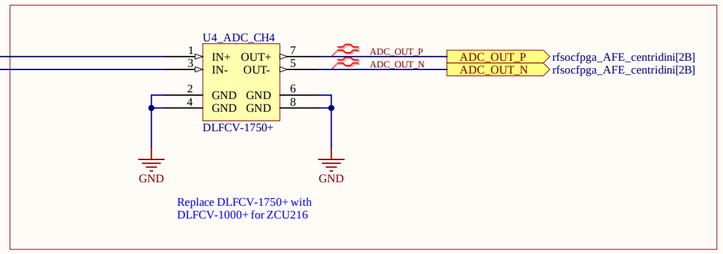

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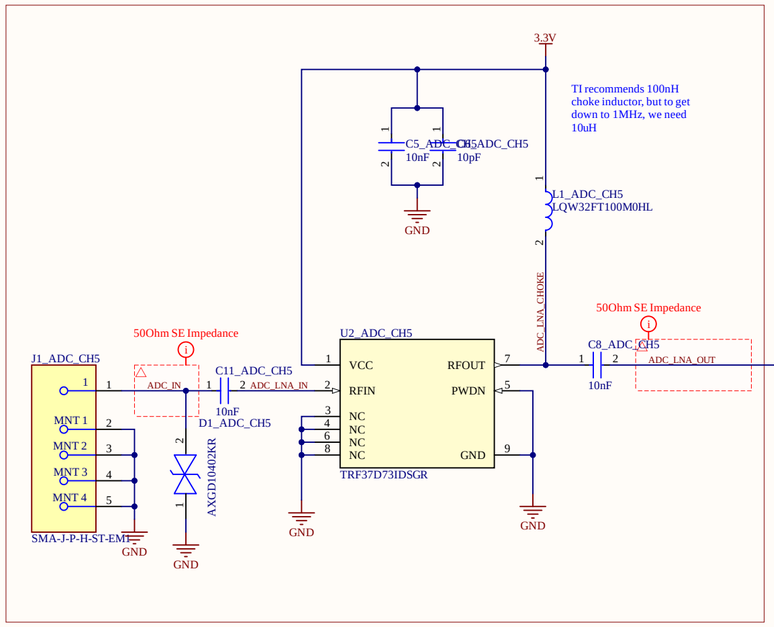

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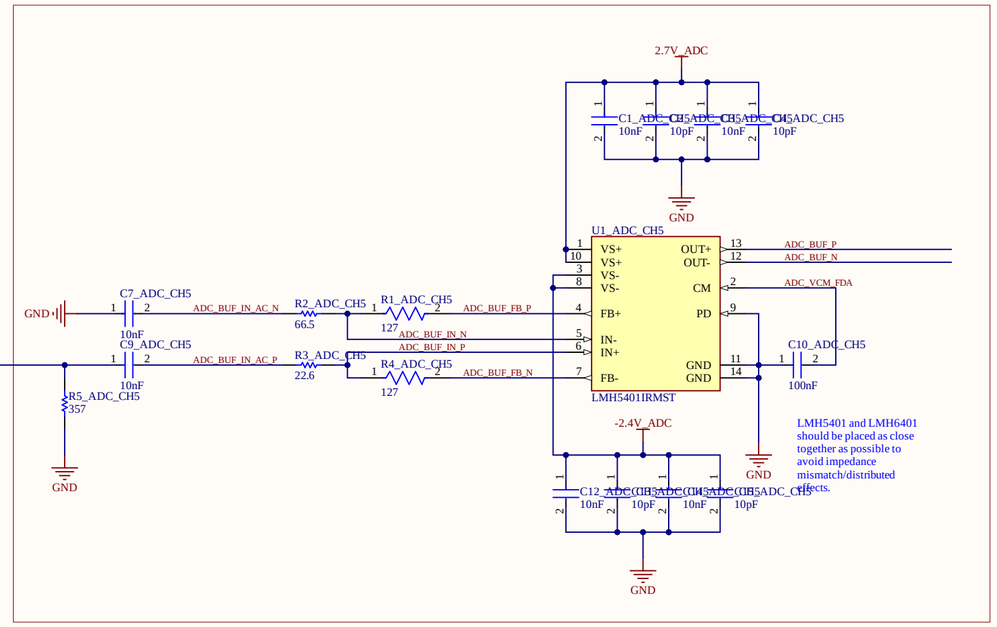

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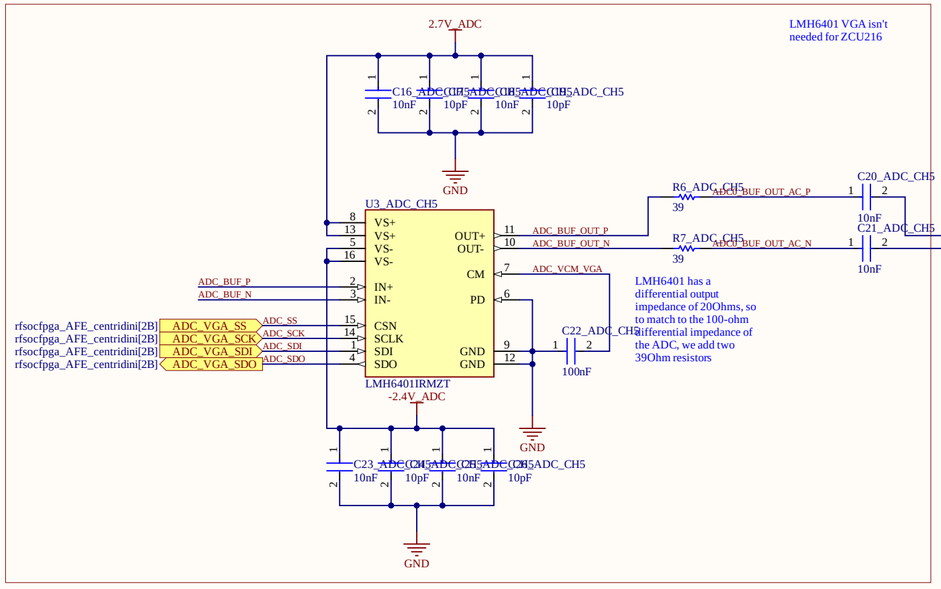

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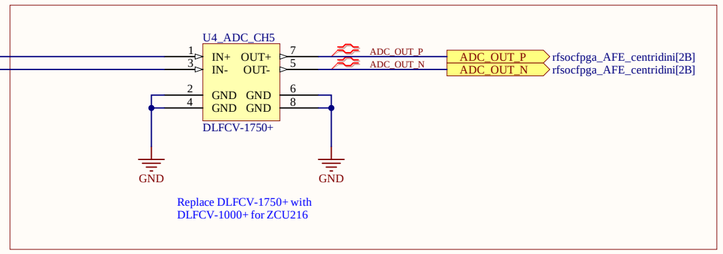

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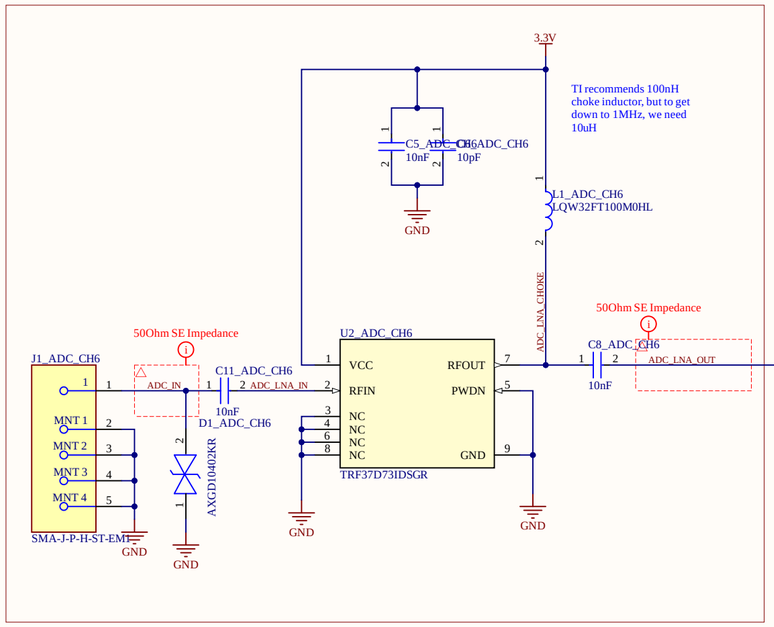

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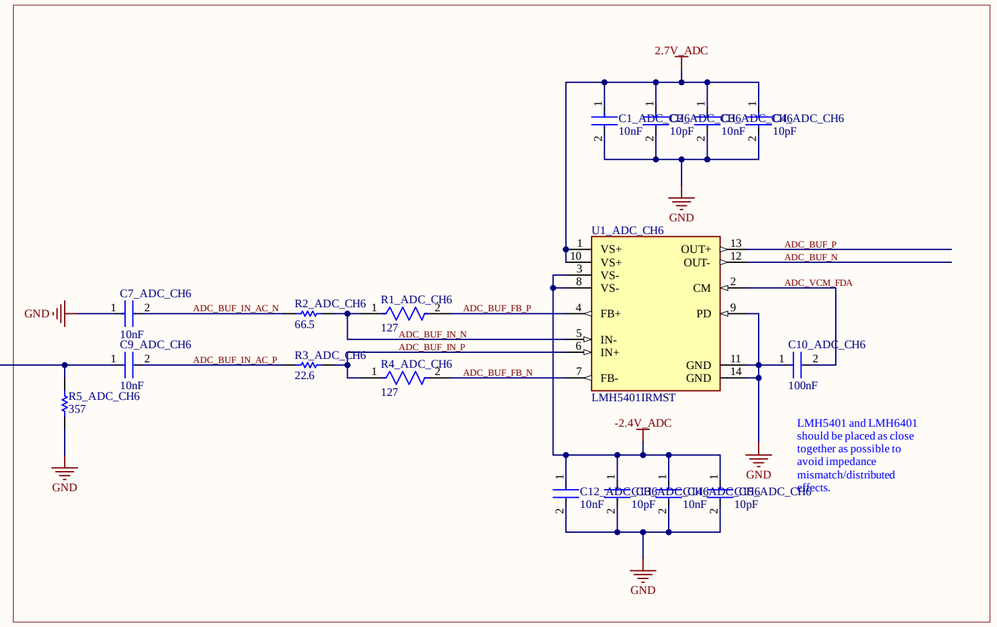

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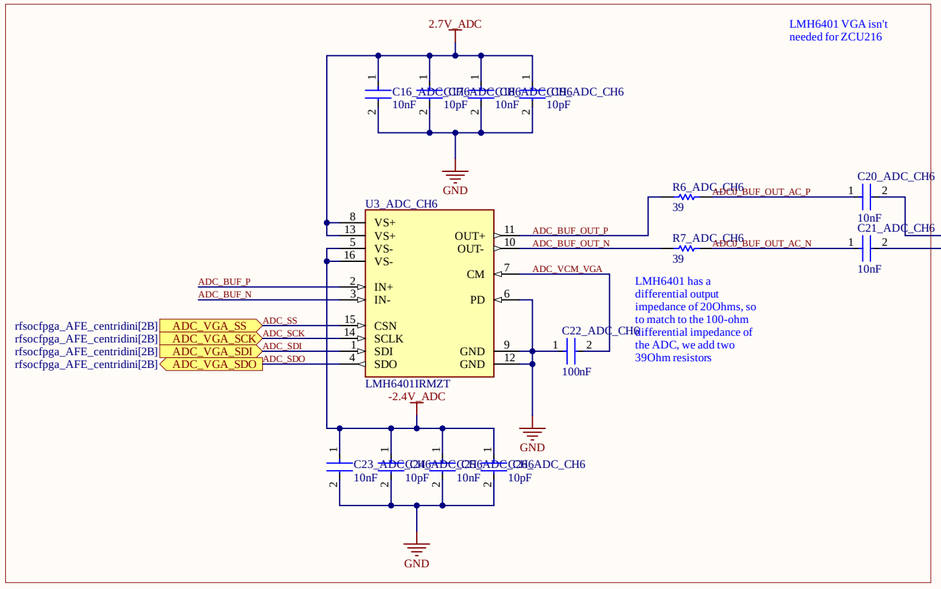

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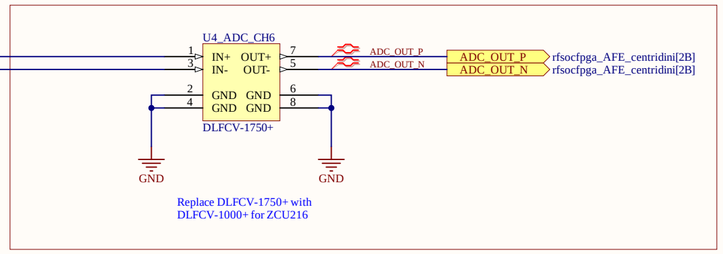

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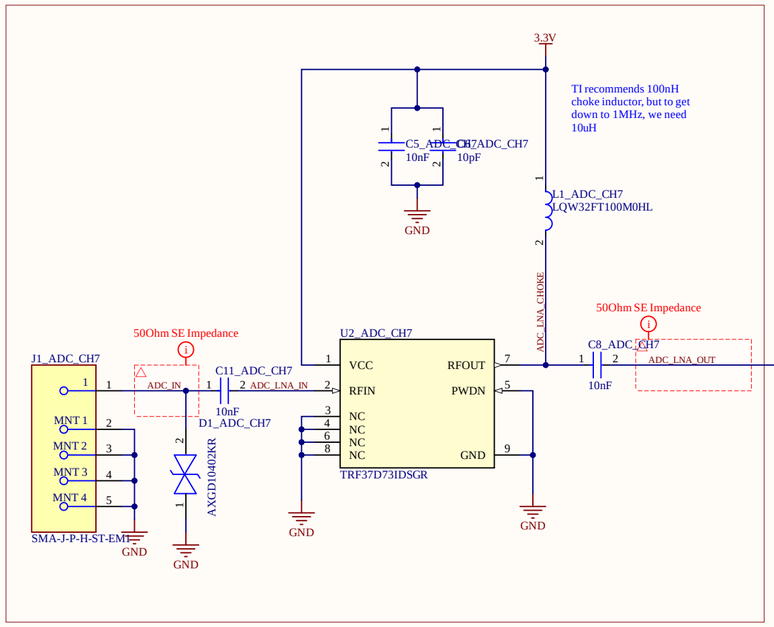

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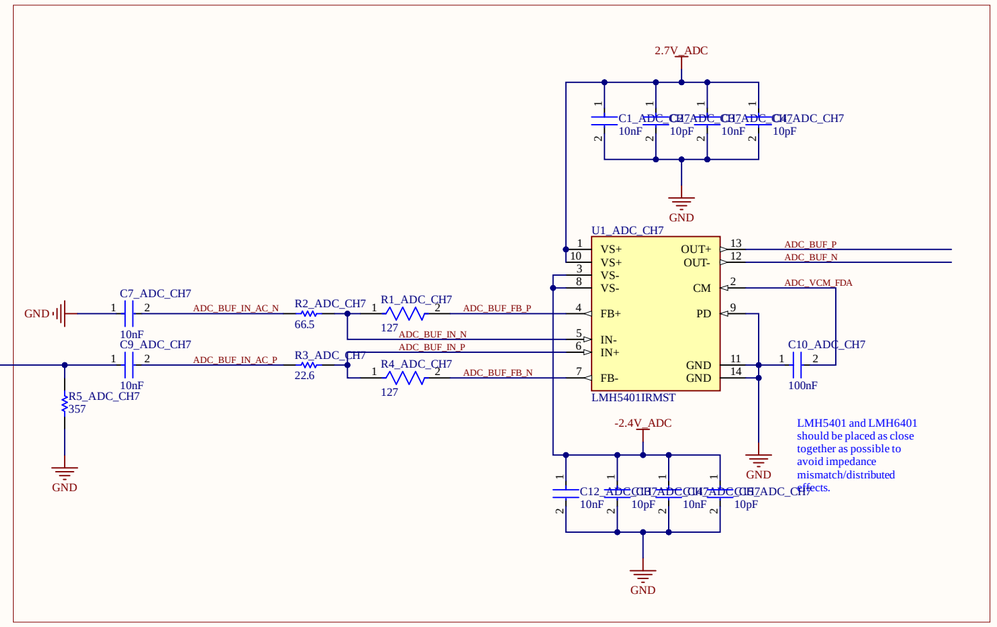

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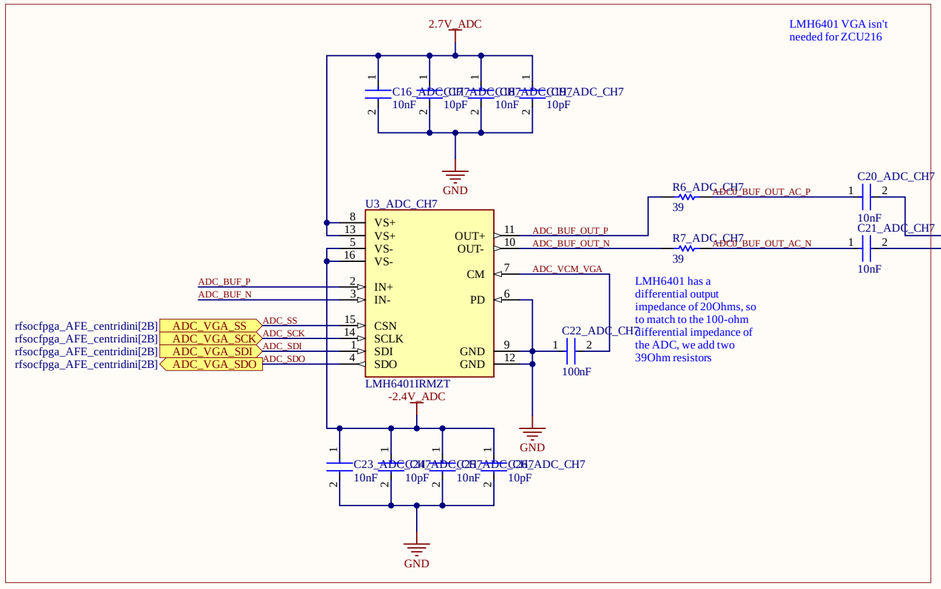

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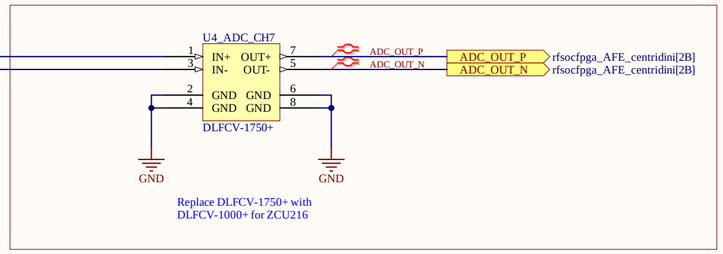

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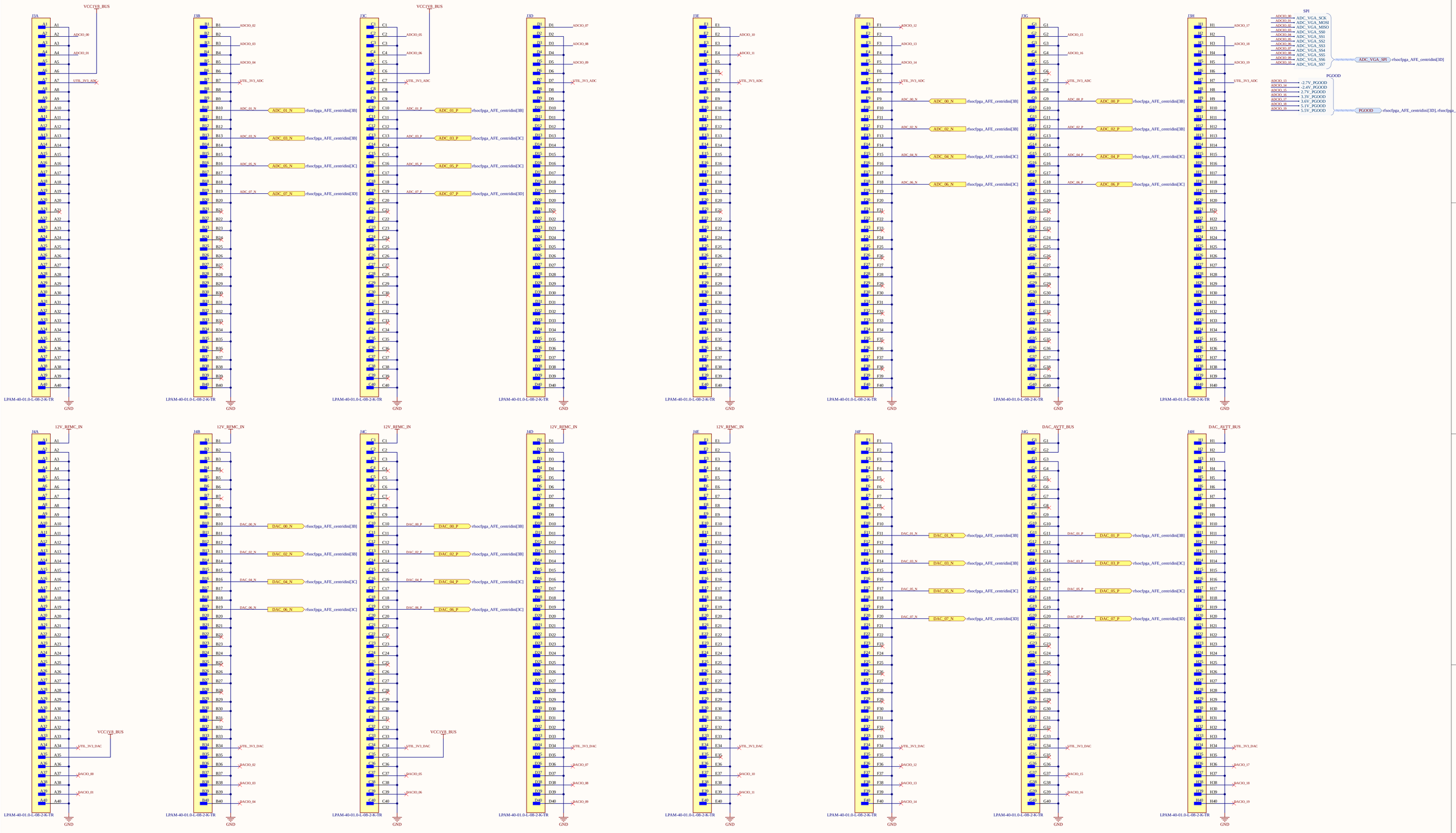
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J3E
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GND
VCC1V8_BUS
UTIL_3V3_ADC
ADCIO_00
ADCIO_01
ADCIO_02
ADCIO_03
ADCIO_04
ADCIO_05
ADCIO_06
ADCIO_07
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ADCIO_12
ADCIO_13
ADCIO_14
ADCIO_15
ADCIO_16
ADCIO_17
ADCIO_18
ADCIO_19
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ADC_03_N
ADC_05_N
ADC_07_N
ADC_01_P
ADC_03_P
ADC_05_P
ADC_07_P
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ADC_06_N
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ADC_04_P
ADC_06_P
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rfsocfpga_AFE_centridini[3C]
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ADC_VGA_MOSI
ADC_VGA_MISO
ADC_VGA_SS0
ADC_VGA_SS1
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ADC_VGA_SS5
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-2.7V_PGOOD
-2.4V_PGOOD
2.7V_PGOOD
3.3V_PGOOD
3.6V_PGOOD
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J4G
J4H
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VCC1V8_BUS
UTIL_3V3_DAC
DACIO_00
DACIO_01
DACIO_02
DACIO_03
DACIO_04
DACIO_05
DACIO_06
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File: rfsocfpga_AFE_connectors.SchDoc
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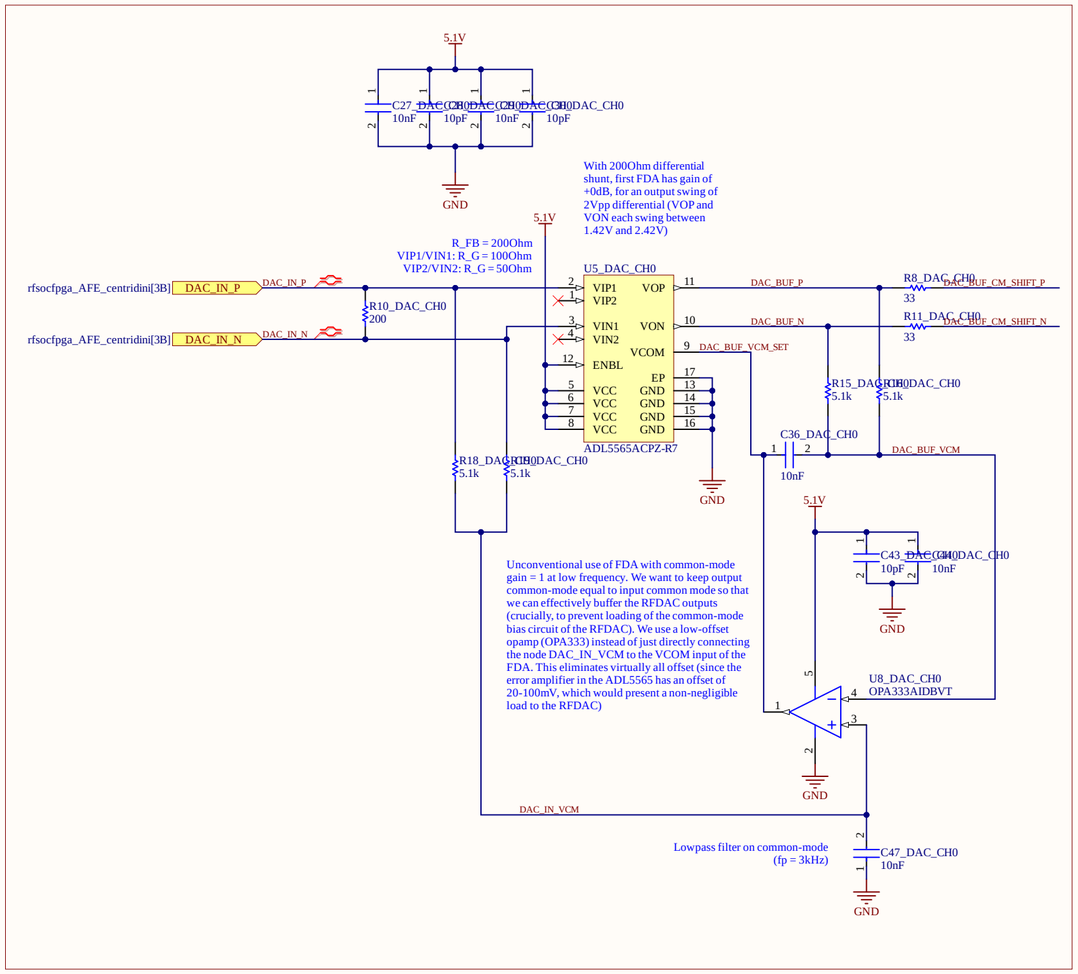

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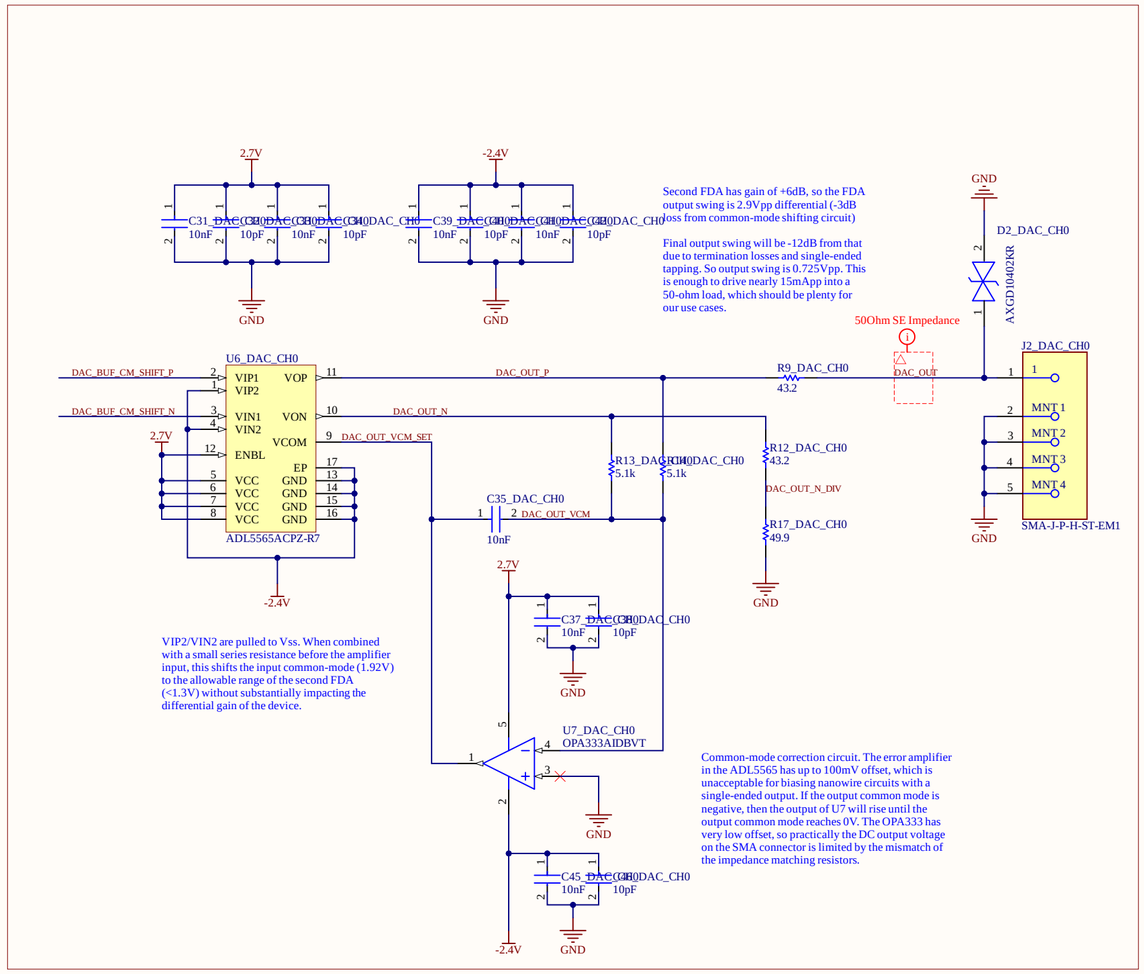

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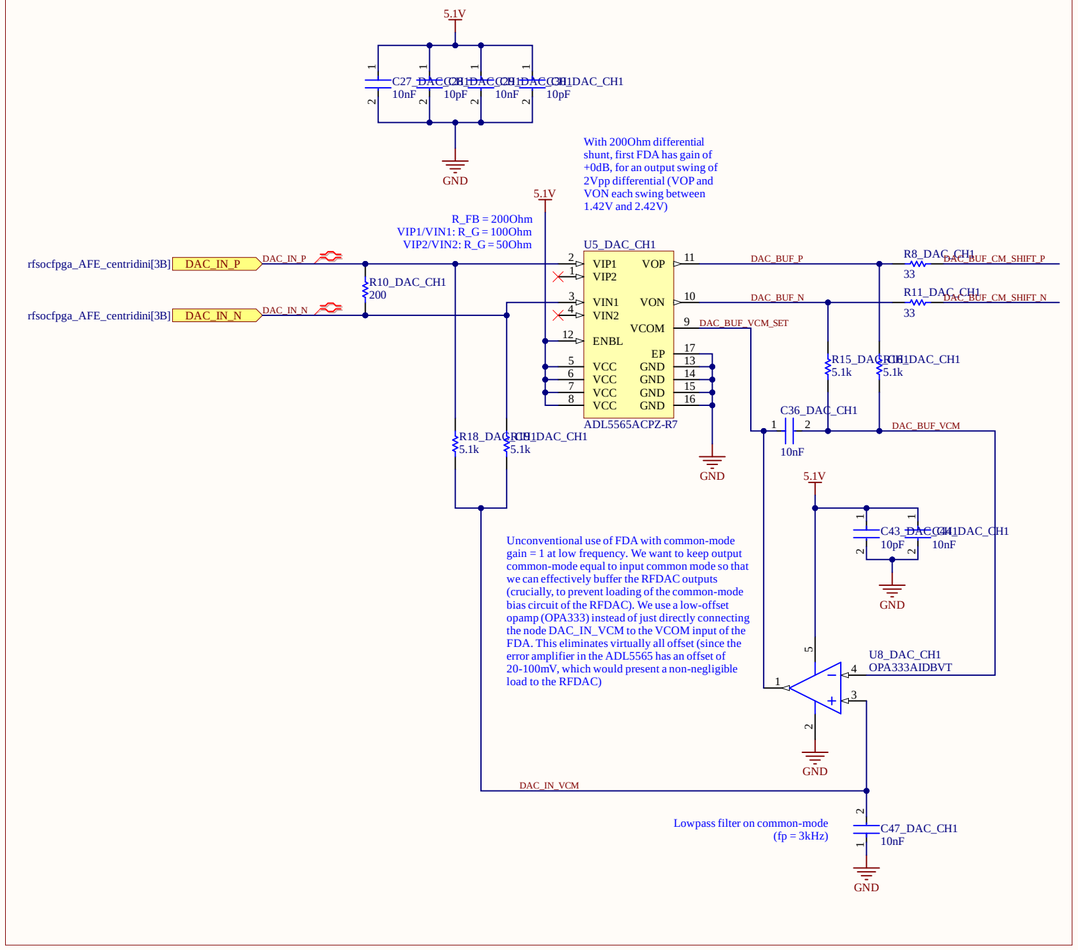

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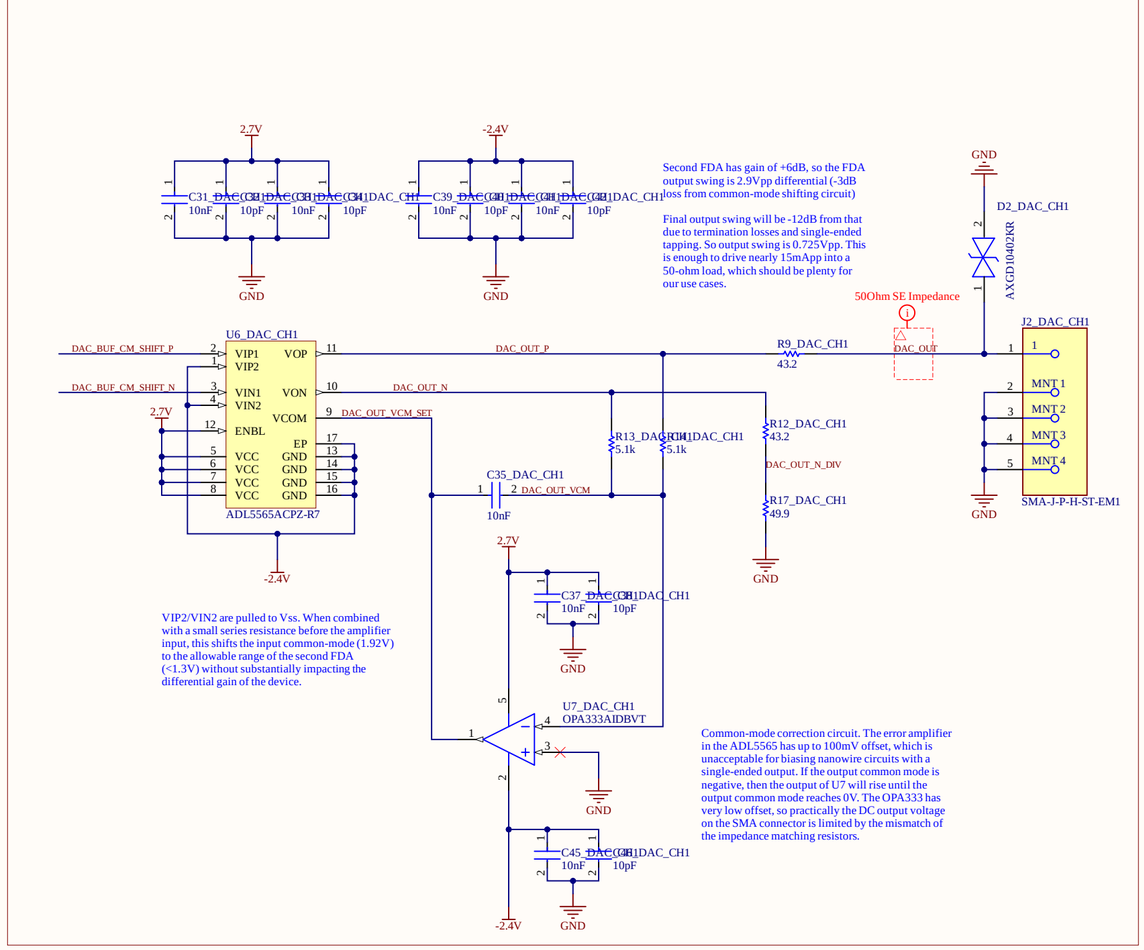

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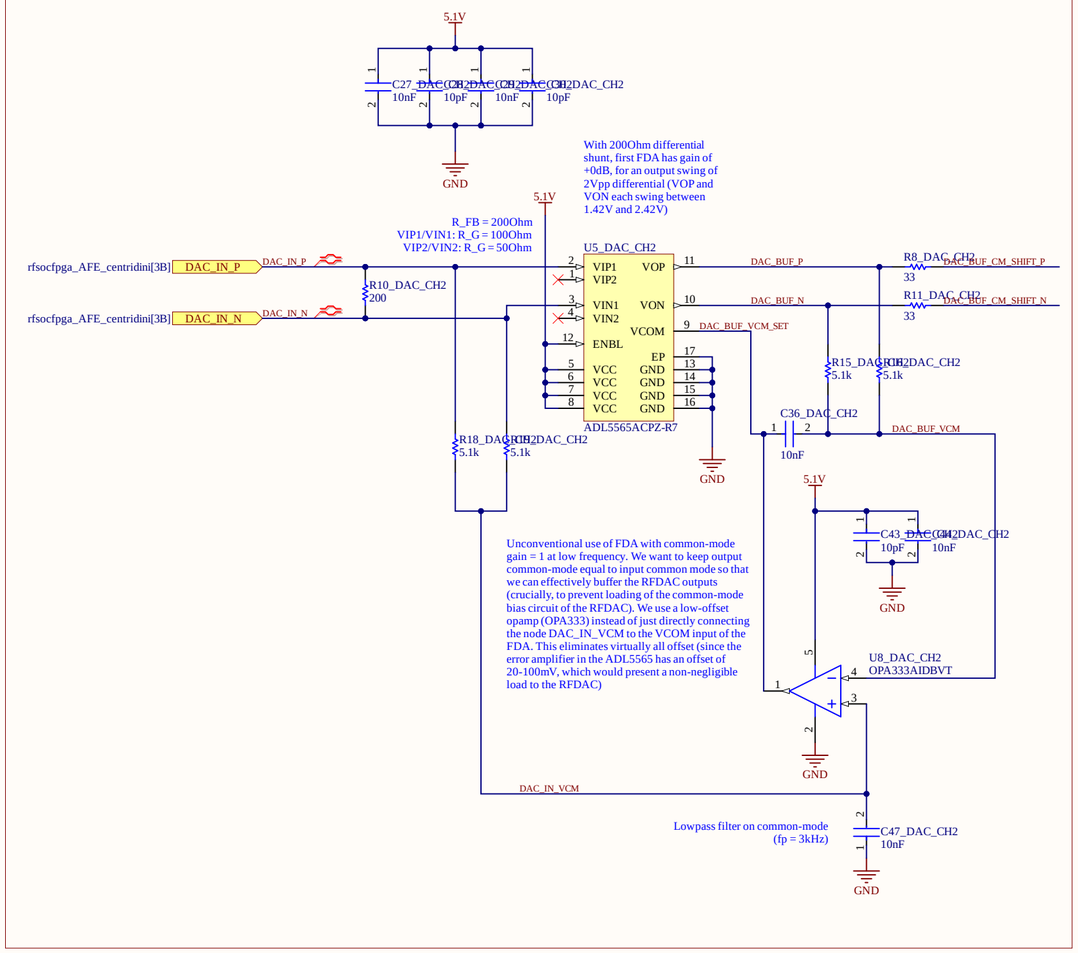

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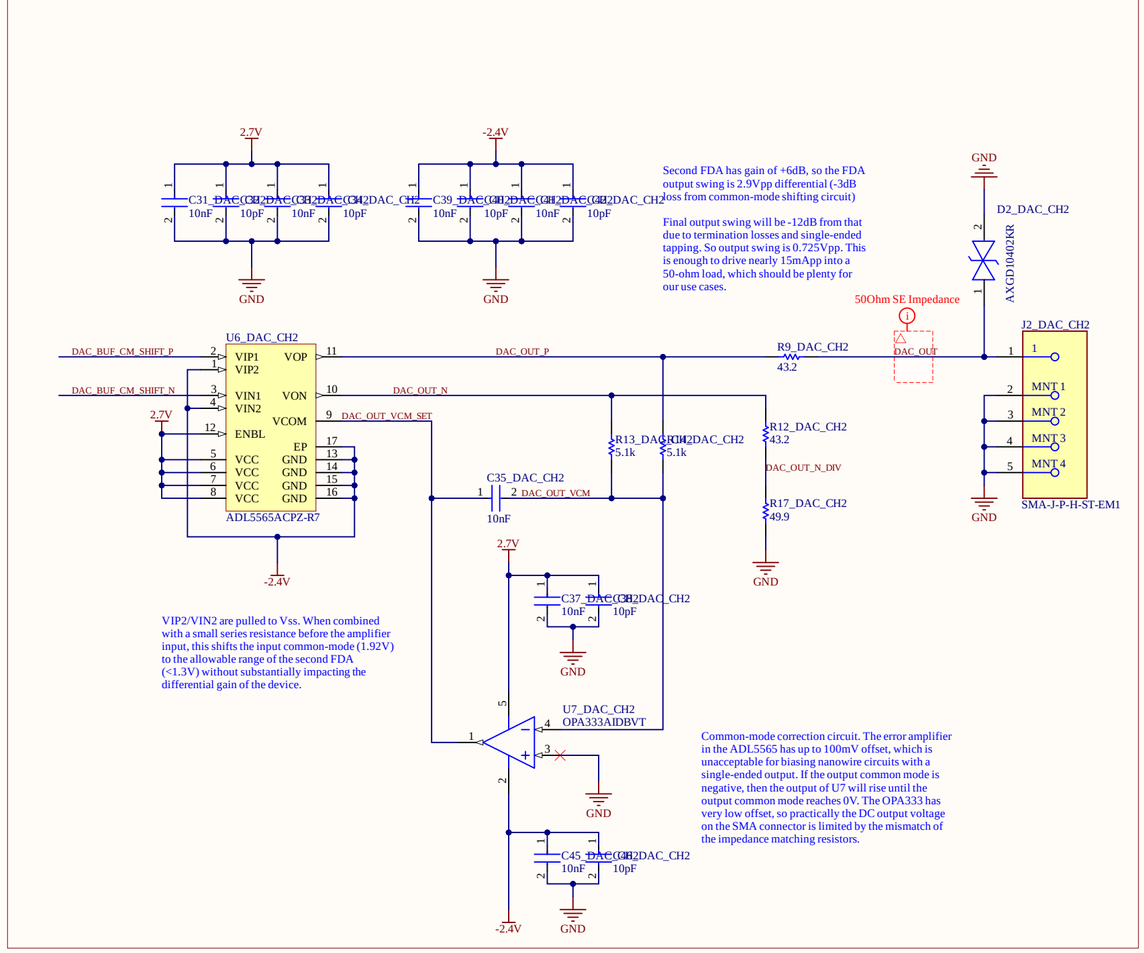

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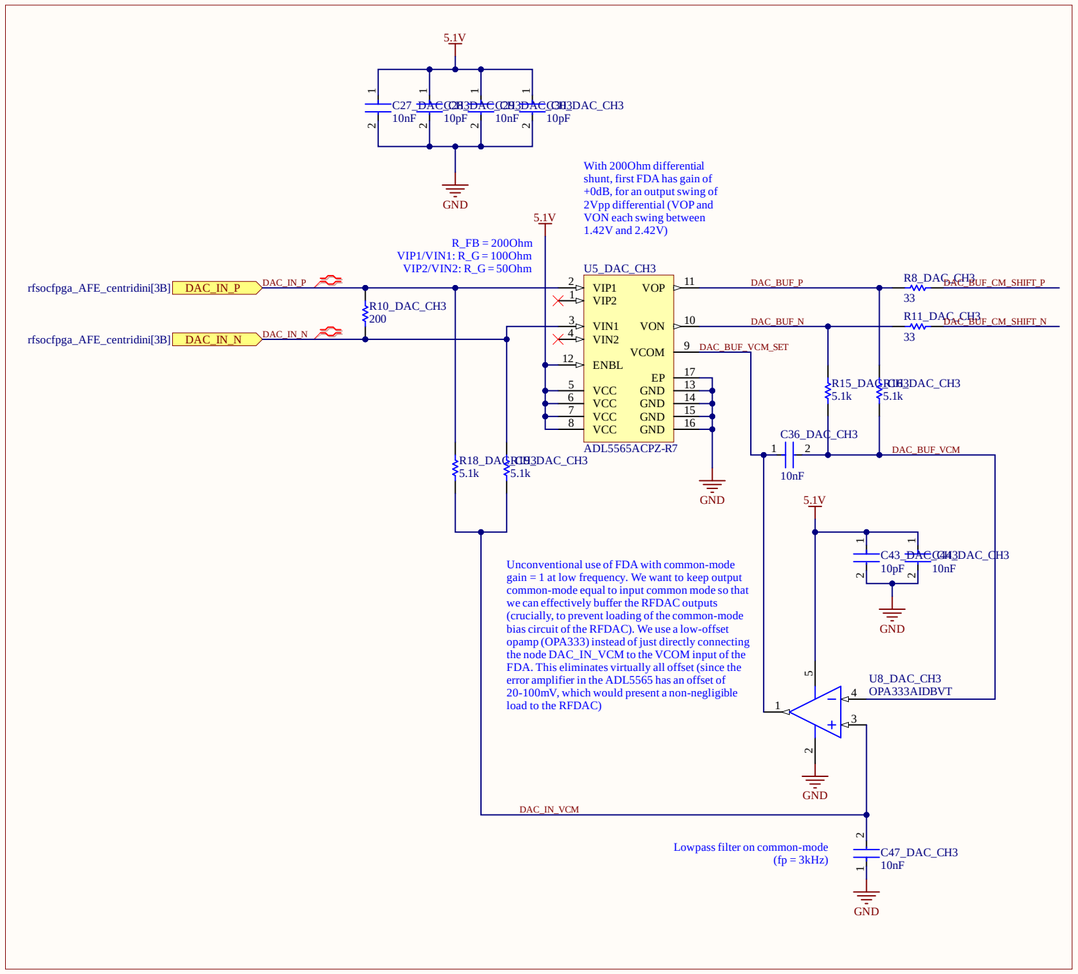

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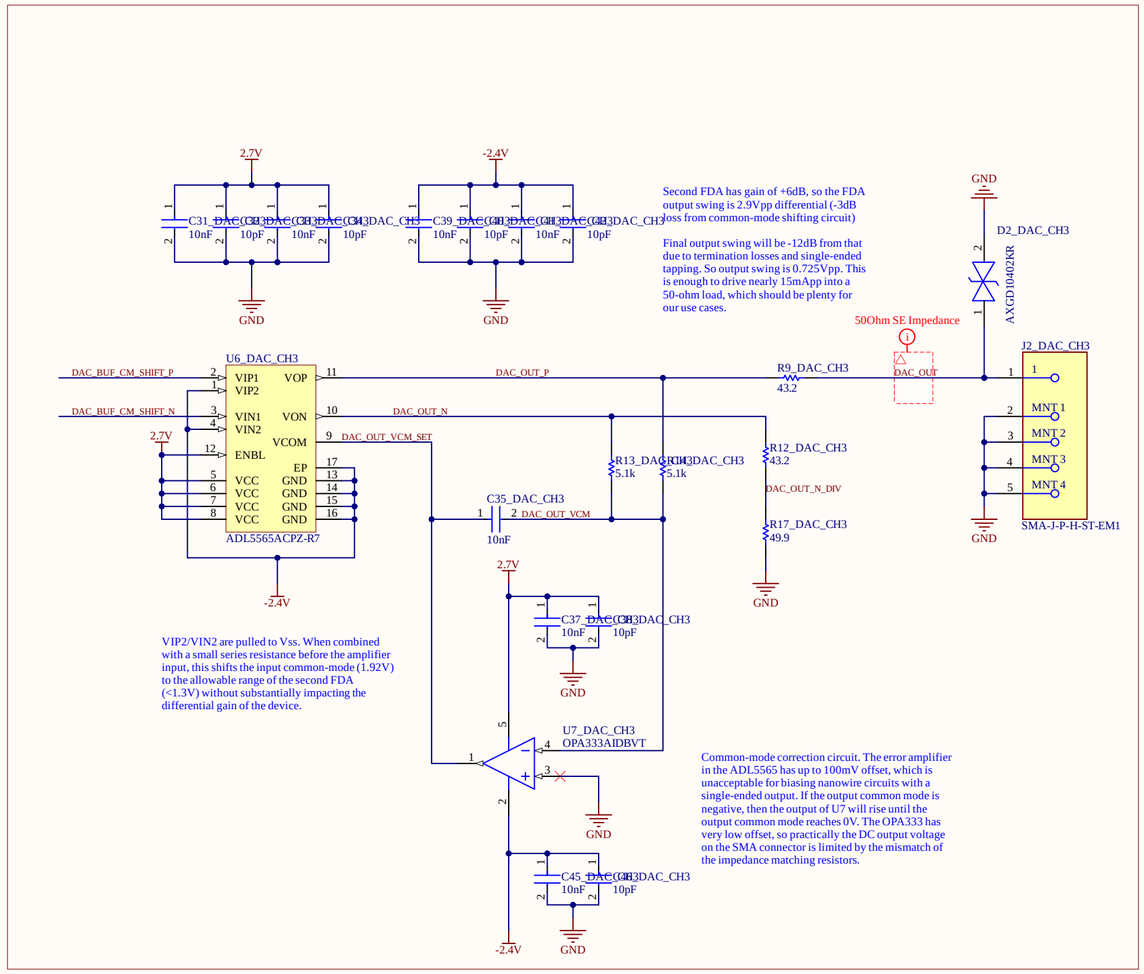

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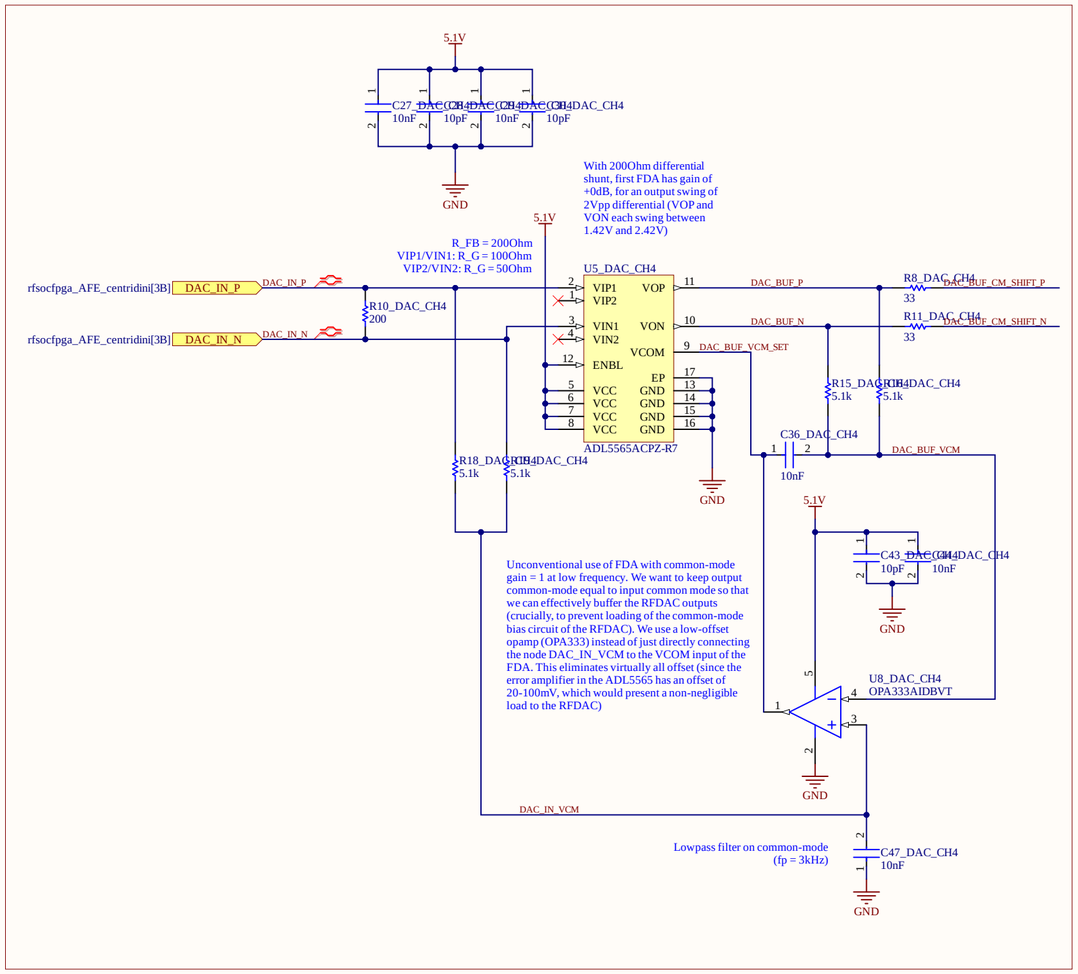
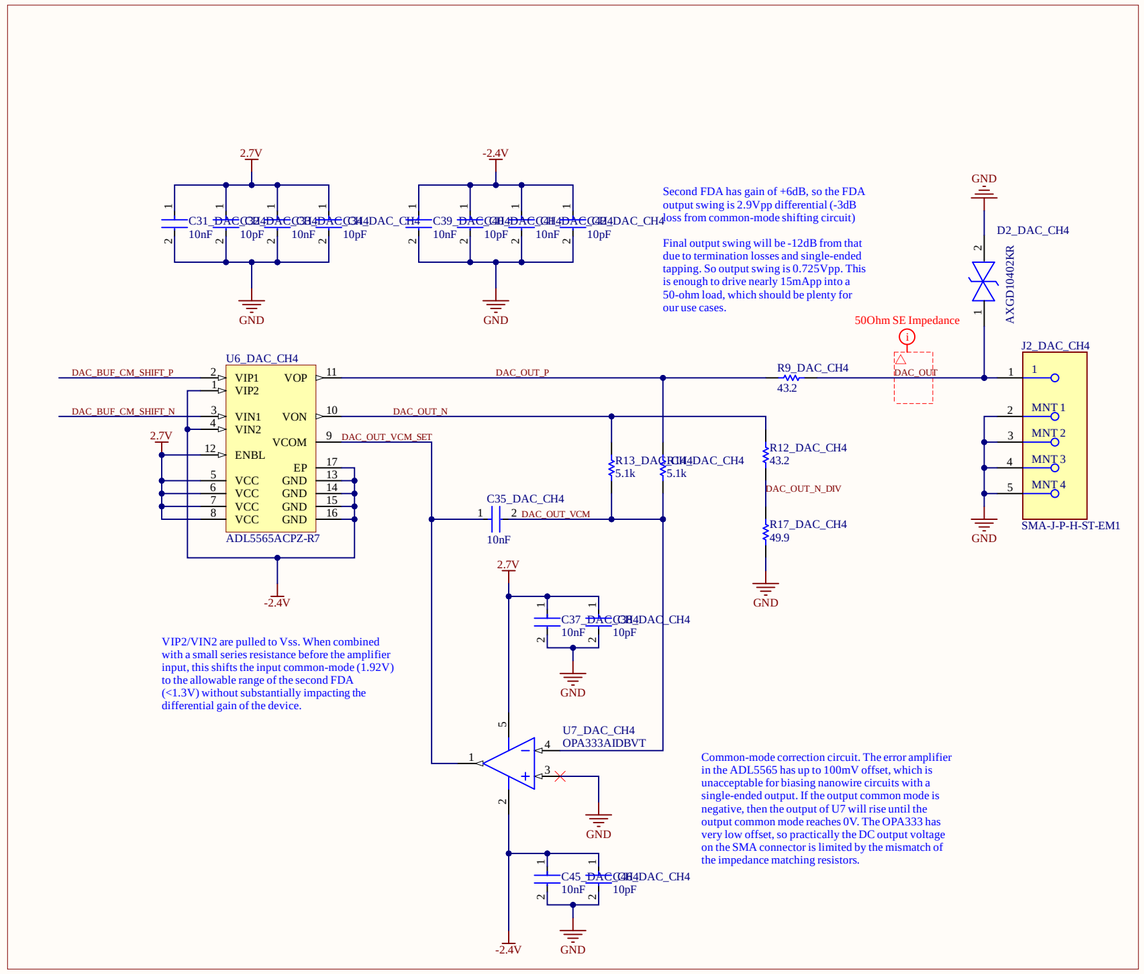


Output FDA to shift common mode to DC



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File: rfsocfpga_AFE_DAC_buffer.SchDoc		Drawn By:	

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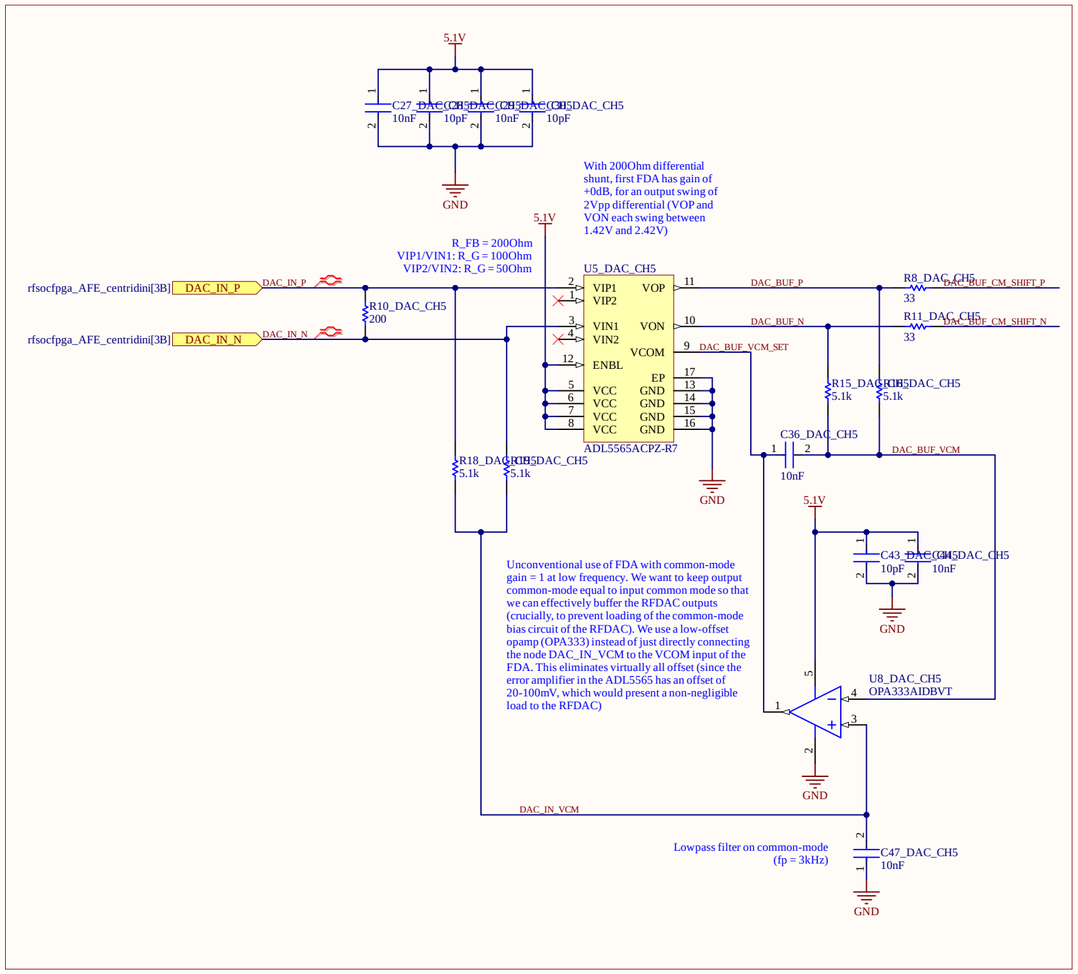

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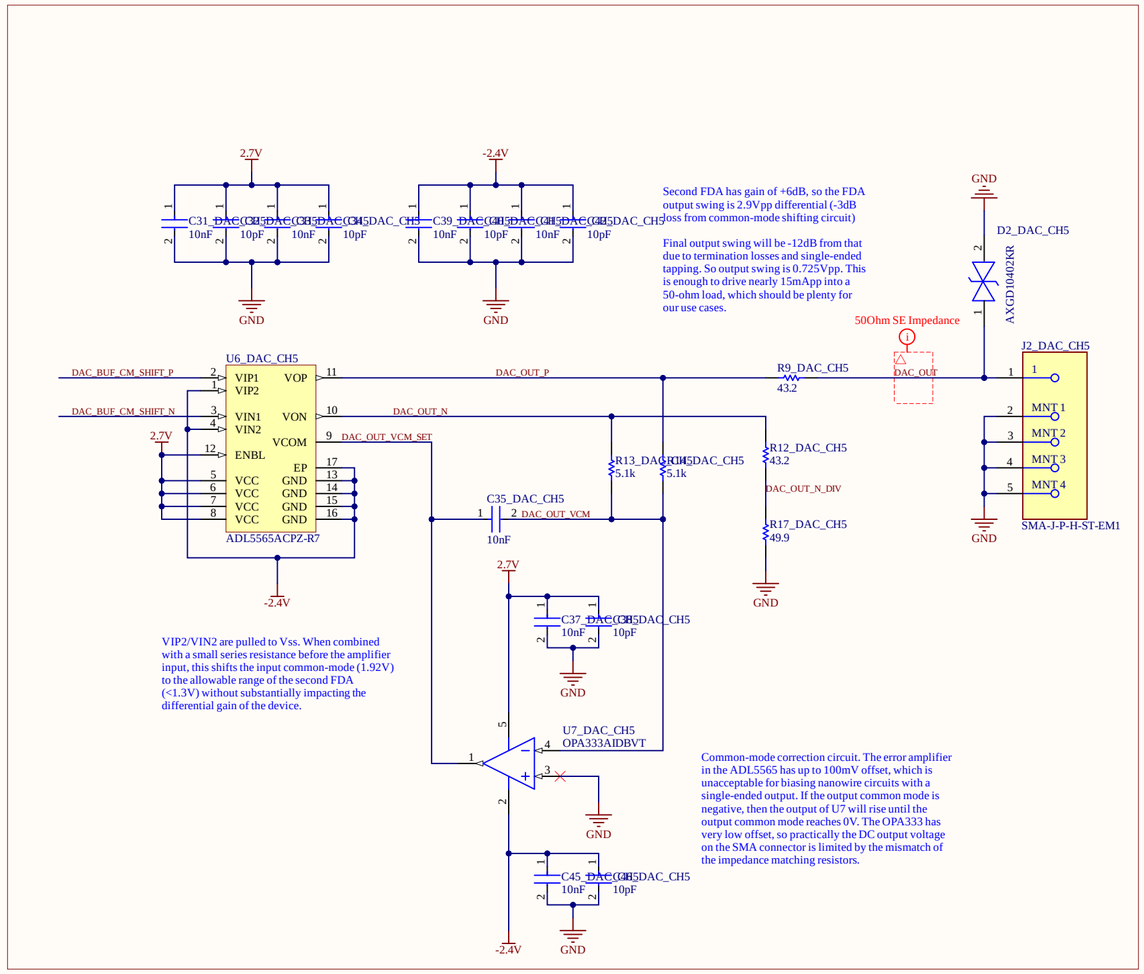

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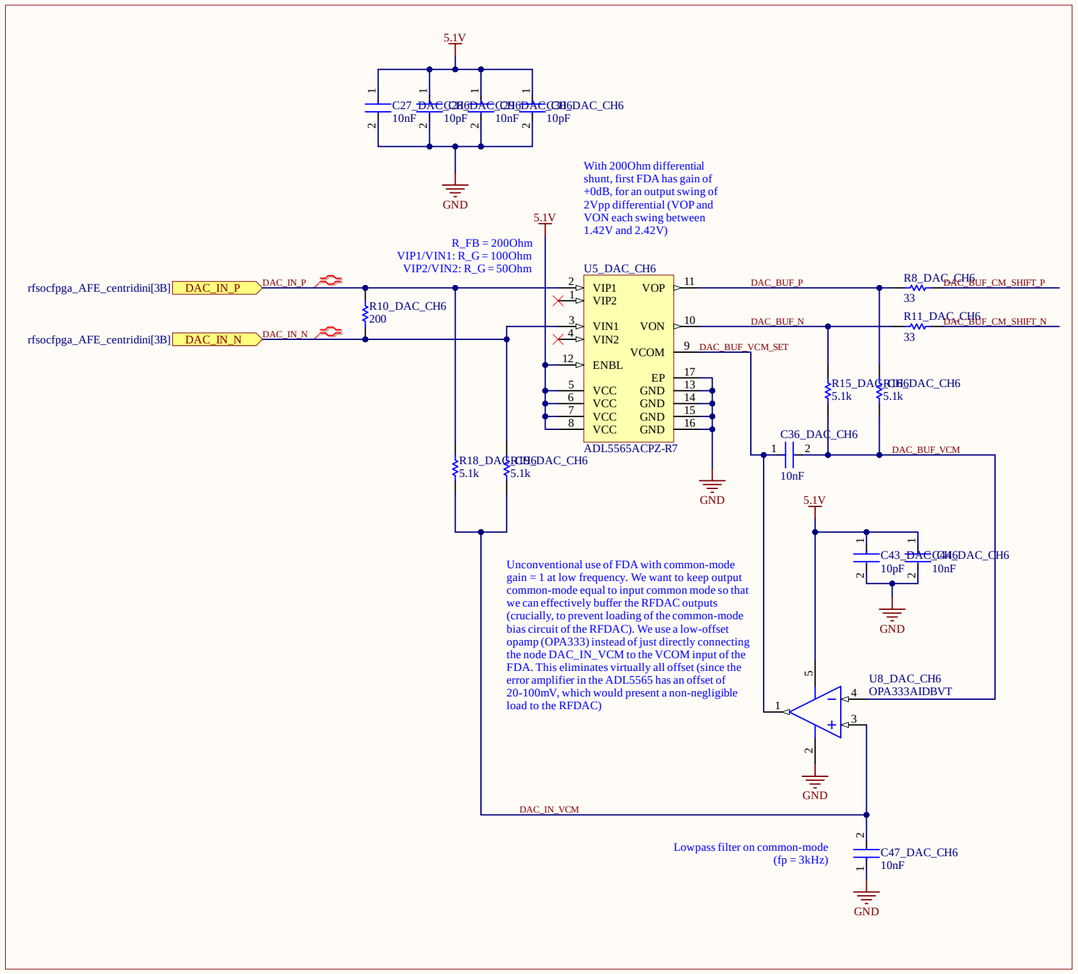

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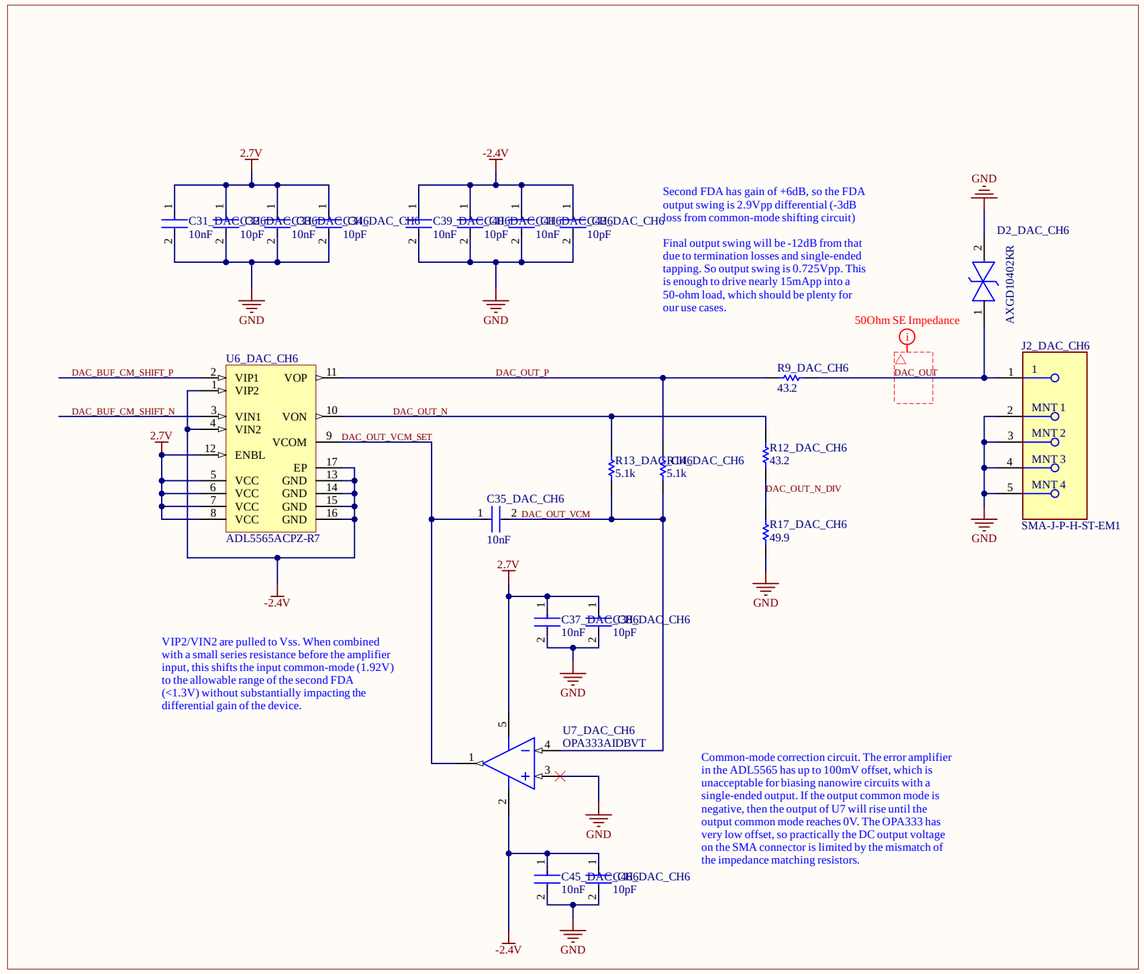

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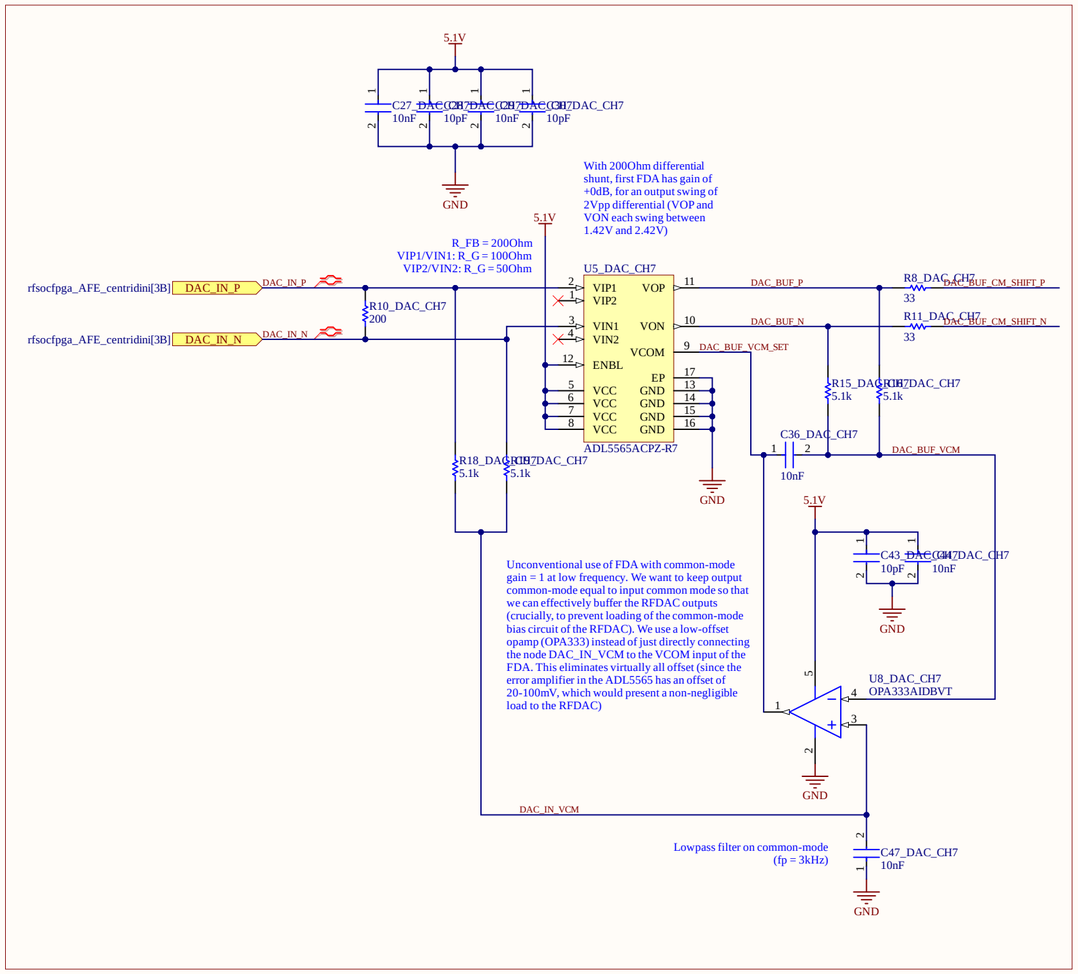
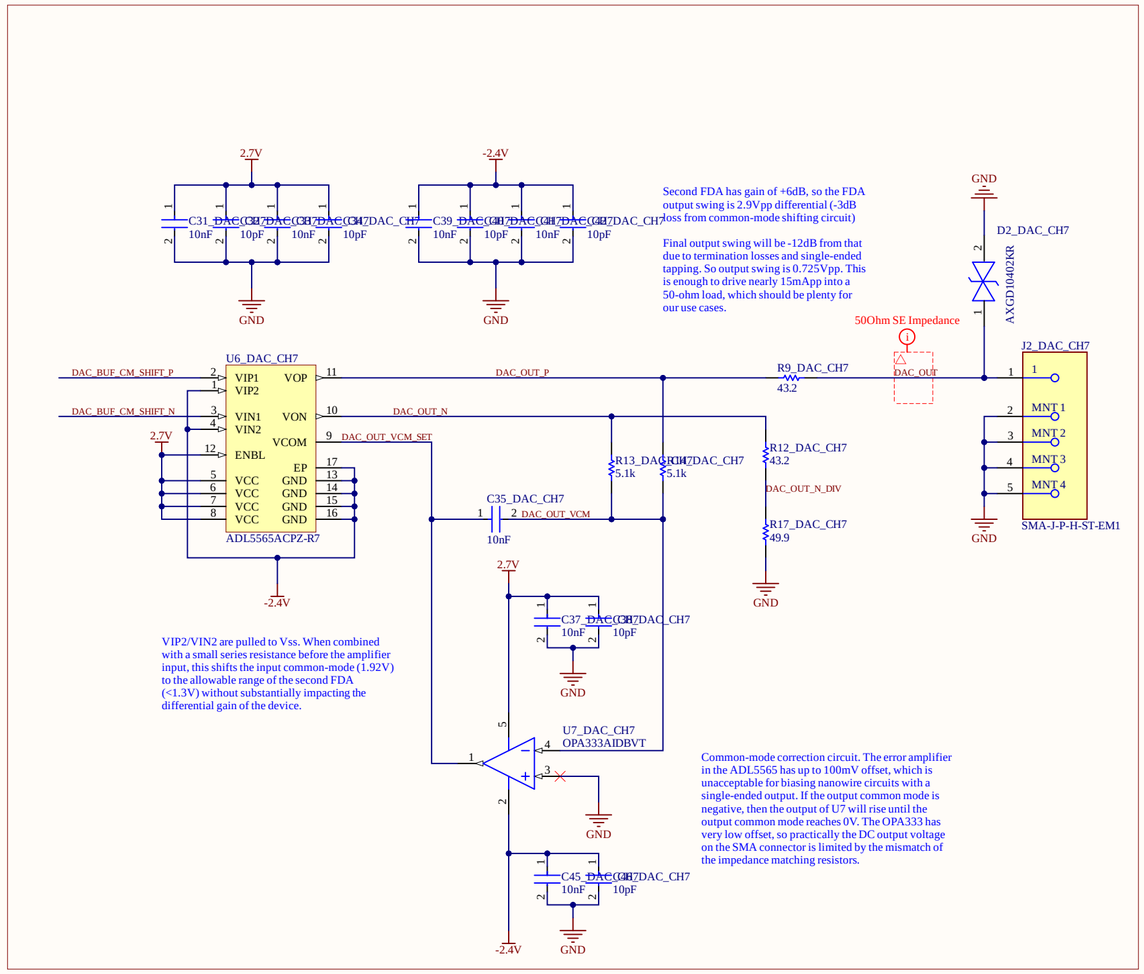


Output FDA to shift common mode to DC



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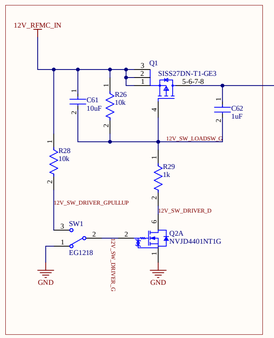
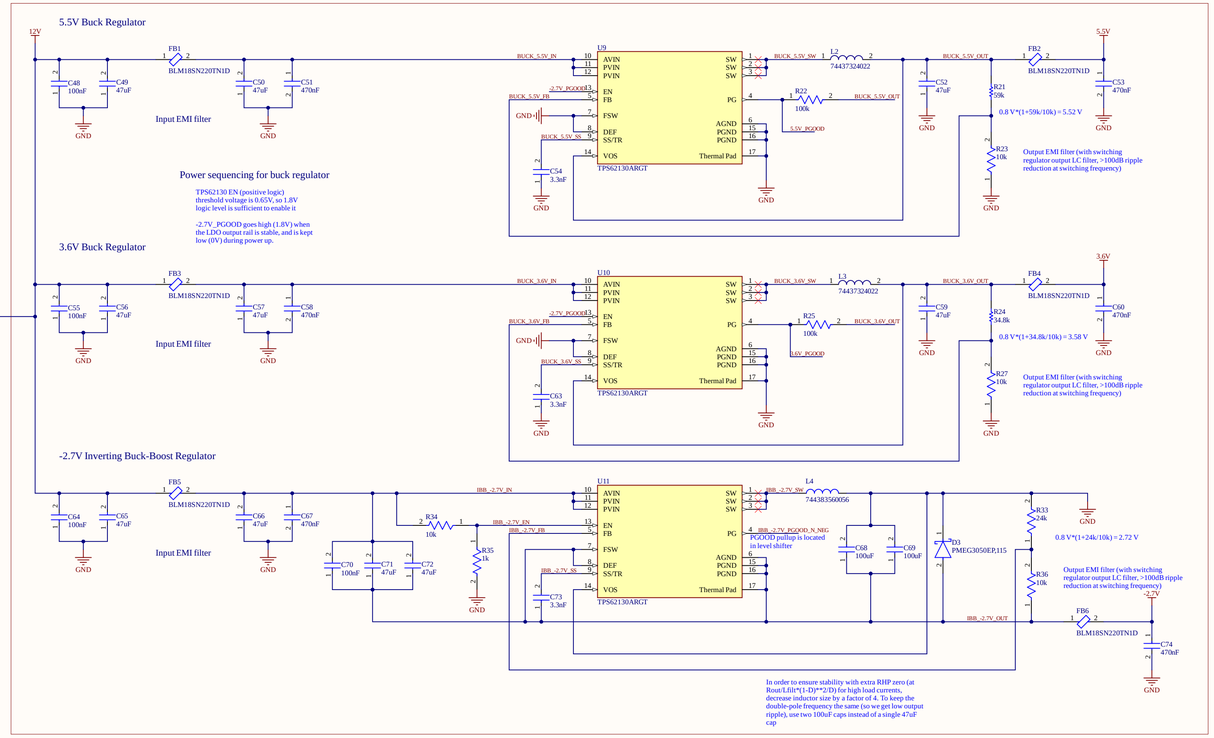
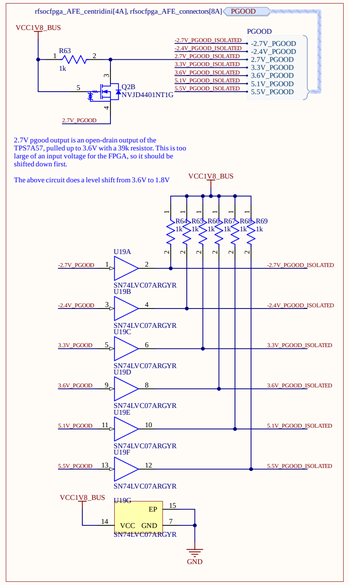
ON/OFF Switch



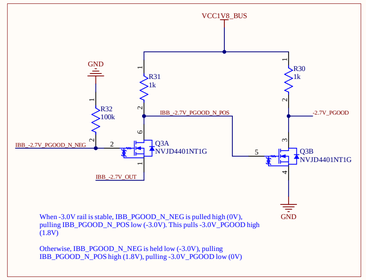
Switching Regulators



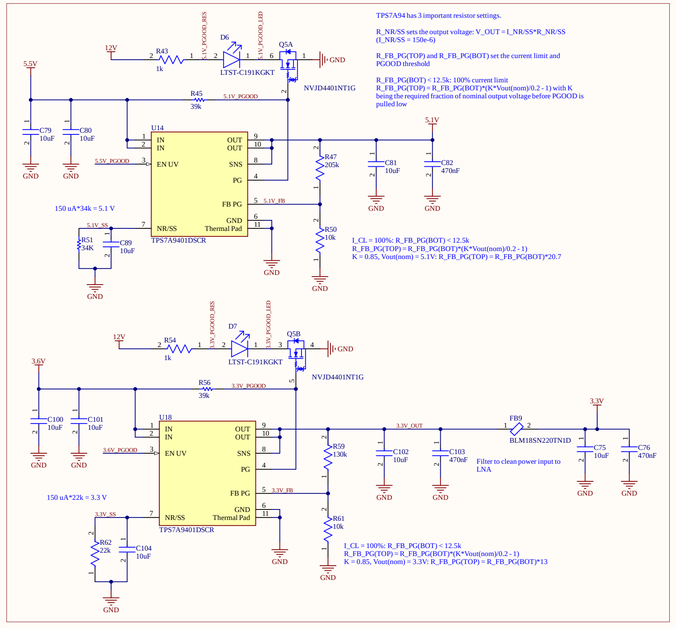
PGOOD BUFFER



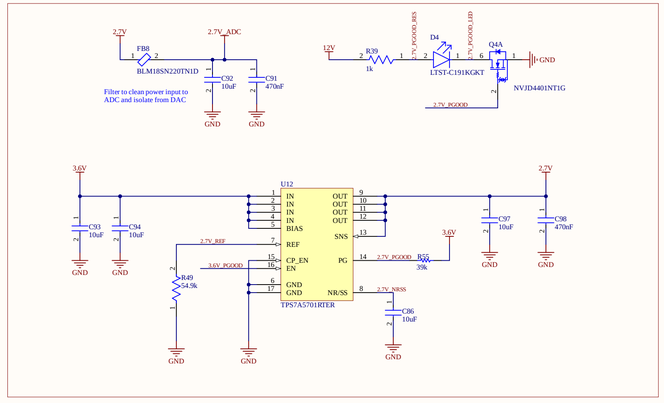
Level shifter for inverting regulator PGOOD



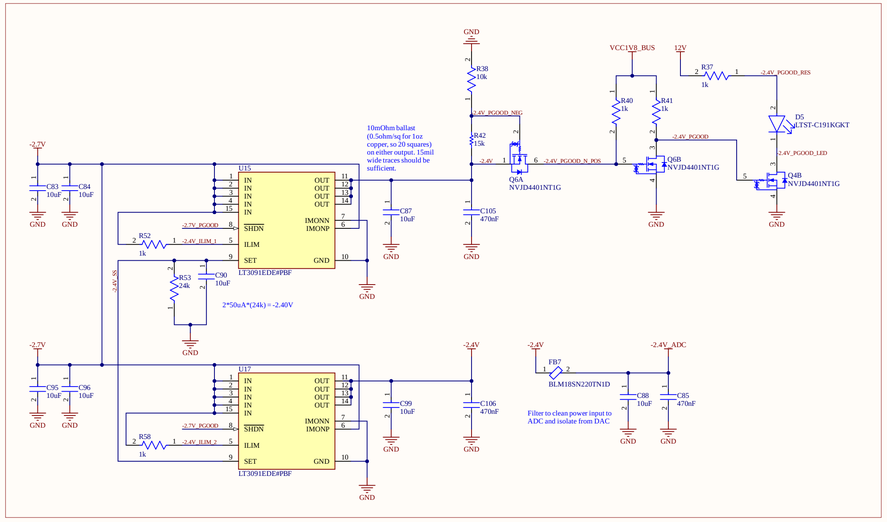
Positive Regulators (5.1V, 3.3V)



Positive Regulator (2.7V)



Negative Regulator (-2.4V, paralleled for higher current)



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