

ARM

Embedded Cross Trigger (TM900)

Errata Notice

ARM Embedded Cross Trigger TM900 Errata Notice

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General suggestion for additions and improvements are also welcome.

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1 ABOUT THIS DOCUMENT

1.1 Change control

1.1.1 Change history

This refers to the revision changes to the Errata document

Issue	Date	Change
1.0	9 th June 03	First Draft

1.2 Errata List

This table summarises the errata notices detailed

Release: Release revision number affected

Section: Section in the document where the issues are described

Status: The status conditions are:

Open: Issue is current

Closed: Issue resolved in subsequent release

Severity: The severity conditions are:

A **Low** level of severity indicates that there is no direct impact to the silicon implementation. Examples are:

- No change to RTL required
- Documentation change
- Test pattern change
- Synthesis Scripts change

A **Medium** level of severity indicates that there is a minor impact to the silicon implementation, which can be overcome by modifications detailed in the errata notes. Examples are:

- A specific sequence of events not adhered to.
- Ambiguous or unexpected operation of the device.

A **High** level of severity indicates that there is a major impact to the silicon implementation, which cannot be overcome without modifications detailed in the errata notes. Examples are:

- Non conformance to the device specification.

Affected: Deliverables impacted by the issue

Description: Issue concerning Errata

Release	Section	Status	Severity	Affected	Description
r0p0-00rel0	2.1	-	-	-	No known issues

2 TM900 RELEASE R0P0-00RELO - ERRATA DETAILS

2.1 No known Issues

2.1.1 Summary

2.1.2 Severity

2.1.3 Symptoms

2.1.4 Affected Items

2.1.5 Short term work around

2.1.6 Proposed Corrective action