

Release Note for Morello Instruction Emulator

2.3-533

Non-Confidential

Issue 533

Copyright © 2022–2023 Arm Limited (or its affiliates). 109492_2.3-533_533_en All rights reserved.



Release Note for Morello Instruction Emulator

Copyright © 2022–2023 Arm Limited (or its affiliates). All rights reserved.

Release information

Document history

Issue	Date	Confidentiality	Change
2068-01	28 July 2022	Non-Confidential	2.0-68 release
2170-01	27 October 2022	Non-Confidential	2.1-70 release
2274-01	28 February 2023	Non-Confidential	2.2-74 release
2.3-533	23 July 2023	Non-Confidential	2.3-533 release

Proprietary Notice

This document is protected by copyright and other related rights and the practice or implementation of the information contained in this document may be protected by one or more patents or pending patent applications. No part of this document may be reproduced in any form by any means without the express prior written permission of Arm. No license, express or implied, by estoppel or otherwise to any intellectual property rights is granted by this document unless specifically stated.

Your access to the information in this document is conditional upon your acceptance that you will not use or permit others to use the information for the purposes of determining whether implementations infringe any third party patents.

THIS DOCUMENT IS PROVIDED "AS IS". ARM PROVIDES NO REPRESENTATIONS AND NO WARRANTIES, EXPRESS, IMPLIED OR STATUTORY, INCLUDING, WITHOUT LIMITATION, THE IMPLIED WARRANTIES OF MERCHANTABILITY, SATISFACTORY QUALITY, NON-INFRINGEMENT OR FITNESS FOR A PARTICULAR PURPOSE WITH RESPECT TO THE DOCUMENT. For the avoidance of doubt, Arm makes no representation with respect to, and has undertaken no analysis to identify or understand the scope and content of, patents, copyrights, trade secrets, or other rights.

This document may include technical inaccuracies or typographical errors.

TO THE EXTENT NOT PROHIBITED BY LAW, IN NO EVENT WILL ARM BE LIABLE FOR ANY DAMAGES, INCLUDING WITHOUT LIMITATION ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES, HOWEVER CAUSED AND

REGARDLESS OF THE THEORY OF LIABILITY, ARISING OUT OF ANY USE OF THIS DOCUMENT, EVEN IF ARM HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

This document consists solely of commercial items. You shall be responsible for ensuring that any use, duplication or disclosure of this document complies fully with any relevant export laws and regulations to assure that this document or any portion thereof is not exported, directly or indirectly, in violation of such export laws. Use of the word "partner" in reference to Arm's customers is not intended to create or refer to any partnership relationship with any other company. Arm may make changes to this document at any time and without notice.

This document may be translated into other languages for convenience, and you agree that if there is any conflict between the English version of this document and any translation, the terms of the English version of the Agreement shall prevail.

The Arm corporate logo and words marked with ® or ™ are registered trademarks or trademarks of Arm Limited (or its affiliates) in the US and/or elsewhere. All rights reserved. Other brands and names mentioned in this document may be the trademarks of their respective owners. Please follow Arm's trademark usage guidelines at https://www.arm.com/company/policies/trademarks.

Copyright © 2022-2023 Arm Limited (or its affiliates). All rights reserved.

Arm Limited. Company 02557590 registered in England.

110 Fulbourn Road, Cambridge, England CB1 9NJ.

(LES-PRE-20349|version 21.0)

Confidentiality Status

This document is Non-Confidential. The right to use, copy and disclose this document may be subject to license restrictions in accordance with the terms of the agreement entered into by Arm and the party that Arm delivered this document to.

Unrestricted Access is an Arm internal classification.

Product Status

The information in this document is Final, that is for a developed product.

Feedback

Arm welcomes feedback on this product and its documentation. To provide feedback on the product, create a ticket on https://support.developer.arm.com

To provide feedback on the document, fill the following survey: https://developer.arm.com/documentation-feedback-survey.

Inclusive language commitment

Arm values inclusive communities. Arm recognizes that we and our industry have used language that can be offensive. Arm strives to lead the industry and create change.

We believe that this document contains no offensive language. To report offensive language in this document, email terms@arm.com.

Contents

1. Release Note for Morello Instruction Emulator 2.3-533

Version 2.3-533

Target platform

Linux AArch64

Date published

23 June 2023

MD5

12c3132b9b3b418c68f59e16c4f272e7

SHA-256

231b17e574c4f09770c47cfa68791742366eac14c6987d2cd0ec139477f5cae7

Description

The Morello Instruction Emulator (Morello IE) is a tool for software developers and researchers who are interested in the Morello architecture. It allows running userspace Morello applications on AArch64 Linux systems in a non-Morello environment and provides a number of analysis and debugging tools. It can be used to help with porting software to Morello, to experiment with capabilities and Morello compartments as well as measure and analyse performance of Morello applications.

System requirements

System requirements are as follows:

- Arm v8.2 64-bit hardware (v8.0 supported with some limitations).
- Existing 64-bit userspace GNU/Linux environment (for example, Debian 10 or Ubuntu 18.04).
- The host system must have Glibc 2.28 or above.

Install

To install, run the downloaded installer in a Bash shell. Use --help option for the usage details. More information can be found in:

- https://developer.arm.com/documentation/102270
- https://developer.arm.com/morello
- https://community.arm.com/support-forums/f/morello-forum

Feedback

Feedback and questions can be sent to morello-emulator-support (at) arm.com.

Highlights

Highlights in this release:

- Support for Armv8.0 devices
- Better support for running apps based on Morello Glibc
- Debugger and tracer improvements

New functionality

New functionality in this release:

- Added LSE atomics emulation to allow running on devices that only provide Armv8.0 architecture features.
- Tracer can now save instruction and memory traces to a file.
- Tracer can be instructed to trace only specific thread.
- Tracer will trace the entire application by default (control of the scope of tracing works as before).
- New debugger commands: list stack frames and list memory reservations.
- Debugger can now print selected registers instead of showing all of them to help focus on what's important.
- Debugger can display the PSTATE and CCTLR ELO registers.
- Capability permission encoding now uses I for Branch SEALED PAIR and E for EXECUTIVE.
- Rebased on the recent DynamoRIO version 9.93 for improvements in the instruction decoder.
- Default cache model parameters are now aligned with the Morello board configuration.

Fixed in this release

Fixed in this release:

- Fixed segmentation fault when running multi-threaded application based on Morello Glibc
- Fixed use of DDC for checking memory accesses in A64 mode.
- Fixed not raising SIGBUS when LSB is set in the 64-bit branch target address in A64 mode.
- Fixed counting load / store register and load / store pair instructions in tracer.