



**ARM CoreSight™  
Serial Wire Debug (TM091)  
Errata Notice**

This document contains all errata known at the date of issue in supported releases up to and including r0p0 of Coresight Serial Wire debug

**Proprietary notice**

Words and logos marked with ® or ™ are registered trademarks or trademarks of ARM Limited in the EU and other countries, except as otherwise stated below in this proprietary notice. Other brands and names mentioned herein may be the trademarks of their respective owners.

Neither the whole nor any part of the information contained in, or the product described in, this document may be adapted or reproduced in any material form except with the prior written permission of the copyright holder.

The product described in this document is subject to continuous developments and improvements. All particulars of the product and its use contained in this document are given by ARM Limited in good faith. However, all warranties implied or expressed, including but not limited to implied warranties of merchantability, or fitness for purpose, are excluded.

This document is intended only to assist the reader in the use of the product. ARM Limited shall not be liable for any loss or damage arising from the use of any information in this document, or any error or omission in such information, or any incorrect use of the product.

**Document confidentiality status**

This document is Non Confidential.

**Web address**

<http://www.arm.com/>

**Feedback on the product**

If you have any comments or suggestions about this product, contact your supplier giving:

- The product name
- A concise explanation of your comments.

**Feedback on this document**

If you have any comments on about this document, please send email to <mailto:errata@arm.com> giving:

- The document title
- The documents number
- The page number(s) to which your comments refer
- A concise explanation of your comments

General suggestion for additions and improvements are also welcome.

## Contents

INTRODUCTION	5
ERRATA SUMMARY TABLE	7
ERRATA - CATEGORY 1	8
There are no Errata in this Category	8
ERRATA - CATEGORY 2	9
There are no Errata in this Category	9
ERRATA - CATEGORY 3	10
There are no Errata in this Category	10
ERRATA - DOCUMENTATION	11
<b>400028:</b> ARM DDI0314C3a CoreSight Technical Reference Manual r1p0: incorrect APBAP revision number and ID Register Value	11
ERRATA – DRIVER SOFTWARE	13
There are no Errata in this Category	13

## Introduction

### Scope

This document describes errata categorised by level of severity. Each description includes:

- the current status of the defect
- where the implementation deviates from the specification and the conditions under which erroneous behavior occurs
- the implications of the erratum with respect to typical applications
- the application and limitations of a 'work-around' where possible

### Categorisation of Errata

Errata recorded in this document are split into three levels of severity:

- |            |   |
|------------|---|
| Category 1 | Behavior that is impossible to work around and that severely restricts the use of the product in all, or the majority of applications, rendering the device unusable.   |
| Category 2 | Behavior that contravenes the specified behavior and that might limit or severely impair the intended use of specified features, but does not render the product unusable in all or the majority of applications. |
| Category 3 | Behavior that was not the originally intended behavior but should not cause any problems in applications.   |

## Change Control

### 01 Aug 2006: Changes in Document v1

Page	Status	ID	Cat	Summary
11	New	400028	Doc	ARM DDI0314C3a CoreSight Technical Reference Manual r1p0: incorrect APBAP revision number and ID Register Value

## Errata Summary Table

The errata associated with this product affect product versions as below.

A cell shown thus **X** indicates that the defect affects the revision shown at the top of that column.

ID	Cat	Summary of Erratum	r0p0
400028	Doc	ARM DDI0314C3a CoreSight Technical Reference Manual r1p0: incorrect APBAP revision number and ID Register Value	X

## **Errata - Category 1**

**There are no Errata in this Category**



## **Errata - Category 2**

**There are no Errata in this Category**

## **Errata - Category 3**

**There are no Errata in this Category**

## Errata - Documentation

### **400028: ARM DDI0314C3a CoreSight Technical Reference Manual r1p0: incorrect APBAP revision number and ID Register Value**

#### **Status**

Affects: product Coresight Serial Wire debug.

Fault status: Doc, Present in: r0p0, Open. New in this document.

#### **Description**

The APB-AP revision number in table 1-1 reads r0p0, it should read r0p1.

The APB-AP Identification Register (IDR, 0xFC) value on table 3-37 reads 0x04770002, when it should read 0x14770002 to indicate the correct revision number of the block (r0p1).

#### **Implications**

Tools reading the APB-AP Identification Register should be aware of its correct value. No other functionality impact.

#### **Workaround**

None



## **Errata – Driver Software**

**There are no Errata in this Category**