

Arm Corstone Package Contents

Arm Corstone provides everything needed to build IoT SoCs faster and more securely, with the right architecture choice, integration, and verification.

Component	Corstone-101	Corstone-102	Corstone-201	Corstone-300	Corstone-310	Corstone-500	Corstone-1000
Corstone Subsystems							
SSE-050 Subsystem (Cortex-M3 based)	✓	✓	✓				
SSE-123 Subsystem (Cortex-M23 based)		✓	✓				
SSE-200 Subsystem (dual Cortex-M33 based)			✓				
SSE-300 Subsystem (Cortex-M55 based)				✓			
SSE-310 Subsystem (Cortex-M85 based)					✓		
SSE-500 Subsystem (Cortex-A5 based)						✓	
SSE-710 Subsystem (Cortex-A32, Cortex-A35 or Cortex-A53 and Cortex-M based)							✓
System IP							
CM0SDK and CMSDK (common IP blocks)	✓	✓	✓	✓	✓	✓	✓
True Random Number Generator (TRNG)	✓	✓	✓	✓		✓	
Real-Time Clock (RTC)	✓	✓	✓	✓	✓	✓	✓
CoreLink AHB Flash Cache	✓	✓	✓	✓	✓		✓
CoreLink GFC-100 (single port)	✓	✓	✓	✓	✓		✓
CoreLink GFC-200 (dual port)		✓	✓	✓	✓		✓
CoreLink SIE-200 (AHB5 TrustZone System IP)		✓	✓	✓	✓		✓
CoreLink SIE-300 (AXI5 TrustZone System IP)				✓	✓		✓
CoreLink PCK-600 (power control)		✓	✓	✓	✓		✓
CoreSight SoC-400M (standard debug)			✓				

Component	Corstone-101	Corstone-102	Corstone-201	Corstone-300	Corstone-310	Corstone-500	Corstone-1000
CoreSight SoC-400 (standard debug)						✓	
CoreSight SoC-600M (enhanced debug)					✓		
CoreSight SoC-600 (enhanced debug)							✓
CoreSight SDC-600 (authenticated debug)			✓		✓		✓
CoreSight STM-500 (system trace macrocell)							✓
CoreLink ADB-400 (AMBA domain bridge)				✓	✓		
CoreLink NIC-400-Lite (network interconnect)				✓			
CoreLink NIC-400 (network interconnect)					✓	✓	
CoreLink NIC-450 (network interconnect)							✓
CoreLink XHB-500 (AHB5 and AXI bridging IP)				✓	✓		✓
CoreLink GIC-400 (interrupt controller)							✓
IntMemAxi (internal memory interface)						✓	✓
PrimeCell UART						✓	✓

For more information, contact your Arm account manager today or explore the Corstone packages in more detail here: <https://developer.arm.com/ip-products/subsystem/corstone>