

ARM PrimeCell

Core Identification Module (PL321)

Errata Notice

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General suggestion for additions and improvements are also welcome.

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INTRODUCTION

Scope

This document describes errata categorised by level of severity. Each description includes:

- where the implementation deviates from the specification
- the conditions under which erroneous behaviour occurs
- the implications of the erratum with respect to typical applications
- the application and limitations of a 'work-around' where possible

Categorisation of Errata

Errata recorded in this document are split into three levels of severity:

- Category 1** Features which are impossible to work around and severely restricts the use of the product in all or the majority of applications rendering the device unusable.
- Category 2** Features which contravene the specified behaviour and may limit or severely impair the intended use of specified features but does not render the product unusable in all or the majority of applications.
- Category 3** Features that were not the originally intended behaviour but should not cause any problems in applications.

Change Control

The following Errata have been updated in this release

Status	Identifier	Category	Summary
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ERRATA SUMMARY TABLE

The errata associated with this product affect product versions as follows:

No Errata are currently documented for this product

ERRATA - CATEGORY 1

No Errata in this category

ERRATA - CATEGORY 2

No Errata in this category

ERRATA - CATEGORY 3

No Errata in this category