



# Qiushuang Tian

## Full stack software developer

 <https://github.com/qtian13>

 <https://qtian13.github.io/react-portfolio>

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## Summary

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- Full stack software developer eager to apply problem-solving techniques learned. Practiced teamwork and made day to day task assignment as techlead on three projects consisting of at least two peers.
- 3.5 years work experience as a hardware developer at Oracle.

## Technical Skills

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- Languages: JavaScript, HTML, CSS, Java, Python, Verilog, System Verilog
- Tools: Node.js, Express.js, MySQL, MongoDB, React.js, Handlebars.js, REST APIs
- Soft Skills: teamwork, communication, problem-solving, project management

## Projects

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### Space Escape: Model-View-Controller (MVC) application of a story based game

Sep 2021

-  <https://github.com/qtian13/space-escape>  <https://space-escape.herokuapp.com/>

- Build MySQL database with Sequelize
- Developed backend API routes for data CRUD
- Implement mini game Asteroids: Player controls the ship to avoid or hit moving asteroids
- Implement mini game Gold Collection: Player controls character to collect golds generated randomly
- Used: JavaScript, Node.js, Express.js, MySQL, Sequelize, React.js, CSS, REST APIs, JWT (Jason Web Tokens), Howler.js, Chart.js, Heroku

### CLOVE: MVC application of an online groceries market

Aug 2021

-  <https://github.com/qtian13/CLOVE>  <https://still-reef-24172.herokuapp.com/>

- Build MySQL database with Sequelize
- Build features for user to search products, send messages and rate other users
- Build profile page for displaying user's picture, products for sale and other information
- Create RESTful API routes and HTML routes for data CRUD and apply Session Storage to check user's authentication
- Used: JavaScript, Node.js, Express.js, MySQL, Sequelize, Handlebars/HTML, CSS, REST APIs, Heroku, Cloudinary.js

## Experience

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### Oracle, Santa Clara, California - Hardware developer

May 2013 - Oct 2016

- Pre-silicon verification and Post-silicon Validation of Siete (SPARC M8)
- Build VMM testbench blocks to verify RTL design of SPARC Core
- Create test plan, write checker for the Coherence & Ordering Unit (COU) interfaces
- Develop and apply stimulus to exercise COU basic operations; Set up and maintain regressions
- Write COU interfaces checker and measure the progress with functional coverage and VCS Coverage Metrics (VCM)
- Inject errors and check the report with Fault Management Architecture (FMA) in post-silicon validation
- Test RAS of Data Analytics Accelerator (DAX) and L3 cache in post-silicon validation

## Education

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### University of California, Berkeley, CA

Jun 2021 - Sep 2021

- Certificate in Coding Boot Camp, Full Stack Web Development

GPA: 4.15/4.3

### University of Michigan, Ann Arbor, Michigan

Aug 2011 - May 2013

- Master in Electrical Engineering: major in VLSI (very large-scale integration)

GPA: 3.75/4.3

### Harbin Institute of Technology, China

Sep 2007 - Jun 2011

- Bachelor in Electrical Engineering: major in Electronics Information Science and Technology