

# Block Diagram Modelling

1. Include a screen capture of the subsystem that you created in Step 9 of the [System Block Diagram Modelling - Lab Procedure](#).
2. Attach the scope output of your block diagram model using the default parameters. How well does the model match the hardware response?
3. What variables would cause the divergence of the model from the hardware? What adjustments were necessary to better match the hardware behavior?
4. Attach the final model result and comment on how well you were able to match the hardware performance. Record the updated values of any parameters you adjusted.