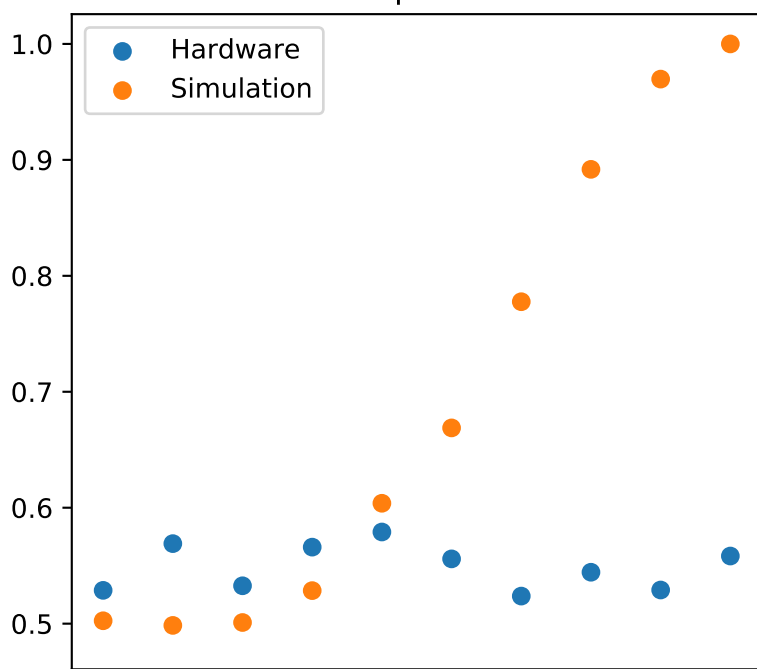
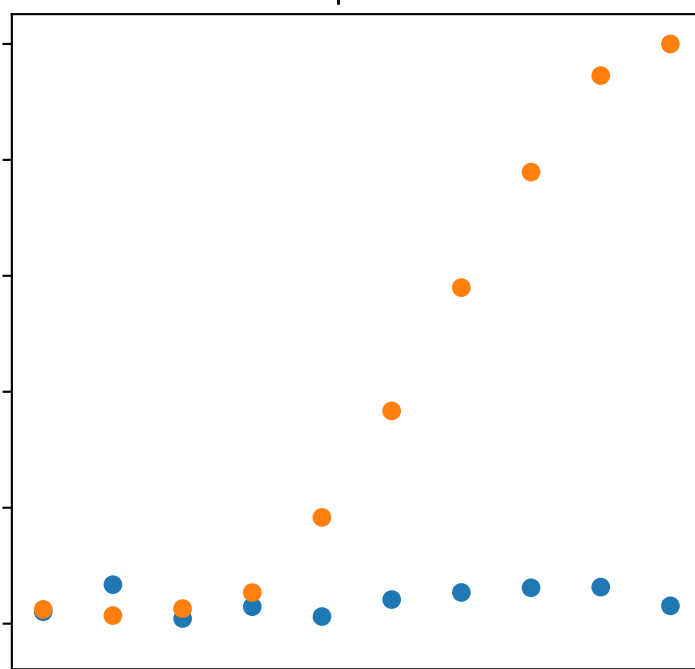


5 qubits, XX circuit, no compiler optimization

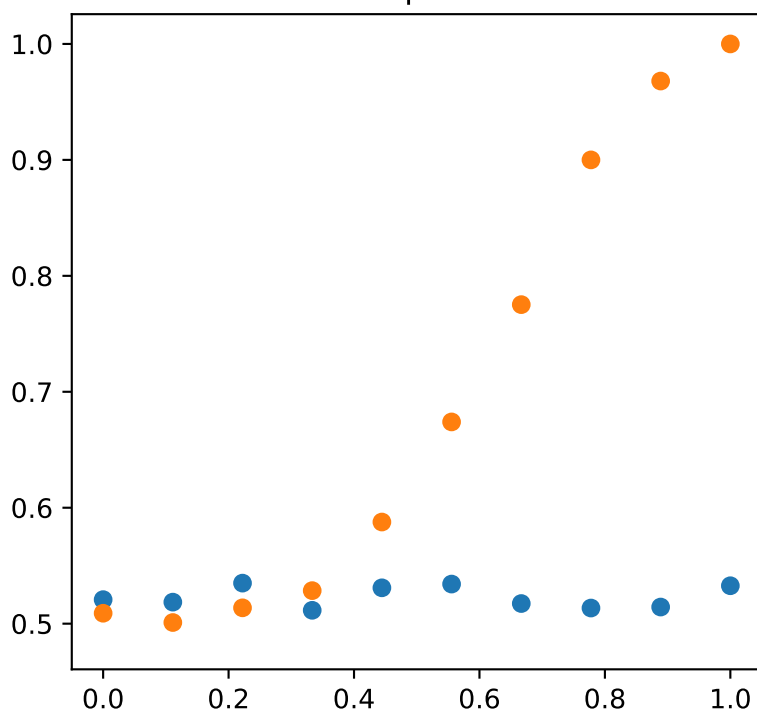
7 qubits



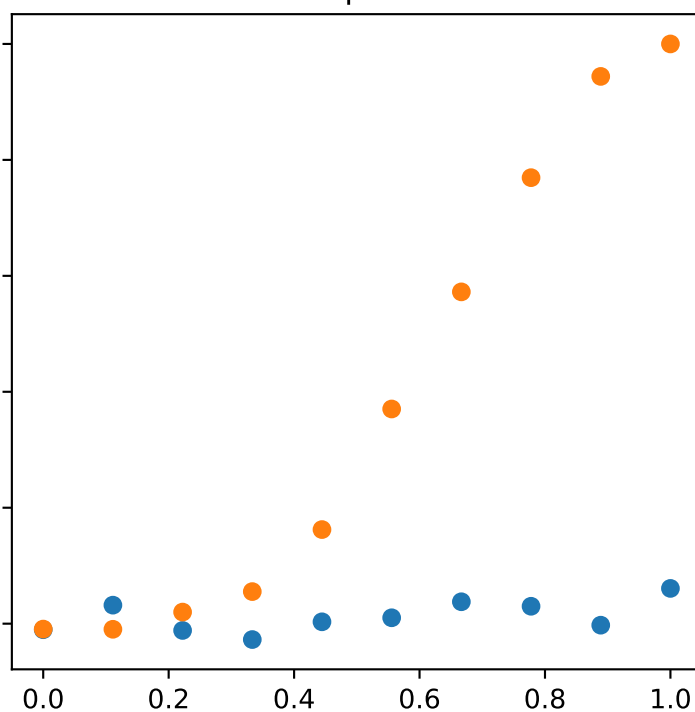
9 qubits



11 qubits



13 qubits



Scrambling parameter