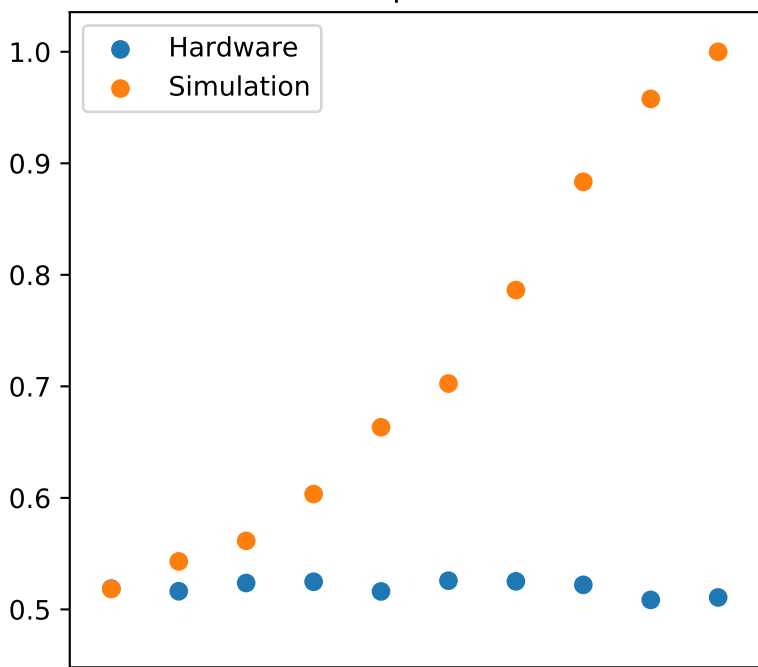
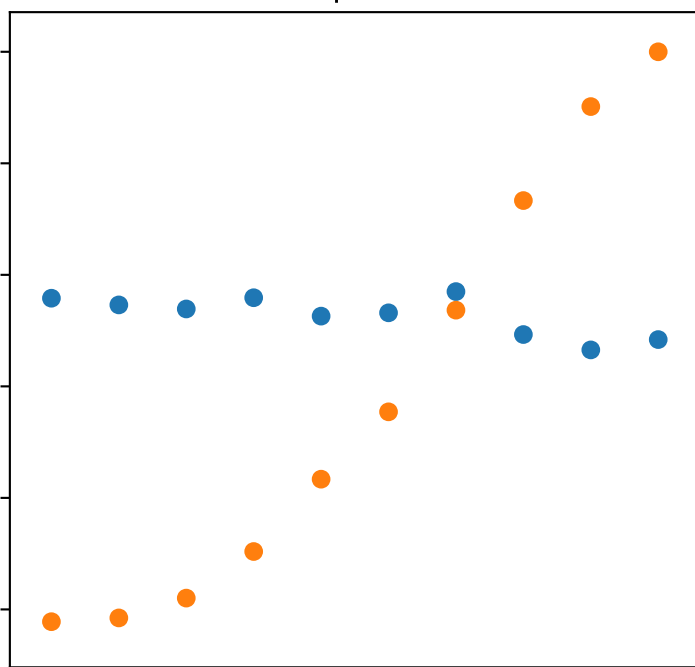


XX circuit, no compiler optimization, ibmq_16_melbourne

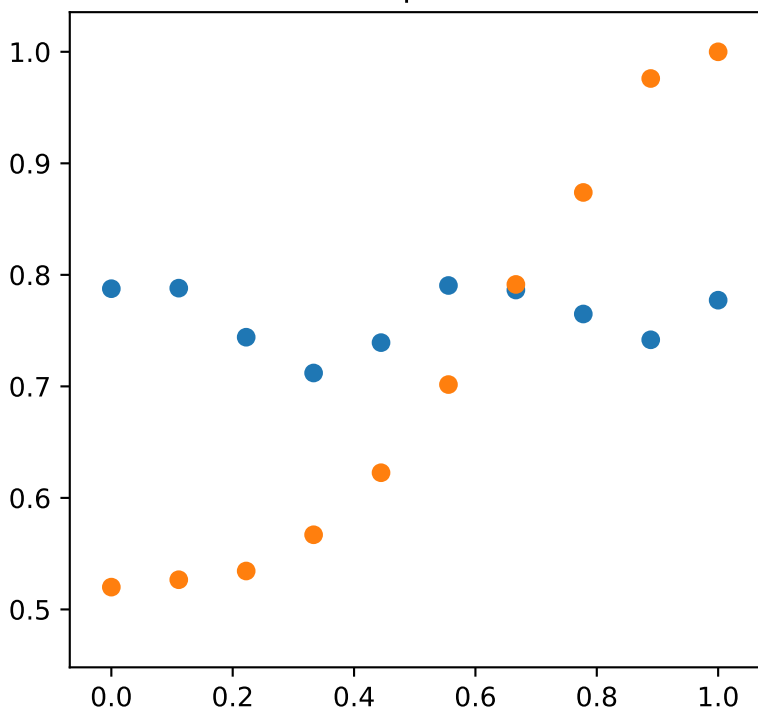
7 qubits



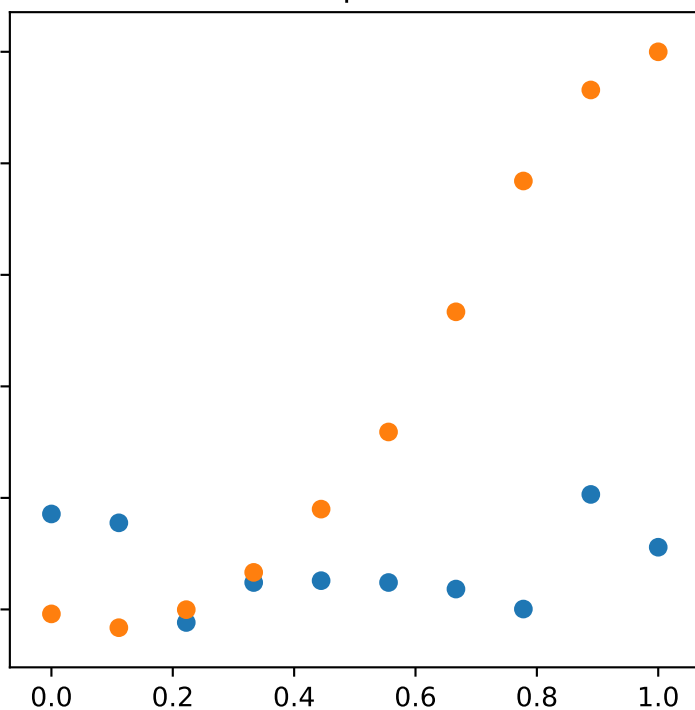
9 qubits



11 qubits



13 qubits



Scrambling parameter