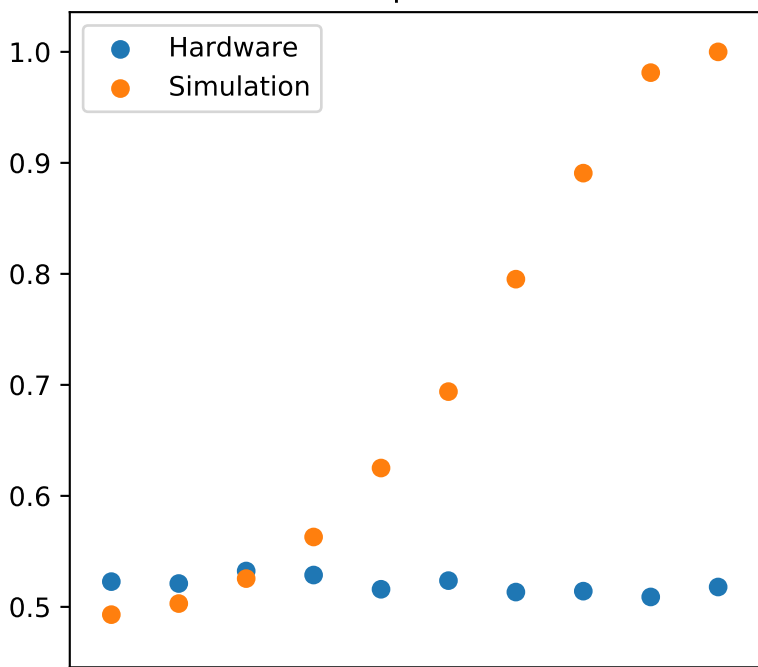
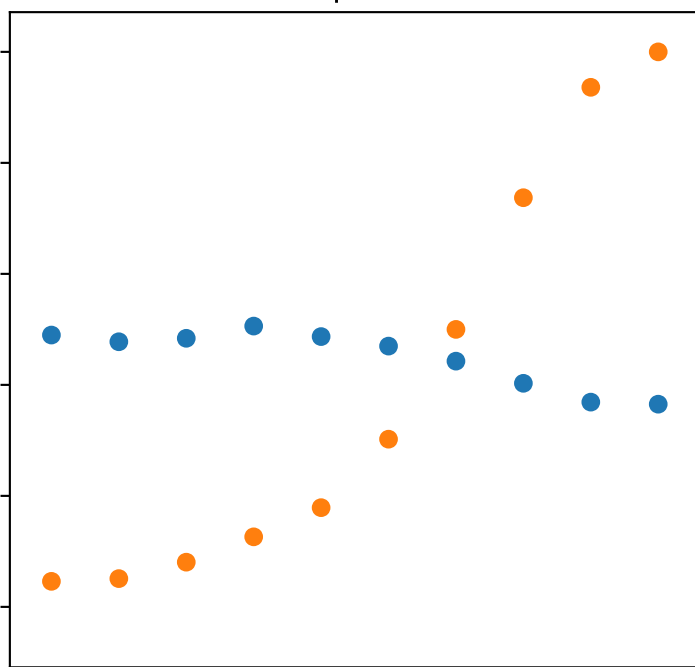


# CZ circuit, no compiler optimization, ibmq\_16\_melbourne

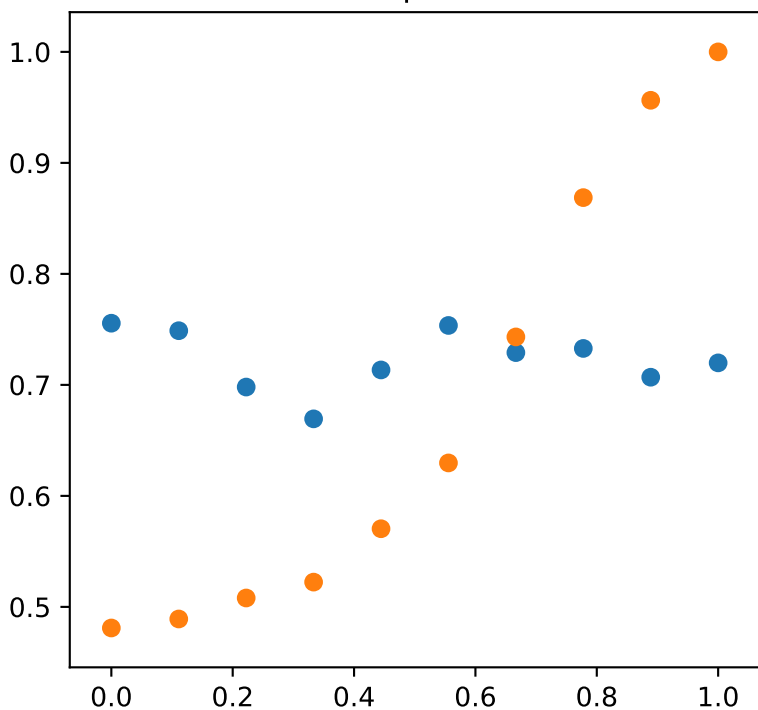
7 qubits



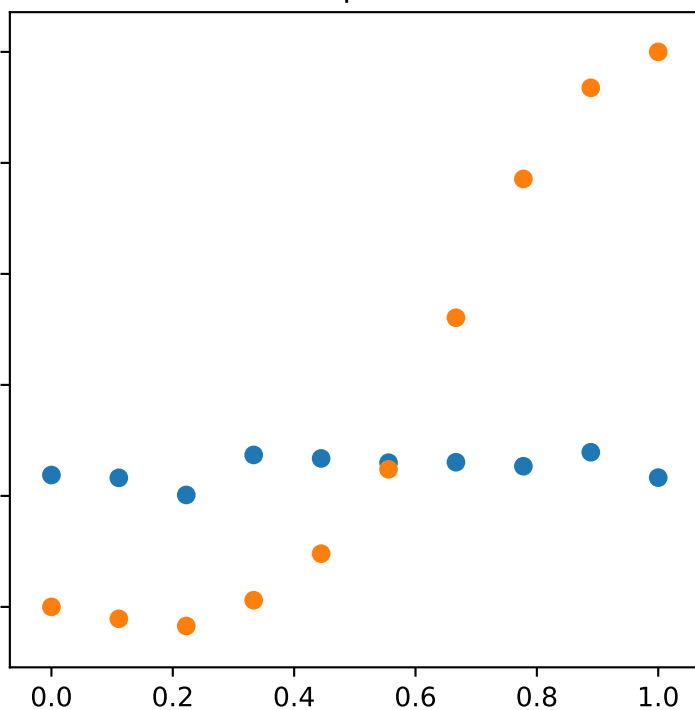
9 qubits



11 qubits



13 qubits



Scrambling parameter