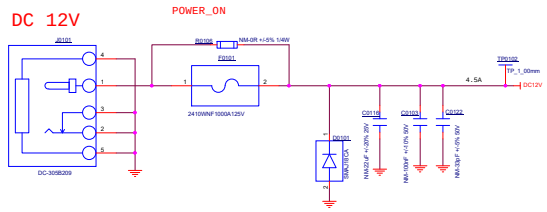
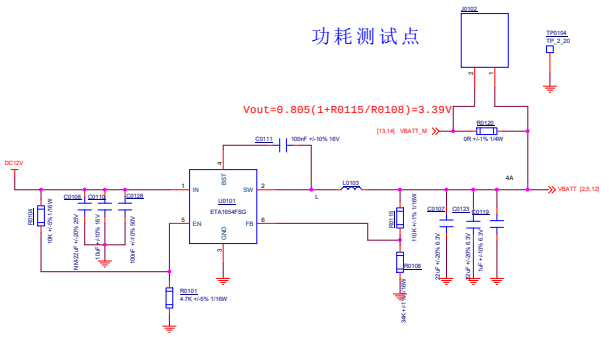
DC 12V



DCDC VBAT 3.39V

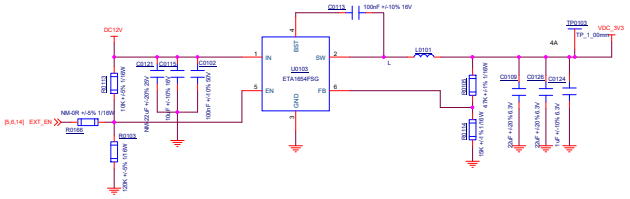
功耗测试点



POWER_ON LED

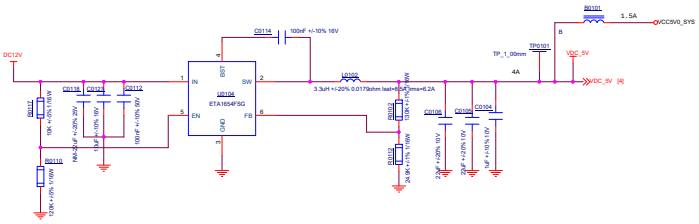
SYSTEM 3V3

Vout=0.805(1+R0105/R0114)=3.327V



SYSTEM 5V

Vout=0.805(1+R0102/R0112)=5.007V



Organization	Quectel Wireless Solutions
Project	QS6M366ZP-WF
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Drawn By	Gibart
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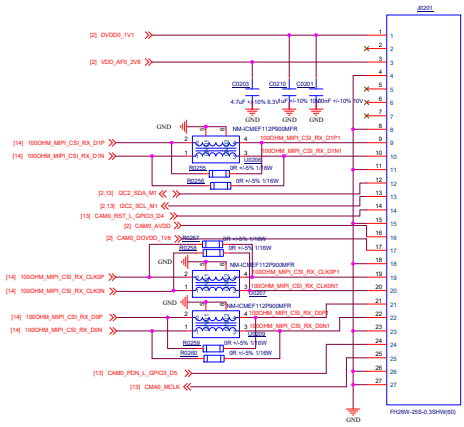
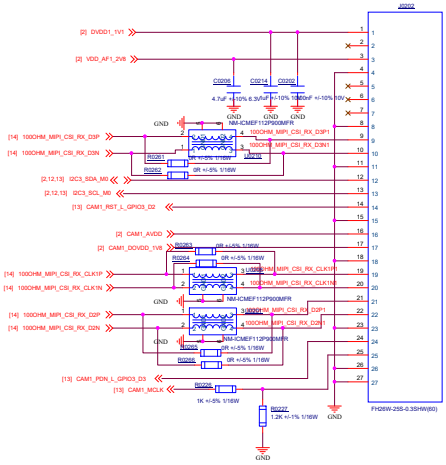
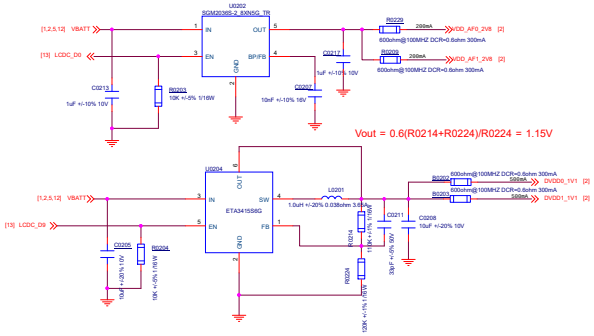
CAMERA 0

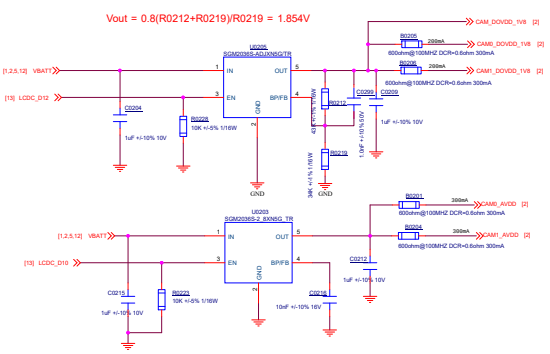


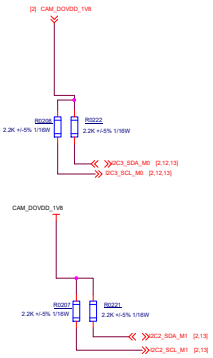
CAMERA 1



CSI POWER







Organization	Quectel Wireless Solutions
Project	QSM166ZP-WF
Ver	V2.1
Drawn By	Gibart
Checked By	Glenn
Size	A2
Date:	Thursday, August 03, 2023
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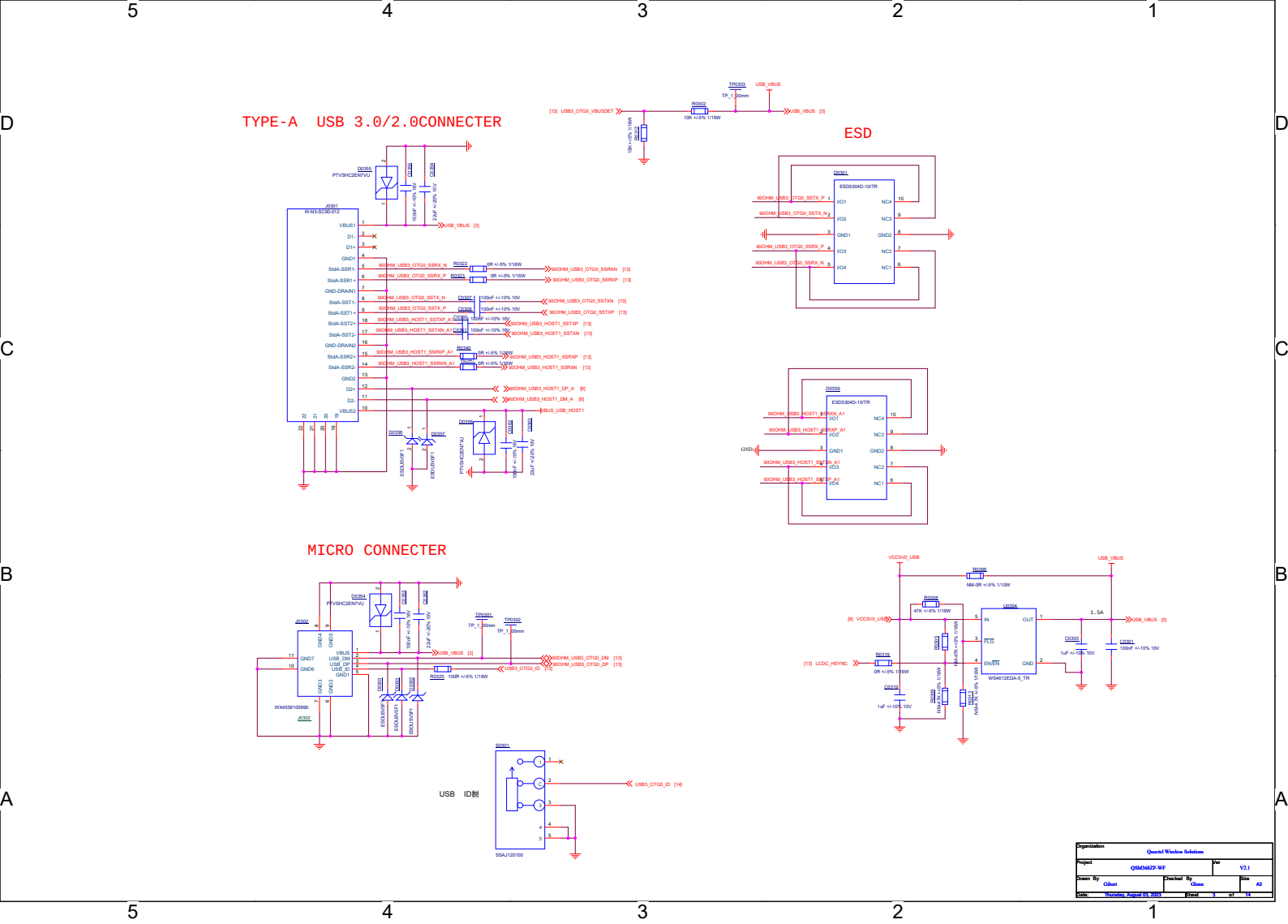

Organization	Quectel Wireless Solutions
Project	QSM36BZF-WF
Ver	V2.1
Drawn By	Gibot
Checked By	Glone
Size	A2
Date	Thursday, August 03, 2023
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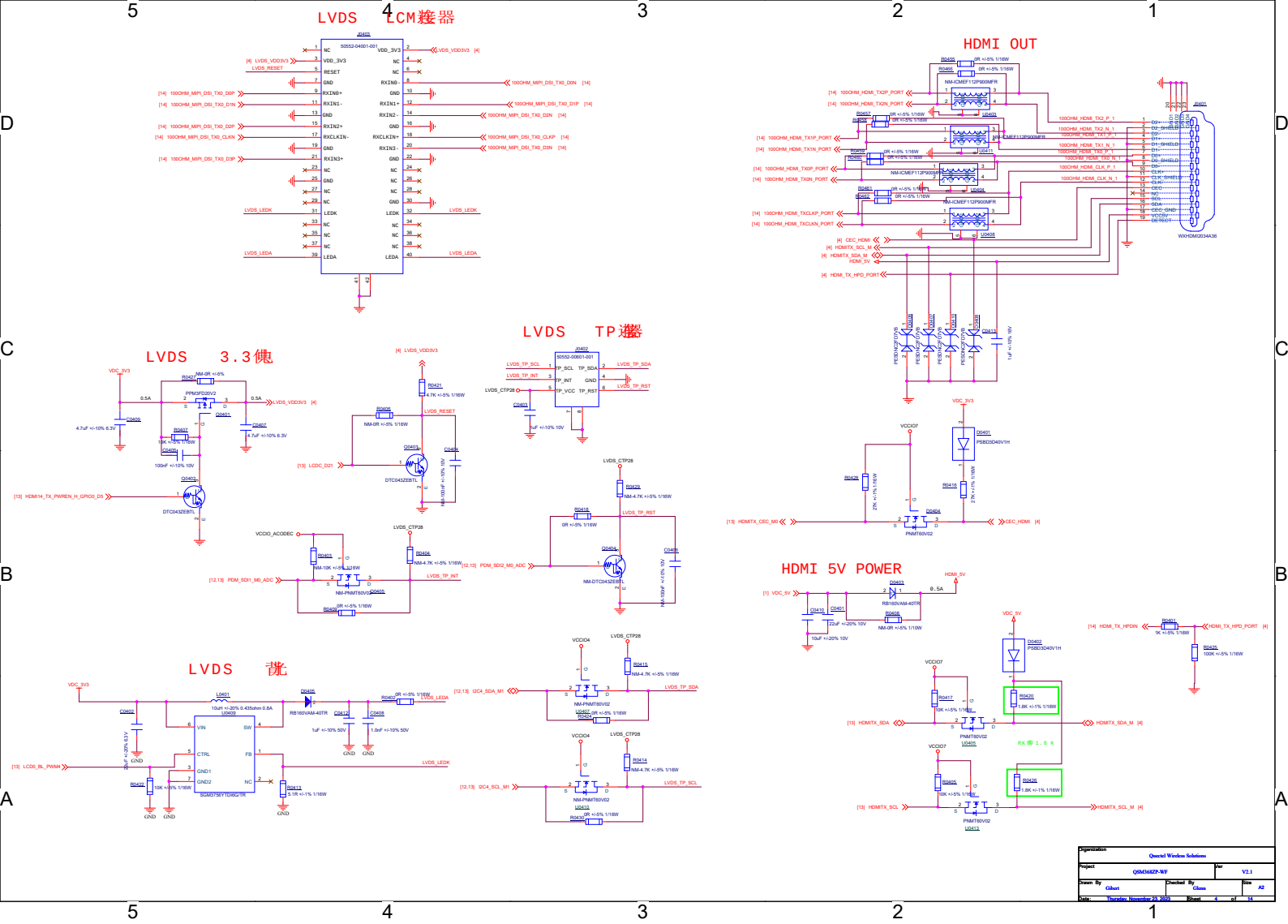
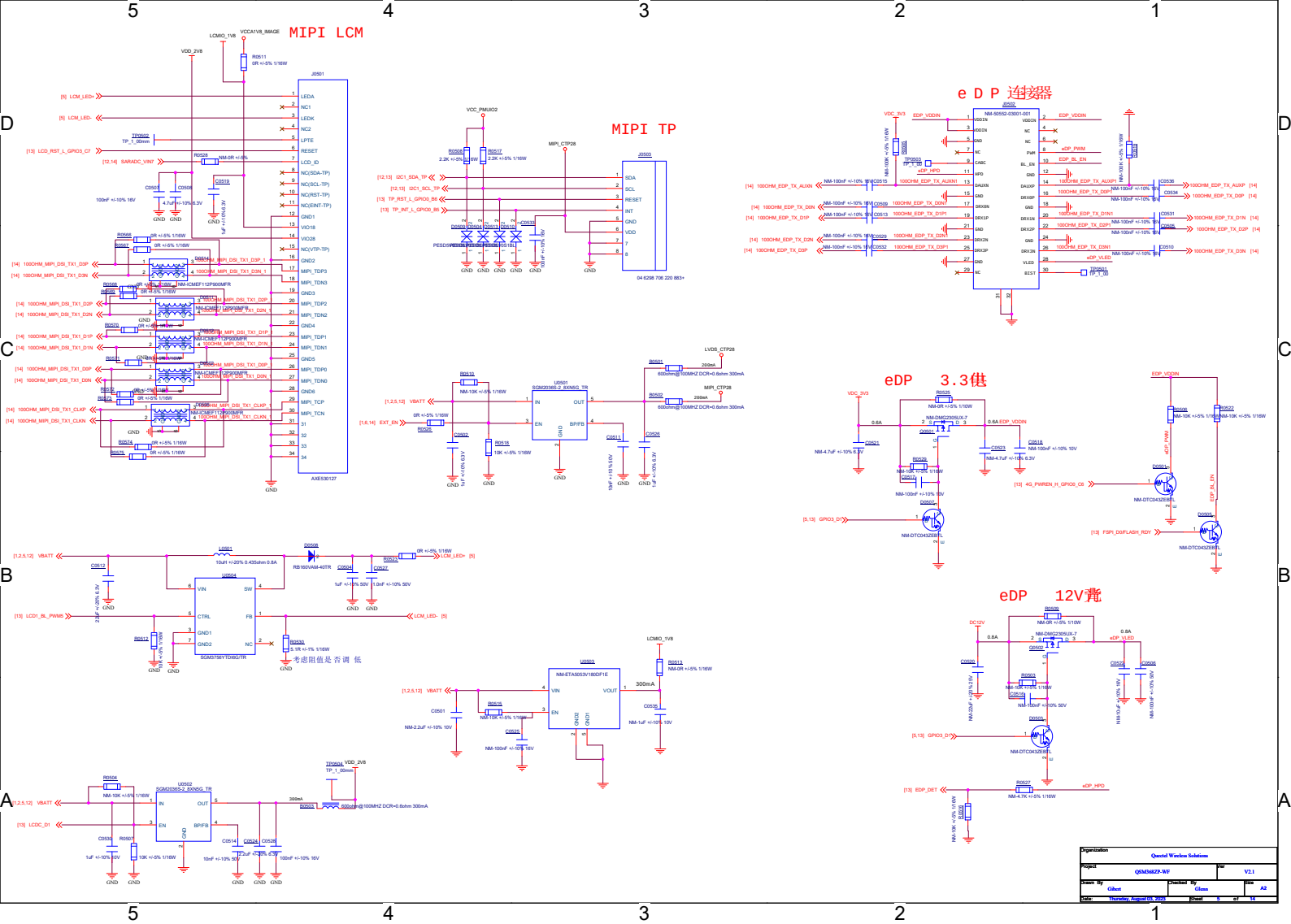
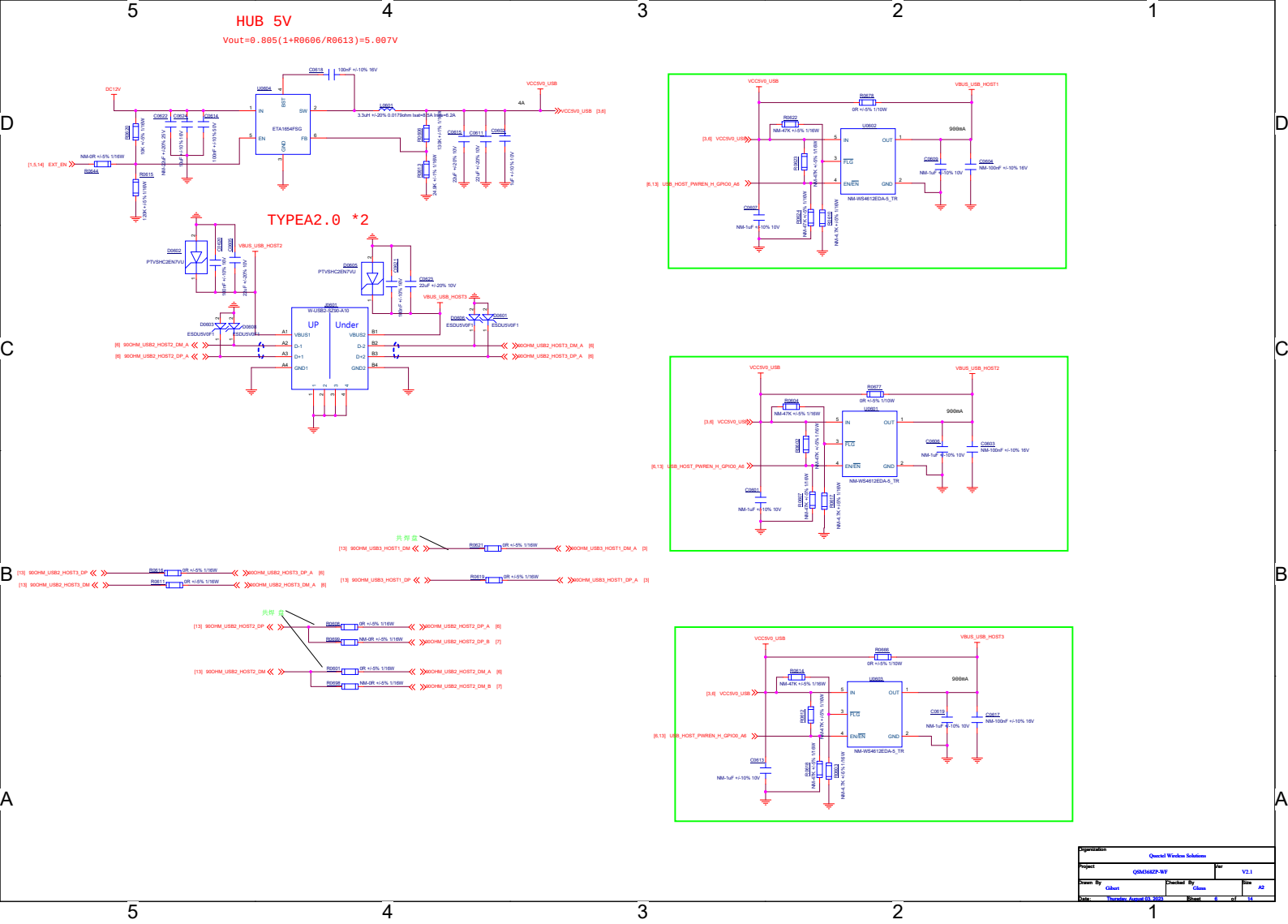

LVDS LCM接器
HDMI OUT
LVDS 3.3供
LVDS TP連器
LVDS 背
HDMI 5V POWER
RK需 1.8 K


MIPI LCM
MIPI TP
e D P 连接器
eDP 3.3供
eDP 12V供
考虑阻值是否调低
Quectel Wireless Solutions
Project
QSM368ZP-WF
V2.1
Drawn By
Gilbert
Checked By
Glenn
A2
Thursday, August 03, 2023



Organization	Quectel Wireless Solutions		
Project	QSM366ZP-WF	Ver	V2.1
Drawn By	Gibart	Checked By	Glenn
Size	A2		
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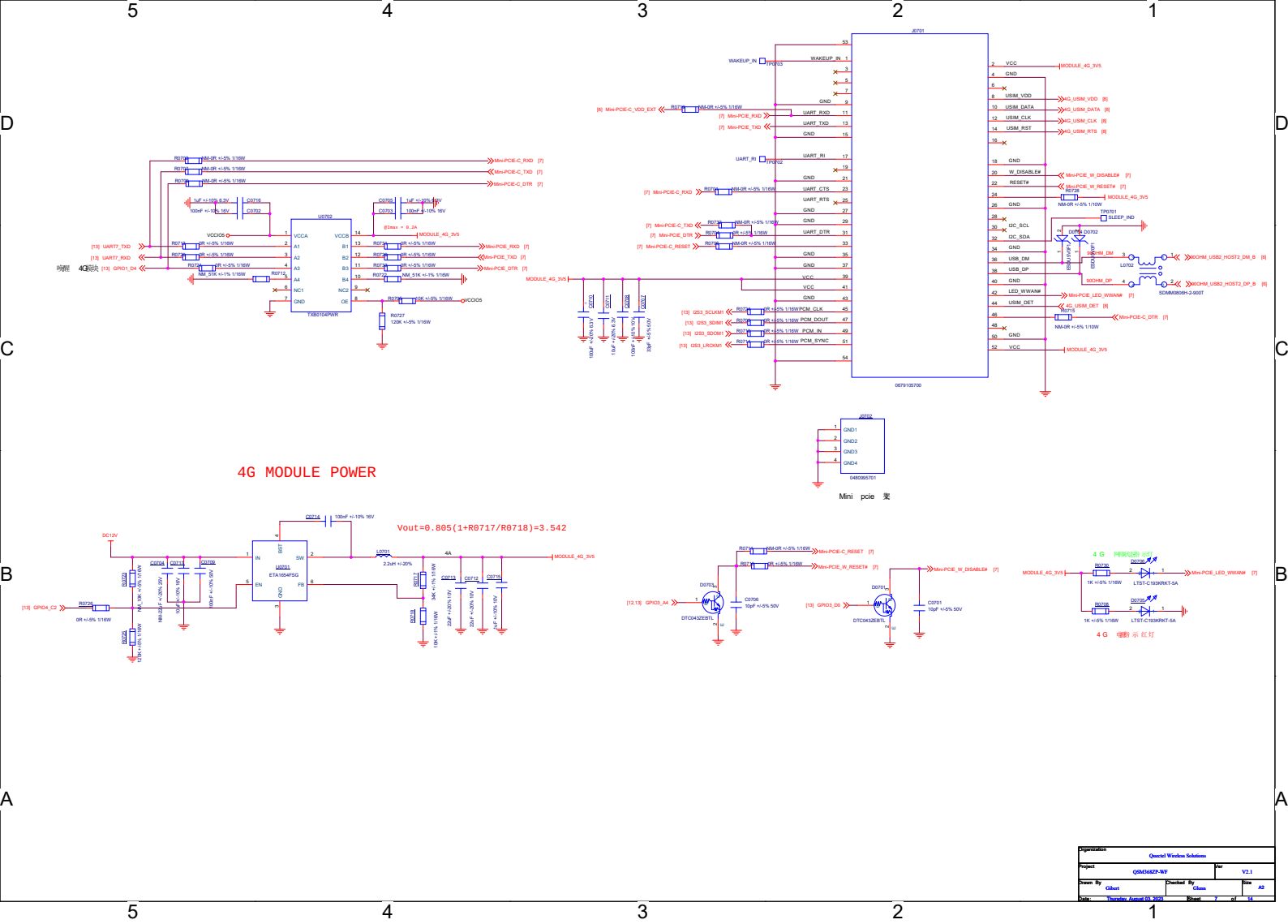

Organization	Quectel Wireless Solutions		
Project	QS6M366ZP-WF	Ver	V2.1
Drawn By	Gibart	Checked By	Glenn
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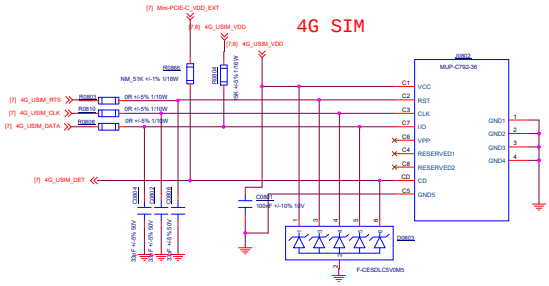
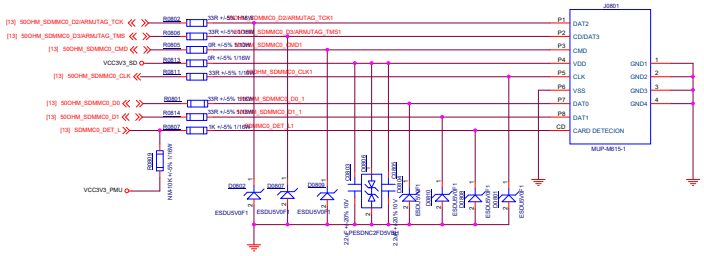
4G SIM



SG368Z SD CARD



Organization	Quectel Wireless Solutions		
Project	QSM368ZP-WF	Ver	V2.1
Drawn By	Gibert	Checked By	Glenn
Size	A2		
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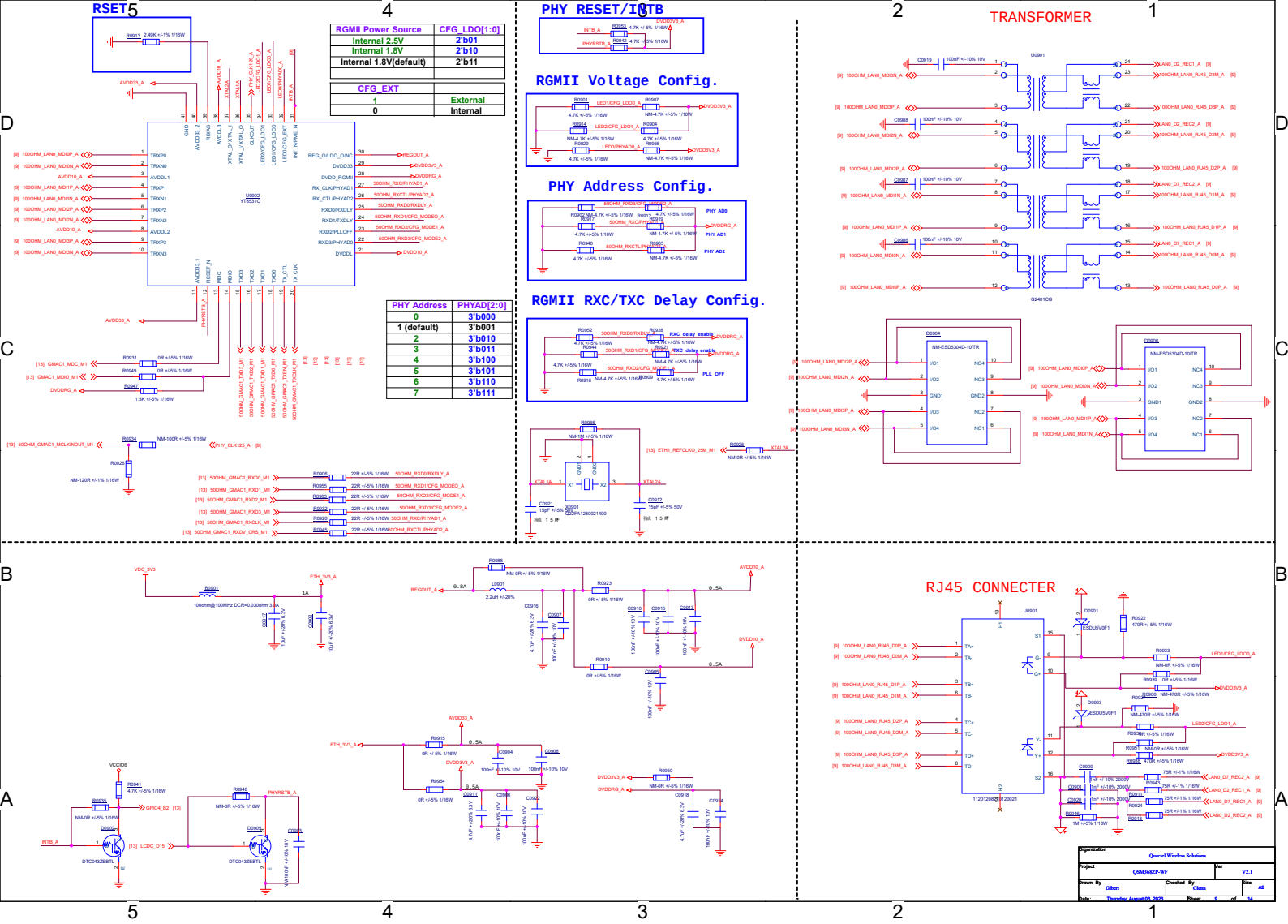
RSET
PHY RESET/INTB
RGMII Voltage Config.
PHY Address Config.
RGMII RXC/TXC Delay Config.
TRANSFORMER
RJ45 CONNECTER
RGMII Power Source
CFG_LDO[1:0]
Internal 2.5V
2'b01
Internal 1.8V
2'b10
Internal 1.8V(default)
2'b11
CFG_EXT
1
External
0
Internal
PHY Address
PHYAD[2:0]
0
3'b000
1 (default)
3'b001
2
3'b010
3
3'b011
4
3'b100
5
3'b101
6
3'b110
7
3'b111
Quectel Wireless Solutions
Project
QSM366ZP-WF
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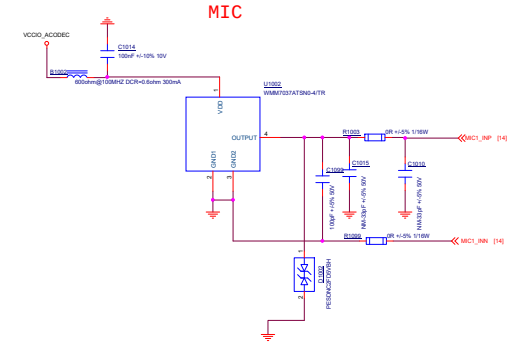
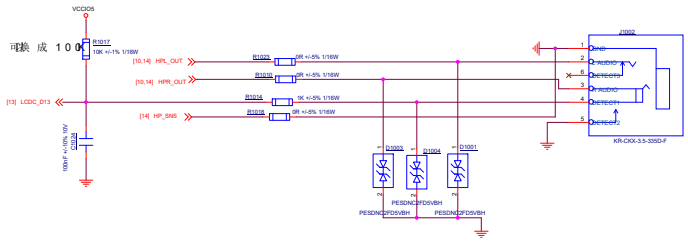
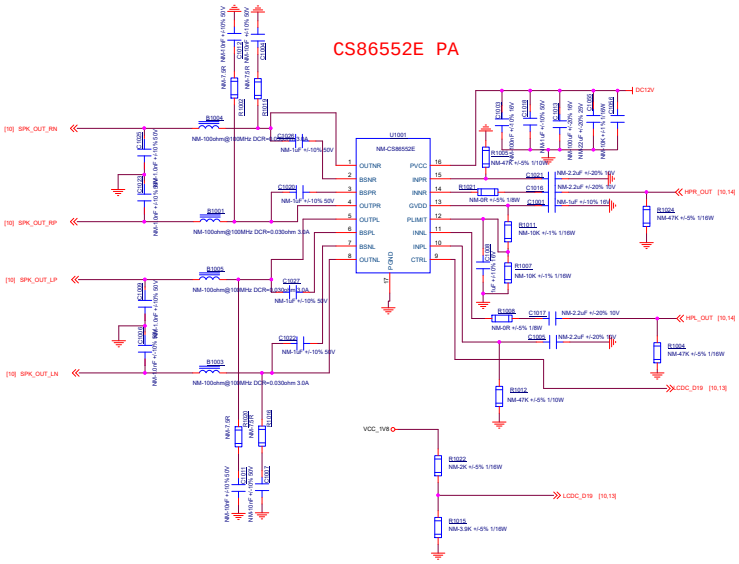
HEADSET

MIC

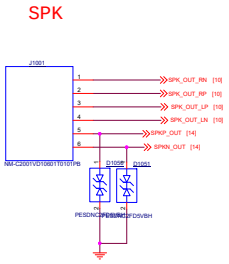




CS86552E PA



SPK



Organization	Quectel Wireless Solutions		
Project	QS6M366ZP-WF	Ver	V2.1
Drawn By	Gibart	Checked By	Glenn
Size	A2		
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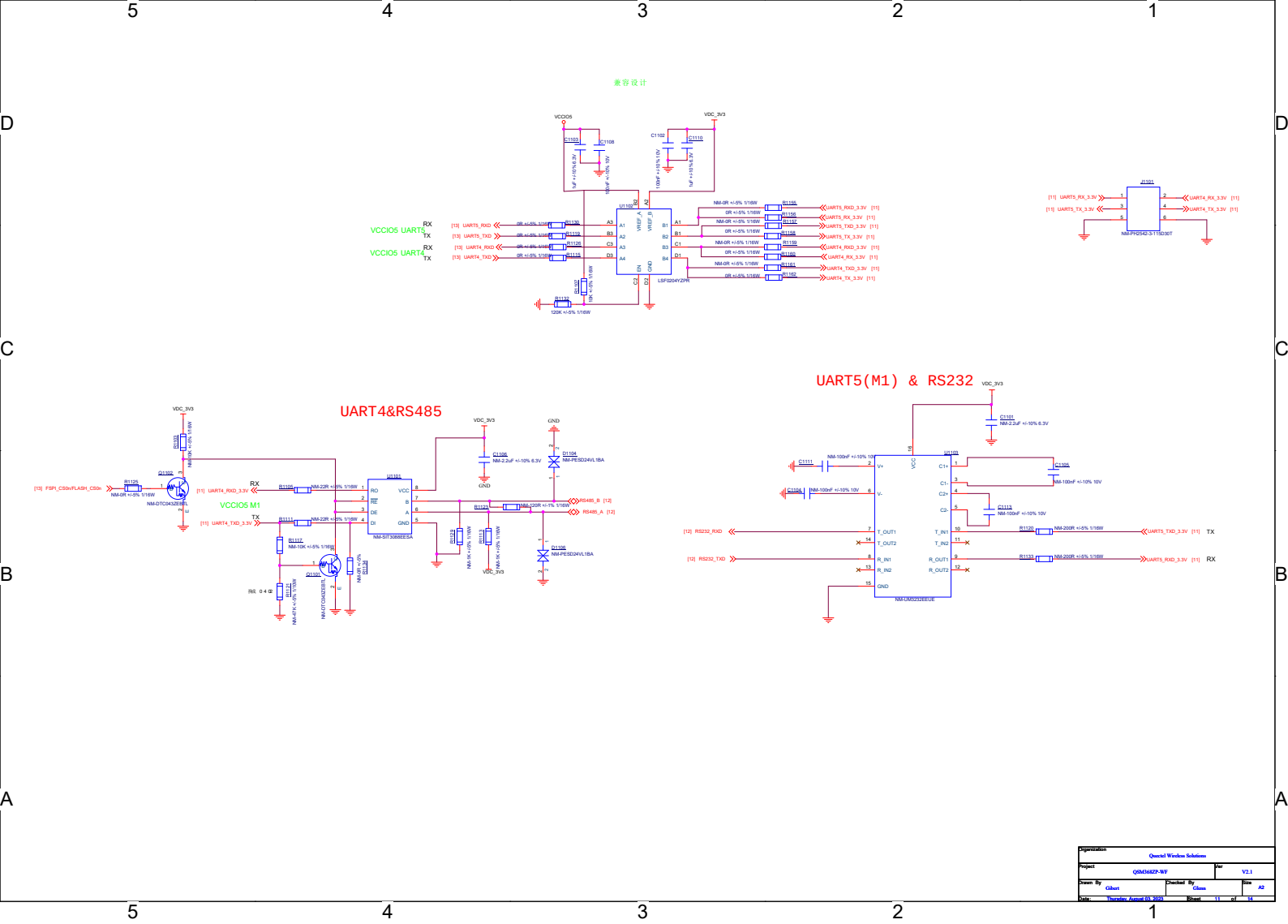

Organization	Quectel Wireless Solutions		
Project	QSM366ZP-WF	Ver	V2.1
Drawn By	Gibart	Checked By	Glenn
Size	A2		
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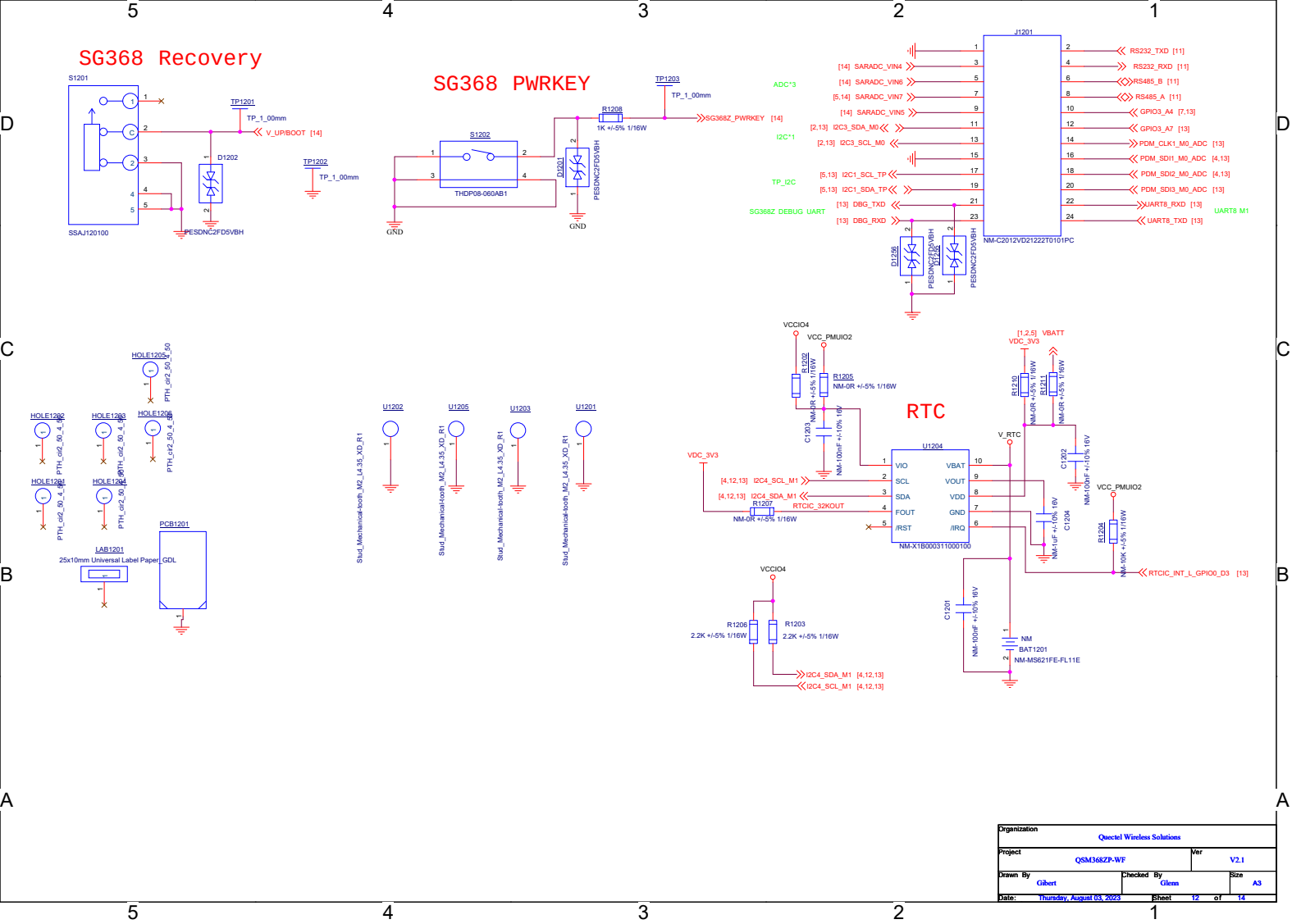

Organization	Quectel Wireless Solutions	
Project	QSM368ZP-WF	Ver V2.1
Drawn By Gibert	Checked By Glenn	Size A3
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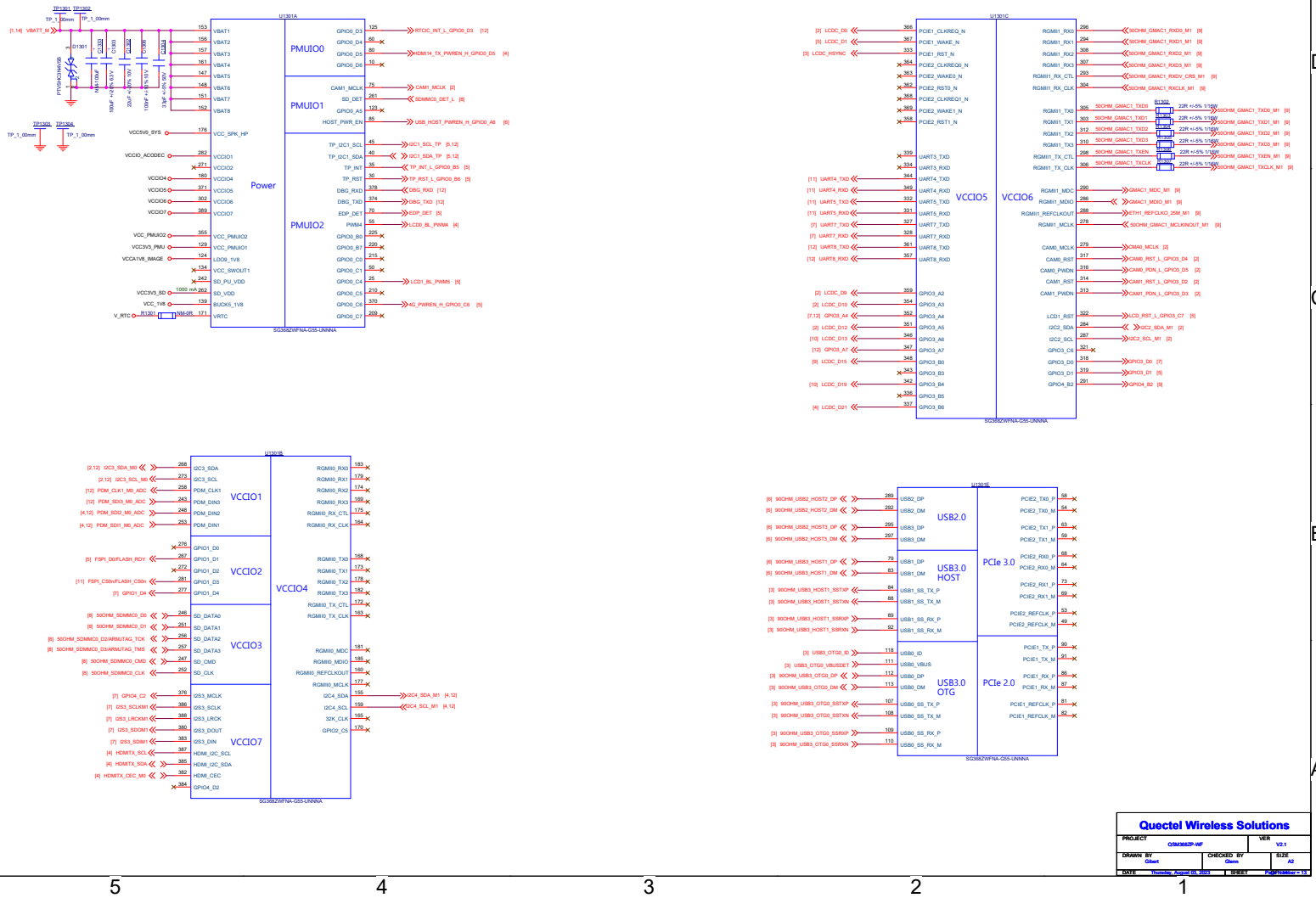
U1301A
PMUIO0
PMUIO1
PMUIO2
Power
VBAT1
VBAT2
VBAT3
VBAT4
VBAT5
VBAT6
VBAT7
VBAT8
VCC_SPK_HP
VCCIO1
VCCIO2
VCCIO4
VCCIO5
VCCIO6
VCCIO7
VCC_PMUIO2
VCC_PMUIO1
LDO9_1V8
VCC_SWOUT1
SD_PU_VDD
SD_VDD
BUCK5_1V8
VRTC
GPIO0_D3
GPIO0_D4
GPIO0_D5
GPIO0_D6
CAM1_MCLK
SD_DET
GPIO0_A5
HOST_PWR_EN
TP_I2C1_SCL
TP_I2C1_SDA
TP_INT
TP_RST
DBG_RXD
DBG_TXD
EDP_DET
PWM4
GPIO0_B5
GPIO0_B7
GPIO0_C6
GPIO0_C1
GPIO0_C4
GPIO0_C5
GPIO0_C6
GPIO0_C7
RTCIC_INT_L_GPIO0_D3
HDMI14_TX_PWREN_H_GPIO0_D5
CAM1_MCLK
SDMMC0_DET_L
USB_HOST_PWREN_H_GPIO0_A6
I2C1_SCL_TP
I2C1_SDA_TP
TP_INT_L_GPIO0_B5
TP_RST_L_GPIO0_B6
DBG_RXD
DBG_TXD
EDP_DET
LCD0_BL_PWM4
4G_PWREN_H_GPIO3_C6
SG368ZWFNA-GS5-UNNNA
U1301C
VCCIO5
VCCIO6
PCIE1_CLKREQ_N
PCIE1_WAKE_N
PCIE1_RST_N
PCIE2_CLKREQ0_N
PCIE2_WAKE0_N
PCIE2_RST0_N
PCIE2_CLKREQ1_N
PCIE2_WAKE1_N
PCIE2_RST1_N
UART3_TXD
UART3_RXD
UART4_TXD
UART4_RXD
UART5_TXD
UART5_RXD
UART7_TXD
UART7_RXD
UART8_TXD
UART8_RXD
GPIO3_A2
GPIO3_A3
GPIO3_A4
GPIO3_A5
GPIO3_A6
GPIO3_A7
GPIO3_B0
GPIO3_B3
GPIO3_B4
GPIO3_B5
GPIO3_B6
RGMII1_RX0
RGMII1_RX1
RGMII1_RX2
RGMII1_RX3
RGMII1_RX_CTL
RGMII1_RX_CLK
RGMII1_TX0
RGMII1_TX1
RGMII1_TX2
RGMII1_TX3
RGMII1_TX_CTL
RGMII1_TX_CLK
RGMII1_MDC
RGMII1_MDIO
RGMII1_REFCLKOUT
RGMII1_MCLK
CAM0_MCLK
CAM0_RST
CAM0_PWDN
CAM1_RST
CAM1_PWDN
LCD1_RST
I2C2_SDA
I2C2_SCL
GPIO3_C6
GPIO3_D0
GPIO3_D1
GPIO4_B2
SG368ZWFNA-GS5-UNNNA
U1301B
VCCIO1
VCCIO2
VCCIO3
VCCIO4
VCCIO7
I2C3_SDA
I2C3_SCL
PDM_CLK1
PDM_DIN3
PDM_DIN2
PDM_DIN1
GPIO1_D0
GPIO1_D1
GPIO1_D2
GPIO1_D3
GPIO1_D4
SD_DATA0
SD_DATA1
SD_DATA2
SD_DATA3
SD_CMD
SD_CLK
I2S3_MCLK
I2S3_SCLK
I2S3_LRCK
I2S3_DOUT
I2S3_DIN
HDMI_I2C_SCL
HDMI_I2C_SDA
HDMI_CEC
GPIO4_D2
RGMII0_RX0
RGMII0_RX1
RGMII0_RX2
RGMII0_RX3
RGMII0_RX_CTL
RGMII0_RX_CLK
RGMII0_TX0
RGMII0_TX1
RGMII0_TX2
RGMII0_TX3
RGMII0_TX_CTL
RGMII0_TX_CLK
RGMII0_MDC
RGMII0_MDIO
RGMII0_REFCLKOUT
RGMII0_MCLK
I2C4_SDA
I2C4_SCL
32K_CLK
GPIO2_C5
I2C4_SDA_M1
I2C4_SCL_M1
SG368ZWFNA-GS5-UNNNA
U1301E
USB2.0
USB3.0 HOST
USB3.0 OTG
PCIe 3.0
PCIe 2.0
USB2_DP
USB2_DM
USB3_DP
USB3_DM
USB1_DP
USB1_DM
USB1_SS_TX_P
USB1_SS_TX_M
USB1_SS_RX_P
USB1_SS_RX_M
USB0_ID
USB0_VBUS
USB0_DP
USB0_DM
USB0_SS_TX_P
USB0_SS_TX_M
USB0_SS_RX_P
USB0_SS_RX_M
PCIE2_TX0_P
PCIE2_TX0_M
PCIE2_TX1_P
PCIE2_TX1_M
PCIE2_RX0_P
PCIE2_RX0_M
PCIE2_RX1_P
PCIE2_RX1_M
PCIE2_REFCLK_P
PCIE2_REFCLK_M
PCIE1_TX_P
PCIE1_TX_M
PCIE1_RX_P
PCIE1_RX_M
PCIE1_REFCLK_P
PCIE1_REFCLK_M
SG368ZWFNA-GS5-UNNNA
Quectel Wireless Solutions
PROJECT
OSM362P-WF
VER
V2.1
DRAWN BY
Gibert
CHECKED BY
Glenn
SIZE
A2
DATE
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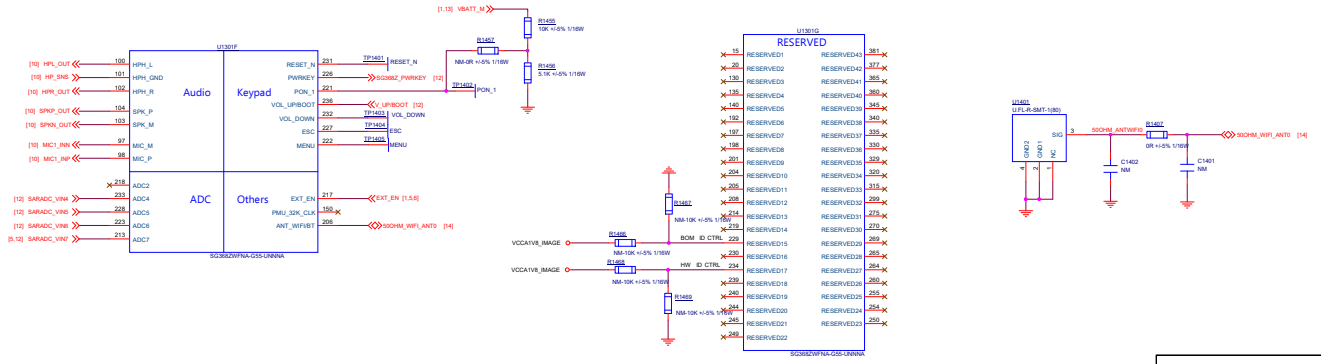

Quectel Wireless Solutions	
PROJECT: QRM368ZP-WF	VER: V2.1
DRAWN BY: Gibert	CHECKED BY: Glenn
SIZE: A2	
DATE: Thursday, August 03, 2023	SHEET: Part Number = 14