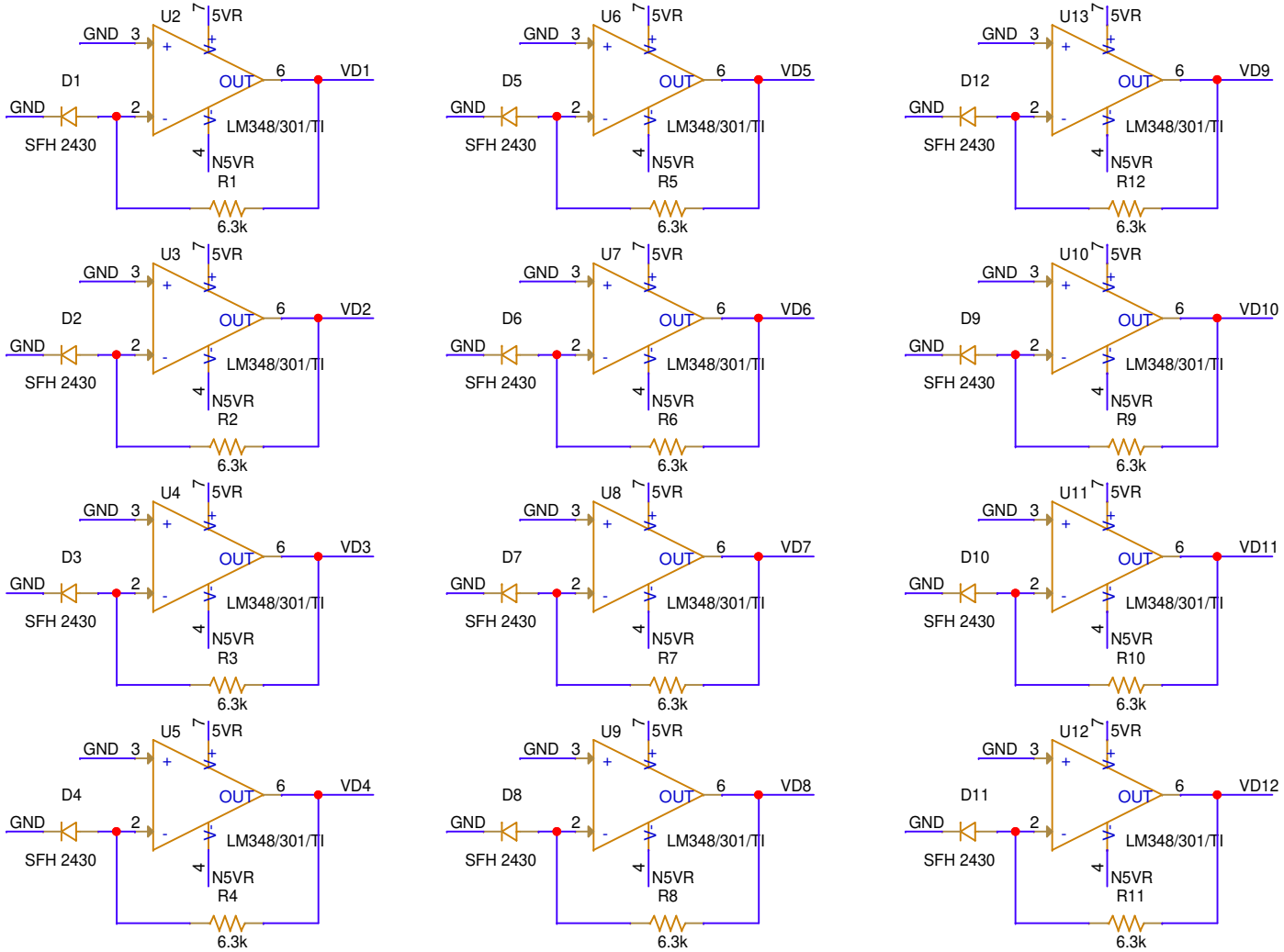

Will not be using 12 single op amps. Each row represents a quad LM348N op amp. Op amps will be centralized, with three diodes on each respective surface of the payload.

NOTE: Resistor values are TBD. It seems that around 6.3k should be sufficient, but more testing is required. For now, will leave as 6.4k.

Author: Aidan Sheedy		
Title: QSET Sunsensor Schematic - Photodiode and Transimpednece Amplifiers		
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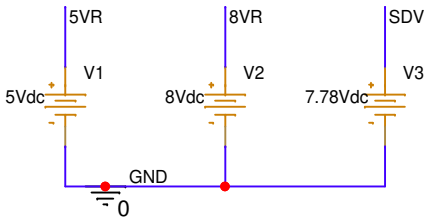
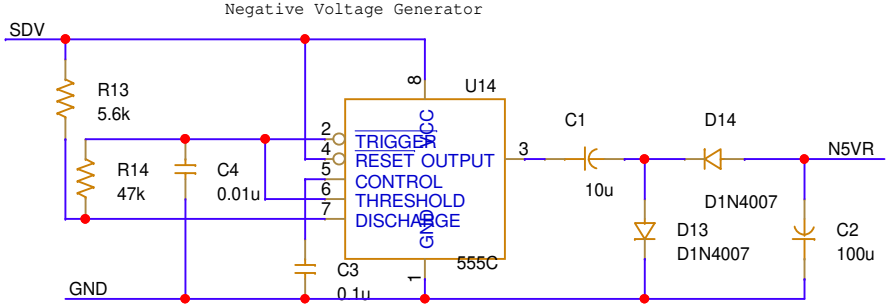
Rails from power



Step Down Voltage:
If the upper
rail is more than about 7.8V, an
LM2596 DC-DC voltage stepper will
be used to step the voltage down
to SDV. The LM2596 has four pins:

IN+: Upper voltage, in from 8VR
IN-: GND
OUT+: Adjustable output. will be set to output 7.78V.
OUT-: GND

Negative Voltage Generator



This should be
about -5V
under load.

Author: Aidan Sheedy		
Title: QSET Sunsensor Schematic - Sources		
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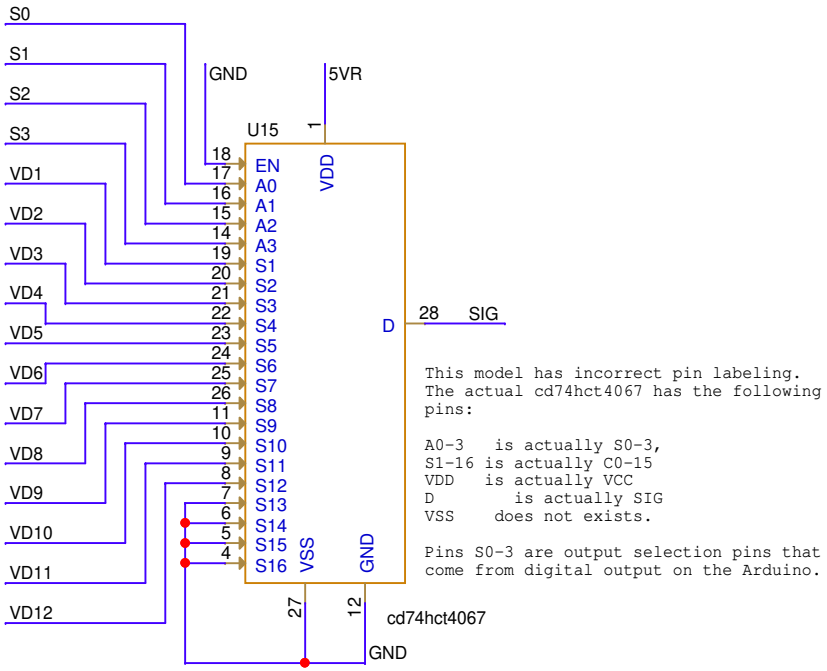


```
This model has incorrect pin labeling.
The actual cd74hct4067 has the following
pins:

A0-3   is actually S0-3,
S1-16 is actually C0-15
VDD   is actually VCC
D        is actually SIG
VSS    does not exists.

Pins S0-3 are output selection pins that
come from digital output on the Arduino.
```

Author: Aidan Sheedy			
Title: QSET Sunsensor Schematic - Multiplexer and Output			
Size: A	Document Number: 1		Rev: 0
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