


# SIMÓN PORTELA QUEIMAÑO

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Madrid, Spain 

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[Simón Portela Queimaño](#) 

12/31/1999 (21 years old)

Graduate in Electronics and Automatic Engineering as the best of his promotion, complementing his academic training with courses, workshops and conferences. Currently, a student of the Master's Degree in Electronycs Systems Engineering at the Polytechnic University of Madrid. Also an especially hardworking and responsibe person, standing out notably among her peers.

## EXPERIENCE

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**Research collaborator** – 10/2021 to 06/2022

**Polytechnic University of Madrid**, Madrid, Spain

- Design the PCIe interfaces of an FPGA UltraScale+ HBM (VCU 128 board)
- Achieve maximum data transfer speed through PCIe and Ethernet

**Junior Systems Engineer** – 08/2020 to 09/2020

**CENTUM-RT**, Vigo, Spain

- Complex algorithm implementation on FPGA from a high-level ecosystem like MATLAB.
- Elaboration of technical documentation.

**Private tutoring** – 09/2018 to 01/2020

Private lessons of Math, Physics and Chemistry to high school children from 14 to 18 years old.

## EDUCATION

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Master of Engineering: **Electronic System Engineering**, 09/2021 – 06/2022

**Polytechnic University of Madrid**, Madrid – Spain

Final Master Thesis: Algorithm acceleration in FPGAs

Bachelor of Engineering: **Electronics and Automation Engineering**, 09/2017 – 07/2021

**University of Vigo**, Vigo – Spain

13<sup>th</sup> best qualified student in the country GPA: 3,68

## SKILL HIGHLIGHTS

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|--------------------------|-------------------|
| • Hardworking            | • Creativity      |
| • Critical thinking      | • Adaptability    |
| • Complex problem solver | • Teambuilding    |
| • Interpersonal skills   | • Public speaking |
| • Teamwork skills        | • Communication   |

## LANGUAGES

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### ENGLISH: HIGH LEVEL

- 11/2016 – 12/2016 - Language immersion stay in England
- 06/2019 - B2 FCE of University of Cambridge
- 07/2019 – 08/2019 - 90-hour intensive English course in Institute of English Language Studies of Malta.
- 07/2024 - C1 Cambridge English Advanced

### SPANISH: NATIVE LEVEL

## ACHIEVEMENTS AND AWARDS

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- ✓ Average grade of the academic record: 8,00/10
- ✓ 1st position prize in the Citython Eindhoven 2021, category: “Proud to travel to campus together”
- ✓ 13th best graduate in Electronics and Automation Engineering in Spain 2020/2021 course awarded by SEDEA.

## CERTIFICATIONS

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### ❖ PROGRAMMING LANGUAGES AND TOOLS:

- C Language for Microcontrollers (PIC and STM): Intermediate-high level.
  - Experience with bare metal systems and with FREE RTOS.
- VHDL/Verilog for FPGA (Xilinx and Altera(Intel)): Intermediate-high level
- Python: Intermediate level
- MATLAB: Intermediate level
- Java: Intermediate level

### ❖ OTHERS:

- AutoCAD: Intermediate level

Thanks to the contents of the subjects studied during the career, he has acquired knowledge of:

- Embedded System Programming
- PCBs design
- ASICs design with Cadence.
- Experience with I2C, SPI, USB, UART, Ethernet, RS232, PCIe...
- Automation programming with TIA Portal
- Database with MySQL
- Microsoft Programs (Word, Excel, PowerPoint)
- During the academic year 20/21 he has attended sessions of the debate club of the University of Vigo