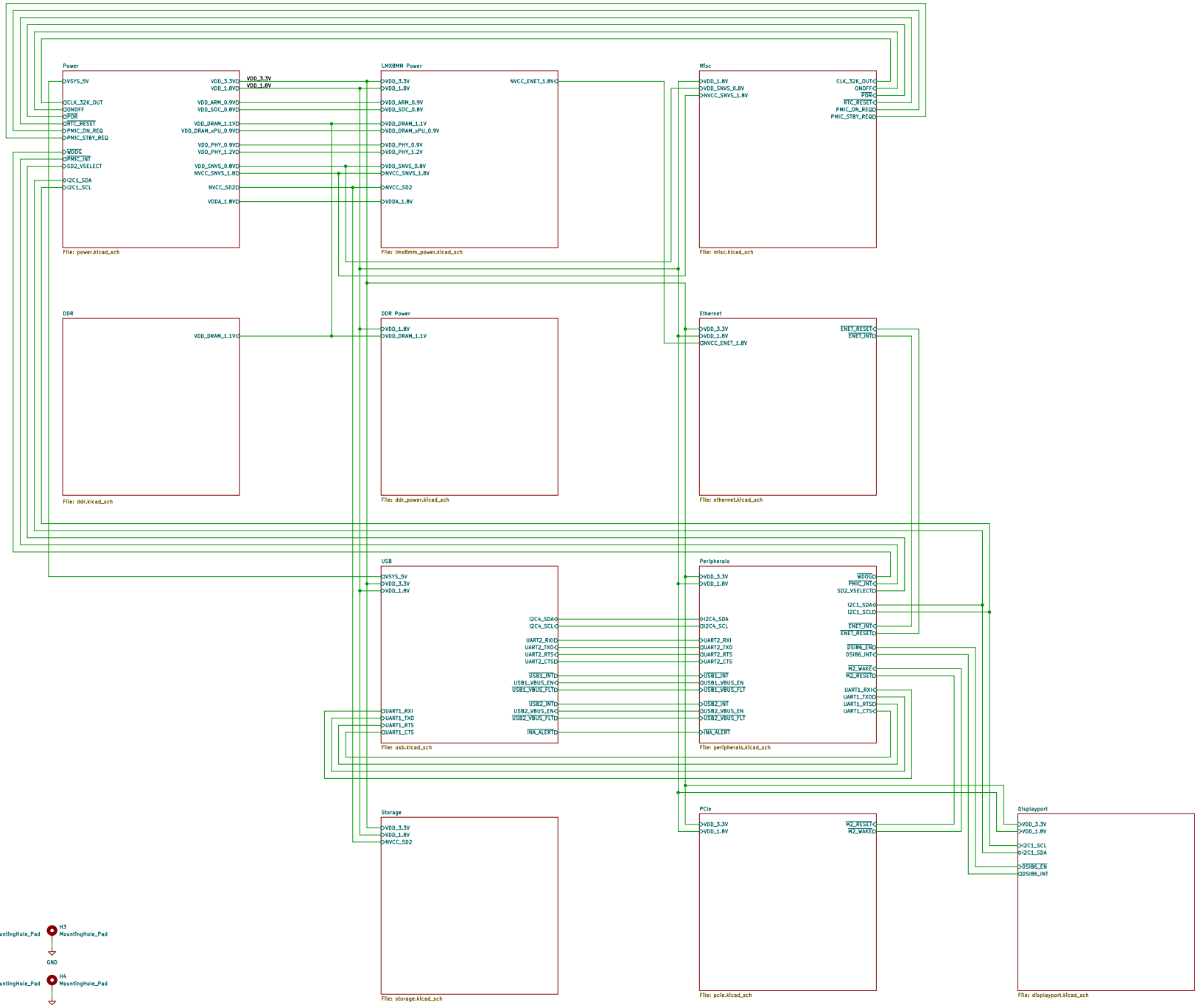
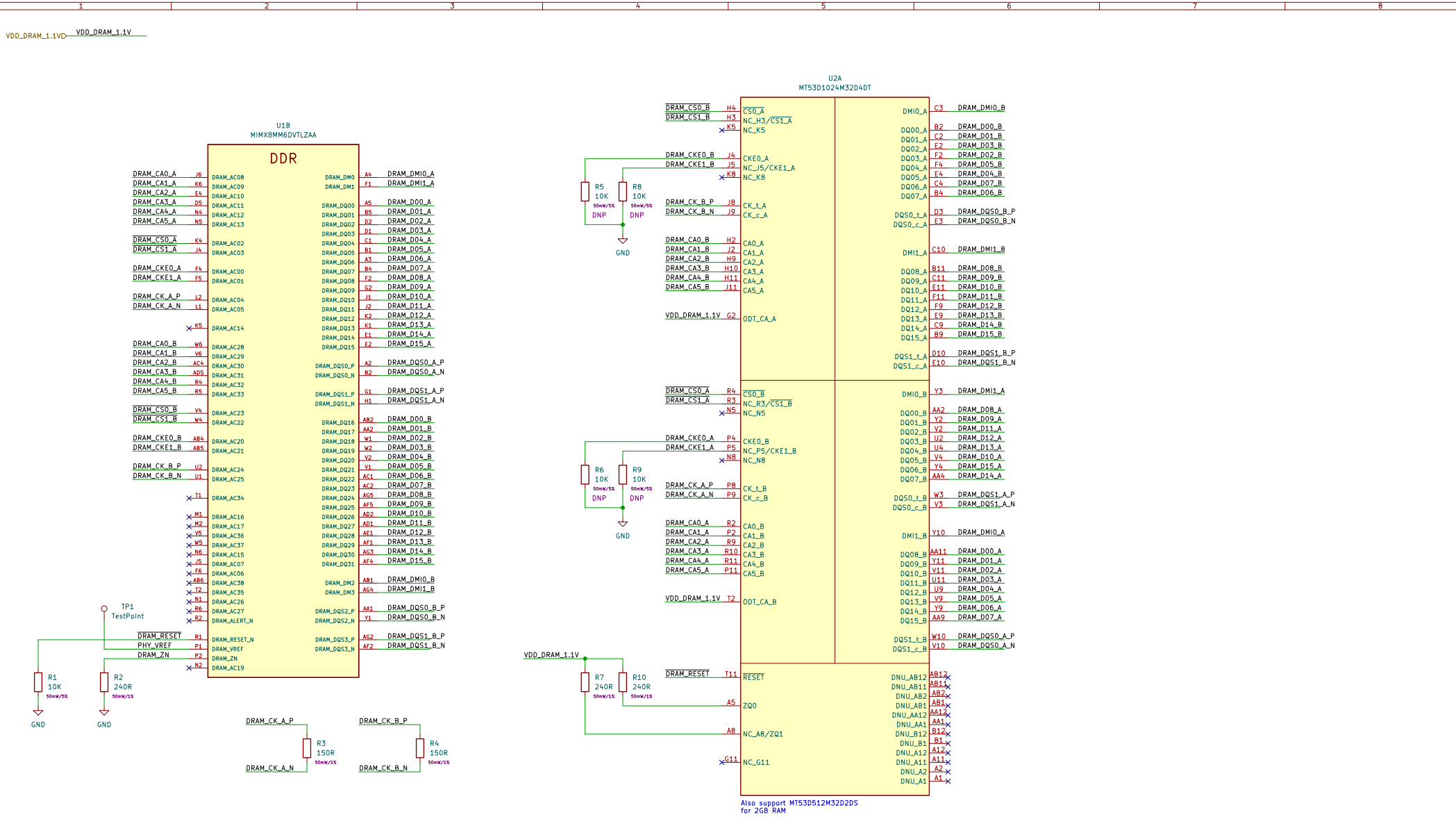
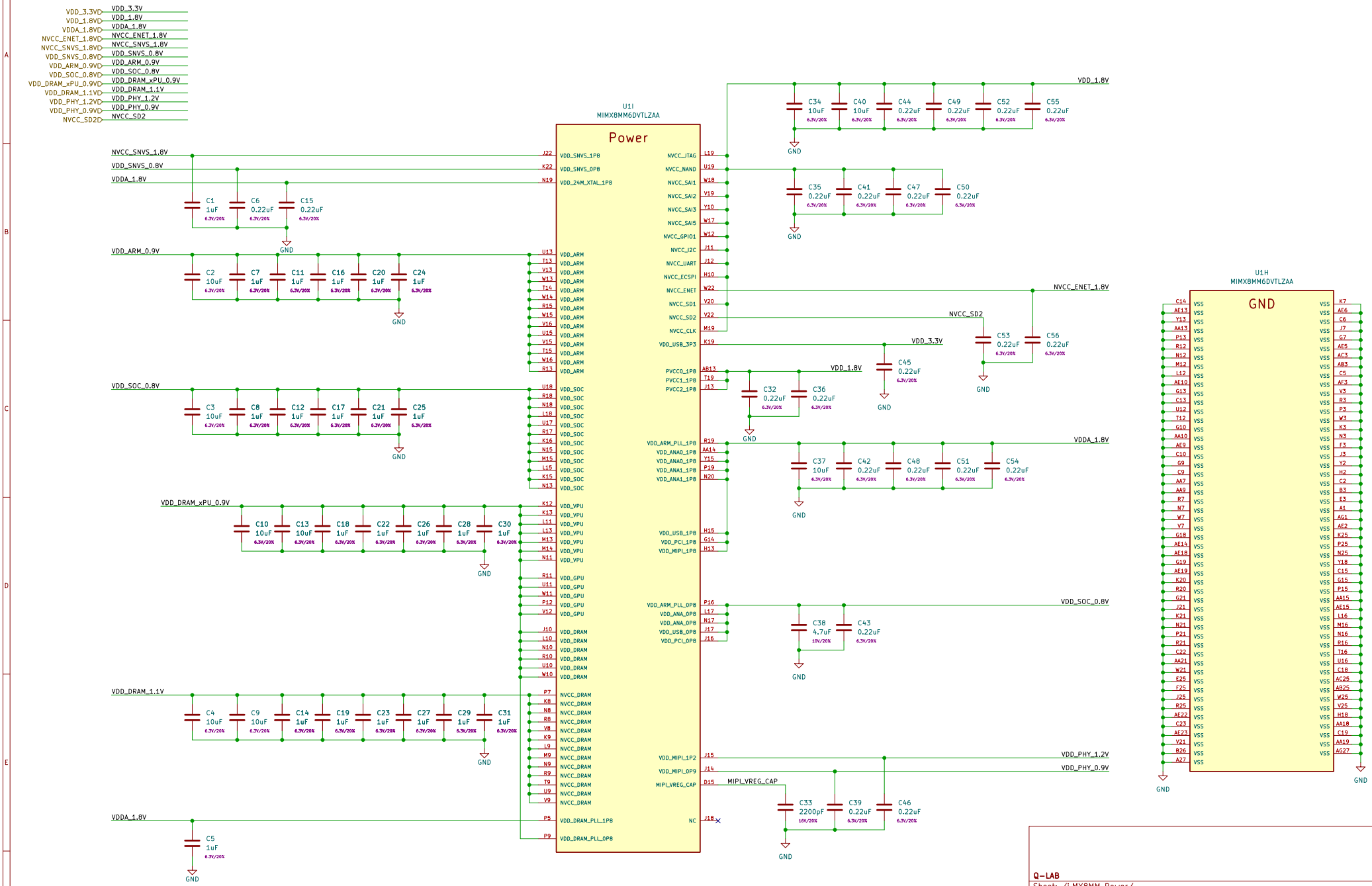
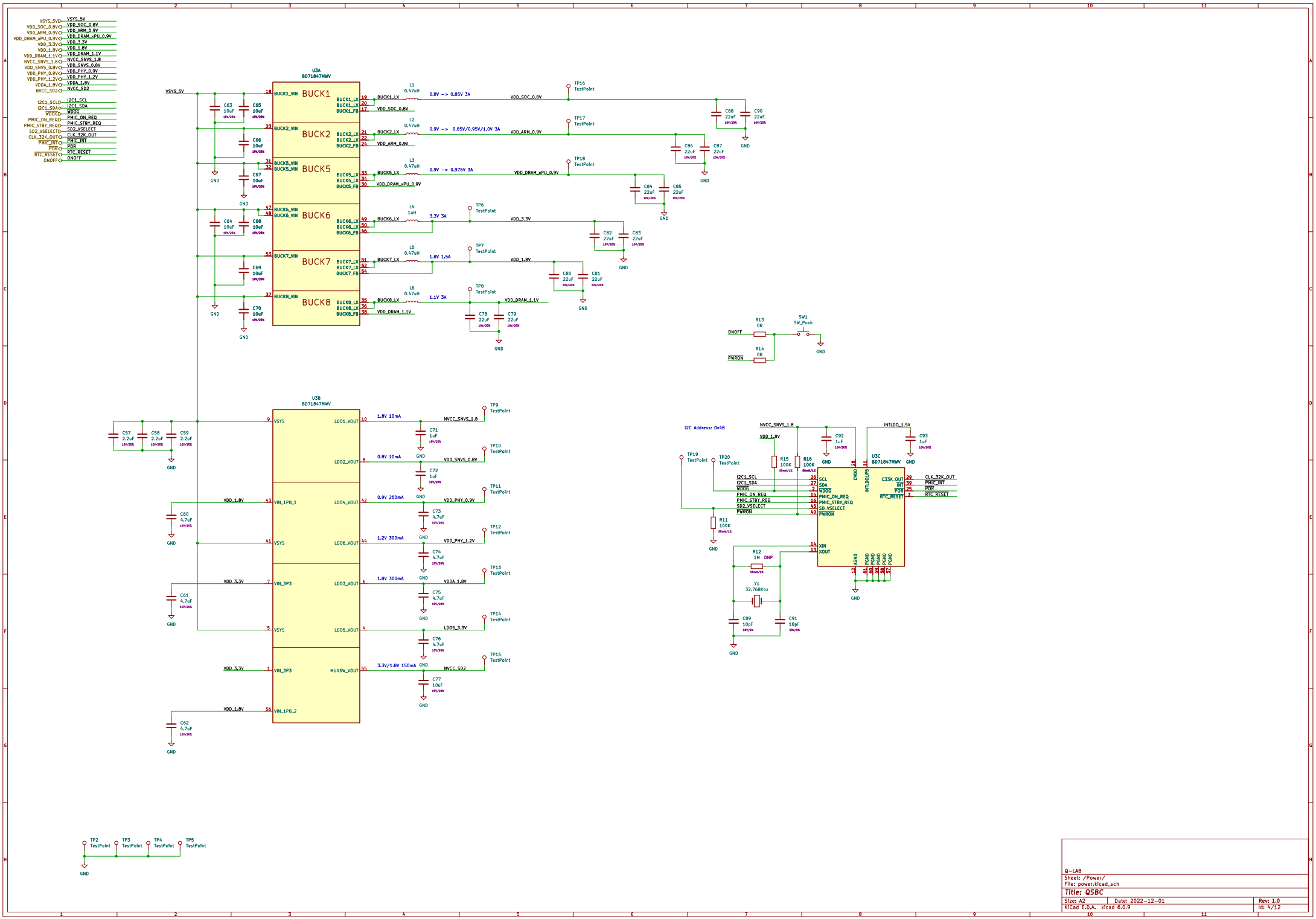
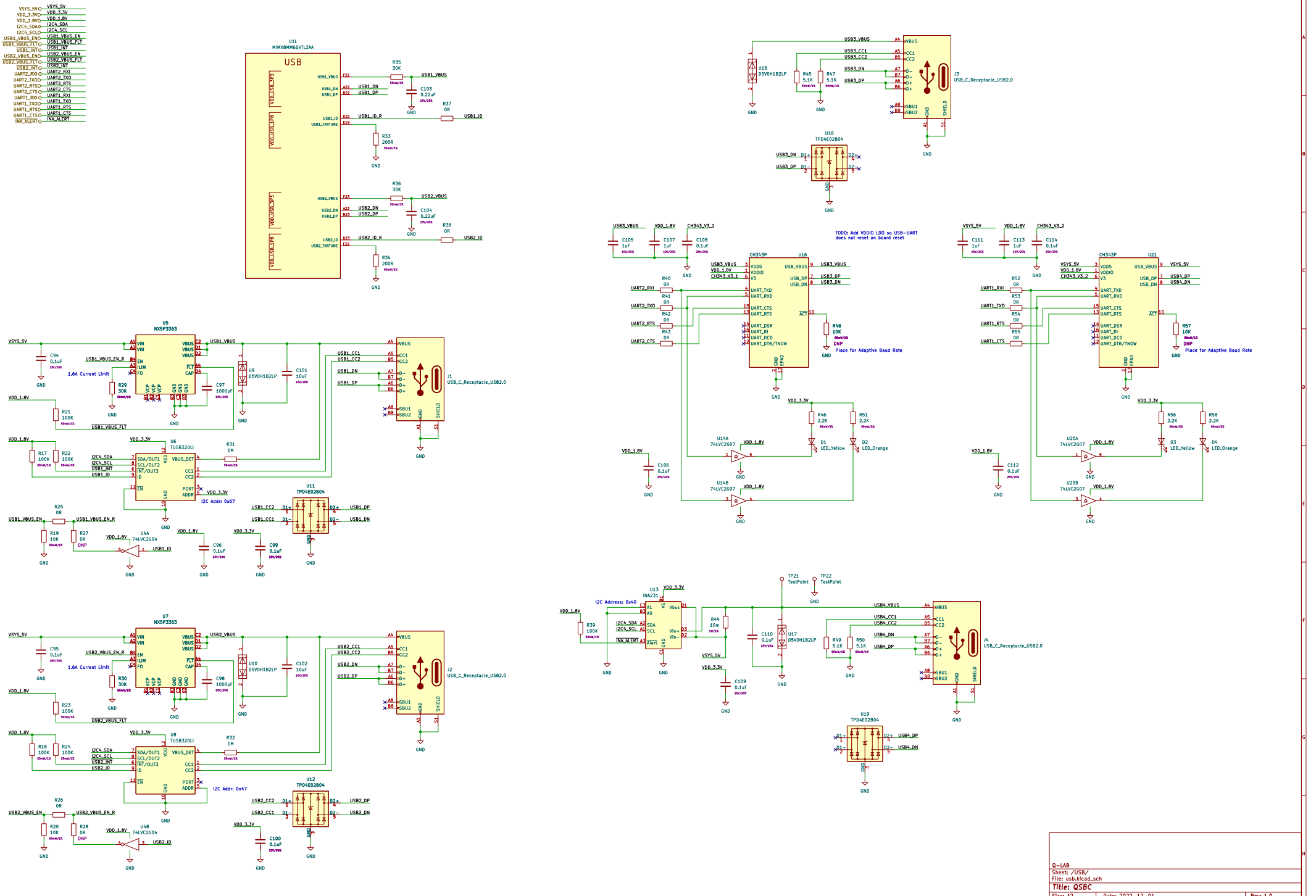

Power
VSYS_5V
CLK_32K_OUT
ONOFF
PDR
RTC_RESET
PMIC_ON_REQ
PMIC_STBY_REQ
WDOG
PMIC_INT
SD2_VSELECT
I2C1_SDA
I2C1_SCL
VDD_3.3V
VDD_1.8V
VDD_ARM_0.9V
VDD_SOC_0.8V
VDD_DRAM_1.1V
VDD_DRAM_xPU_0.9V
VDD_PHY_0.9V
VDD_PHY_1.2V
VDD_SNVS_0.8V
NVCC_SNVS_1.8V
NVCC_SD2
VDDA_1.8V
File: power.kicad_sch
i.MX8MM Power
VDD_3.3V
VDD_1.8V
NVCC_ENET_1.8V
VDD_ARM_0.9V
VDD_SOC_0.8V
VDD_DRAM_1.1V
VDD_DRAM_xPU_0.9V
VDD_PHY_0.9V
VDD_PHY_1.2V
VDD_SNVS_0.8V
NVCC_SNVS_1.8V
NVCC_SD2
VDDA_1.8V
File: imx8mm_power.kicad_sch
Misc
VDD_1.8V
VDD_SNVS_0.8V
NVCC_SNVS_1.8V
CLK_32K_OUT
ONOFF
PDR
RTC_RESET
PMIC_ON_REQD
PMIC_STBY_REQD
File: misc.kicad_sch
DDR
VDD_DRAM_1.1V
File: ddr.kicad_sch
DDR Power
VDD_1.8V
VDD_DRAM_1.1V
File: ddr_power.kicad_sch
Ethernet
VDD_3.3V
VDD_1.8V
NVCC_ENET_1.8V
ENET_RESET
ENET_INT
File: ethernet.kicad_sch
USB
VSYS_5V
VDD_3.3V
VDD_1.8V
I2C4_SDA
I2C4_SCL
UART2_RXD
UART2_TXD
UART2_RTS
UART2_CTS
USB1_INT
USB1_VBUS_EN
USB1_VBUS_FLT
USB2_INT
USB2_VBUS_EN
USB2_VBUS_FLT
UART1_RXI
UART1_TXO
UART1_RTS
UART1_CTS
INA_ALERT
File: usb.kicad_sch
Peripherals
VDD_3.3V
VDD_1.8V
WDOGD
PMIC_INTD
SD2_VSELECTD
I2C1_SDAD
I2C1_SCLD
ENET_INTD
ENET_RESETD
I2C4_SDA
I2C4_SCL
UART2_RXI
UART2_TXO
UART2_RTS
UART2_CTS
USB1_INT
USB1_VBUS_EN
USB1_VBUS_FLT
USB2_INT
USB2_VBUS_EN
USB2_VBUS_FLT
INA_ALERT
DSI86_END
DSI86_INTD
W2_WAKED
W2_RESETD
UART1_RXI
UART1_TXOD
UART1_RTSD
UART1_CTSD
File: peripherals.kicad_sch
Storage
VDD_3.3V
VDD_1.8V
NVCC_SD2
File: storage.kicad_sch
PCIe
VDD_3.3V
VDD_1.8V
W2_RESET
W2_WAKE
File: pcie.kicad_sch
Displayport
VDD_3.3V
VDD_1.8V
I2C1_SCL
I2C1_SDA
DSI86_EN
DSI86_INT
File: displayport.kicad_sch
H1
MountingHole_Pad
H2
MountingHole_Pad
H3
MountingHole_Pad
H4
MountingHole_Pad
GND
VDD_1.8V
VDD_3.3V
Fix for hidden power pin net names
Q-LAB
Sheets: /
File: qsbc.kicad_sch
Title: QSBC
Size: A2
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 1/12

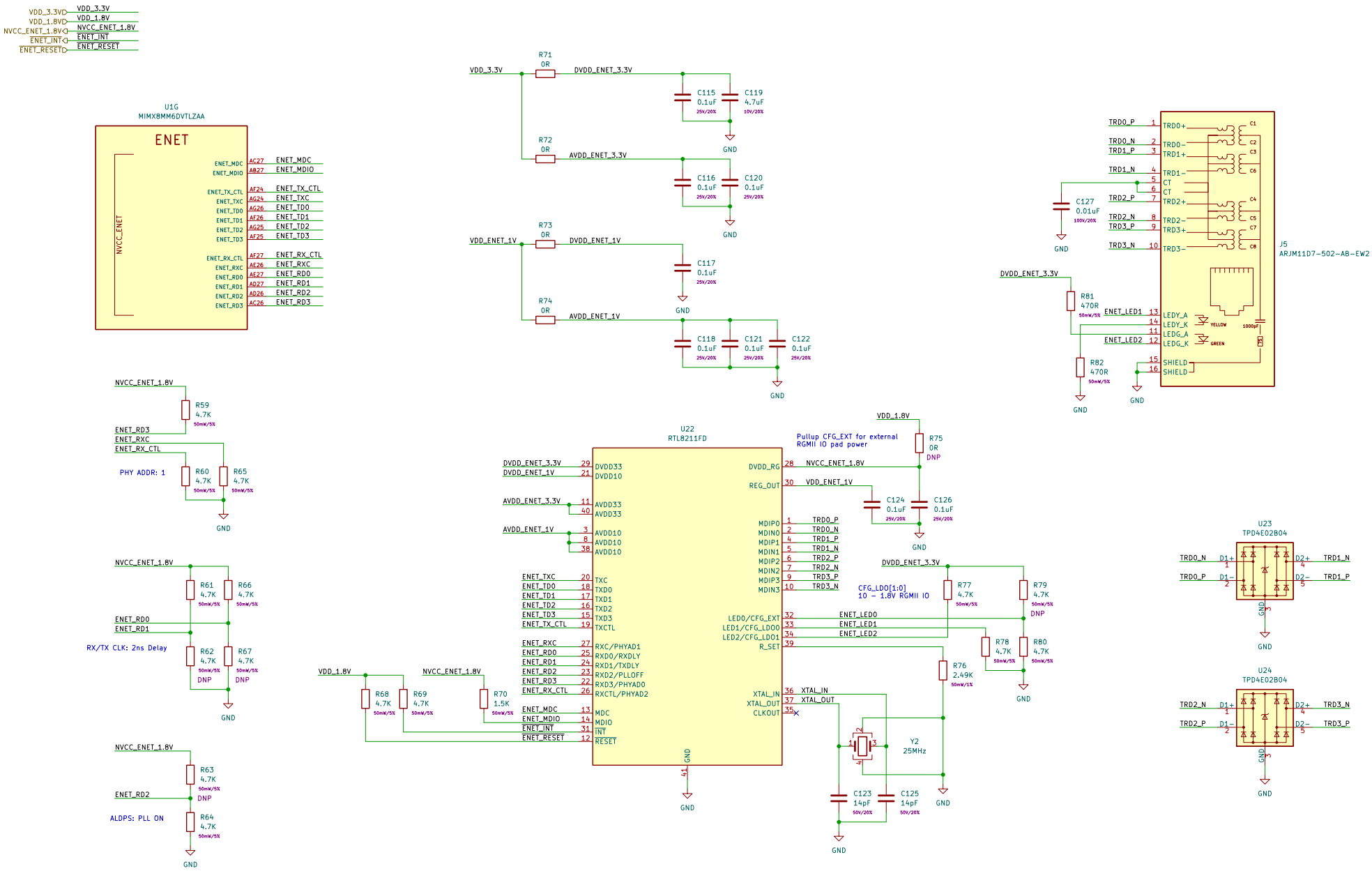

VDD_DRAM_1.1V
VDD_DRAM_1.1V
U1B
MIMX8MM6DVTLZAA
DDR
DRAM_CA0_A J6
DRAM_CA1_A K6
DRAM_CA2_A E4
DRAM_CA3_A D5
DRAM_CA4_A N4
DRAM_CA5_A N5
DRAM_CS0_A K4
DRAM_CS1_A J4
DRAM_CKE0_A F4
DRAM_CKE1_A F5
DRAM_CK_A_P L2
DRAM_CK_A_N L1
DRAM_CA0_B W6
DRAM_CA1_B V6
DRAM_CA2_B AC4
DRAM_CA3_B AD5
DRAM_CA4_B R4
DRAM_CA5_B R5
DRAM_CS0_B V4
DRAM_CS1_B W4
DRAM_CKE0_B AB4
DRAM_CKE1_B AB5
DRAM_CK_B_P U2
DRAM_CK_B_N U1
DRAM_RESET R1
PHY_VREF P1
DRAM_ZN P2
DRAM_AC08
DRAM_AC09
DRAM_AC10
DRAM_AC11
DRAM_AC12
DRAM_AC13
DRAM_AC02
DRAM_AC03
DRAM_AC00
DRAM_AC01
DRAM_AC04
DRAM_AC05
DRAM_AC14
DRAM_AC28
DRAM_AC29
DRAM_AC30
DRAM_AC31
DRAM_AC32
DRAM_AC33
DRAM_AC23
DRAM_AC22
DRAM_AC20
DRAM_AC21
DRAM_AC24
DRAM_AC25
DRAM_AC34
DRAM_AC16
DRAM_AC17
DRAM_AC36
DRAM_AC37
DRAM_AC15
DRAM_AC07
DRAM_AC06
DRAM_AC38
DRAM_AC35
DRAM_AC26
DRAM_AC27
DRAM_ALERT_N
DRAM_RESET_N
DRAM_VREF
DRAM_ZN
DRAM_AC19
DRAM_DM0 A4
DRAM_DM1 F1
DRAM_DQ00 A5
DRAM_DQ01 B5
DRAM_DQ02 D2
DRAM_DQ03 D1
DRAM_DQ04 C1
DRAM_DQ05 B1
DRAM_DQ06 A3
DRAM_DQ07 B4
DRAM_DQ08 F2
DRAM_DQ09 G2
DRAM_DQ10 J1
DRAM_DQ11 J2
DRAM_DQ12 K2
DRAM_DQ13 K1
DRAM_DQ14 E1
DRAM_DQ15 E2
DRAM_DQS0_P A2
DRAM_DQS0_N B2
DRAM_DQS1_P G1
DRAM_DQS1_N H1
DRAM_DQ16 AB2
DRAM_DQ17 AA2
DRAM_DQ18 W1
DRAM_DQ19 W2
DRAM_DQ20 V2
DRAM_DQ21 V1
DRAM_DQ22 AC1
DRAM_DQ23 AC2
DRAM_DQ24 AG5
DRAM_DQ25 AF5
DRAM_DQ26 AD2
DRAM_DQ27 AD1
DRAM_DQ28 AE1
DRAM_DQ29 AF1
DRAM_DQ30 AG3
DRAM_DQ31 AF4
DRAM_DM2 AB1
DRAM_DM3 AG4
DRAM_DQS2_P AA1
DRAM_DQS2_N Y1
DRAM_DQS3_P AG2
DRAM_DQS3_N AF2
DRAM_DMI0_A
DRAM_DMI1_A
DRAM_D00_A
DRAM_D01_A
DRAM_D02_A
DRAM_D03_A
DRAM_D04_A
DRAM_D05_A
DRAM_D06_A
DRAM_D07_A
DRAM_D08_A
DRAM_D09_A
DRAM_D10_A
DRAM_D11_A
DRAM_D12_A
DRAM_D13_A
DRAM_D14_A
DRAM_D15_A
DRAM_DQS0_A_P
DRAM_DQS0_A_N
DRAM_DQS1_A_P
DRAM_DQS1_A_N
DRAM_D00_B
DRAM_D01_B
DRAM_D02_B
DRAM_D03_B
DRAM_D04_B
DRAM_D05_B
DRAM_D06_B
DRAM_D07_B
DRAM_D08_B
DRAM_D09_B
DRAM_D10_B
DRAM_D11_B
DRAM_D12_B
DRAM_D13_B
DRAM_D14_B
DRAM_D15_B
DRAM_DMI0_B
DRAM_DMI1_B
DRAM_DQS0_B_P
DRAM_DQS0_B_N
DRAM_DQS1_B_P
DRAM_DQS1_B_N
TP1
TestPoint
R1
10K
50mW/5%
R2
240R
50mW/1%
GND
GND
DRAM_CK_A_P
DRAM_CK_A_N
R3
150R
50mW/1%
DRAM_CK_B_P
DRAM_CK_B_N
R4
150R
50mW/1%
U2A
MT53D1024M32D4DT
DRAM_CS0_B H4
DRAM_CS1_B H3
K5
DRAM_CKE0_B J4
DRAM_CKE1_B J5
K8
DRAM_CK_B_P J8
DRAM_CK_B_N J9
DRAM_CA0_B H2
DRAM_CA1_B J2
DRAM_CA2_B H9
DRAM_CA3_B H10
DRAM_CA4_B H11
DRAM_CA5_B J11
VDD_DRAM_1.1V G2
CS0_A
NC_H3/CS1_A
NC_K5
CKE0_A
NC_J5/CKE1_A
NC_K8
CK_t_A
CK_c_A
CA0_A
CA1_A
CA2_A
CA3_A
CA4_A
CA5_A
ODT_CA_A
R5
10K
50mW/5%
DNP
R8
10K
50mW/5%
DNP
GND
DMI0_A C3
DQ00_A B2
DQ01_A C2
DQ02_A E2
DQ03_A F2
DQ04_A F4
DQ05_A E4
DQ06_A C4
DQ07_A B4
DQS0_t_A D3
DQS0_c_A E3
DMI1_A C10
DQ08_A B11
DQ09_A C11
DQ10_A E11
DQ11_A F11
DQ12_A F9
DQ13_A E9
DQ14_A C9
DQ15_A B9
DQS1_t_A D10
DQS1_c_A E10
DRAM_CS0_A R4
DRAM_CS1_A R3
N5
DRAM_CKE0_A P4
DRAM_CKE1_A P5
N8
DRAM_CK_A_P P8
DRAM_CK_A_N P9
DRAM_CA0_A R2
DRAM_CA1_A P2
DRAM_CA2_A R9
DRAM_CA3_A R10
DRAM_CA4_A R11
DRAM_CA5_A P11
VDD_DRAM_1.1V T2
CS0_B
NC_R3/CS1_B
NC_N5
CKE0_B
NC_P5/CKE1_B
NC_N8
CK_t_B
CK_c_B
CA0_B
CA1_B
CA2_B
CA3_B
CA4_B
CA5_B
ODT_CA_B
R6
10K
50mW/5%
DNP
R9
10K
50mW/5%
DNP
GND
DMI0_B Y3
DQ00_B AA2
DQ01_B Y2
DQ02_B V2
DQ03_B U2
DQ04_B U4
DQ05_B V4
DQ06_B Y4
DQ07_B AA4
DQS0_t_B W3
DQS0_c_B V3
DMI1_B Y10
DQ08_B AA11
DQ09_B Y11
DQ10_B V11
DQ11_B U11
DQ12_B U9
DQ13_B V9
DQ14_B Y9
DQ15_B AA9
DQS1_t_B W10
DQS1_c_B V10
VDD_DRAM_1.1V
DRAM_RESET T11
A5
A8
G11
RESET
ZQ0
NC_A8/ZQ1
NC_G11
R7
240R
50mW/1%
R10
240R
50mW/1%
DNU_AB12 AB12
DNU_AB11 AB11
DNU_AB2 AB2
DNU_AB1 AB1
DNU_AA12 AA12
DNU_AA1 AA1
DNU_B12 B12
DNU_B1 B1
DNU_A12 A12
DNU_A11 A11
DNU_A2 A2
DNU_A1 A1
Also support MT53D512M32D2DS
for 2GB RAM
Q-LAB
Sheet: /DDR/
File: ddr.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 2/12

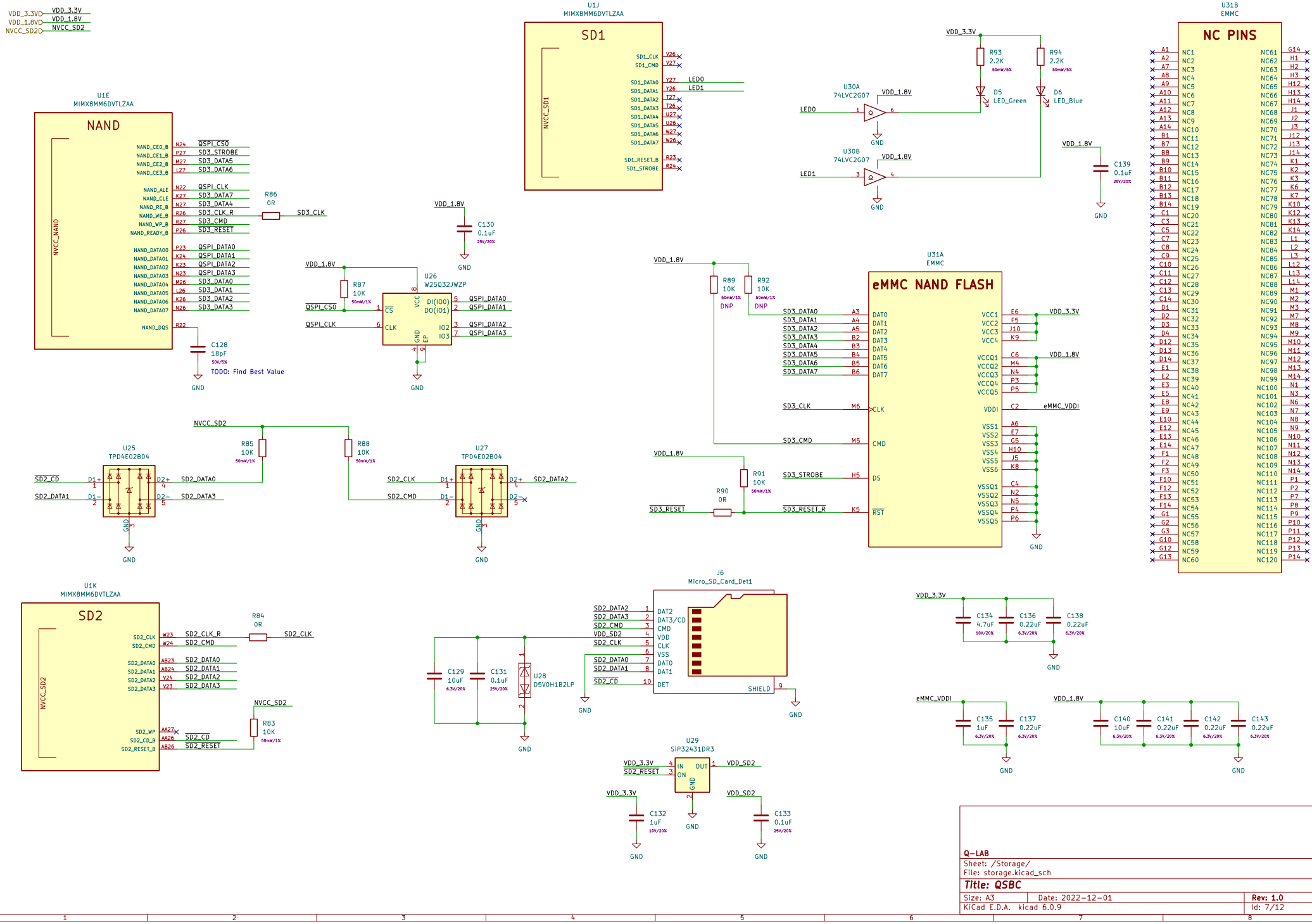

VDD_3.3V
VDD_1.8V
VDDA_1.8V
NVCC_ENET_1.8V
NVCC_SNVS_1.8V
VDD_SNVS_0.8V
VDD_ARM_0.9V
VDD_SOC_0.8V
VDD_DRAM_xPU_0.9V
VDD_DRAM_1.1V
VDD_PHY_1.2V
VDD_PHY_0.9V
NVCC_SD2
U1I
MIMX8MM6DVTLZAA
Power
VDD_SNVS_1P8
VDD_SNVS_0P8
VDD_24M_XTAL_1P8
VDD_ARM
VDD_SOC
VDD_VPU
VDD_GPU
VDD_DRAM
NVCC_DRAM
VDD_DRAM_PLL_1P8
VDD_DRAM_PLL_0P8
NVCC_JTAG
NVCC_NAND
NVCC_SAI1
NVCC_SAI2
NVCC_SAI3
NVCC_SAI5
NVCC_GPIO1
NVCC_I2C
NVCC_UART
NVCC_ECSPI
NVCC_ENET
NVCC_SD1
NVCC_SD2
NVCC_CLK
VDD_USB_3P3
PVCC0_1P8
PVCC1_1P8
PVCC2_1P8
VDD_ARM_PLL_1P8
VDD_ANA0_1P8
VDD_ANA0_1P8
VDD_ANA1_1P8
VDD_ANA1_1P8
VDD_USB_1P8
VDD_PCI_1P8
VDD_MIPI_1P8
VDD_ARM_PLL_0P8
VDD_ANA_0P8
VDD_ANA_0P8
VDD_USB_0P8
VDD_PCI_0P8
VDD_MIPI_1P2
VDD_MIPI_0P9
MIPI_VREG_CAP
NC
U1H
MIMX8MM6DVTLZAA
GND
VSS
GND
Q-LAB
Sheet: /i.MX8MM Power/
File: imx8mm_power.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 3/12


VSYS_5V
VDD_SOC_0.8V
VDD_ARM_0.9V
VDD_DRAM_xPU_0.9V
VDD_3.3V
VDD_1.8V
VDD_DRAM_1.1V
NVCC_SNVS_1.8
VDD_SNVS_0.8V
VDD_PHY_0.9V
VDD_PHY_1.2V
VDDA_1.8V
NVCC_SD2
I2C1_SCL
I2C1_SDA
WDOG
PMIC_ON_REQ
PMIC_STBY_REQ
SD2_VSELECT
CLK_32K_OUT
PMIC_INT
POR
RTC_RESET
ONOFF
U3A
BD71847MWV
BUCK1
BUCK2
BUCK5
BUCK6
BUCK7
BUCK8
L1 0.47uH
0.8V -> 0.85V 3A
L2 0.47uH
0.9V -> 0.85V/0.95V/1.0V 3A
L3 0.47uH
0.9V -> 0.975V 3A
L4 1uH
3.3V 3A
L5 0.47uH
1.8V 1.5A
L6 0.47uH
1.1V 3A
U3B
BD71847MWV
LDO1_VOUT
LDO2_VOUT
LDO4_VOUT
LDO6_VOUT
LDO3_VOUT
LDO5_VOUT
MUXSW_VOUT
1.8V 10mA
0.8V 10mA
0.9V 250mA
1.2V 300mA
1.8V 300mA
3.3V/1.8V 150mA
NVCC_SNVS_1.8
VDD_SNVS_0.8V
VDD_PHY_0.9V
VDD_PHY_1.2V
VDDA_1.8V
LDO5_3.3V
NVCC_SD2
I2C Address: 0x4B
U3C
BD71847MWV
INTLDO_1.5V
C33K_OUT
INT
POR
RTC_RESET
CLK_32K_OUT
PMIC_INT
R11 100K
R12 1M DNP
R13 0R
R14 0R
R15 100K
R16 100K
Y1 32.768Khz
C89 18pF
C91 18pF
SW1 SW_Push
TP2 TestPoint
TP3 TestPoint
TP4 TestPoint
TP5 TestPoint
GND
Q-LAB
Sheets: /Power/
File: power.kicad_sch
Title: QSBC
Size: A2
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 4/12

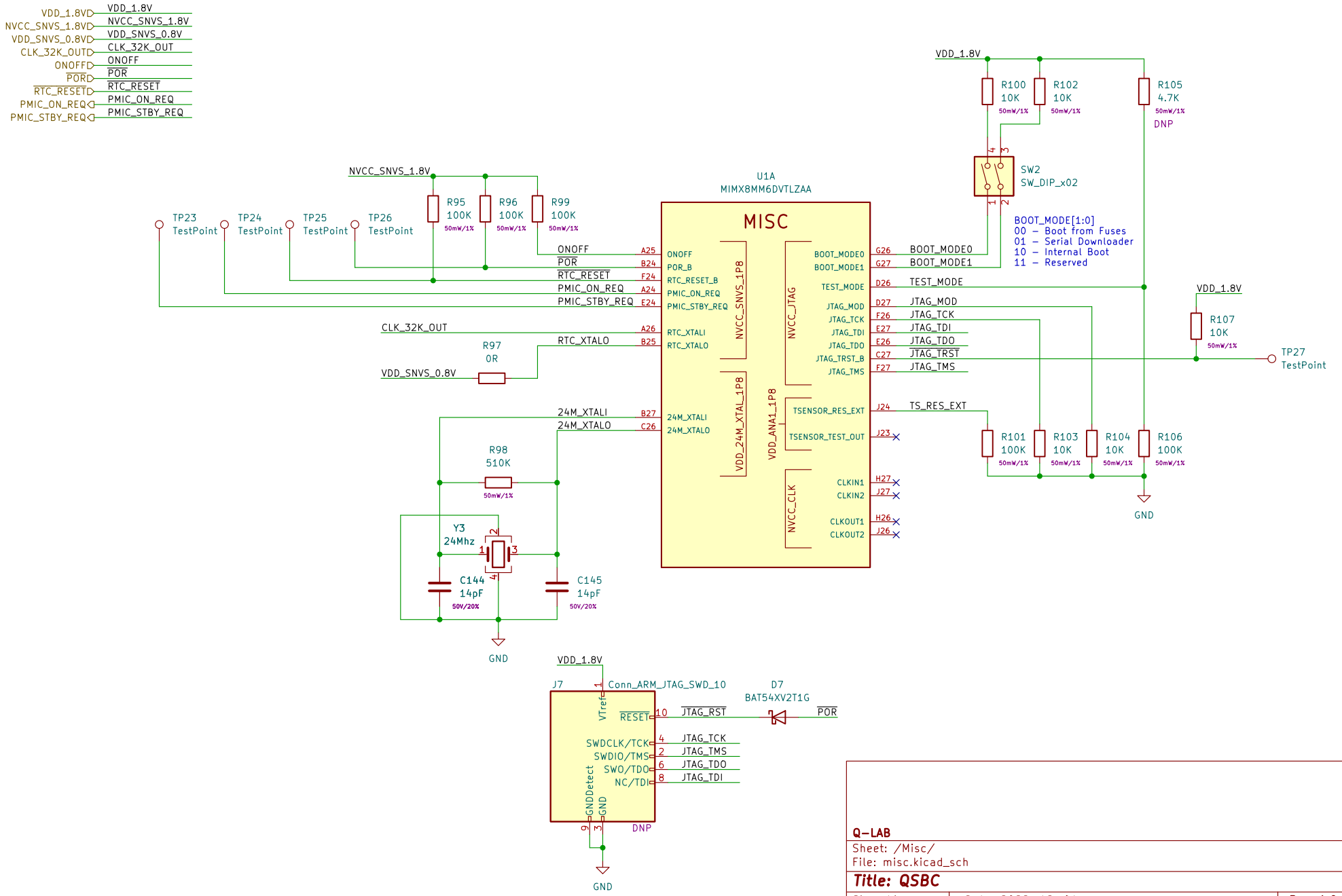


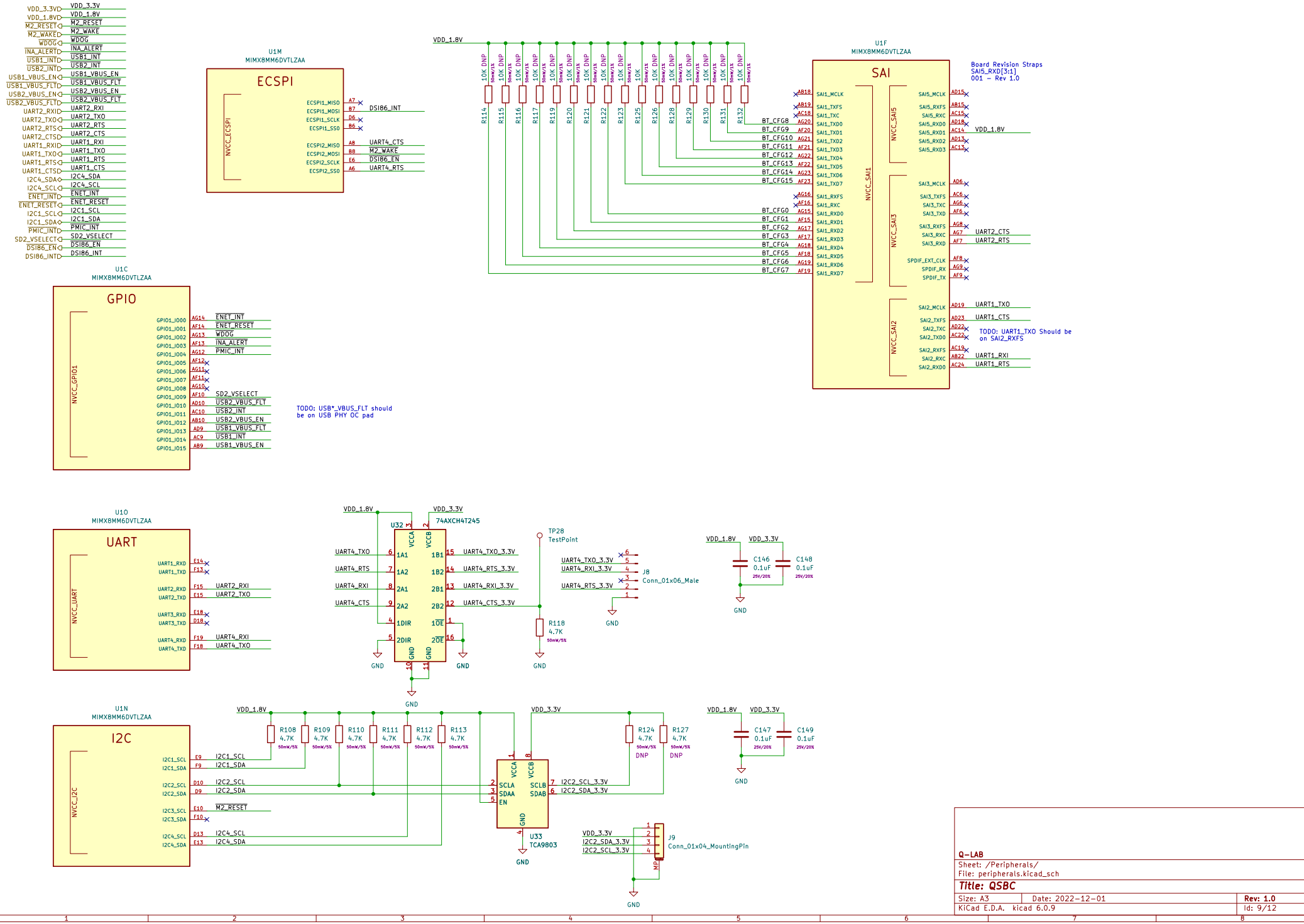
Q-LAB	
Sheet: /USB/	
File: usb.kicad_sch	
Title: QSBC	
Size: A2	Date: 2022-12-01
KiCad E.D.A. kicad 6.0.9	Rev: 1.0
	Id: 5/12

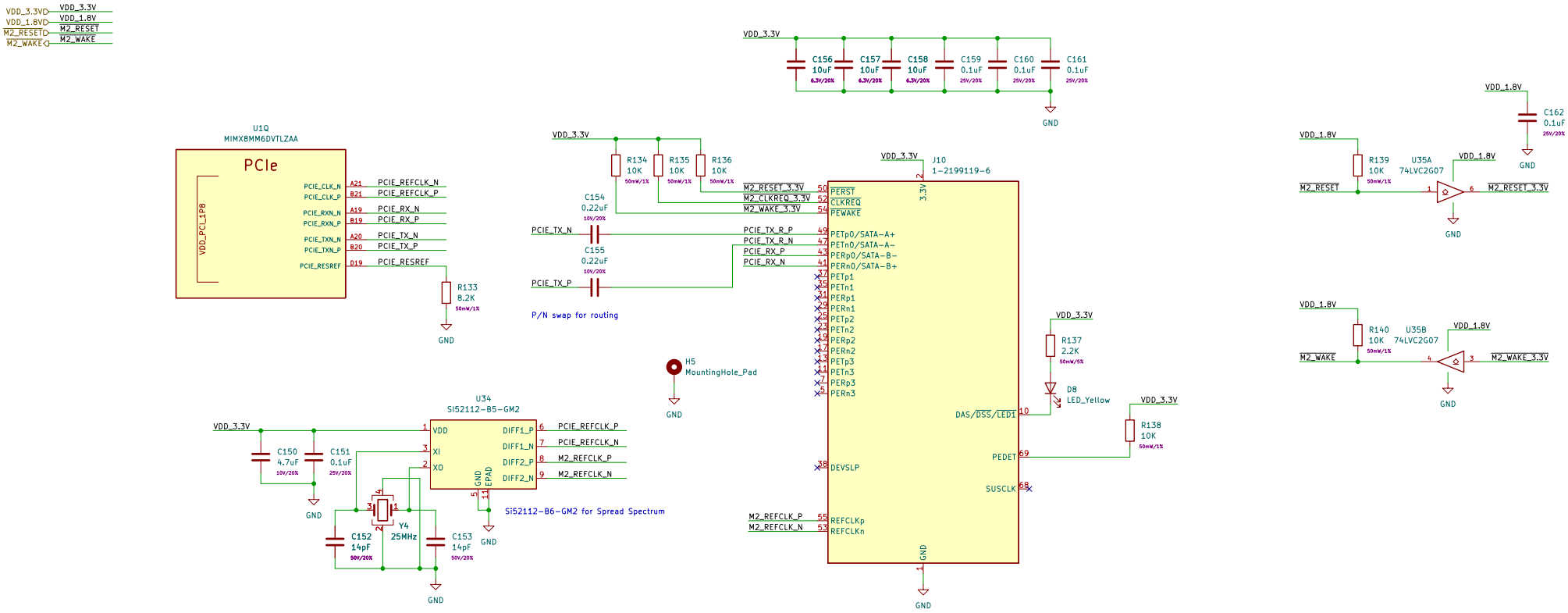

VDD_3.3V
VDD_1.8V
NVCC_ENET_1.8V
ENET_INT
ENET_RESET
U1G
MIMX8MM6DVTLZAA
ENET
NVCC_ENET
ENET_MDC
ENET_MDIO
ENET_TX_CTL
ENET_TXC
ENET_TD0
ENET_TD1
ENET_TD2
ENET_TD3
ENET_RX_CTL
ENET_RXC
ENET_RD0
ENET_RD1
ENET_RD2
ENET_RD3
R71 0R
R72 0R
R73 0R
R74 0R
DVDD_ENET_3.3V
AVDD_ENET_3.3V
DVDD_ENET_1V
AVDD_ENET_1V
C115 0.1uF
C119 4.7uF
C116 0.1uF
C120 0.1uF
C117 0.1uF
C118 0.1uF
C121 0.1uF
C122 0.1uF
GND
J5
ARJM11D7-502-AB-EW2
TRD0+
TRD0-
TRD1+
TRD1-
TRD2+
TRD2-
TRD3+
TRD3-
CT
C127 0.01uF
R81 470R
R82 470R
ENET_LED1
ENET_LED2
LEDY_A
LEDY_K
LEDG_A
LEDG_K
YELLOW
GREEN
SHIELD
R59 4.7K
R60 4.7K
R65 4.7K
PHY ADDR: 1
R61 4.7K
R66 4.7K
R62 4.7K
R67 4.7K
DNP
RX/TX CLK: 2ns Delay
R63 4.7K
R64 4.7K
ALDPS: PLL ON
U22
RTL8211FD
DVDD33
DVDD10
AVDD33
AVDD10
TXC
TXD0
TXD1
TXD2
TXD3
TXCTL
RXC/PHYAD1
RXD0/RXDLY
RXD1/TXDLY
RXD2/PLLOFF
RXD3/PHYAD0
RXCTL/PHYAD2
MDC
MDIO
INT
RESET
DVDD_RG
REG_OUT
MDIP0
MDIN0
MDIP1
MDIN1
MDIP2
MDIN2
MDIP3
MDIN3
LED0/CFG_EXT
LED1/CFG_LDO0
LED2/CFG_LDO1
R_SET
XTAL_IN
XTAL_OUT
CLKOUT
Pullup CFG_EXT for external RGMII IO pad power
R75 0R DNP
C124 0.1uF
C126 0.1uF
CFG_LDO[1:0]
10 – 1.8V RGMII IO
R77 4.7K
R79 4.7K DNP
R78 4.7K
R80 4.7K
R76 2.49K
Y2
25MHz
C123 14pF
C125 14pF
R68 4.7K
R69 4.7K
R70 1.5K
U23
TPD4E02B04
U24
TPD4E02B04
D1+
D1-
D2+
D2-
Q-LAB
Sheet: /Ethernet/
File: ethernet.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 6/12

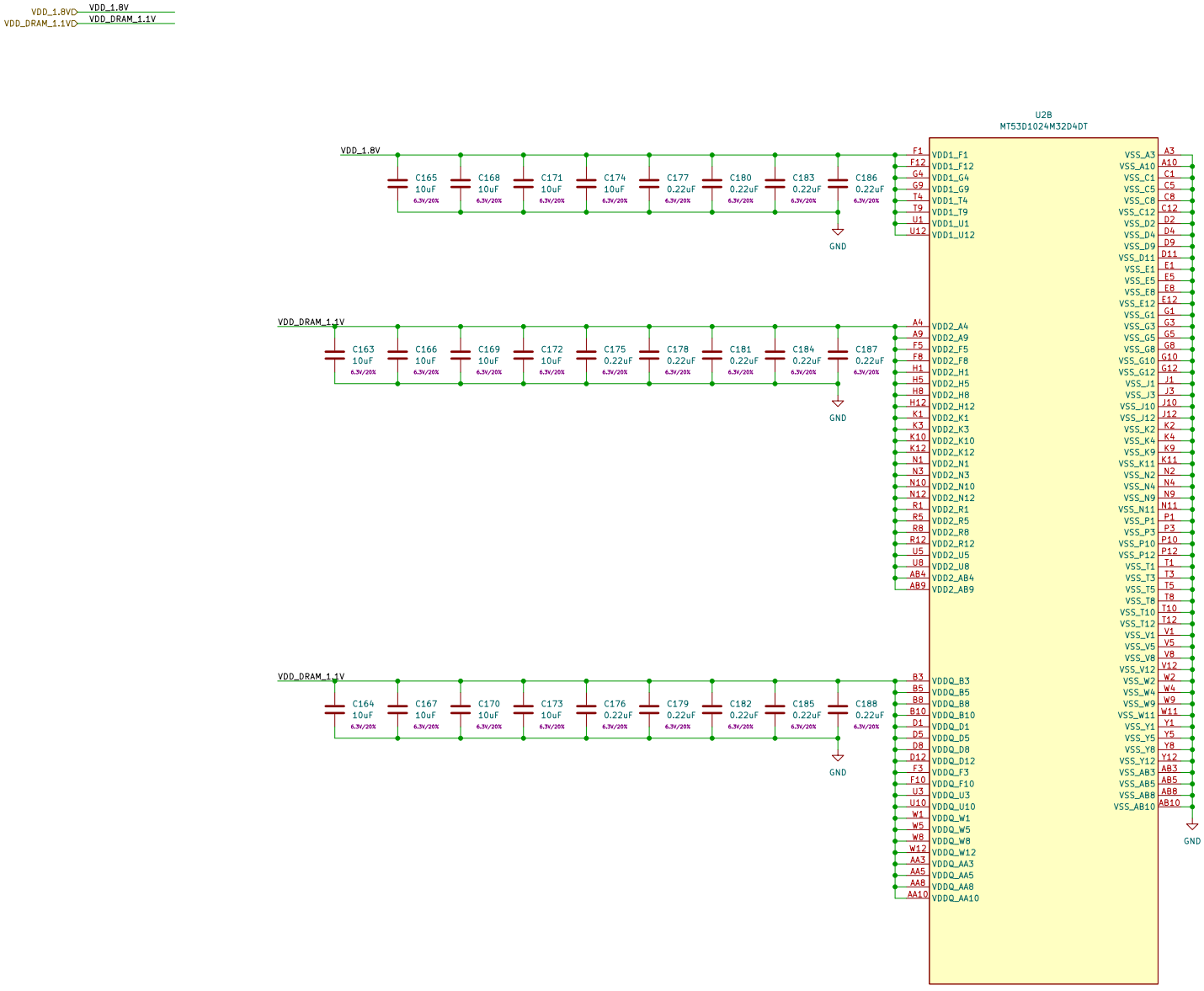


Q-LAB	
Sheet: /Storage/	
File: storage.kicad_sch	
Title: QSBC	
Size: A3	Date: 2022-12-01
KiCad E.D.A. kicad 6.0.9	Rev: 1.0
	Id: 7/12

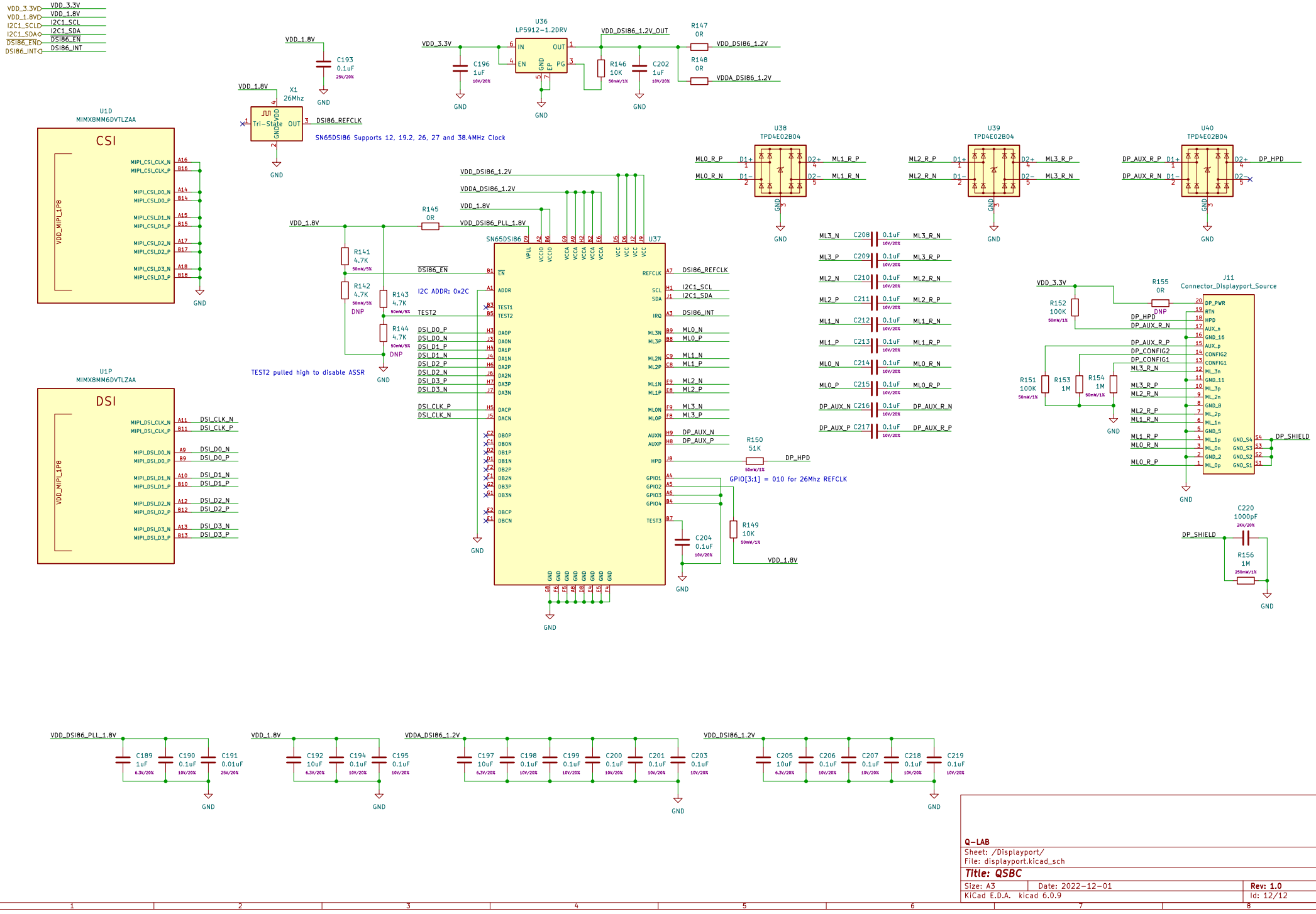

VDD_1.8V
NVCC_SNVS_1.8V
VDD_SNVS_0.8V
CLK_32K_OUT
ONOFF
POR
RTC_RESET
PMIC_ON_REQ
PMIC_STBY_REQ
U1A
MIMX8MM6DVTLZAA
MISC
R95 100K
R96 100K
R99 100K
R97 0R
R98 510K
R100 10K
R102 10K
R105 4.7K DNP
R101 100K
R103 10K
R104 10K
R106 100K
R107 10K
SW2 SW_DIP_x02
BOOT_MODE[1:0]
00 – Boot from Fuses
01 – Serial Downloader
10 – Internal Boot
11 – Reserved
TP23 TestPoint
TP24 TestPoint
TP25 TestPoint
TP26 TestPoint
TP27 TestPoint
Y3 24Mhz
C144 14pF 50V/20%
C145 14pF 50V/20%
J7 Conn_ARM_JTAG_SWD_10
D7 BAT54XV2T1G
GND
DNP
Q-LAB
Sheet: /Misc/
File: misc.kicad_sch
Title: QSBC
Size: A4
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 8/12


VDD_3.3V
VDD_1.8V
M2_RESET
M2_WAKE
WDOG
INA_ALERT
USB1_INT
USB2_INT
USB1_VBUS_EN
USB1_VBUS_FLT
USB2_VBUS_EN
USB2_VBUS_FLT
UART2_RXI
UART2_TXO
UART2_RTS
UART2_CTS
UART1_RXI
UART1_TXO
UART1_RTS
UART1_CTS
I2C4_SDA
I2C4_SCL
ENET_INT
ENET_RESET
I2C1_SCL
I2C1_SDA
PMIC_INT
SD2_VSELECT
DSI86_EN
DSI86_INT
U1M
MIMX8MM6DVTLZAA
ECSPI
NVCC_ECSPI
ECSPI1_MISO
ECSPI1_MOSI
ECSPI1_SCLK
ECSPI1_SS0
ECSPI2_MISO
ECSPI2_MOSI
ECSPI2_SCLK
ECSPI2_SS0
DSI86_INT
UART4_CTS
M2_WAKE
DSI86_EN
UART4_RTS
U1F
SAI
NVCC_SAI1
NVCC_SAI5
NVCC_SAI3
NVCC_SAI2
Board Revision Straps
SAI5_RXD[3:1]
001 – Rev 1.0
VDD_1.8V
UART2_CTS
UART2_RTS
UART1_TXO
UART1_CTS
UART1_RXI
UART1_RTS
TODO: UART1_TXO Should be on SAI2_RXFS
U1C
GPIO
NVCC_GPIO1
ENET_INT
ENET_RESET
WDOG
INA_ALERT
PMIC_INT
SD2_VSELECT
USB2_VBUS_FLT
USB2_INT
USB2_VBUS_EN
USB1_VBUS_FLT
USB1_INT
USB1_VBUS_EN
TODO: USB*_VBUS_FLT should be on USB PHY OC pad
U1O
UART
NVCC_UART
UART2_RXI
UART2_TXO
UART4_RXI
UART4_TXO
U32
74AXCH4T245
VCCA
VCCB
UART4_TXO
UART4_RTS
UART4_RXI
UART4_CTS
UART4_TXO_3.3V
UART4_RTS_3.3V
UART4_RXI_3.3V
UART4_CTS_3.3V
TP28
TestPoint
J8
Conn_01x06_Male
R118
4.7K
C146
C148
0.1uF
U1N
I2C
NVCC_I2C
I2C1_SCL
I2C1_SDA
I2C2_SCL
I2C2_SDA
I2C3_SCL
I2C3_SDA
I2C4_SCL
I2C4_SDA
M2_RESET
R108
R109
R110
R111
R112
R113
R124
R127
U33
TCA9803
SCLA
SDAA
EN
SCLB
SDAB
I2C2_SCL_3.3V
I2C2_SDA_3.3V
C147
C149
J9
Conn_01x04_MountingPin
MP
GND
Q-LAB
Sheet: /Peripherals/
File: peripherals.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 9/12


VDD_3.3V
VDD_1.8V
M2_RESET
M2_WAKE
U1Q
MIMX8MM6DVTLZAA
PCIe
VDD_PCL1P8
PCIE_CLK_N
PCIE_CLK_P
PCIE_RXN_N
PCIE_RXN_P
PCIE_TXN_N
PCIE_TXN_P
PCIE_RESREF
PCIE_REFCLK_N
PCIE_REFCLK_P
PCIE_RX_N
PCIE_RX_P
PCIE_TX_N
PCIE_TX_P
R133
8.2K
GND
C156
10uF
C157
10uF
C158
10uF
C159
0.1uF
C160
0.1uF
C161
0.1uF
R134
10K
R135
10K
R136
10K
C154
0.22uF
C155
0.22uF
P/N swap for routing
H5
MountingHole_Pad
J10
1-2199119-6
PERST
CLKREQ
PEWAKE
PETp0/SATA-A+
PETn0/SATA-A-
PERp0/SATA-B-
PERn0/SATA-B+
DAS/DSS/LED1
DEVSLP
PEDET
SUSCLK
REFCLKp
REFCLKn
R137
2.2K
D8
LED_Yellow
R138
10K
C162
0.1uF
R139
10K
U35A
74LVC2G07
R140
10K
U35B
74LVC2G07
U34
SI52112-B5-GM2
VDD
XI
XO
DIFF1_P
DIFF1_N
DIFF2_P
DIFF2_N
GND
EPAD
C150
4.7uF
C151
0.1uF
Y4
25MHz
C152
14pF
C153
14pF
SI52112-B6-GM2 for Spread Spectrum
Q-LAB
Sheet: /PCIe/
File: pcie.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 10/12



Q-LAB		
Sheet: /DDR Power/		
File: ddr_power.kicad_sch		
Title: QSBC		
Size: A3	Date: 2022-12-01	**Rev: 1.0**
KiCad E.D.A. kicad 6.0.9		Id: 11/12


VDD_3.3V
VDD_1.8V
I2C1_SCL
I2C1_SDA
DSI86_EN
DSI86_INT
U36
LP5912-1.2DRV
VDD_DSI86_1.2V_OUT
R147 0R
R148 0R
VDD_DSI86_1.2V
VDDA_DSI86_1.2V
C196 1uF
R146 10K
C202 1uF
X1 26Mhz
DSI86_REFCLK
SN65DSI86 Supports 12, 19.2, 26, 27 and 38.4MHz Clock
U1D
MIMX8MM6DVTLZAA
CSI
VDD_MIPI_1P8
MIPI_CSI_CLK_N
MIPI_CSI_CLK_P
MIPI_CSI_D0_N
MIPI_CSI_D0_P
MIPI_CSI_D1_N
MIPI_CSI_D1_P
MIPI_CSI_D2_N
MIPI_CSI_D2_P
MIPI_CSI_D3_N
MIPI_CSI_D3_P
U37
SN65DSI86
R145 0R
R141 4.7K
R142 4.7K DNP
R143 4.7K
R144 4.7K DNP
DSI86_EN
I2C ADDR: 0x2C
TEST2
TEST2 pulled high to disable ASSR
DSI_D0_P
DSI_D0_N
DSI_D1_P
DSI_D1_N
DSI_D2_P
DSI_D2_N
DSI_D3_P
DSI_D3_N
DSI_CLK_P
DSI_CLK_N
ML0_N
ML0_P
ML1_N
ML1_P
ML2_N
ML2_P
ML3_N
ML3_P
DP_AUX_N
DP_AUX_P
R150 51K
DP_HPD
GPIO[3:1] = 010 for 26Mhz REFCLK
R149 10K
C204 0.1uF
U1P
MIMX8MM6DVTLZAA
DSI
U38
TPD4E02B04
U39
TPD4E02B04
U40
TPD4E02B04
C208 0.1uF
C209 0.1uF
C210 0.1uF
C211 0.1uF
C212 0.1uF
C213 0.1uF
C214 0.1uF
C215 0.1uF
C216 0.1uF
C217 0.1uF
J11
Connector_Displayport_Source
R155 0R DNP
R152 100K
R151 100K
R153 1M
R154 1M
C220 1000pF
R156 1M
DP_SHIELD
C189 1uF
C190 0.1uF
C191 0.01uF
C192 10uF
C194 0.1uF
C195 0.1uF
C197 10uF
C198 0.1uF
C199 0.1uF
C200 0.1uF
C201 0.1uF
C203 0.1uF
C205 10uF
C206 0.1uF
C207 0.1uF
C218 0.1uF
C219 0.1uF
GND
Q-LAB
Sheet: /Displayport/
File: displayport.kicad_sch
Title: QSBC
Size: A3
Date: 2022-12-01
Rev: 1.0
KiCad E.D.A. kicad 6.0.9
Id: 12/12