Ardhendu Sekhar Dash Featured

Proven experience as embedded tester, embedded developer and firmware developer.Knowledge and experience in ARM processor.posses good knowledge on Embedded C ,data structure ,linux ,python, Image Processing, pwm, communication protocol UART and SPI.

Current Designation: Embedded Software Engineer

Current Company: Atop Technology

Current Location: Bengaluru / Bangalore

Pref. Location: Bengaluru / Bangalore,Hyderabad

Functional Area: IT Software - Embedded /EDA /VLSI /ASIC /Chip Des.

Role: Software Developer

Industry: Semiconductors/Electronics

Marital Status: Single/unmarried

Total Experience: 1 Year(s) 5 Month(s)

Notice Period: 1 Month

Highest Degree: M.Tech [Electronics/Telecommunication]

Key Skills: embedded software development,embedded testing,data structures,shell scripting,linux,sdk,keil,embedded c,ARM Processor,Python,Firmware Development,PWM

Verified : **Phone Number |** **Email - id**

ID: 08437ecb4b290a6629 Last Active: 2-Feb-20 Last Modified: 27-Jan-20

Summary

Result-oriented Professional with 1.5 years of experience in Embedded Software Development,Embedded Design,Embedded Testing,Firmware Development.Analyzing and enhancing efficiency, stability of system. Having good experience in writing test cases based on requirement. good experience in C,Python,linux and data structure.

Work Experience

Atop Technology as Embedded Software Engineer
Dec 2018 to Till Date

1. Worked on protocols like Modbus in API devlopment.
2.Experience in Xilinx FPGA processor-based software development by using Xilinx SDK.
3.Good knowledge on IRIG-B time synchronization .
4.Worked on SPI and UART communication protocols.
5.Worked on debug tools such as JTAG.
6.Worked on BRAM.
7.Sound knowledge on SOC.
8. Hands on experience in computer vision.

Education

UG: **B.Tech/B.E. (Electronics/Telecommunication)** from **Siksha O Anusandhan University** in **2018**

PG: **M.Tech (Electronics/Telecommunication)** from **SIKSHA O ANUSANDHAN UNIVERSITY** in **2018**

Other Qualifications/Certifications/Programs:

Professional Development Course in Embedded System Design

IT Skills

Skill Name	Version	Last Used	Experience
Eclipse			
SDK (2018.1), Keil			
Mac OS X, Windows, Ubuntu			
LaTeX, Pack Office			2 Year(s)
C		2019	2 Year(s)
Linux			1 Year(s) 5 Month(s)
Shell Scripting			
Data Structures		2019	1 Year(s) 0 Month(s)
Python	2.7	2019	1 Year(s) 0 Month(s)

Languages Known

Language	Proficiency	Read	Write	Speak
English	Expert			
Hindi	Expert			
Oriya	Expert			

Projects

Project Title: PWM Pulse Generation for IRIG-B
Client: Product Development
Nature of Employment: Full Time
Role: Programmer

Duration: Sep 2019 - Dec 2019
Onsite / Offsite: Offsite
Team Size: 2

Skill Used: embedded c
Project Details: Projects Worked On

Description : IRIG-B frame consist of 100 pps (pulse per second) having time information in B.C.D. format. Each pulse is of 10ms duration. The reference and position identifier is of 8ms duration, Bit 1 is of 5ms duration and Bit 0 is of 2ms duration for generating pwm pulse i have used TTC(Triple Timer Counter).
Role : Designing and writing code to generate pwm waveform.
Hardware used : Xilinx Zynq Processor and Oscilloscope
Team Size : 2

Project Title: Modbus Protocol device Development
Client: Product Development
Nature of Employment: Full Time
Duration: Dec 2018 - Aug 2019
Role: Programmer
Onsite / Offsite: Offsite
Team Size: 3

Skill Used: Linux, C
Project Details: Xilinx Zynq processor-based module loaded with Modbus server firmware. Module used to receive Modbus data and store it in a memory (Block RAM). This data shall be shared with another processor via UART/SPI. The processor shall receive the data and do further processing. API calls and memory mapping information shall be provided so that other processor can get data from the memory via UART/SPI interface. By collecting the data, the processor can use it for further applications.
Role : Speed Example coding,Interface document creation, Reading and understanding the Test cases,Testing and validating the Module,Test report creation. Processor : Xilinx Zynq Processor and TI AM3359 Processor
Team Size : 3

Project Title: Face Detection Using Boosted Cascade Approach
Client: Final year Project
Nature of Employment: Full Time
Duration: Aug 2017 - Apr 2018
Role: Programmer
Onsite / Offsite: Offsite
Team Size: 2

Skill Used: PythonOpencv
Project Details: Viola-Jones objection detection approach is used to detect faces in video sequences. Here we train the boosted cascade classifier using only the frontal faces. The method for face recognition process has two phases training and detecting. In training phase, grayscale frontal face images from Yale database and non-face images has been used. In detection phase real time video has taken from webcam to detect frontal face and eye blinks.
Role : Preparing Design and Test Description Documents,Application coding.

Affirmative Action

Category: General

Physically Challenged: No

Work Authorization

Job Type: Permanent

Employment Status: Full time