test.log Page 1

```
Command: /home/BRN22/SKaRthik/uvm_methods/uDef_phase/./simv -a test.log +ntb_random_
seed_automatic
Chronologic VCS simulator copyright 1991-2022
Contains Synopsys proprietary information.
Compiler version T-2022.06-SP1_Full64; Runtime version T-2022.06-SP1_Full64; Nov 15
 12:58 2023
NOTE: automatic random seed used: 371048655
UVM-1.1d.Synopsys
(C) 2007-2013 Mentor Graphics Corporation
(C) 2007-2013 Cadence Design Systems, Inc.
(C) 2006-2013 Synopsys, Inc.
(C) 2011-2013 Cypress Semiconductor Corp.
  *****
                    IMPORTANT RELEASE NOTES
                                                       ******
  You are using a version of the UVM library that has been compiled
  with `UVM_NO_DEPRECATED undefined.
  See http://www.eda.org/svdb/view.php?id=3313 for more details.
  You are using a version of the UVM library that has been compiled
  with `UVM_OBJECT_MUST_HAVE_CONSTRUCTOR undefined.
  See http://www.eda.org/svdb/view.php?id=3770 for more details.
       (Specify +UVM_NO_RELNOTES to turn off this notice)
UVM_INFO @ 0: reporter [RNTST] Running test test...
UVM_INFO @ 0: uvm_test_top.m_env [reset] reset_phase called
UVM_INFO @ 1: uvm_test_top.m_env [rest] reset_phase returning
UVM_INFO @ 1: uvm_test_top.m_env [configure] configure_phase called
UVM_INFO @ 2: uvm_test_top.m_env [configure] configure_phase returning UVM_INFO @ 2: uvm_test_top.m_env [karthi] karthi_phase called UVM_INFO @ 3: uvm_test_top.m_env [karthi] karthi_phase returning
UVM_INFO @ 3: uvm_test_top.m_env [main] main_phase called
UVM_INFO @ 4: uvm_test_top.m_env [main] main_phase returning
UVM_INFO @ 4: uvm_test_top.m_env [shutdown] shutdown_phase called UVM_INFO @ 5: uvm_test_top.m_env [shutdown] shutdown_phase returning
--- UVM Report Summary ---
** Report counts by severity
UVM_INFO: 11
UVM_WARNING :
UVM_ERROR : 0
UVM_FATAL : 0
** Report counts by id
[RNTST] 1
[configure]
             2
[karthi]
[main]
           1
[reset]
[rest]
[shutdown]
$finish called from file "/home/cad/eda/SYNOPSYS/VCS/vcs/T-2022.06-SP1/etc/uvm/base/
uvm_root.svh", line 437.
$finish at simulation time
           VCS Simulation Report
Time: 5 ns
CPU Time:
                1.260 seconds; Data structure size: 0.5Mb
Wed Nov 15 12:58:54 2023
```