

**Swinburne University of Technology**

*Ho Chi Minh Campus*

**COS10004**

***Computer System***

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**Student ID:** 104198996

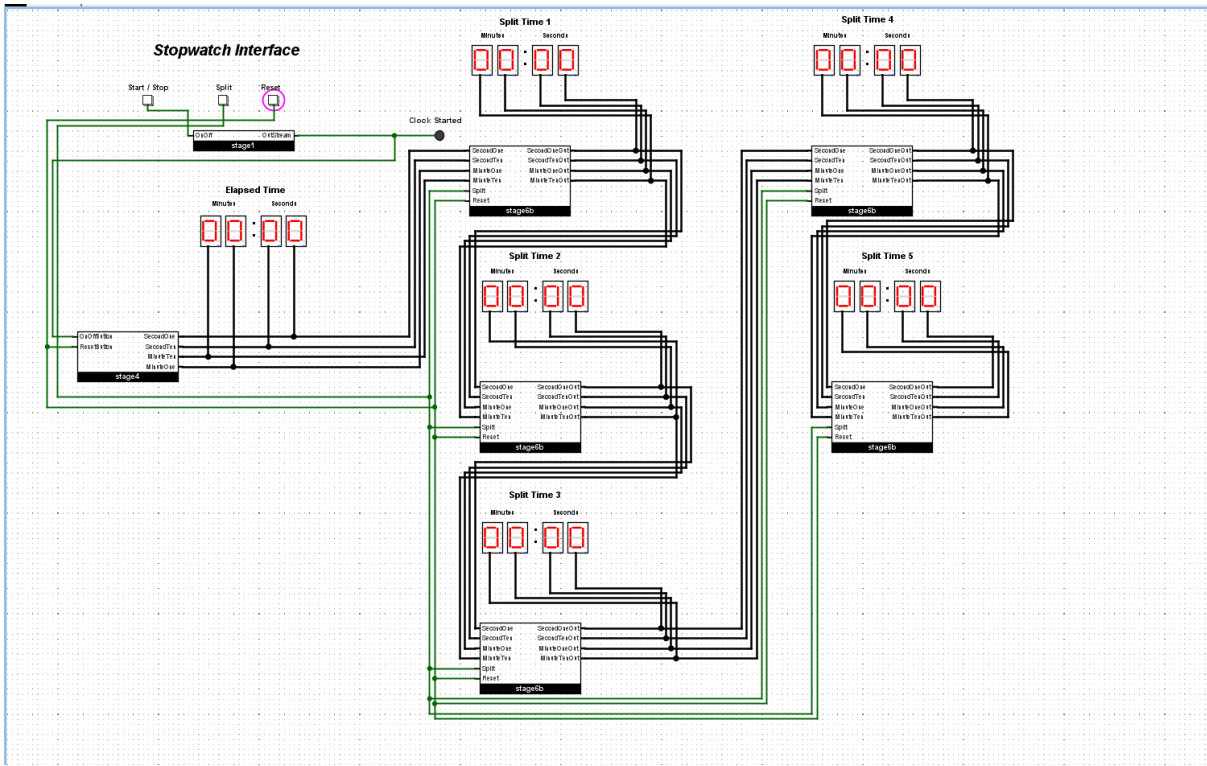
## **Assignment 1 - Stop Watch**

***Link to video demonstrating:***

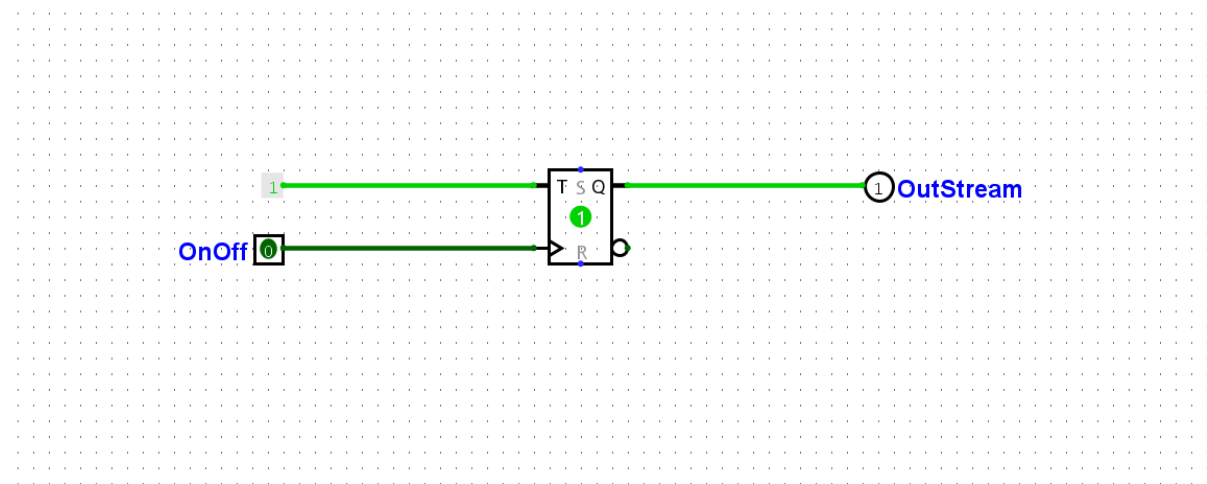
***Github:*** <https://github.com/quynezz/COS10004>

***Youtube:*** <https://www.youtube.com/watch?v=T77IJbmTtZQ>

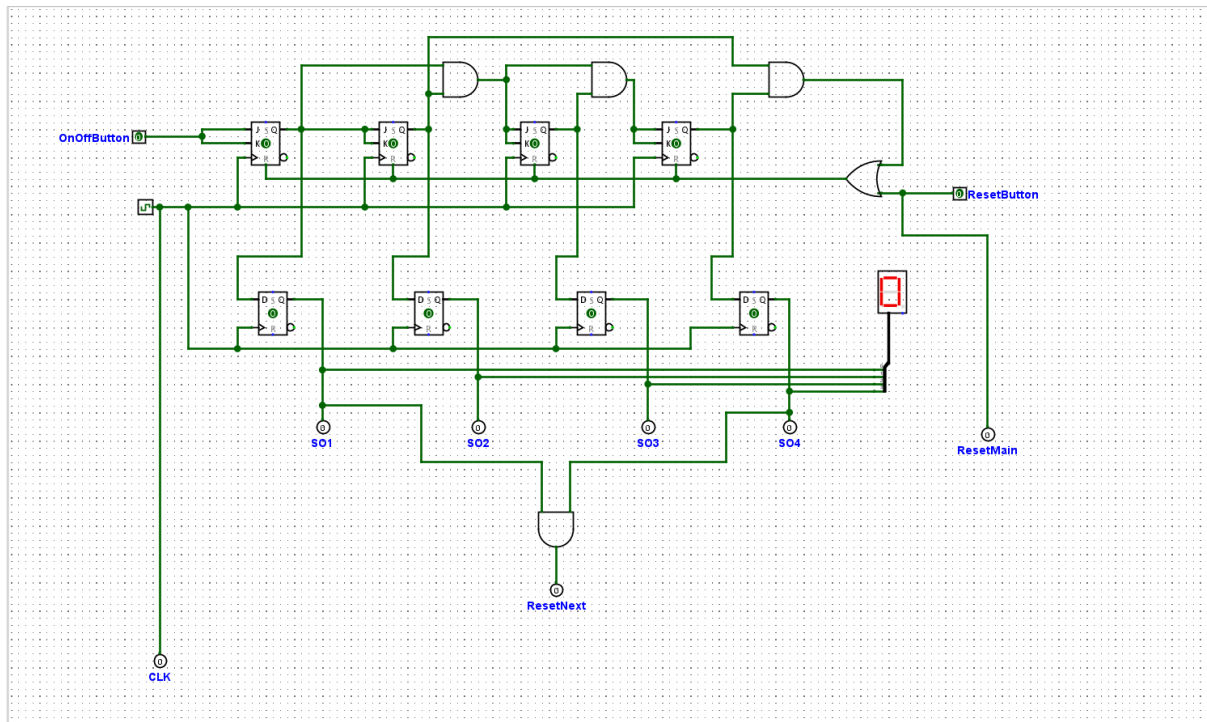
Main Circuit:



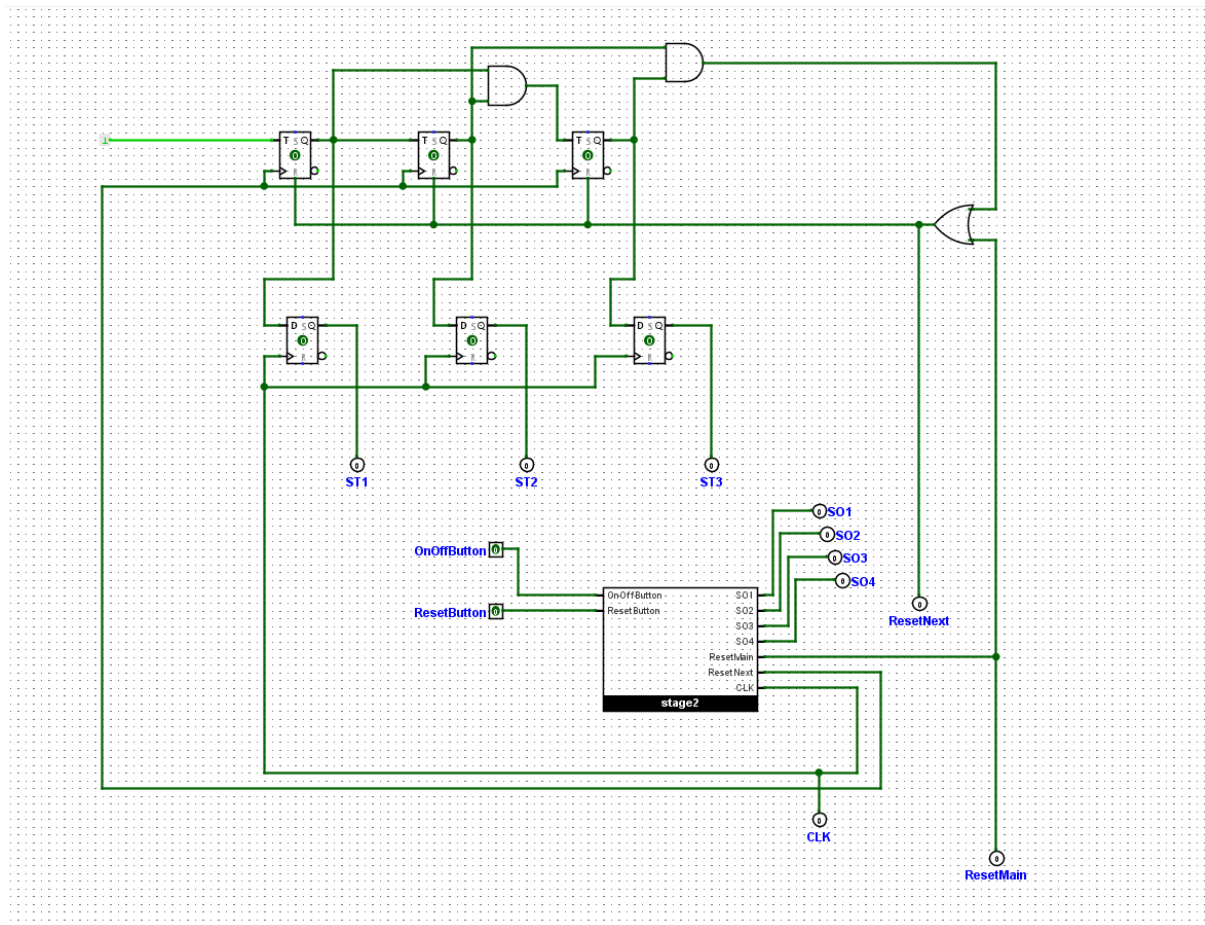
Stage 1:



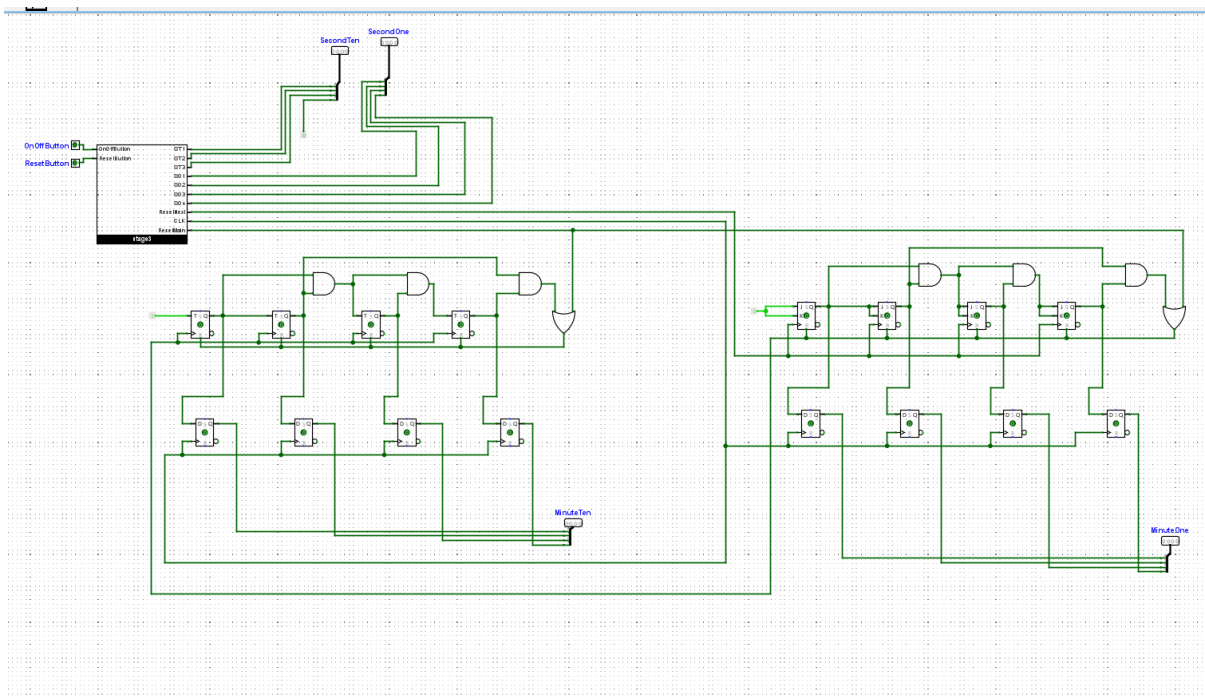
## Stage 2:



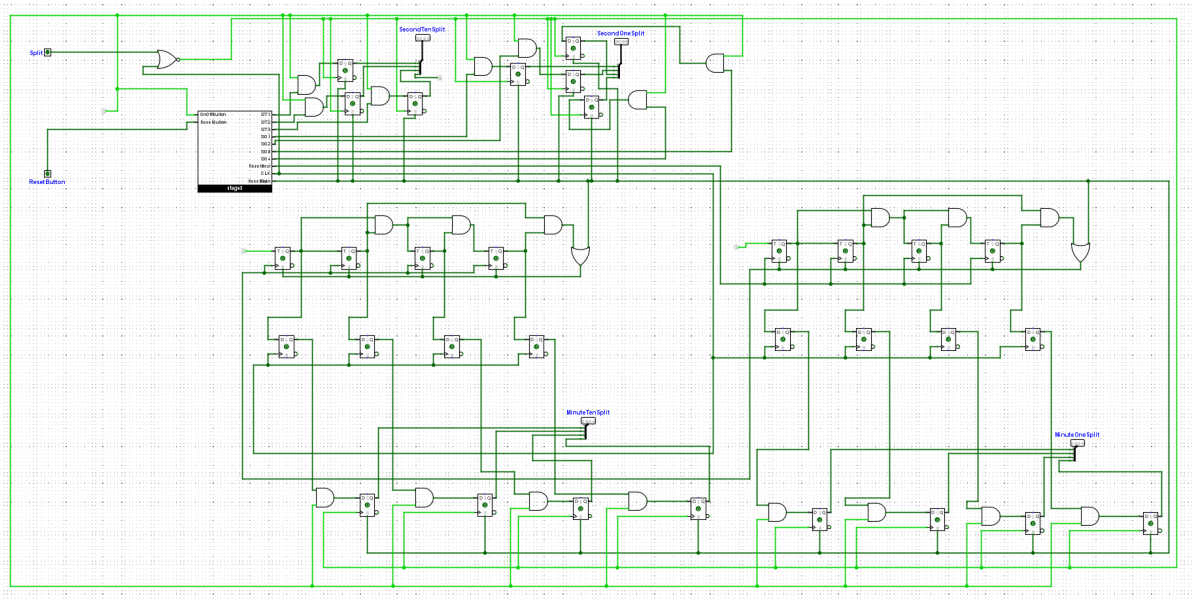
### Stage 3:



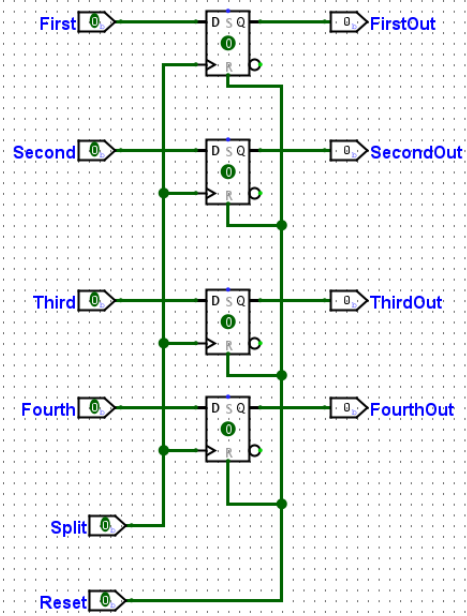
### Stage 4:



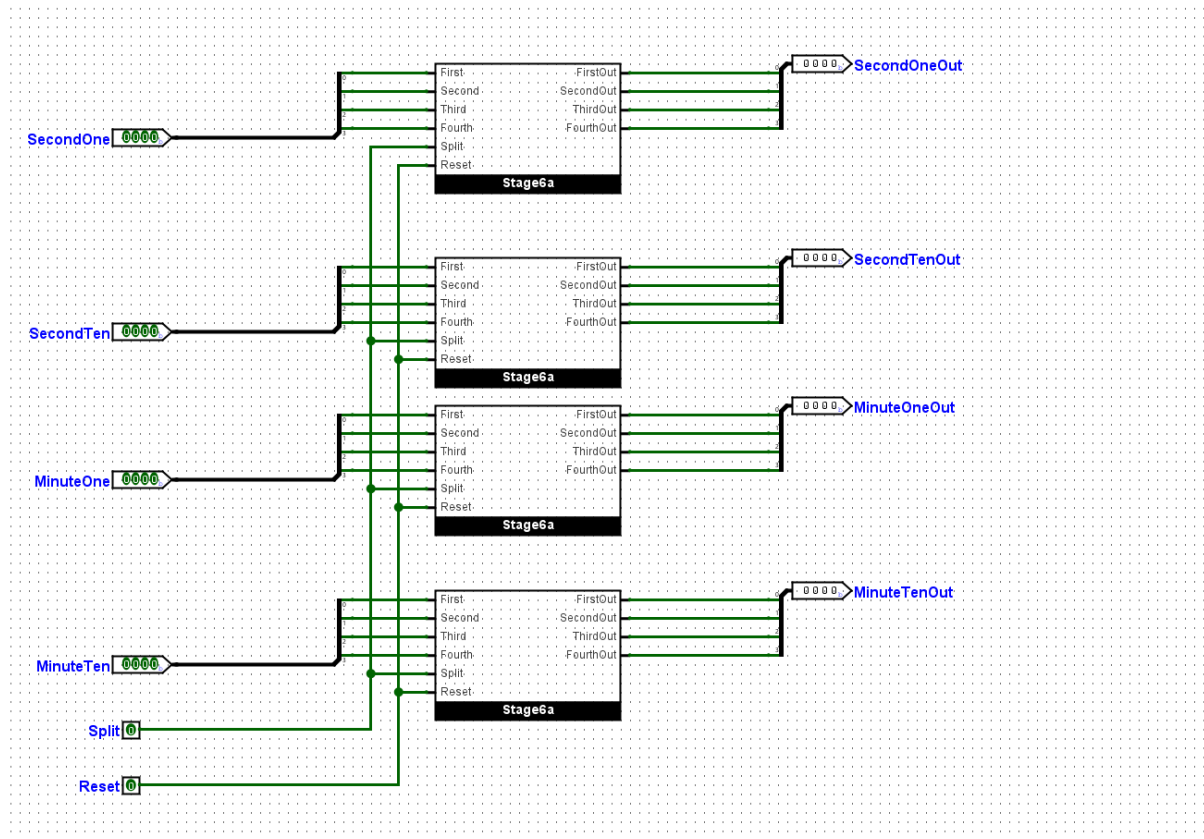
## Stage 5:



## Stage 6a:



## Stage6b:



## Each Stage and Complete Circuit:

Name	Date modified	Type	Size
Assignment1-4	9/25/2024 5:45 PM	Foxit Reader PDF ...	292 KB
Assignment1-StopWatchComplete	10/10/2024 8:53 PM	Logisim circuit file	91 KB
StopWatch-Stage1	10/3/2024 11:14 PM	Logisim circuit file	3 KB
StopWatch-Stage2	10/3/2024 11:17 PM	Logisim circuit file	11 KB
StopWatch-Stage3	10/3/2024 11:18 PM	Logisim circuit file	20 KB
StopWatch-Stage4	10/3/2024 11:19 PM	Logisim circuit file	36 KB
StopWatch-Stage5	10/3/2024 11:20 PM	Logisim circuit file	65 KB
StopWatch-Stage6	10/3/2024 11:20 PM	Logisim circuit file	75 KB