# ELF Relocatable Object File Format

ELF header (16 B)		Bootstrapping information for file
.text		Machine code of compiled module
.rodata		Read-only data (e.g., printf format strings, jump tables)
.data		Initialized global / static variables
.bss		Uninitialized static variables + those initialized to 0
.symtab		Symbol table
.rel.text		List of .text locations that need to be modified
.rel.data		List of .data locations that need to be modified
.debug	optional	Debugging symbol table
.line	optional	Mapping between source line #s and .text instructions
.strtab		String table for symbols in .symtab, .debug, and section names
Section Header Table		Fixed-size entries describing each section

- Input: A collection  $M = \{M_1, ..., M_k\}$  of relocatable object modules.
  - Let module  $M_i=(T_i,D_i,\Sigma_i)$ , with  $T_i$  denoting its .text section,  $D_i$  its .data section, and  $\Sigma_i$  its symbol table.
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- Relocation: Merge .text and .data sections from multiple relocatable object modules in a coordinated manner.
  - Create a single .text section T from  $\{T_1, \dots, T_k\}$ , and a single .data section D from  $\{D_1, \dots, D_k\}$ .
  - Adjust any text and data symbol references that need to change as a result of this merge.

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- Output: The binary executable or shared object file.

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  - These are compile-time symbols. They are not even described in the module's symbol table.
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  - An activation record is instantiated on the run-time stack for each invocation of the method.
- Procedure-local static variables are allocated in the
   data or .bss sections of the module.
  - These are also compile-time symbols, but they are described in the module's symbol table as they require relocation.
  - The compiler ensures that there is only one definition per module for each procedure-local static variable, and creates a unique name for it.

# More on Link-Time Symbols

- Useful Boolean function on symbols:
  - DEF (s, m) = true, if module m contains a definition of symbol s; false, otherwise.
  - USE (s, m) = true, if module m contains one or more references to symbol s; **false**, otherwise.
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- With reference to module m, link-time symbol s is said to be:
  - Local, if DEF $(s, m) \land \neg VISIBLE(s)$ .
  - Global, if  $DEF(s, m) \wedge VISIBLE(s)$ .
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- The linker resolves external symbol references with global symbol definitions across modules.
  - When the compiler encounters an external symbol, it generates a linktime symbol in the module's symbol table, and passes it on to the linker to resolve.
  - The linker tries to find a unique definition for this symbol reference among the symbol definitions across all the modules that it is linking.

# Link-Time Symbols: Examples

main.c

```
sum.c
```

```
int sum(int*, int);
int array[2] = {1,2};
int main(void) {
   int val = sum(array, 2);
   return val;
}
```

```
int sum(int *a, int n) {
   int s = 0;
   for (int i = 0; i < n; i++)
        s += a[i];
   return s;
}</pre>
```

• The symbol sum is global.

- The symbol sum is **external**.
- The symbol array is **global**.
- The symbol main is **global**.

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}</pre>
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- The symbol sum is **external**.
- The symbol array is global.
- The symbol main is **global**.

#### sum1.c

```
static int sum(int *a, int n) {
   int s = 0;
   for (int i = 0; i < n; i++)
       s += a[i];
   return s;
}</pre>
```

• The symbol sum is **local**.

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- The *global* symbols of module m are further divided into *strong* and *weak* symbols.
  - Function names and initialized variables are strong symbols.
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  - Function names and initialized variables are strong symbols.
  - Uninitialized variables are weak symbols.
- Let s and w be the total number of strong and weak definitions matching an external reference r.
  - If s = w = 0, declare a link-time error: no definition found.
  - If s > 1, declare a link-time error: multiple strong definitions.
  - If s = 1, resolve r to the *unique* strong definition.
  - If  $s = 0 \land w = 1$ , resolve r to the *unique* weak definition.
  - If  $s = 0 \land w > 1$ , resolve r to an *arbitrary* weak definition.