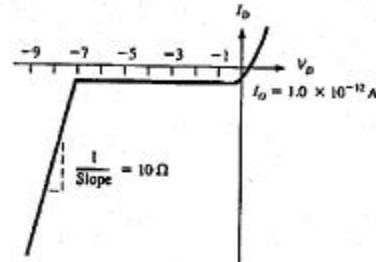
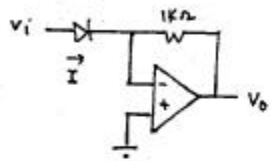


7. In the ideal op amp circuit, the diode has the reverse characteristics as shown. The

diode equation is  $I_D = I_0 \exp \frac{V_D}{26mV}$ . (a) Find  $I$  and  $V_o$  if  $V_i = 0.6V$ .

(b) Find  $I$  and  $V_o$  if  $V_i = -7.1V$ . Sketch also the model of the diode at breakdown. (27)



$$\begin{aligned} (a) \quad I &= I_0 \exp \frac{V_D}{26m} \\ &= (10^{-12}) \exp \left( \frac{600m}{26m} \right) \\ &= 10.5 \text{ mA} \end{aligned}$$

$$\therefore V_o = -I(1k) = -10.5V$$

12

(b) diode breakdown

$$I = -\frac{0.1V}{10\Omega} = -0.01A$$

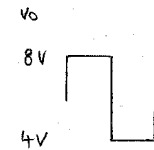
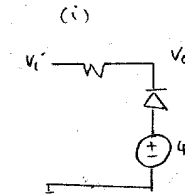
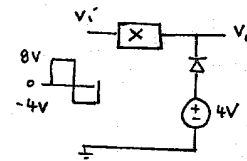
$$\therefore V_o = -I(1k) = 10V$$

12

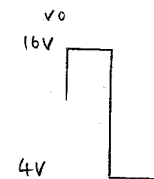
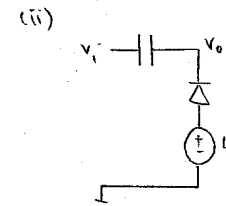


3

8. (a) In the ideal diode circuit, sketch  $V_o(t)$  (i) if  $X$  is a resistor  $R$ , and (ii) if  $X$  is a capacitor  $C$ . Show clearly the voltages in your sketch. (19)

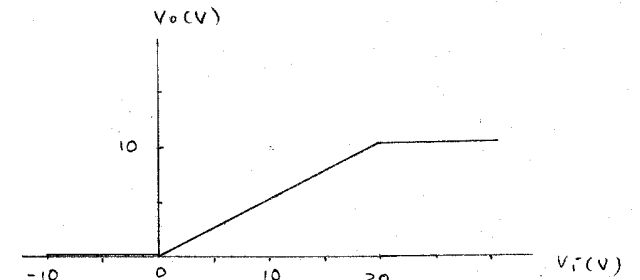
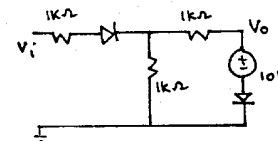


7



12

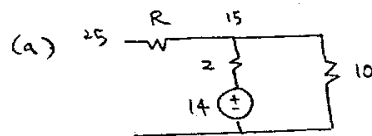
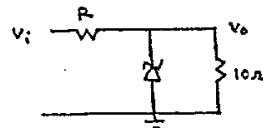
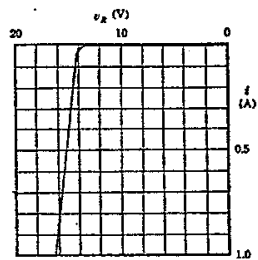
(b) In the ideal diode circuit, plot  $V_o$  versus  $V_i$  for  $-10V \leq V_i \leq 30V$ . Show clearly all voltages in your sketch. (12)



12

5. In the following regulator circuit, the Zener diode has the reverse characteristics as shown.

(a) If  $V_o = 15V$  when  $V_i = 25V$ , show that  $R$  is around  $5\Omega$ . (b) If  $R$  is  $5\Omega$ , estimate the minimum  $V_i$  for the regulator circuit. (16)



$$r_z \sim \frac{2V}{1A} \sim 2\Omega$$

$$V_z \sim 14V$$

$$\frac{25 - 15}{R} = \frac{15 - 14}{2} + \frac{15}{10}$$

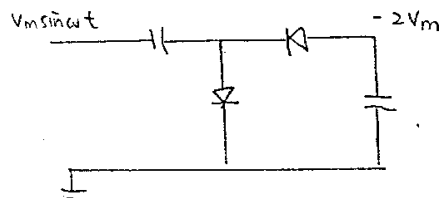
$$\therefore R = 5\Omega$$

(b) min  $V_i$ : When  $I_z \sim 0.05A$ ,  $V_o \sim 14.1V$

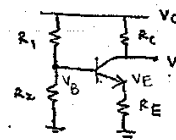
$$\frac{V_i - 14.1}{5} = \frac{14.1}{10} + 0.05$$

$$V_i = 21.4V$$

(6) Design a diode circuit that can give an output voltage of  $-2V_m$  if the input voltage is  $V_m \sin \omega t$ . (10)



9. For the following self-bias CE BJT amplifier, explain briefly how this circuit stabilises the collector current. (6)



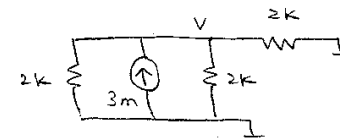
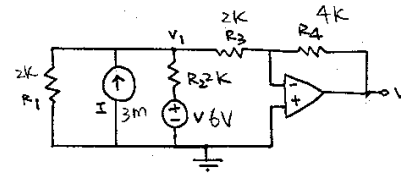
$$\text{e.g. } I_c \uparrow \quad I_E \uparrow \quad V_E \uparrow$$

$$\therefore V_B = V_{BE} + V_E = \text{constant}$$

$$\therefore V_{BE} \downarrow \quad I_B \downarrow \quad I_c \downarrow$$

6

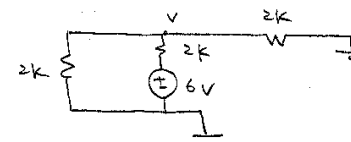
10. Use superposition theorem or otherwise, find  $V_1$  and show that  $V_o = -8V$  for the circuit below. Assume the op amp is ideal and operating in the linear region. Given  $V = 6V$ ,  $R_1 = R_2 = R_3 = 2k\Omega$ ,  $R_4 = 4k\Omega$ ,  $I = 3mA$ . (16)



$$V_{3mA} = 1m \times 2k$$

$$= 2V$$

6



$$V_{6V} = 6V \times \frac{1k}{3k}$$

$$= 2V$$

6

$$\therefore V_o = -\frac{4k}{2k} (2V + 2V)$$

$$= -8V$$

4

(c) at resonance,  $Z = R = \frac{1}{2} \Omega$  3

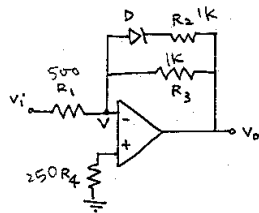
$$\therefore P = \frac{V^2}{R} = \frac{\left(\frac{1}{\sqrt{2}}\right)^2}{\frac{1}{2}} \quad 5$$

$$= 1W$$

15. Assume the op amp and diode are ideal. The op amp is operating in the linear region.

Given  $R_2 = R_3 = 1k\Omega$ ,  $R_1 = 500\Omega$ ,  $R_4 = 250\Omega$ . (a) Show that  $V = 0V$ . (2)

(b) For  $-2V \leq V_i \leq 2V$ , find  $V_o/V_i$  and plot  $V_o$  versus  $V_i$ . Label all slopes and intercepts. (18)



$$\therefore V_+ = 0$$

$$\therefore V = V_- = 0V \quad 2$$

For  $V_i > 0$  D on

$$\frac{V_o}{V_i} = -\frac{500}{500} = -1 \quad 6$$

For  $V_i < 0$  D off

$$\frac{V_o}{V_i} = -\frac{1k}{500} = -2 \quad 6$$

