



Ruth Jocelyn De Guzman

Singapore PR |  98264782 |  ruthjf.16@gmail.com

12 years of progressive experience with **Project Management** and **Data Analyst** in a MNC company. Highly analytical and process oriented data analyst with significant experience in research, data management, qualitative and quantitative analysis and in-depth knowledge of database types. Extensive skills with Product and Project Management: Software and Hardware related. A **Six Sigma Green Belt** Certified with Top Company project award. A self-motivator with **“can-do-attitude”** always excited to learn new things, resourceful, persistent, fast learner, results-oriented, adaptable, organized, decisive, energetic, innovative, having good interpersonal skills, strong analytical and quantitative skills and a positive work ethic. Interested in pursuing a **challenging analyst** role in a reputable company with an opportunity for career growth and advancement utilizing my technical skills, abilities and strong passion with data in achieving the target and developing the best performance in the organization

SKILLS and COMPETENCIES

- Data Analyst
- Python Scripting
- Perl Scripting
- Visual Basic
- MATLAB
- C++
- Minitab
- Jira Integration (Confluence)
- Agile PLM (Change Analyst)
- IBM SPSS Modeler
- SQL & Data Warehouse DB
- Six Sigma Green Belt Certified
- Excel Expert Accelerated
- Bill of Materials (Electronics)
- PCBA ASM & Schematic DRWGs
- New Product Introduction
- Product on-bench validation

WORK EXPERIENCES



SENIOR ENGINEER, PRODUCT DEVELOPMENT | MICRON SEMICONDUCTOR ASIA | JAN 2021 – PRESENT

Products: World leader in innovative memory solutions such as: DRAM, NAND, NOR, and 3D XPoint™ memory

- Responsible for Development of New Product Line and Yield/Quality Improvement
- Understanding system/customer requirement and optimization of the products to meet requirement
- Work with the wafer fab process/integration group to address process-related defects affecting product yield
- Identify design marginalities and recommend design fix for circuit-related problems
- Define algorithms to be implemented in our production test flows to better optimize RWB, performance, reliability, and yield at a die-level, and to reduce variation across the manufacturing line;
- Participate in cross-functional efforts to document, standardize, and improve our capability to optimize RWB on future products.

SENIOR ENGINEER, INDUSTRIAL AUTOMATION | ST MICROELECTRONICS ASIA PACIFIC | JUNE '18 – SEP '19

Products: Integrated Device Manufacturer delivering solutions that are key to Smart Driving and the Internet of Things



- Business Project Management on start-up to deployment of Industry 4.0/Automation projects (e.g. Single Device Traceability) across ST Backend High Volume Manufacturing Plants located at: China, Morocco and Malaysia
- Capital Expenditure (CapEx) Procurement Management globally
 - Collaborate with cross-functional team globally to review yearly cost budgets for product and process improvement
 - Engage with key stakeholders across multiple business units to ensure month-end receivables target are achieved

SENIOR PRODUCT ENGINEER/DATA ANALYST | WESTERN DIGITAL (formerly Hitachi Singapore) | MAY '07 – JULY'17

Products: Enterprise HDDs (SAS, FC) and SSDs (SAS, PCIe)



Data Analyst Responsibilities:

- Extract, Analyze and Interpret large sets of structured and unstructured data from SQL server and Data Warehouse database using a variety of techniques from statistical modeling to machine learning algorithms, using different statistical and analytical tools and programs such as Minitab, IBM SPSS Clementine, Python, Perl or MATLAB
- Build analytic modeling tools that utilize the data pipeline to provide actionable insights for resolving certain manufacturing error code, improving operational efficiency and other key business performance metrics
- Support customer integration failures by data analysis and recommend design modification or specification optimization to attain highest performance capability
- Explore Manufacturing Test Process by applying statistical and data analysis, put up the proposal of enhanced test algorithm for test time reduction, yield or quality improvement

Test Code Integration Responsibilities:

- Supervise software installation of multi-functional coders in Tortoise SVN CCB of manufacturing branches to ensure only proven effective code and approve test codes will check-in

- Review of test coders output (in C++ script) to ensure that it fits the technical proposal before proceeding with Engineering trial and Deploying to High Volume Manufacturing
- Chair weekly conference calls and web meetings with internal and overseas cross-functional teams to lead discussions in qualifying test code changes for the purpose of yield improvement, process enhancement, high quality product, cost savings or customer requirement.

Product Change Management Responsibilities:

- Lead a team of new project deployment on manufacturing test enhancement from brainstorming, data review, qualification, and full implementation to support business profit and revenue
- Develop test procedures for new Electronic components, perform engineering evaluation testing and release to manufacturing in a timely manner
- Create Firmware package, integrate into Drive ASM BOM (Bill of Materials) and perform product validation On-bench
- Debug and carry out experiments to resolve test issue, while seeking R&D team support for high level cases
- Drive timely Electronic components setup: new BOM and drawing releases and collaborating with cross-functional teams for trial build, execution, and release to production to support new product introduction and sustaining of existing products
- Manage supplier change request that could affect drive performance such as: tooling, new line set-up and maintenance, system installation, new test plan development and design improvement, ensuring that proper control system and protection methods are always in place
- Chair weekly conference calls and web meetings with internal and overseas cross-functional/non-functional teams for a lead discussions in qualifying new electronic components for the implementation of new designs, new/revised specifications, process or material changes with the objective for yield improvement, process enhancement, high quality product, 2nd source qualification, parts EOL replacement, cost savings or customer requirement.
- Lead a team in re-processing Drive RMA customer returns from field processing to re-shipping

PROCESS & FAILURE ANALYSIS ENGINEER | Toshiba Information Equipment (Phils) Inc | JUN 2004 – APR 2007

Products: Desktop/Mobile/Consumer Electronics HDDs



- Key channel to communicate with Development Engineering Team in Japan for NPI knowledge transfer from Design Phase to Manufacturing Phase (HVM)
- Key player in conducting pre-production of new model prior to mass production to determine manufacturability and compatibility in production lines, which include test and process design and Shop floor system compatibility assessment
- Continuous product and process improvement through data analysis and DOE
- Process documentation and management; Work Instruction and Process flow
- Conduct meeting with multi-functional teams for all process related instructions
- Weekly yield data monitoring, compilation, analysis and reporting to drive for process/product yield Improvement
- Perform Drive Failure Analysis On-Bench using Oscilloscope and Niagara software to determine the root cause and provide immediate and long term solutions

EDUCATION

Bachelors Degree: Major in Electronics and Communications Engineering

Institution: Technological University of the Philippines, Manila

SEMINARS and TRAININGS

Singapore	<ul style="list-style-type: none"> • Understanding SQL Scripting (ITE) • Effective Technical Presentations (NTU) • Stack-Up Tolerance for Design Evaluation GD&T (SQI) • 8D Problem Solving Methodology and RCA (GTCTS) 	<ul style="list-style-type: none"> • Visual Basic Application (Eagle IC) • MSO Advanced Power Point (SWS) • MSO Excel Expert Accelerated (Eagle IC)
Philippines	<ul style="list-style-type: none"> • Six Sigma Methodology (DMAIC) • Process Reliability 	<ul style="list-style-type: none"> • Failure Mode and Effect Analysis

Availability 1 month notice

Reference Available upon request